

New Product Data Sheets

Data Sheet	Description
ispLSI 2064V	New 3.3 Volt ispLSI Device: 70 or 37 I/O, 64 Macrocells, ispLSI 2000 Architecture
ispLSI 2096V	New 3.3 Volt ispLSI Device: 104 I/O, 96 Macrocells, ispLSI 2000 Architecture
ispLSI 2128V	New 3.3 Volt ispLSI Device: 138 or 74 I/O, 128 Macrocells, ispLSI 2000 Architecture
ispLSI 3160	New ispLSI Device: 160 I/O, 160 Macrocells, ispLSI 3000 Architecture
ispLSI 3256E	New ispLSI Device: 258 I/O, 256 Macrocells, ispLSI 3000 Architecture
ispLSI 3256A	New ispLSI Device: 128 I/O, 256 Macrocells, ispLSI 3000 Architecture

Updates to Existing Data Sheets

Data Sheet	Description
ispLSI 1016E	Addition of New 80 MHz Speed Grade
ispLSI 1048C	Final SMD Numbers for Military Grade Devices
ispLSI 2032	Addition of New 180 MHz Speed Grade
ispLSI 2032V/LV	Addition of New 100 MHz Speed Grade
GAL16V8	Addition of New Speed Grades for the 16V8D
GAL16LV8	New 5V Compatible Inputs for GAL16LV8D
GAL20LV8	New 5V Compatible Inputs for GAL20LV8D
GAL22LV10	New 5V Compatible Inputs for GAL22LV10D
ispGAL22V10	Addition of 28-Pin SSOP Pin Configuration
pDS Software	Addition of Multiple Edit Windows
ISP Synario System	New Support for Windows 95 and Additional Devices
pDS+ Synario Software	New Support for Windows 95 and Additional Devices

New Application Notes

- [Lattice ISP in Cellular Switching Stations](#)
- [Implementing Lattice ISP and Boundary Scan Daisy Chains](#)
- [The Basics of One-Wire ISP with an ISP-IrDA Example](#)
- [DMA Controller Using an ispLSI 6192SM](#)
- [PCMCIA Interface in an ispLSI 2064](#)

What's New

[Multiple FIFO Configuration in ispLSI 6192](#)

[24-Bit Adder Implementation in a CPLD](#)

[ispLSI 3192 to 6192 Design Conversion](#)

[Interfacing SDRAMs to Pentium Processors](#)

[Optimizing an ispLSI Design](#)

Other

Document	Description
Tape and Reel Specifications	Addition of Tape and Reel Specifications for TQFP, PQFP and MQFP Packages
Thermal Management	Addition of Thermal Resistance Specifications for 48-pin TQFP, 160-pin PQFP and 304-pin MQFP Packages
Package Diagrams	Addition of 160-pin PQFP Diagram
Worldwide Web Site	New Section Describing Lattice's Worldwide Web Site