

Package Diagrams

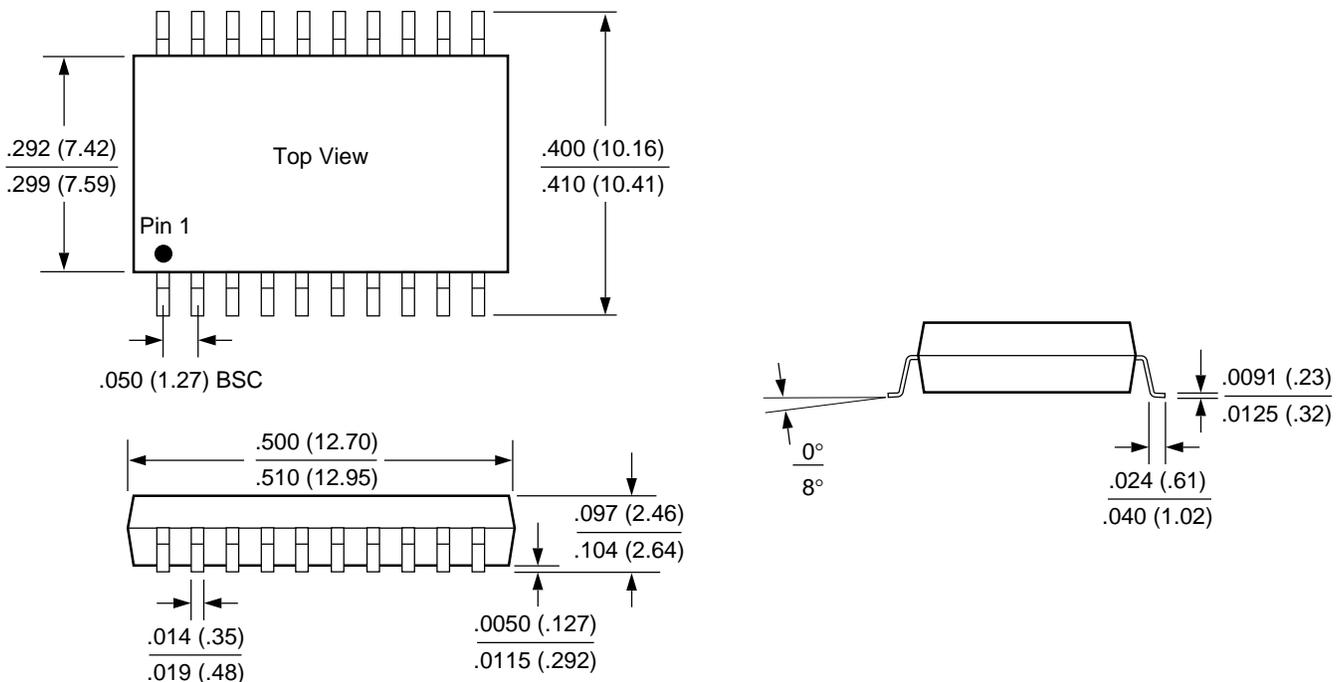
Index of Package Diagrams

20-Pin SOIC	1	120-Pin PQFP	12
20-Pin PLCC	2	128-Pin PQFP	12
20-Pin Plastic DIP	2	128-Pin TQFP	13
20-Pin LCC	3	133-Pin CPGA	13
20-Pin Cerdip	3	160-Pin MQFP	14
24-Pin Plastic DIP	4	160-Pin PQFP	14
24-Pin Cerdip	4	167-Pin CPGA	15
28-Pin SSOP	5	176-Pin TQFP	15
28-Pin PLCC	5	208-Pin MQFP	16
28-Pin Plastic DIP	6	240-Pin MQFP	16
28-Pin LCC	6	304-Pin MQFP	17
44-Pin PLCC	7		
44-Pin TQFP (T44)	7		
44-Pin TQFP (T)	8		
44-Pin JLCC	8		
48-Pin TQFP	9		
68-Pin PLCC	9		
68-Pin JLCC	10		
84-Pin PLCC	10		
84-Pin CPGA	11		
100-Pin TQFP	11		

20-Pin SOIC Package

Dimensions in Inches MIN. / MAX.

(Dimensions in millimeters, shown in parentheses, are for reference only)

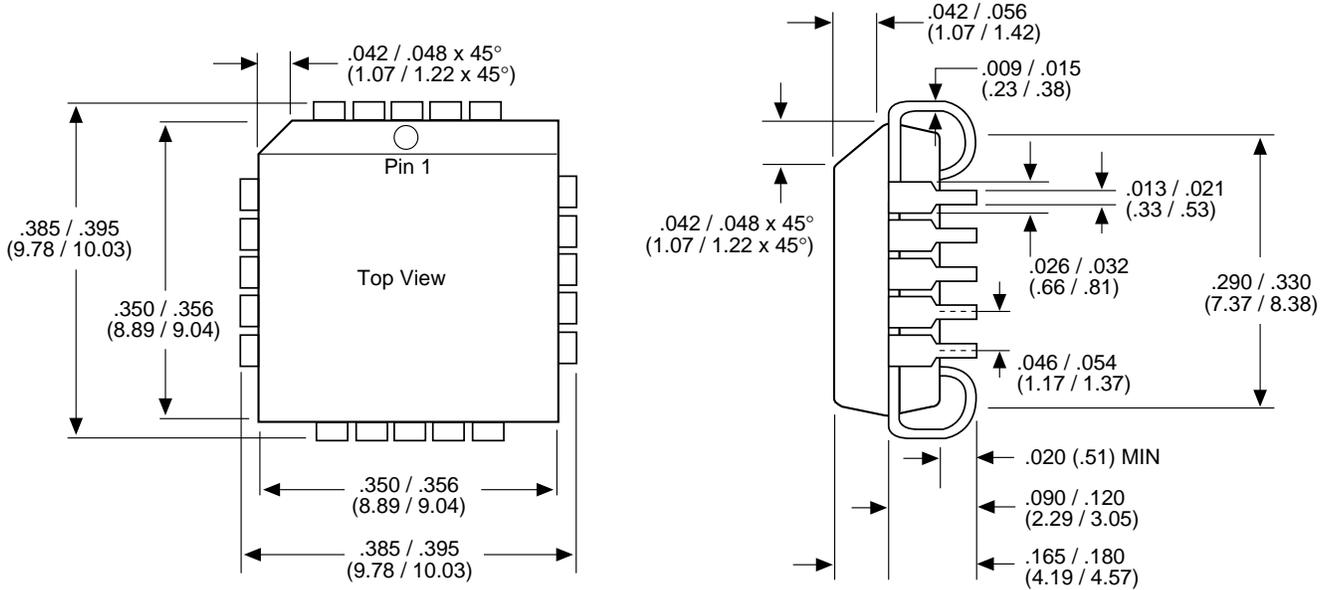


Package Diagrams

20-Pin PLCC Package

Dimensions in Inches MIN. / MAX.

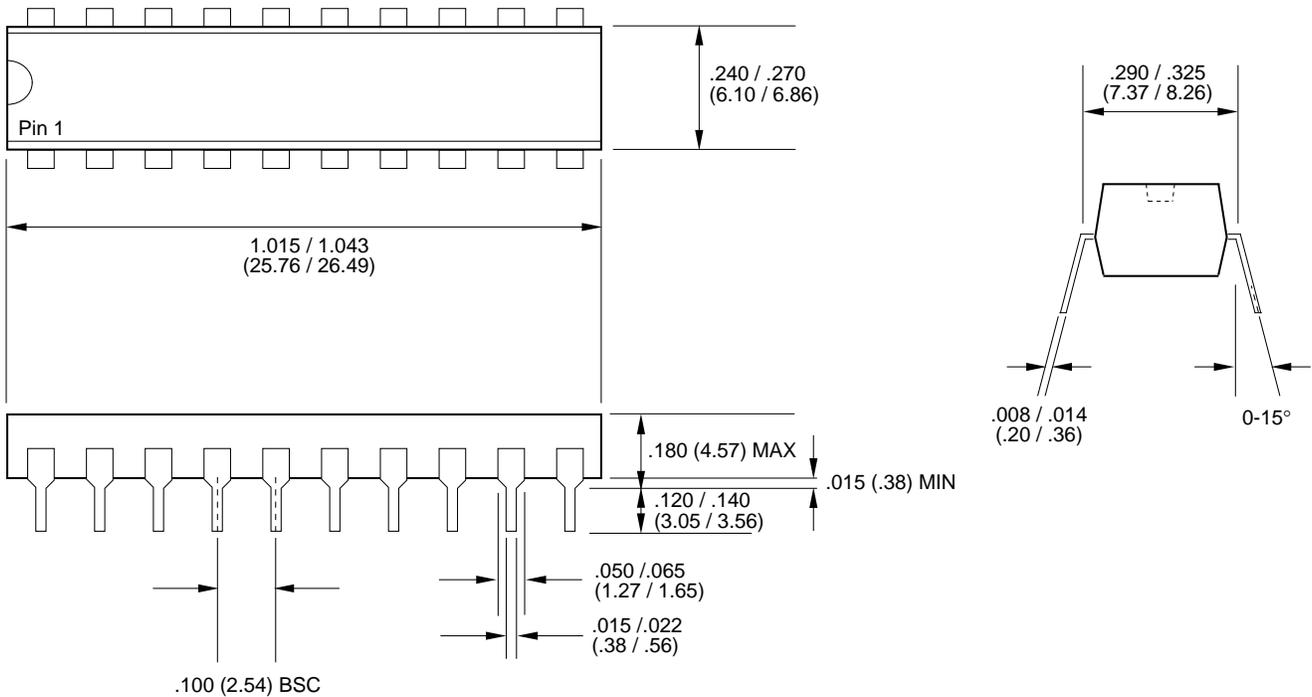
(Dimensions in millimeters, shown in parentheses, are for reference only)



20-Pin Plastic DIP

Dimensions in Inches MIN. / MAX.

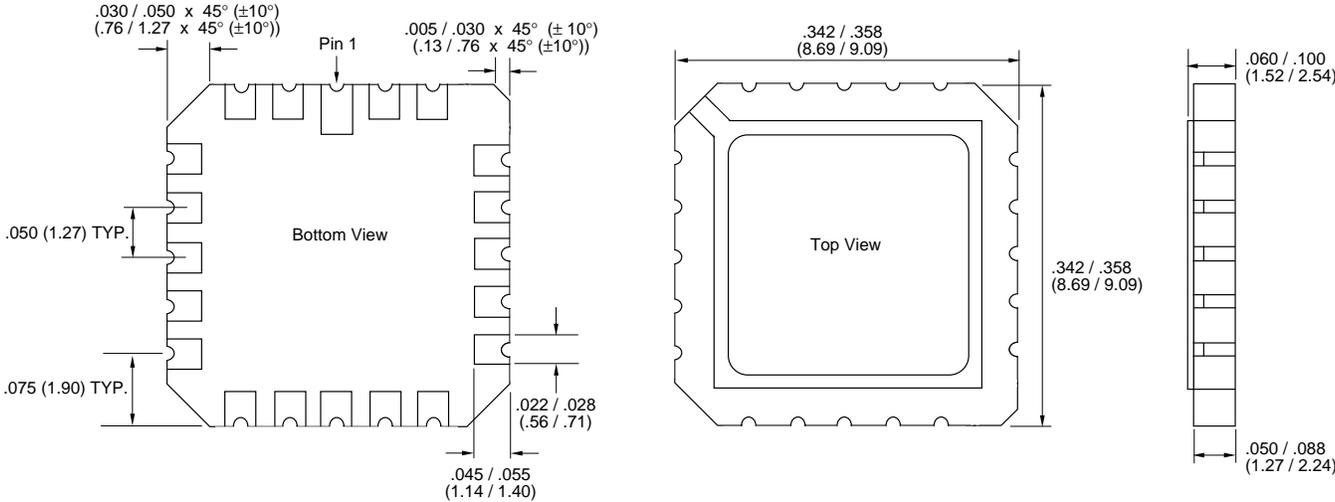
(Dimensions in millimeters, shown in parentheses, are for reference only)



20-Pin LCC

Dimensions in Inches MIN. / MAX.

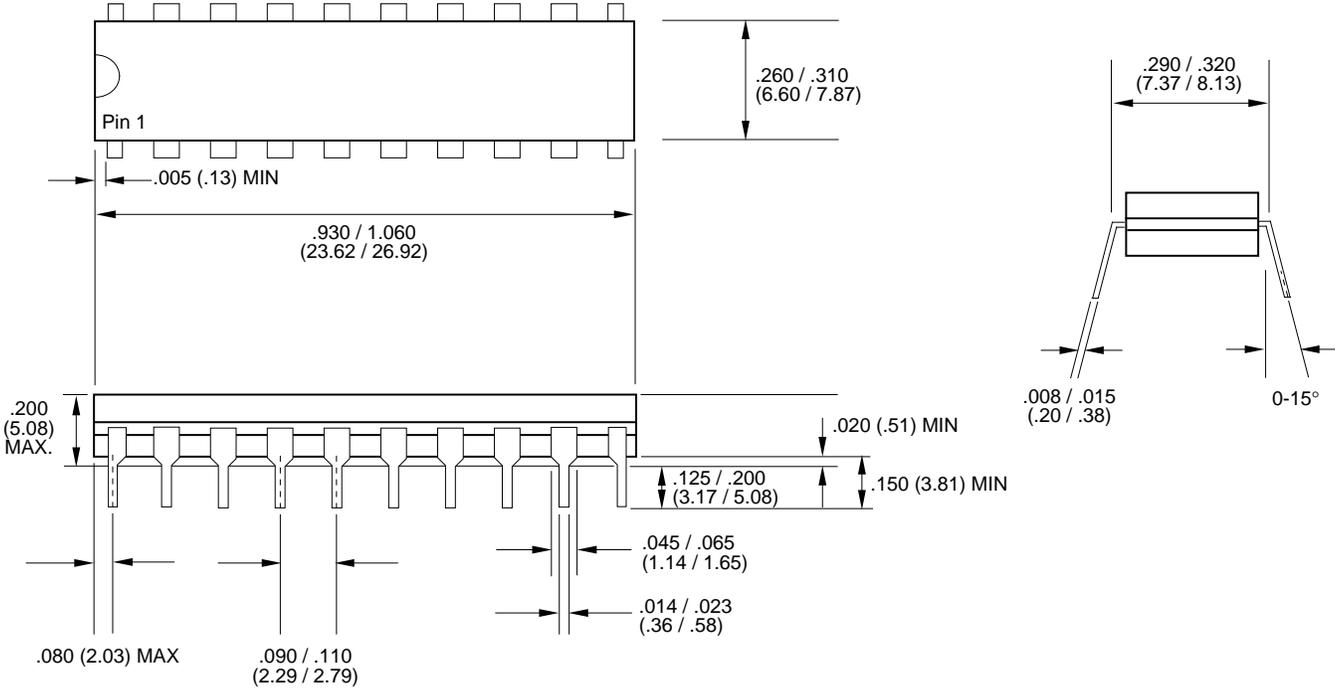
(Dimensions in millimeters, shown in parentheses, are for reference only)



20-Pin (300-Mil) Cerdip

Dimensions in Inches MIN. / MAX.

(Dimensions in millimeters, shown in parentheses, are for reference only)

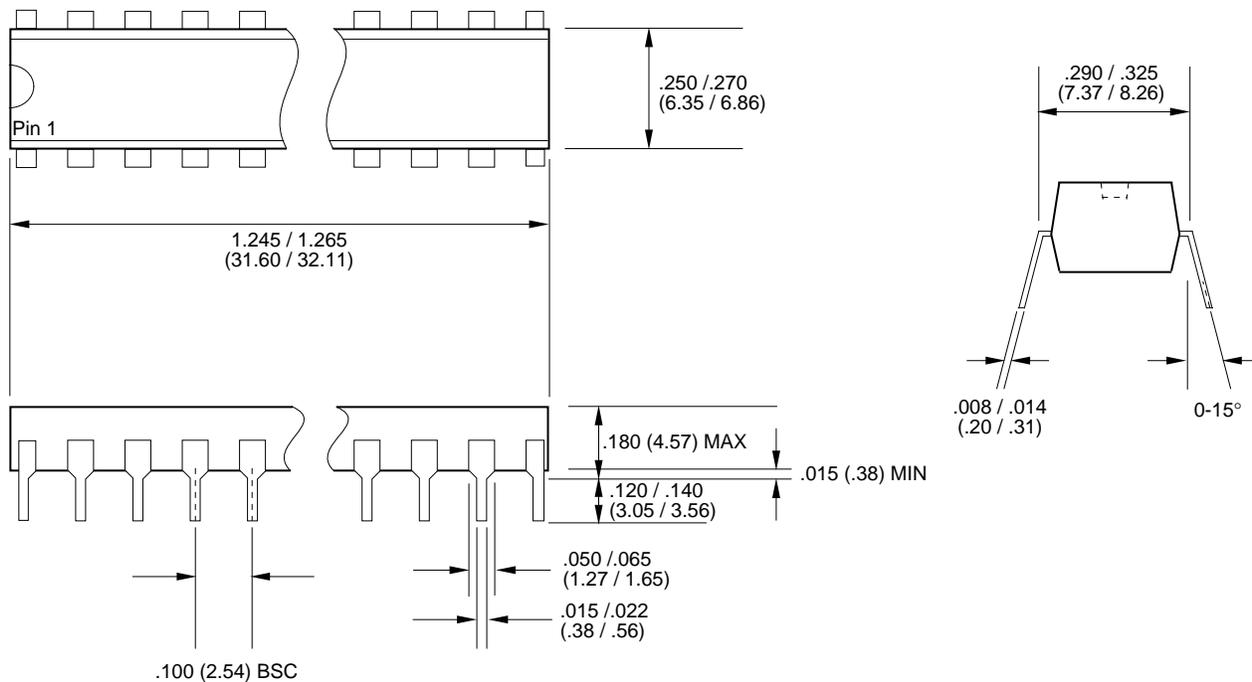


Package Diagrams

24-Pin Plastic DIP

Dimensions in Inches MIN. / MAX.

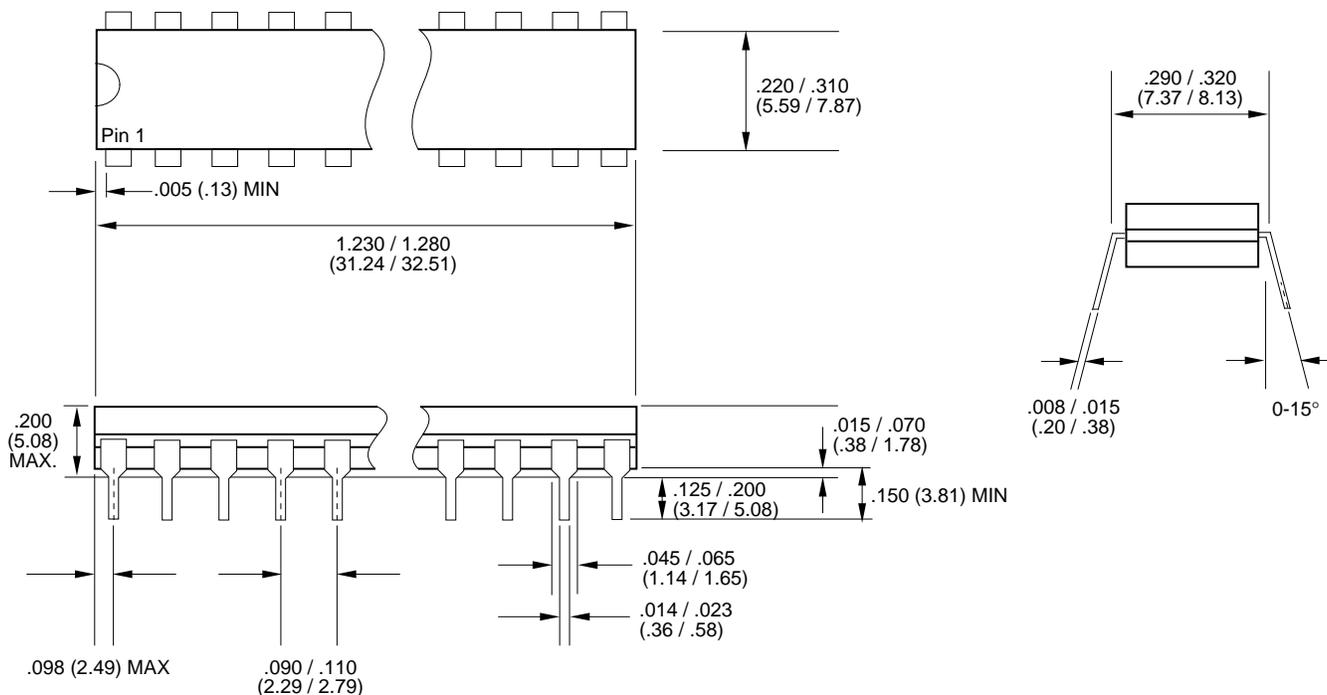
(Dimensions in millimeters, shown in parentheses, are for reference only)



24-Pin (300-Mil) Cerdip

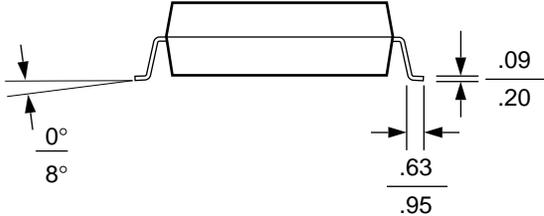
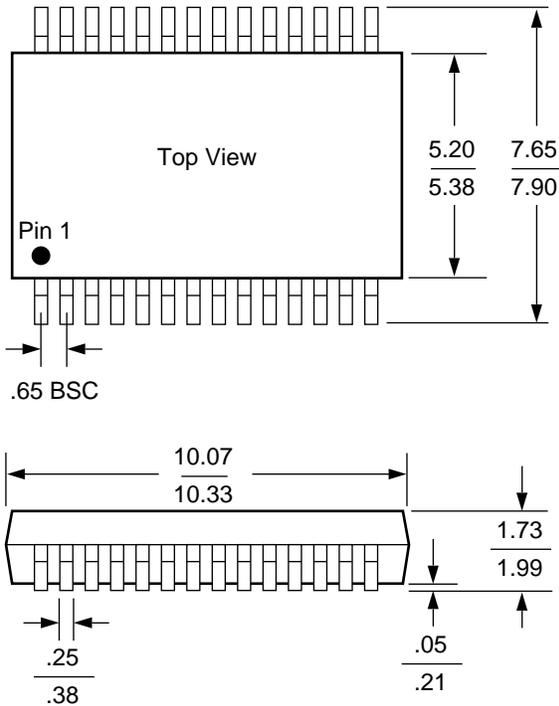
Dimensions in Inches MIN. / MAX.

(Dimensions in millimeters, shown in parentheses, are for reference only)



28-Pin SSOP

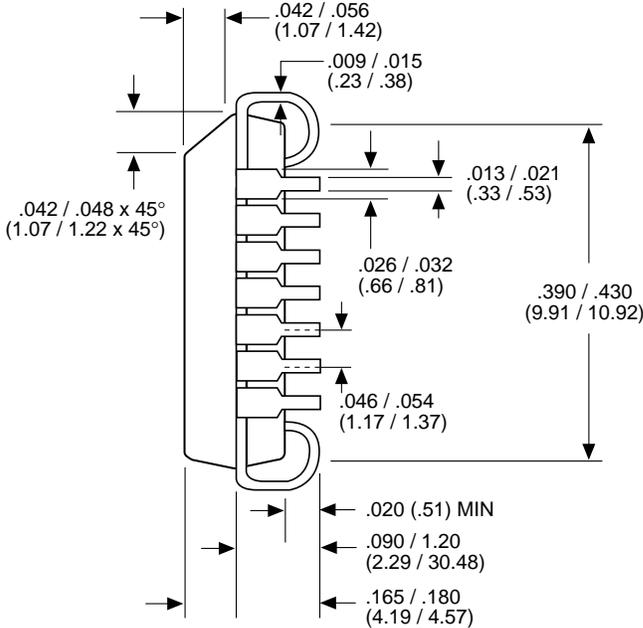
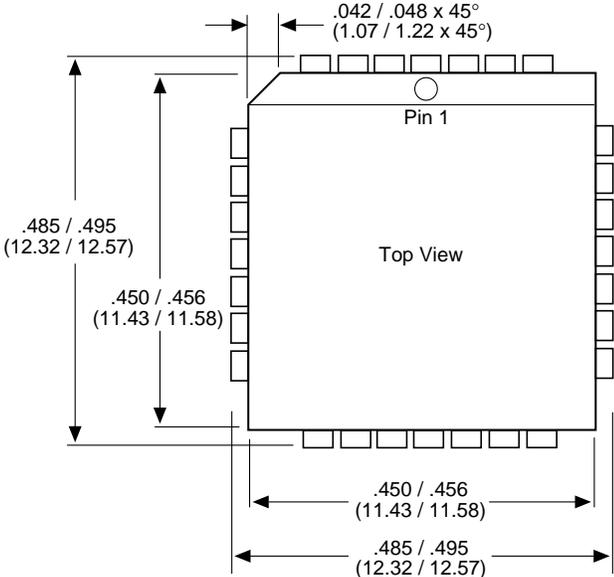
Dimensions in Millimeters MIN. / MAX.



28-Pin PLCC Package

Dimensions in Inches MIN. / MAX.

(Dimensions in millimeters, shown in parentheses, are for reference only)

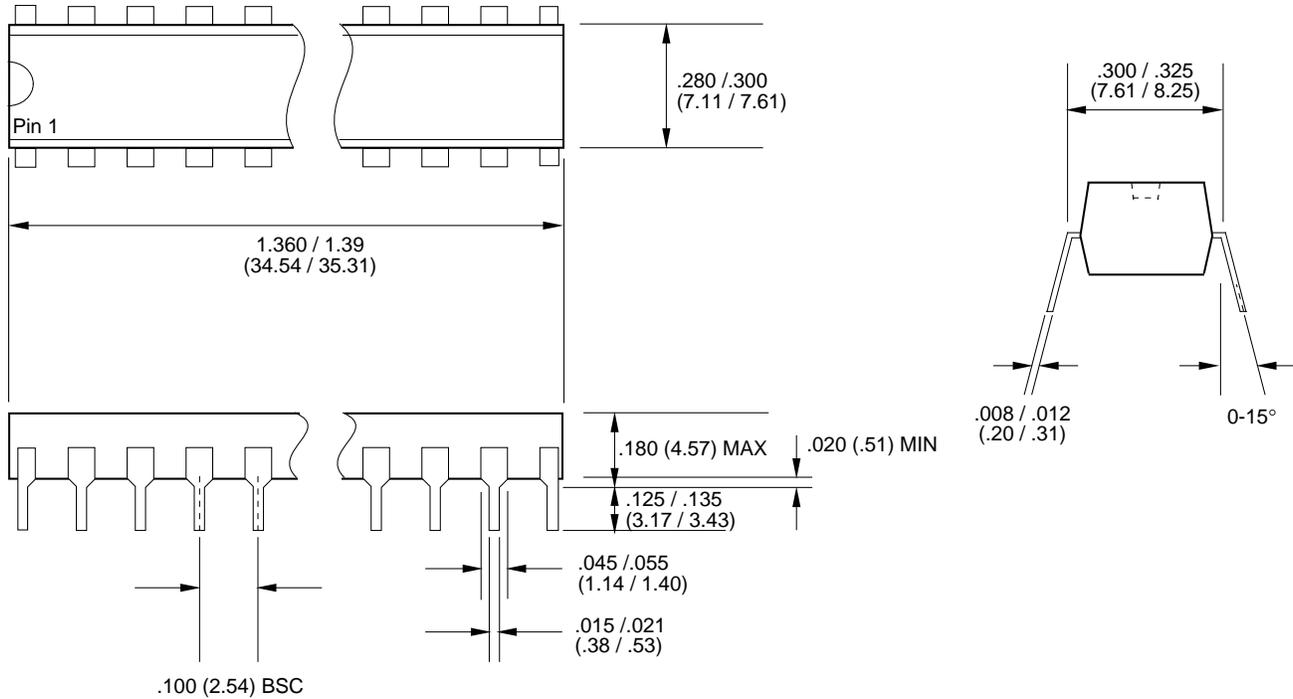


Package Diagrams

28-Pin Plastic DIP

Dimensions in Inches MIN. / MAX.

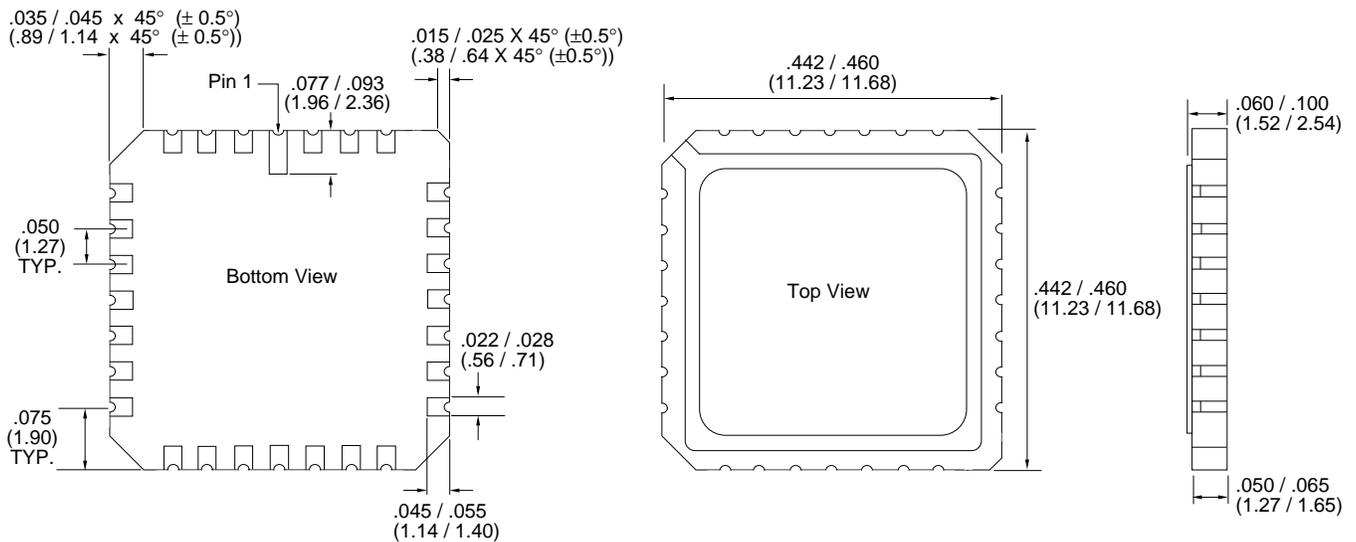
(Dimensions in millimeters, shown in parentheses, are for reference only)



28-Pin LCC

Dimensions in Inches MIN. / MAX.

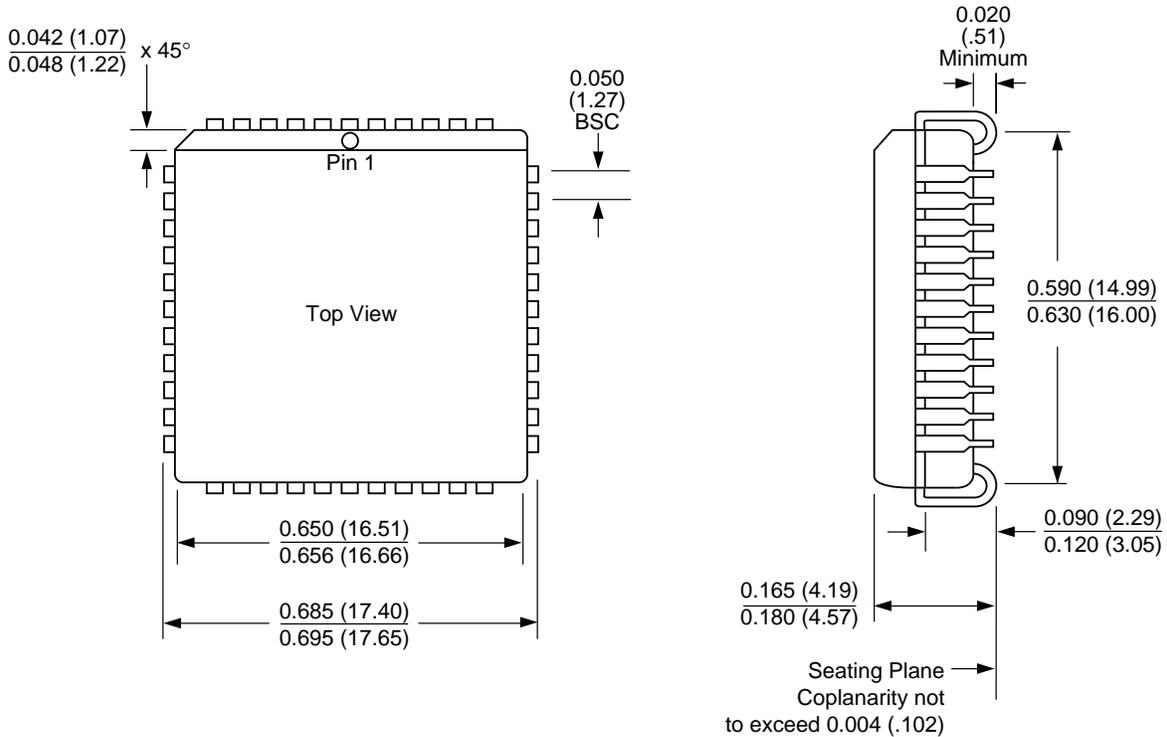
(Dimensions in millimeters, shown in parentheses, are for reference only)



44-Pin PLCC Package

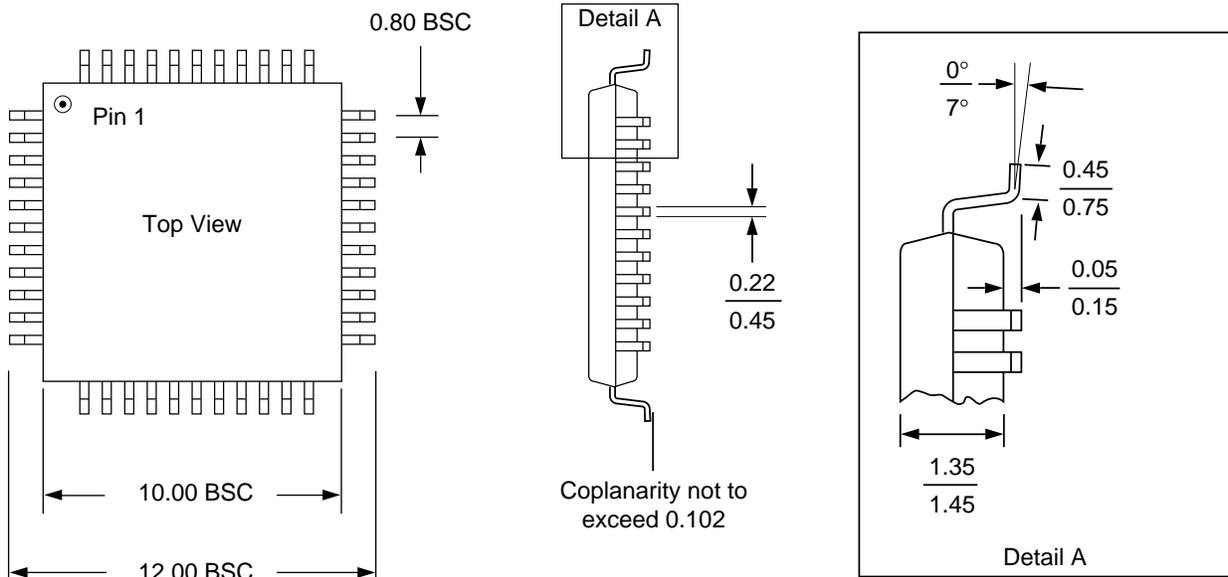
Dimensions in Inches MIN. / MAX.

(Dimensions in millimeters, shown in parentheses, are for reference only)



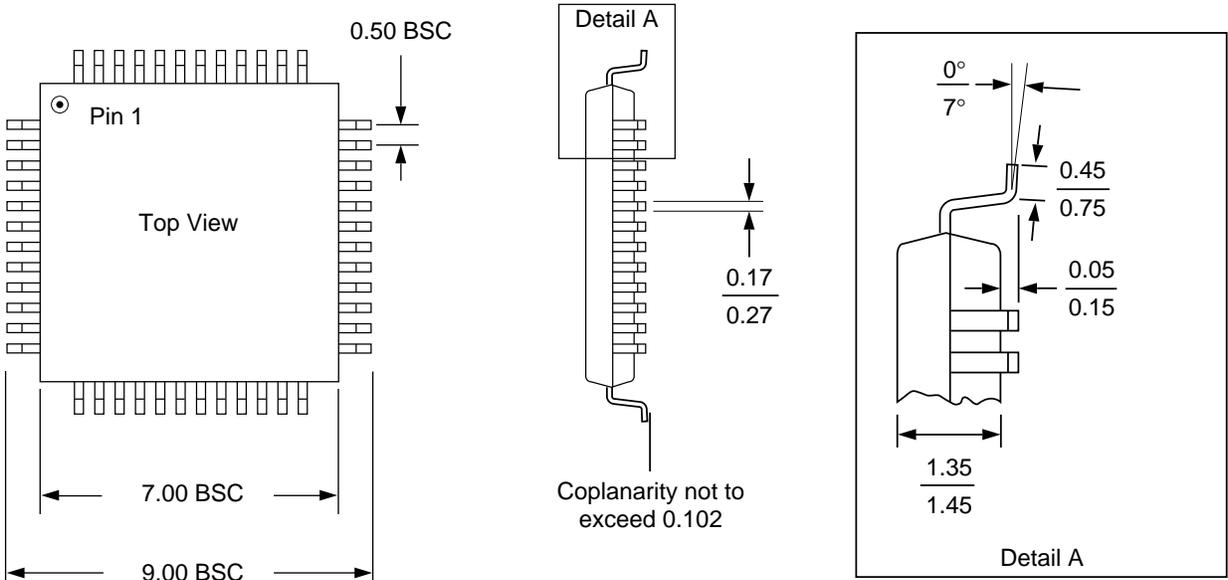
44-Pin TQFP Package (T44)

Dimensions in Millimeters MIN. / MAX.



48-Pin TQFP Package

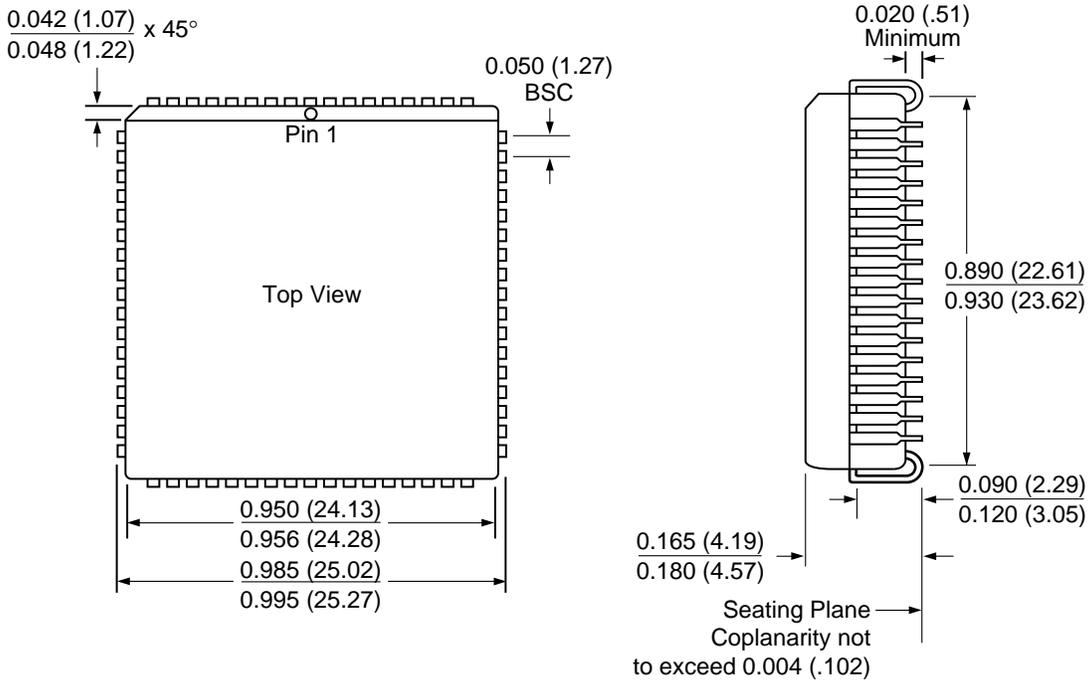
Dimensions in Millimeters MIN. / MAX.



68-Pin PLCC Package

Dimensions in Inches MIN. / MAX.

(Dimensions in millimeters, shown in parentheses, are for reference only)

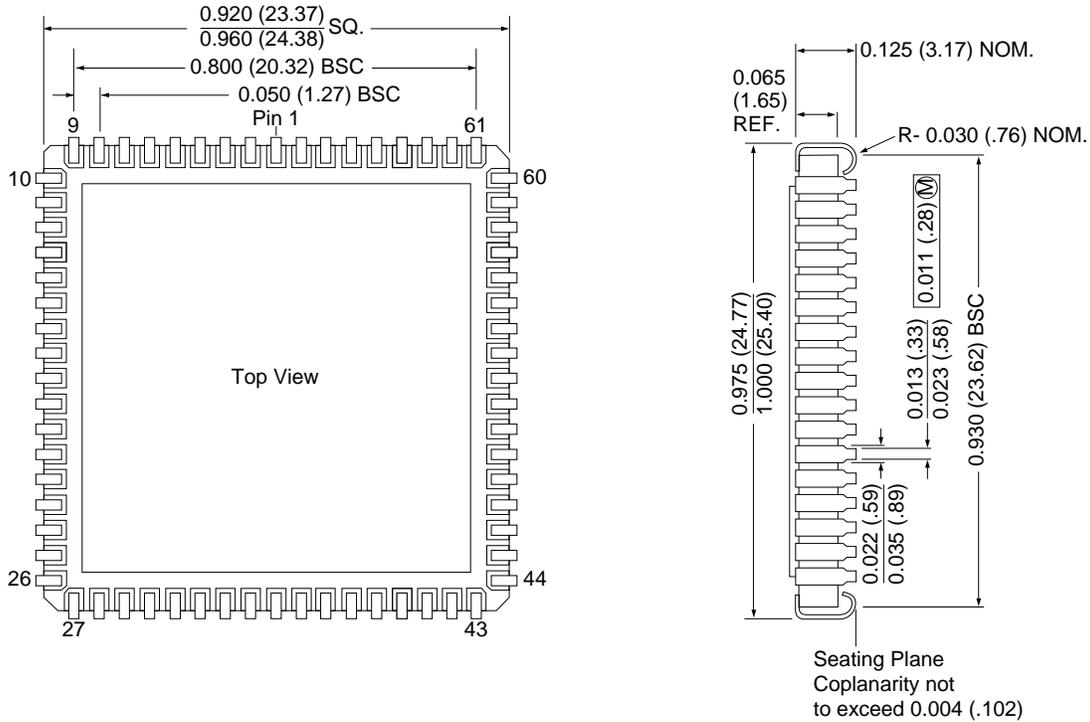


Package Diagrams

68-Pin JLCC Package

Dimensions in Inches MIN. / MAX.

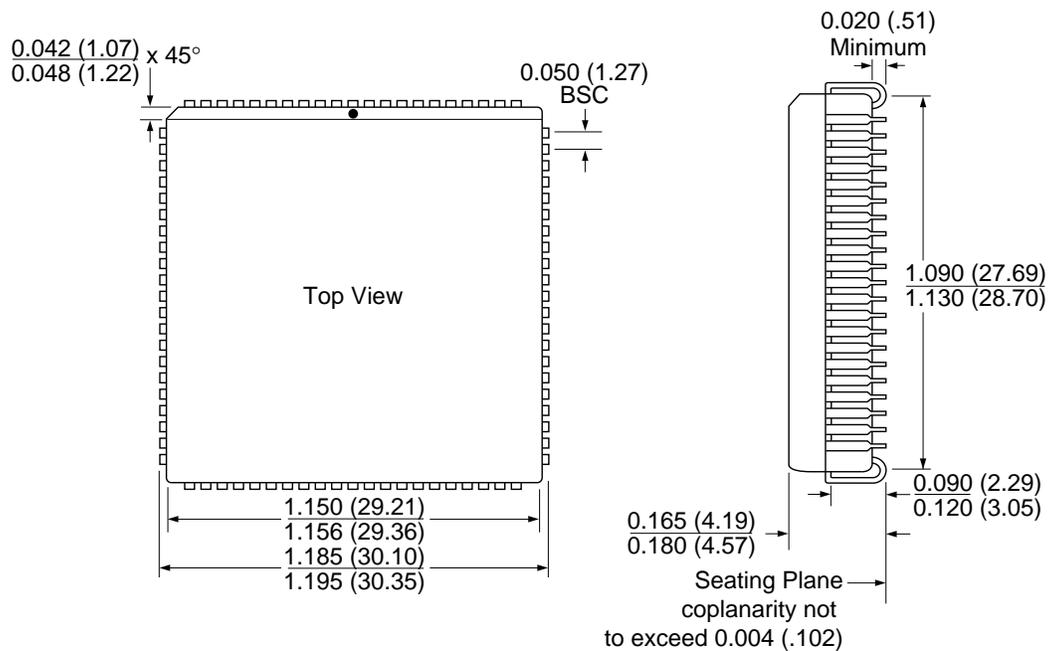
(Dimensions in millimeters, shown in parentheses, are for reference only)



84-Pin PLCC Package

Dimensions in Inches MIN. / MAX.

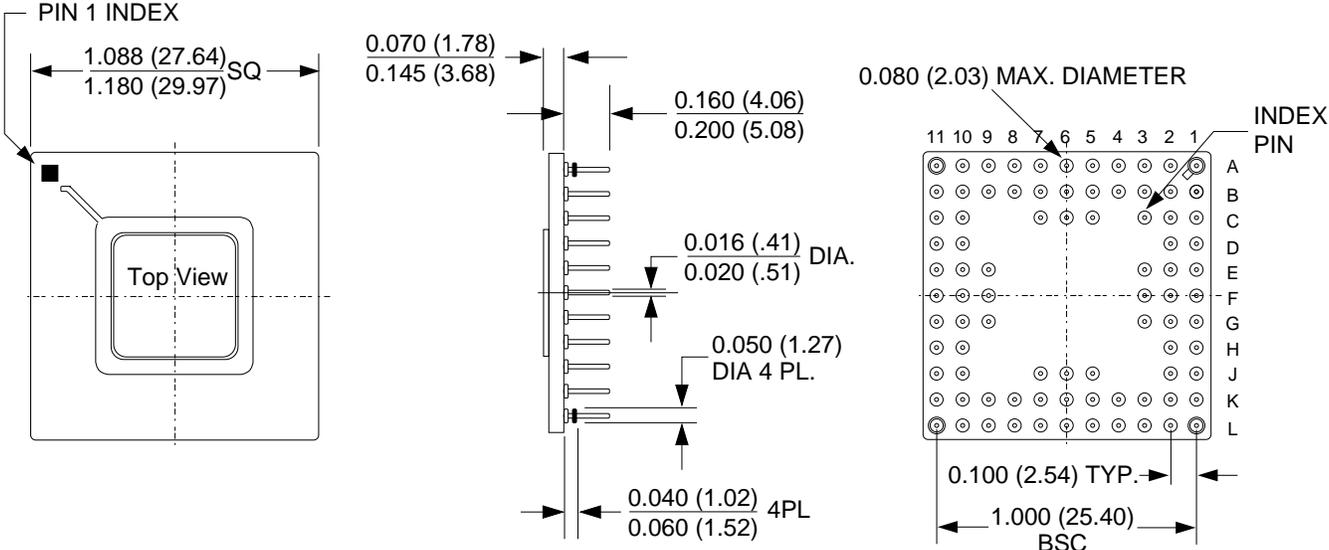
(Dimensions in millimeters, shown in parentheses, are for reference only)



84-Pin CPGA Package

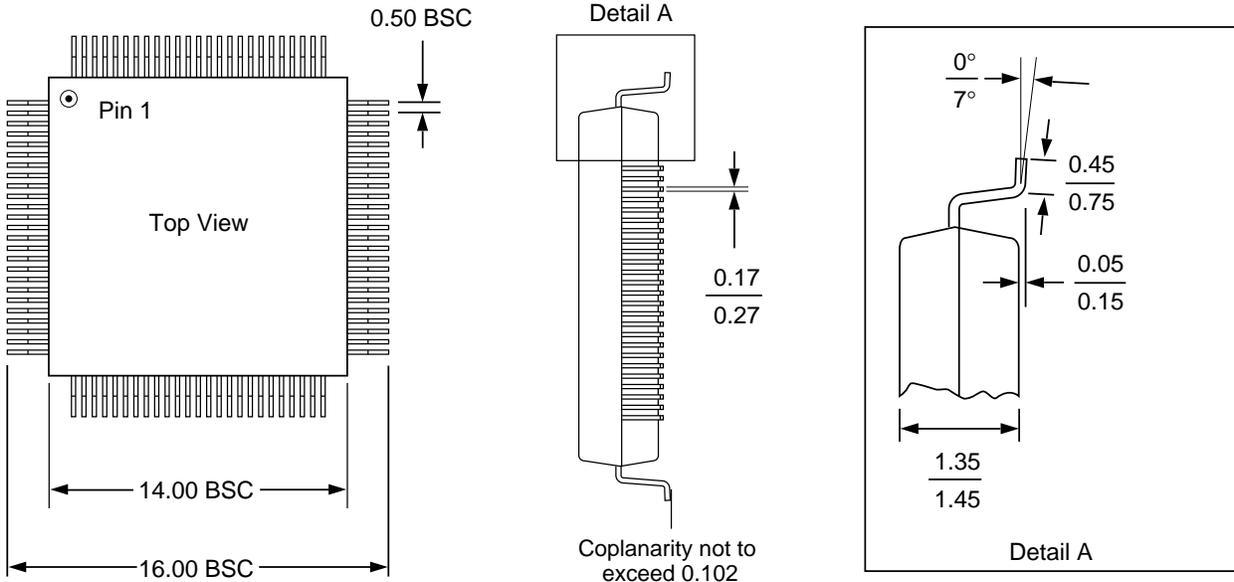
Dimensions in Inches MIN. / MAX.

(Dimensions in millimeters, shown in parentheses, are for reference only)



100-Pin TQFP Package

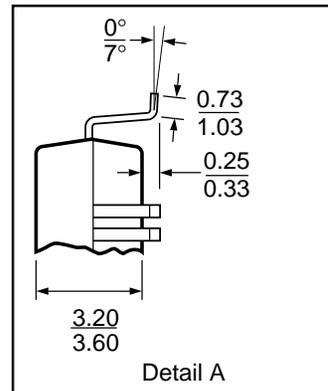
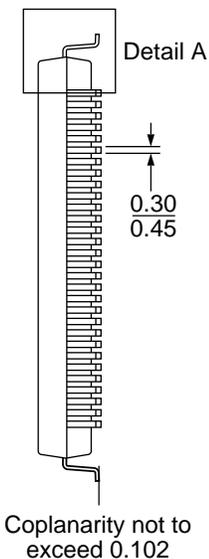
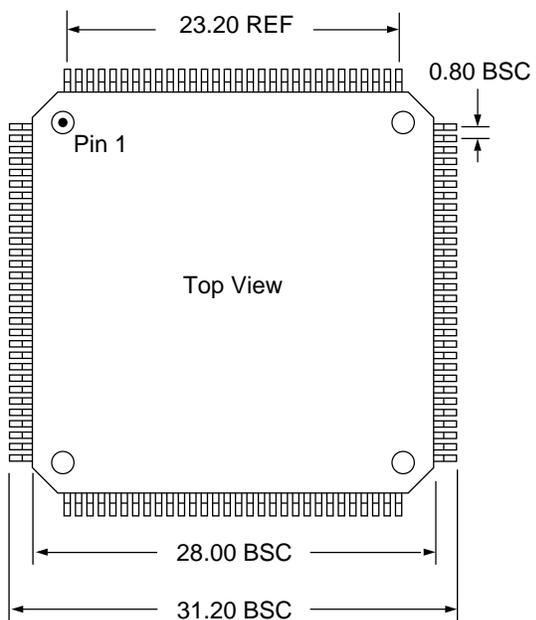
Dimensions in Millimeters MIN. / MAX.



Package Diagrams

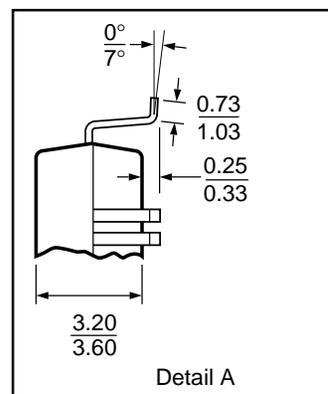
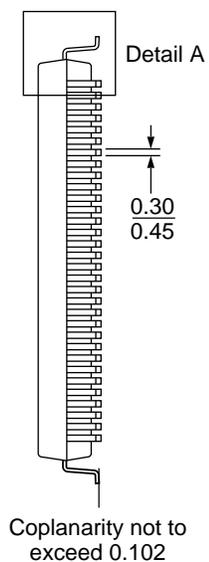
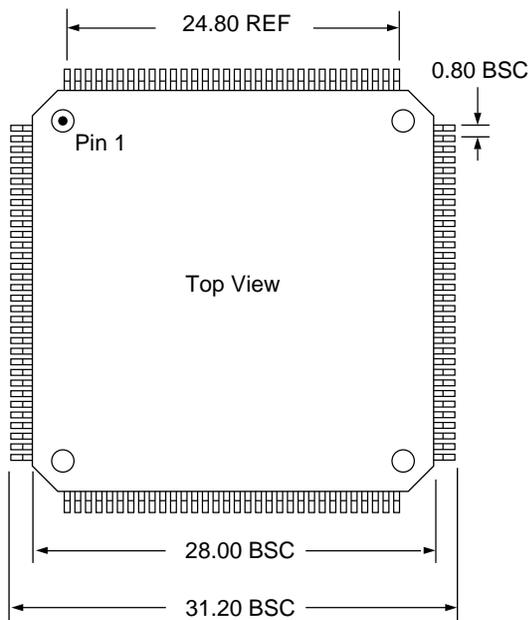
120-Pin PQFP Package

Dimensions in Millimeters MIN. / MAX.



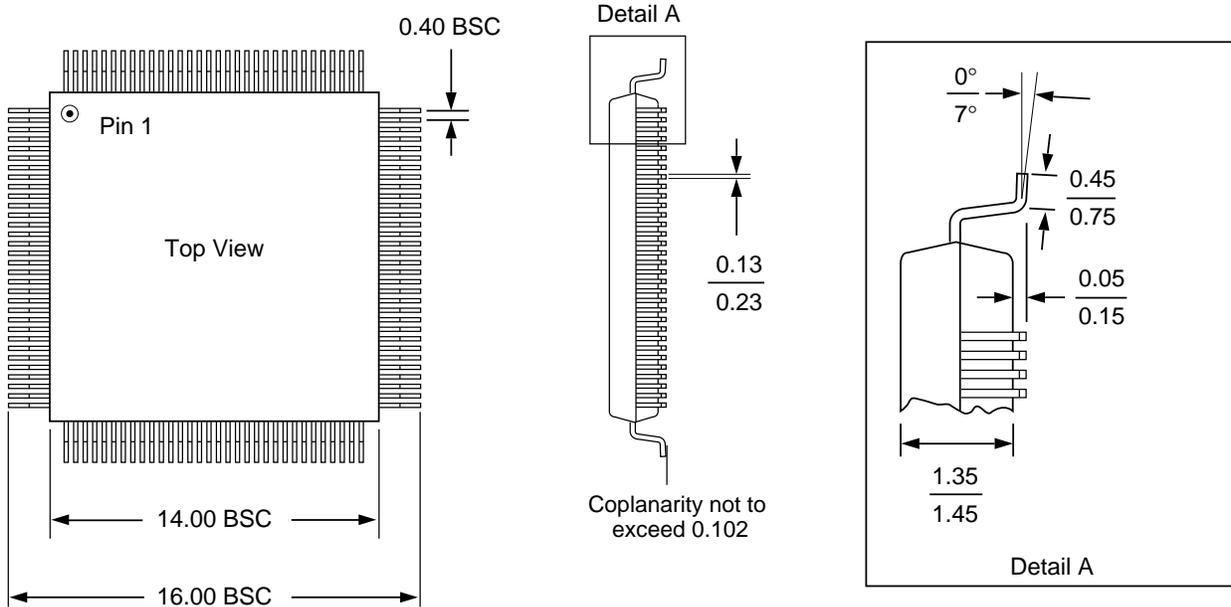
128-Pin PQFP Package

Dimensions in Millimeters MIN. / MAX.



128-Pin TQFP Package

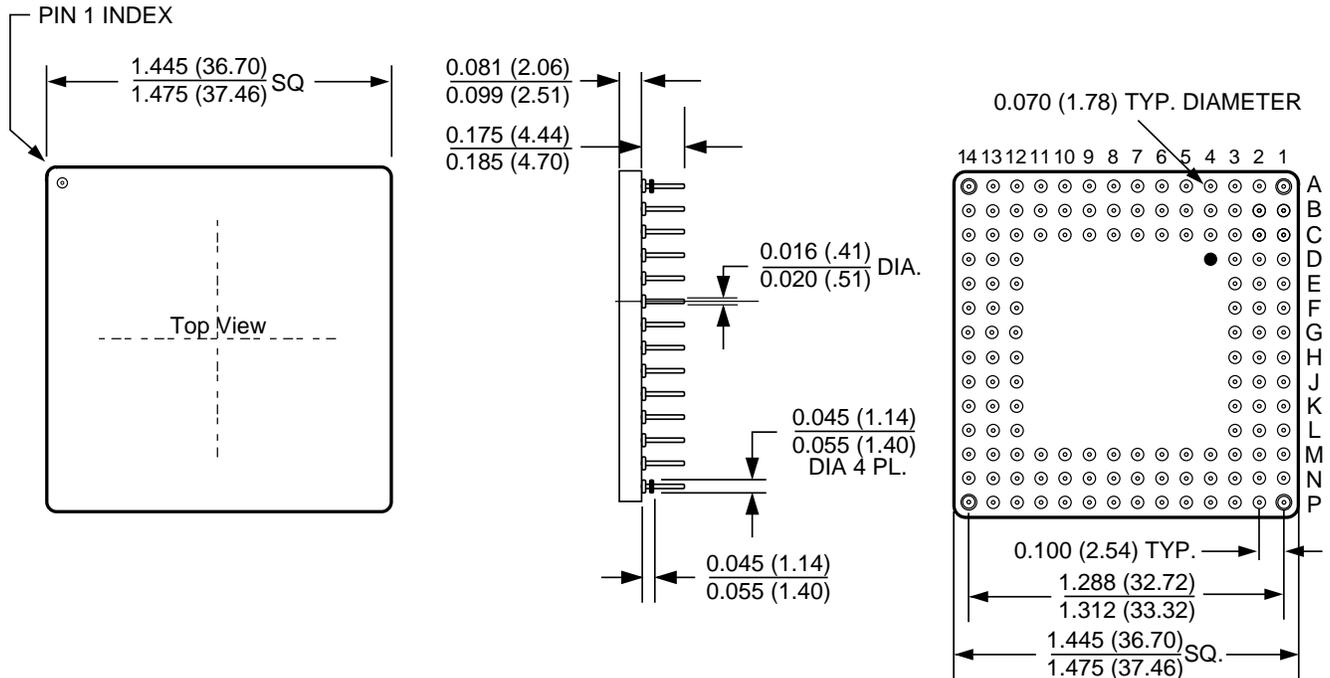
Dimensions in Millimeters MIN. / MAX.



133-Pin CPGA Package

Dimensions in Inches MIN. / MAX.

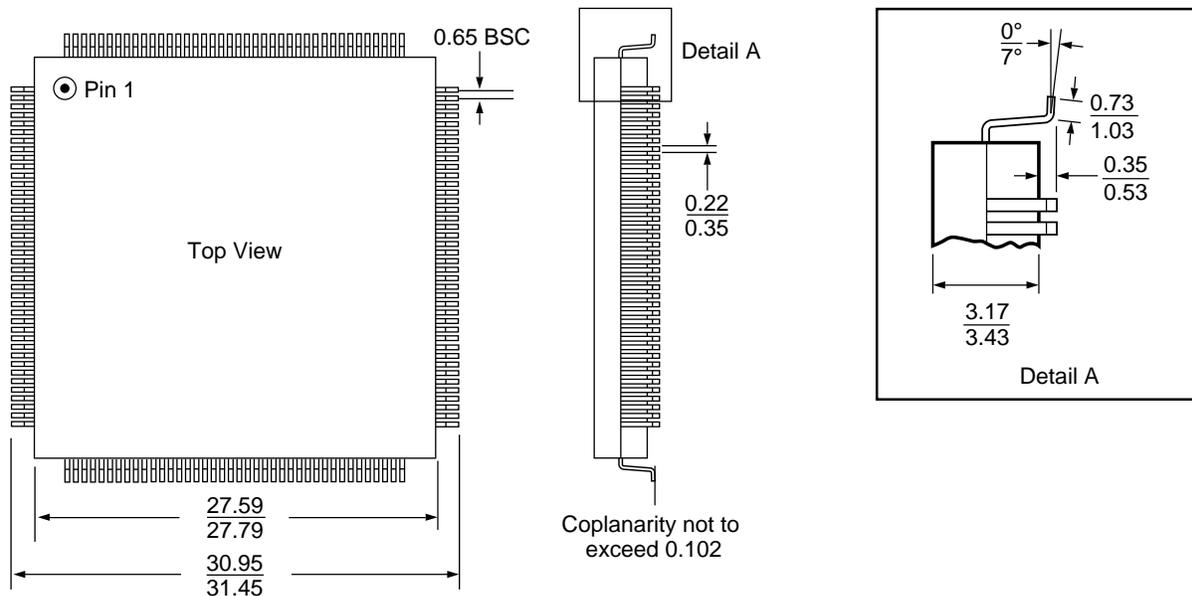
(Dimensions in millimeters, shown in parentheses, are for reference only)



Package Diagrams

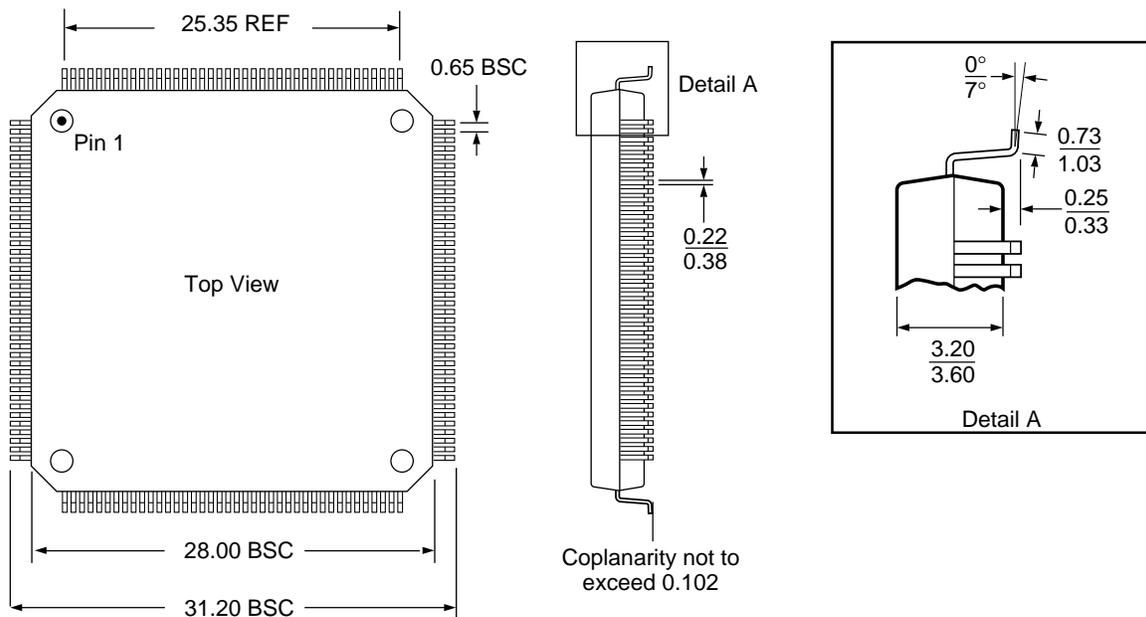
160-Pin MQFP Package

Dimensions in Millimeters MIN. / MAX.



160-Pin PQFP Package

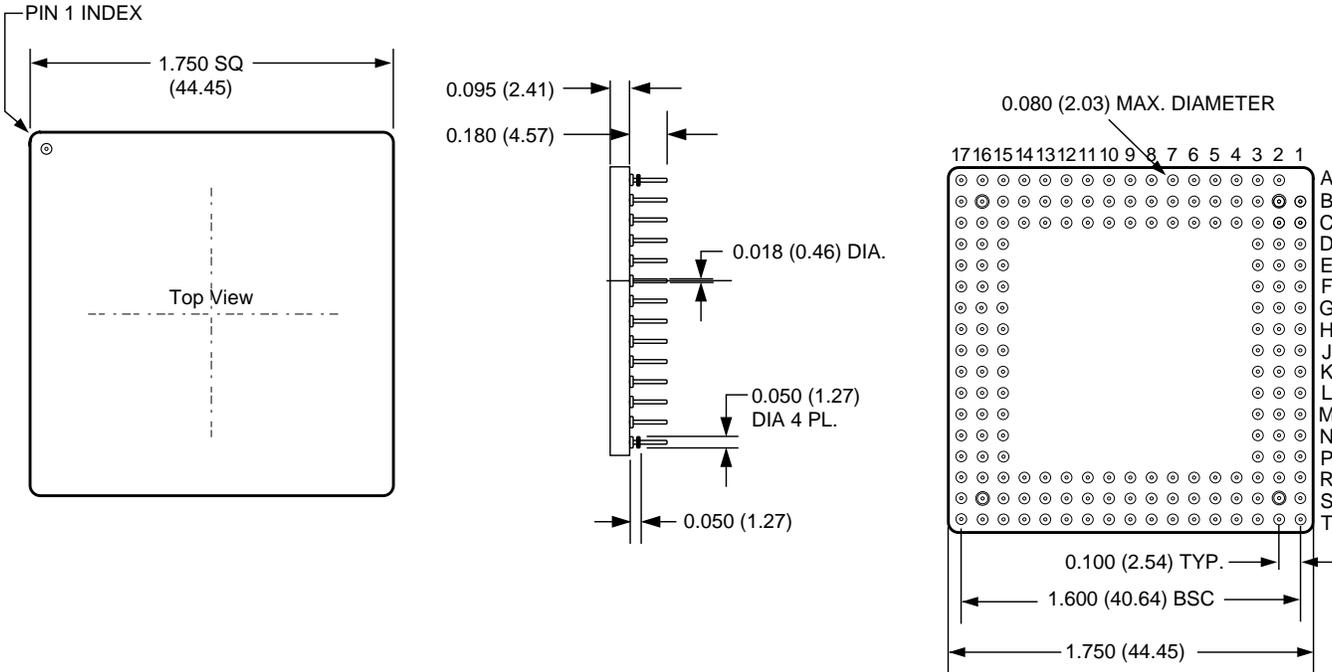
Dimensions in Millimeters MIN. / MAX.



167-Pin CPGA Package

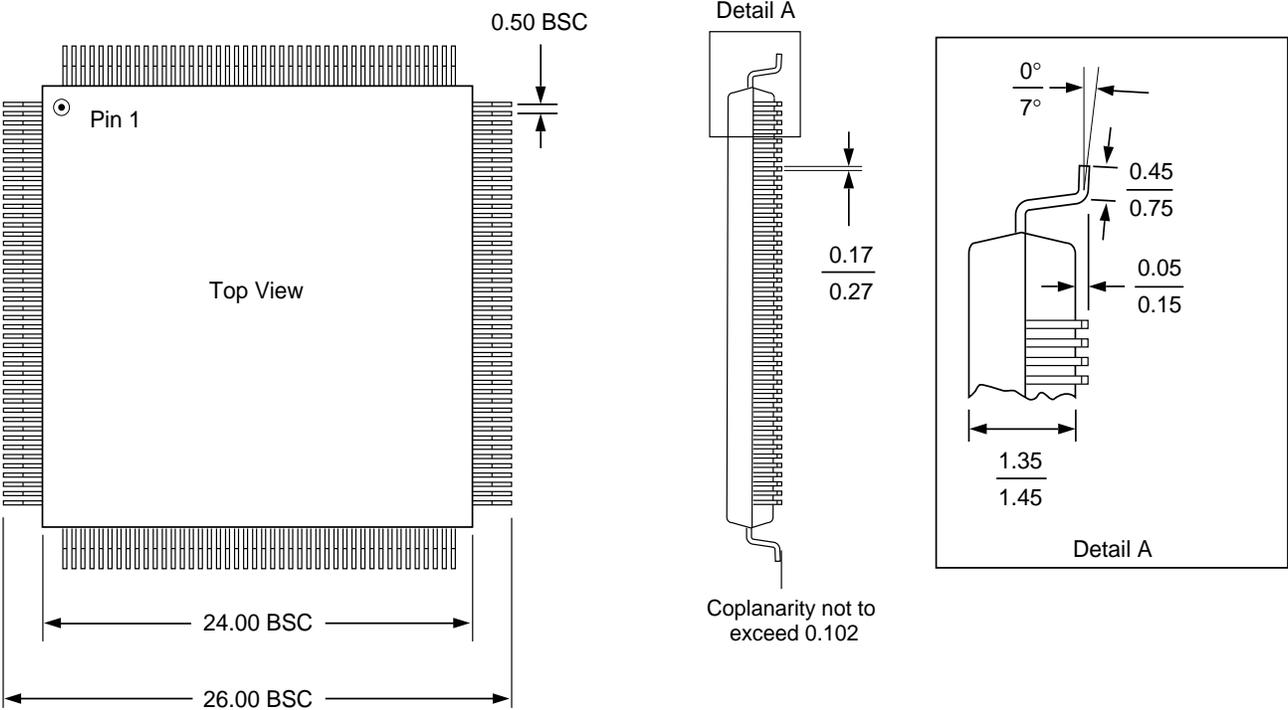
Dimensions in Inches MIN. / MAX.

(Dimensions in millimeters, shown in parentheses, are for reference only)



176-Pin TQFP Package

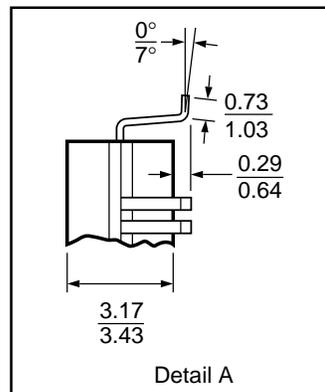
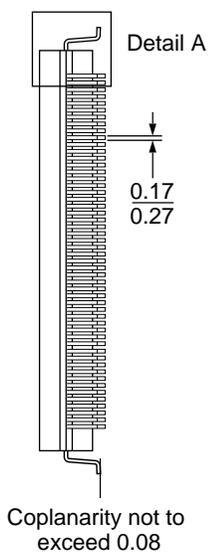
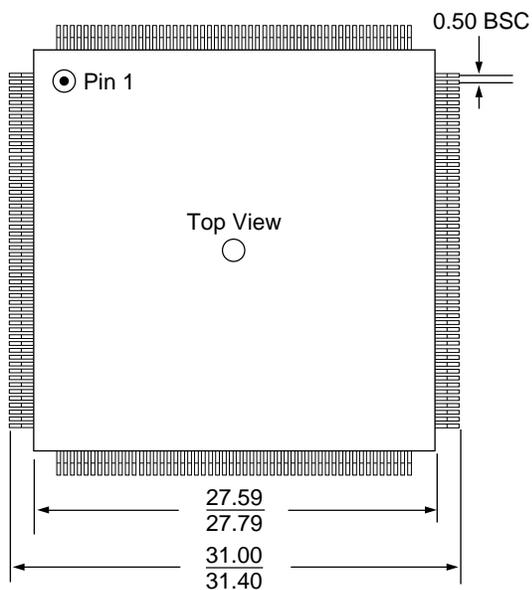
Dimensions in Millimeters MIN. / MAX.



Package Diagrams

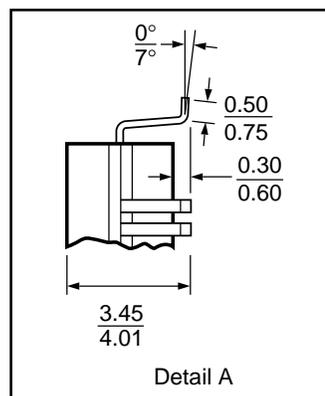
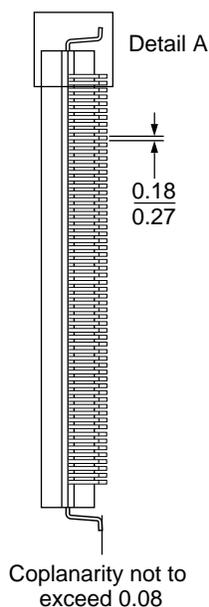
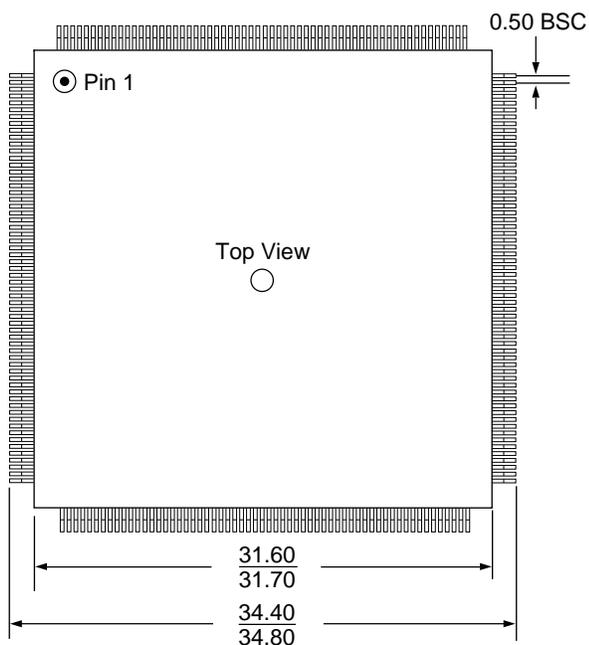
208-Pin MQFP Package

Dimensions in Millimeters MIN. / MAX.

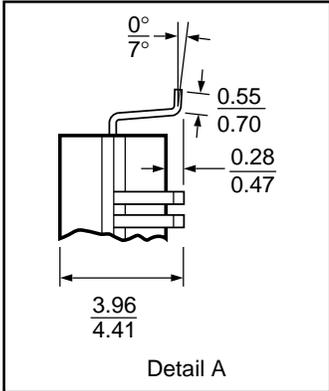
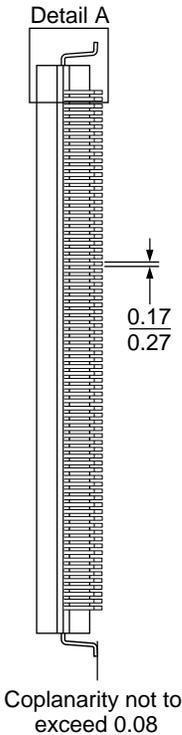
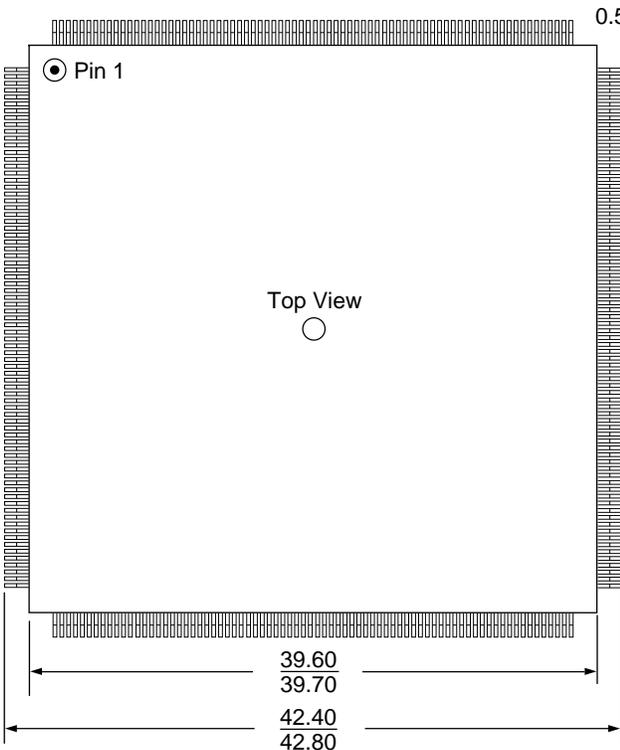


240-Pin MQFP Package

Dimensions in Millimeters MIN. / MAX.



304-Pin MQFP Package
Dimensions in Millimeters MIN. / MAX.





Copyright © 1996 Lattice Semiconductor Corporation.

E²CMOS, GAL, ispGAL, ispLSI, pLSI, pDS, Silicon Forest, UltraMOS, Lattice Logo, L with Lattice Semiconductor Corp. and L (Stylized) are registered trademarks of Lattice Semiconductor Corporation (LSC). The LSC Logo, Generic Array Logic, In-System Programmability, In-System Programmable, ISP, ispATE, ispCODE, ispDOWNLOAD, ispGDS, ispStarter, ispSTREAM, ispTEST, ispTURBO, Latch-Lock, pDS+, RFT, Total ISP and Twin GLB are trademarks of Lattice Semiconductor Corporation. ISP is a service mark of Lattice Semiconductor Corporation. All brand names or product names mentioned are trademarks or registered trademarks of their respective holders.

Lattice Semiconductor Corporation (LSC) products are made under one or more of the following U.S. and international patents: 4,761,768 US, 4,766,569 US, 4,833,646 US, 4,852,044 US, 4,855,954 US, 4,879,688 US, 4,887,239 US, 4,896,296 US, 5,130,574 US, 5,138,198 US, 5,162,679 US, 5,191,243 US, 5,204,556 US, 5,231,315 US, 5,231,316 US, 5,237,218 US, 5,245,226 US, 5,251,169 US, 5,272,666 US, 5,281,906 US, 5,295,095 US, 5,329,179 US, 5,331,590 US, 5,336,951 US, 5,353,246 US, 5,357,156 US, 5,359,573 US, 5,394,033 US, 5,394,037 US, 5,404,055 US, 5,418,390 US, 5,493,205 US, 0194091 EP, 0196771B1 EP, 0267271 EP, 0196771 UK, 0194091 GB, 0196771 WG, P3686070.0-08 WG. LSC does not represent that products described herein are free from patent infringement or from any third-party right.

The specifications and information herein are subject to change without notice. Lattice Semiconductor Corporation (LSC) reserves the right to discontinue any product or service without notice and assumes no obligation to correct any errors contained herein or to advise any user of this document of any correction if such be made. LSC recommends its customers obtain the latest version of the relevant information to establish, before ordering, that the information being relied upon is current.

LSC warrants performance of its products to current and applicable specifications in accordance with LSC's standard warranty. Testing and other quality control procedures are performed to the extent LSC deems necessary. Specific testing of all parameters of each product is not necessarily performed, unless mandated by government requirements.

LSC assumes no liability for applications assistance, customer's product design, software performance, or infringements of patents or services arising from the use of the products and services described herein.

LSC products are not authorized for use in life-support applications, devices or systems. Inclusion of LSC products in such applications is prohibited.

LATTICE SEMICONDUCTOR CORPORATION

5555 Northeast Moore Court
Hillsboro, Oregon 97124 U.S.A.
Tel.: (503) 681-0118
FAX: (503) 681-3037
<http://www.latticesemi.com>

November 1996
