

IBM posts best 4-way server performance result on industry-standard TPC-H 300GB benchmark

February 22, 2005 ... IBM® continues to set the pace for leadership performance in the high-end Intel® server market. Using the latest Intel Xeon™ Processor MP, the IBM eServer® xSeries® 366 server and IBM DB2® UDB 8.2, have delivered the highest 4-way server performance result ever achieved on the TPC-H 300GB benchmark.

The x366 and DB2 UDB set a new record for 4-way server performance running the TPC-H benchmark, which models a decision-support system for business intelligence applications. The x366 and DB2 UDB achieved a Composite Query-per-Hour metric of 7,731.9 QphH@300GB and price/performance of \$33/QphH@300GB. (1) These results rank in the Top Ten TPC-H by Performance and first in the Top Ten TPC-H by Price/Performance for the 300GB database. (2)

For this benchmark, the x366 server used four Intel Xeon 3.6GHz Processors MP, each with a 1MB L2 cache, and ran IBM DB2 Universal Database 8.2 and Microsoft® Windows® Server 2003 Enterprise Edition.

About the x366 Server

The IBM eServer™ xSeries 366 delivers break-through 4-socket performance in a 3U rack-optimized server design combining the high availability of mainframe-inspired IBM eServer X3 technologies with the 64-bit capabilities of the latest Intel 64-bit Xeon MP processor to create the leading 64-bit solution for commercial enterprise applications, virtualization, and Web services.

Powered by the IBM XA-64e™ third-generation Enterprise X-Architecture chipset, the IBM eServer xSeries 366 is your stateful transaction accelerator delivering unprecedented 64-bit 4-socket performance on either 32-bit or 64-bit applications.

Whether you are deploying today or developing for tomorrow, the dual-core-capable IBM eServer x366 is the development platform of choice combining proven industry-standard compatibility on the most widely deployed server instruction set architecture in the world to power the transition to 64-bit compatible x86 applications and take advantage of the future of multi-core x86 processors.

(1) Total solution availability is August 20, 2005.

(2) Results are current as of February 22, 2005. To view all results for the TPC-H benchmark, visit www.tpc.org.

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