

LAL	Rev	Description	Chk	Date	Approved
	A	ENGRG RELEASE	BN	10-23-78	
	B	M/L ITEM 37 PART DESCRIPTION WAS S27; ITEM 48 WAS 300K P/N 116447-304; ITEM 54 QTY WAS 4 (REMOVED R7 & 8); ITEM 55 QTY WAS 2 (ADDED R7 & 8); ITEM 69 WAS .47, SPRAGUE 474G025HS. ADDED NOTE 2(ADD/DELETE) M/L ITEM 52 QTY WAS 4, REMOVED R22, 23; ITEM 53 QTY WAS 2, REMOVED R24. ADDED M/L ITEM 57 (CR5). ITEM 62 QTY WAS 1, ADDED C5, C6. ITEM 66 QTY WAS 6, REMOVED C5, C6.	BN	1-19-79 2-28-79	
X	C	ADDED NOTE 2 86), 87). M/L ITEM 47 ADDED R17, ADDED ITEM 49, ITEM 51 REMOVED R17, ITEM 62 ADDED C7, ITEM 63 REMOVED C7.	BN	3-27-79	
X	D	ADDED NOTE 2-88), 89), 91).	BN	4-2-79	
X	E	ADDED NOTE 2-91) THRU 2-118)	BN	4-23-79	

**ENGINEERING
RELEASE**

THIS DOCUMENT CONTAINS 19 SHEETS

NTIOP1-PWASSY.SIL (PNTIOP-PWASSY.DM)

Dist Code **SPG**

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	1. Tolerances .xx \pm .03 Angular .xxx \pm .010 \pm 1/2° 2. Break All Sharp Edges .010 Approx — 3. Mach. Surfaces ✓ 4. All Dim. In Inches	Check BN 4-23-79	ASSEMBLY, PRINTED WIRING- I/O PROCESSOR (NOTETAKER)		
		Appr.			
		Material			
Model No. NOTETAKER First Use	Finish	Code Ident 18338	Size A	Dwg. No. 217606	Change Letter E
Next Assy. First Use 217684		Scale NONE	Do Not Scale Drawing		Sheet 1 OF

NOTES: UNLESS OTHERWISE SPECIFIED

1. ASSEMBLE PER MODULE ASSEMBLY SPEC, DWG NO. 216207

2. THE FOLLOWING MODIFICATIONS ARE REQUIRED USING "A" REVISION PW BOARD:

- 1) CUT ETCH BETWEEN 6E16 AND THE FEED-THRU NEAR 6E14 (CONNECTS TO 4E33), COMPONENT SIDE.
- 2) CUT ETCH BETWEEN 8E13 AND FEED-THRU UNDER 1E (COMPONENT SIDE) CONNECTS TO 1G18.
- 3) CUT ETCH BETWEEN 10D1 AND 10D2 (ETCH SIDE).
- 4) CUT ETCH BETWEEN 5D3 AND FEED-THRU NEAR 5D9 (CONNECTS TO 4D8) (ETCH SIDE).
- 5) CUT ETCH BETWEEN 1B9 AND "T" JUNCTION POINT (ETCH SIDE).
- 6) CUT ETCH BETWEEN "T" JUNCTION POINT (ETCH CONNECTS TO ETCH FROM 8H19 TO 8H1) AND FEED-THRU NEAR 11H1. SEE FIG 1.

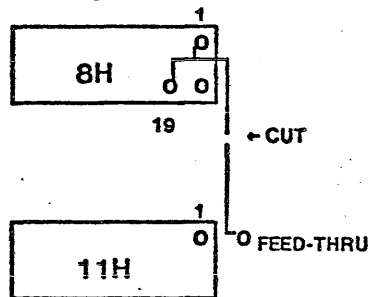


FIG 1

- 7) CUT ETCH BETWEEN 9H12 AND 10H4 (ETCH SIDE).
- 8) CUT ETCH BETWEEN 9H16 AND 10H6 (COMPONENT SIDE).
- 9) CUT ETCH BETWEEN 9H15 AND 10F3 (ETCH SIDE).
- 10) CUT ETCH BETWEEN 9H7 AND 8H6 (COMPONENT SIDE).
- 11) CUT ETCH BETWEEN 9H8 AND 8H7 (COMPONENT SIDE).
- 12) CUT ETCH BETWEEN 9H18 AND 8H4 (COMPONENT SIDE).
- 13) CUT ETCH BETWEEN 9H17 AND 8H5 (COMPONENT SIDE).
- 14) DRILLOUT C8 TERMINAL (NEAR 10A1) UNTIL NO CONTINUITY EXISTS BETWEEN 10A12 AND GND.

(NOTE 2 CONT'D ON SH 3)

NTIOP2-PWASSY.SIL

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(NOTE 2 CONT'D)

- 15) DRILL OUT 9H13 SO THERE IS NO CONTINUITY TO 7H7.
- 16) DRILL OUT 9H14 SO THERE IS NO CONTINUITY TO 7H6.
- 17) DRILL OUT 9H6 SO THERE IS NO CONTINUITY TO 4C3.
- 18) DRILL OUT 9H19 SO THERE IS NO CONTINUITY TO 10H5.
- 19) DRILL OUT 9H9 SO THERE IS NO CONTINUITY TO 4C1.
- 20) ADD JUMPER FROM Q1-B TO C8 LEG (ABOVE 10A1) TO 10A12 (ETCH SIDE).

21) ADD JUMPER FROM 10H4 TO 9H9 (ETCH SIDE).

22) 10H5 9H12

23) 10H6 9H15

24) 9H6 10F3

25) 9H7 7H6

26) 9H8 7H7

27) 9H13 8H4

28) 9H14 8H5

29) 9H16 4C3

30) 9H19 4C1

31) 9H17 8H6

32) 9H18 8H7

33) 6E16 1B2

34) 1B1 1C3

35) 1B3 8E13

36) 10C4 10D2

37) 5D3 4E27

38) 1D15 1C2

39) 1C3 3C6

40) ADD JUMPER FROM 1C1 TO 4C11 (ETCH SIDE).

(NOTE 2 CONT'D ON SH 4)

NTIOP3-PWASSY.SIL

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(NOTE 2 CONT'D)

- 41) ADD JUMPER FROM 4C10 TO 1B9 (ETCH SIDE).
- 42) 11E10 11E5
- 43) 11E5 11E6
- 44) 11E4 8H19
- 45) 10H15 10H16
- 46) ADD JUMPER FROM 10H8 TO 10H10 (ETCH SIDE).
- 47) CUT ETCH BETWEEN 3C11 AND FEED THRU (ETCH SIDE).
- 48) 10D12 FEED THRU NEAR 10D8 (ETCH SIDE).
- 49) 10D12 FEED THRU NEAR 10C1 (ETCH SIDE).
- 50) 4C5 FEED THRU UNDER 10C (COMP. SIDE).
- 51) 1A2 FEED THRU NEAR Q2 (COMP. SIDE).
- 52) 10D9 FEED THRU NEAR 11D7 (COMP. SIDE).
- 53) 4E38 2E3 (COMP. SIDE).
- 54) 4E38 FEED THRU NEAR 5D8 (ETCH SIDE).
- 55) 4E37 2E4 (COMP. SIDE).
- 56) 4E37 FEED THRU NEAR 5D14 (ETCH SIDE).
- 57) 4336 2E14 (COMP. SIDE).
- 58) 4E36 5D17 (ETCH SIDE).
- 59) 4E35 2E13 (COMP. SIDE).
- 60) 4E35 5D18 (ETCH SIDE).
- 61) CUT ETCH BETWEEN 6C12 AND FEED THRU UNDER 10C (COMP. SIDE).
- 62) ADD JUMPER FROM MOTHERBD CONN P1 PIN 7 TO 10C9 (ETCH SIDE).
- 63) ADD JUMPER FROM 10C8 1B12 (ETCH SIDE).
- 64) ADD JUMPER FROM 10D12 1B13 (ETCH SIDE).
- 65) ADD JUMPER FROM 1B11 5A11 (ETCH SIDE).

(NOTE 2 CONT'D ON SHEET 5)

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(NOTE 2 CONT'D FROM SH 4)

- | | | | | | |
|-----|---|-------|----|-------|--------------|
| 66) | ADD JUMPER FROM | 5A11 | TO | 7D11 | (ETCH SIDE). |
| 67) | | 4C5 | | 10D13 | |
| 68) | | 5C1 | | 10D9 | |
| 69) | | 1A2 | | 10C2 | |
| 70) | | 10C1 | | 4D6 | |
| 71) | | 4D5 | | 10D6 | |
| 72) | | 4D4 | | 8E2 | |
| 73) | | 2E3 | | 4E35 | |
| 74) | | 4E35 | | 5D13 | |
| 75) | | 2E4 | | 4E36 | |
| 76) | | 4E36 | | 5D14 | |
| 77) | | 2E14 | | 4E37 | |
| 78) | | 4E37 | | 5D17 | |
| 79) | | 2E13 | | 4E38 | |
| 80) | | 4E38 | | 5D18 | |
| 81) | ADD JUMPER FROM | 8G16 | TO | 8G17 | (ETCH SIDE). |
| 82) | ADD JUMPER FROM MOTHERBD CONN P1 PIN 54 TO 10C11 (ETCH SIDE). | | | | |
| 83) | ADD JUMPER FROM | 10C10 | TO | 6E25 | (ETCH SIDE). |
| 84) | ADD JUMPER FROM | 6C5 | TO | 11E2 | (ETCH SIDE). |
| 85) | ADD JUMPER FROM 4A14 TO MOTHERBD CON P1 PIN 51 (ETCH SIDE). | | | | |
| 86) | ADD RESISTOR (ITEM 49) FROM 11D2 TO 11D14 (ETCH SIDE). | | | | |
| 87) | ADD RESISTOR (ITEM 49) FROM 11D14 TO 11D12 (ETCH SIDE). | | | | |
| 88) | CUT ETCH BETWEEN 11C1 AND END OF R1 (ETCH SIDE). | | | | |
| 89) | CUT ETCH BETWEEN 11D13 AND FEED-THRU UNDER 11D (COMP. SIDE). | | | | |
| 90) | ADD WIRE FROM 11D13 TO END OF R1 NEAR Q7. (ETCH SIDE). | | | | |
| 91) | ADD WIRE FROM 11D11 TO 11C1 (ETCH SIDE). | | | | |

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(NOTE 2 CONT'D FROM SH 5)

- 92) ADD WIRE FROM 1F5 TO P1-94 (ETCH SIDE).
- 93) REMOVE WIRE FROM 1B1 TO 1C3 TO 3C6 (ETCH SIDE).
- 94) CUT ETCH BETWEEN 8E8 AND FEED THRU (CONNECTS TO 3C6) (ETCH SIDE).
- 95) CUT ETCH BETWEEN 8E9 AND FEED THRU (CONNECTS TO 4E28) (ETCH SIDE).
- 96) CUT ETCH BETWEEN 4E28 AND P2-39 (ETCH SIDE).
- 97) REMOVE WIRE FROM 1B2 TO 6E16 (ETCH SIDE).
- 98) REMOVE WIRE FROM 1B3 TO 8E13 (ETCH SIDE).
- 99) CUT ETCH BETWEEN 9E9 AND FEED THRU (CONNECTS TO 8E12) (ETCH SIDE).
- 100) CUT ETCH BETWEEN 9E10 AND FEED THRU (CONNECTS TO 4E26) (ETCH SIDE).
- 101) CUT ETCH BETWEEN 9E8 AND FEED THRU NEAR 11E8 (CONNECTS TO 4G19) (ETCH SIDE).
- 102) ADD WIRE FROM 6E16 TO 8E13 (ETCH SIDE).
- 103) 8E12 TO 3D3
- 104) 4E26 TO 3D4
- 105) 2D5 TO 3D5
- 106) 8E8 TO P2-39
- 107) 8E9 TO 3D6 (ETCH SIDE).
- 108) 8E8 TO FEED THRU NEAR 11E8 (CONNECTS TO 4G19) (ETCH SIDE).
- 109) ADD WIRE FROM 3D6 TO 3C6 (ETCH SIDE).
- 110) REMOVE WIRE FROM 1C2 TO 1D15 (ETCH SIDE).
- 111) REMOVE WIRE FROM 1C1 TO 4C11 (ETCH SIDE).
- 112) REMOVE WIRE FROM 4C10 TO 1B9 (ETCH SIDE).
- 113) ADD WIRE FROM 3D5 TO 4C11 (ETCH SIDE).
- 114) ADD WIRE FROM 4C10 TO 9E10 (ETCH SIDE).
- 115) ADD WIRE FROM 9E9 TO 1D15 (ETCH SIDE).

(NOTES CONT'D ON SH 5C)

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(NOTES CONT'D FROM SH 5B)

116) ADD WIRE FROM 9E8 TO 1B9 (ETCH SIDE).

117) CUT ETCH BETWEEN 9E4 AND FEED THRU (CONNECTS TO 7F2) (ETCH SIDE).

118) ADD WIRE FROM 9E4 TO 9E5 (ETCH SIDE).

119) Use lower profile 100 uf, 200, caps.

120) add AND gate shown on page 11

121) Slowed ROM down to use IPT07.

NTIOP5C-PWASSY.SIL

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MATERIAL LIST

ML	Drawing No. 217606	Rev. E
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Rev. E 2 1 7 6 0 6	Drawing Title ASSEMBLY, PRINTED WIRING- I/O PROCESSOR (NOTETAKER)	These drawings and specifications, and the data contained therein, are the exclusive property of Xerox Corporation and or Rank Xerox, Ltd. Issued in strict confidence and shall not, without the prior written permission of Xerox Corporation Rank Xerox, Ltd., be reproduced, copied or used for any purpose whatsoever, except the manufacture of articles for Xerox Corporation or Rank Xerox, Ltd.		
	NTIOP6-PWASSY.SIL	Model No. NOTETAKER	Date 4-2-79	Sheet 7 of

Item No.	Drawing Title	Drawing No.	No. Req.	Remarks
1	BOARD, PW - I/O PROCESSOR	217607	1	
2				
3	MICROCIRCUIT 8086 (INTEL)		1	4E
4	8284 (INTEL)		1	7E
5	8259A (INTEL)		1	6E
6	MP240 (CTS)		1	7C
7				
8	74C162 (NATIONAL)		1	9F
9	DAC1200 (NATIONAL)		1	8G
10	HA2425 (HARRIS)		2	11G,11H
11	HD-6402 (HARRIS)		1	6G
12	i2716 (INTEL)		2	1G,1H
13	LM325S (NATIONAL)		1	10A
14	LM377 (NATIONAL)		1	4A
15	LM309H (NATIONAL)		1	Q2
16	LM340LH-18 (NATIONAL)		1	Q1
17	74LS00 (TI)		1	10G
18	74LS02		2	1C,11E
19	74LS04		4	1E,2F,4C,8E
20	74LS08		2	1A,4D
21	74LS109		1	10F
22	74LS138		1	3C
23	74LS155		1	8F
24	74LS164		2	6C,10D
25	74LS245		2	4G,4H
26	74LS273		2	1F,8H
27	74LS32		2	8C,9E
28	74LS368		2	7F,8D
29	74LS373		8	2E,3G,3H,5A,5D,6A,6D,7D
30	MICROCIRCUIT 74LS74 (TI)		2	5C,9D

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		Model No. NOTETAKER	Date 4-2-79	Sheet 8 of

Item No.	Drawing Title	Drawing No.	No. Req.	Remarks
31	MICROCIRCUIT MC14568B (MOTOROLA)		1	10H
32	74S00 (TI)		1	2D,9C
33	74S04		1	10C
34	74S08		3	1B,11C,11D
35	74S133		1	1D
36	74S225		3	7G,7H,8H
37	74LS27		1	3D
38	MICROCIRCUIT 74S74 (TI)		1	2C
39				
40				
41	OSCILLATOR K1114A 960KHZ (MOTOROLA)		1	11F
42				
43	LED INDICATOR HLMP-6620 (HEWLETT-P)		4	CR1,2,3,4
44				
45	RESISTOR, SIP 500 OHM (CTS-750-81-500)		1	A1
46				
47	RESISTOR 1K 5% 1/4W FILM OR CARBON COMP.		3	R12,13,17
48	3.9K		2	R20,21
49	2K		2	R29,30
50	56K		1	R2
51	10K		10	R1,3,4,11,14,15,16,18,19
52	50K		2	R25,26
53	10M		1	R27
54	75K		2	R5,6
55	4.7K		4	R7,8,9,10
56	RESISTOR 2.2K 5% 1/4W FILM OR CARBON COMP.		1	R28
57	DIODE 1N914 (MOTOROLA)		1	CR5
58	TRANSISTOR, FET 2N5116		1	O7
59	TRANSISTOR, PNP 2N2905		2	Q5,6
60	TRANSISTOR, NPN 2N3253		2	Q3,4

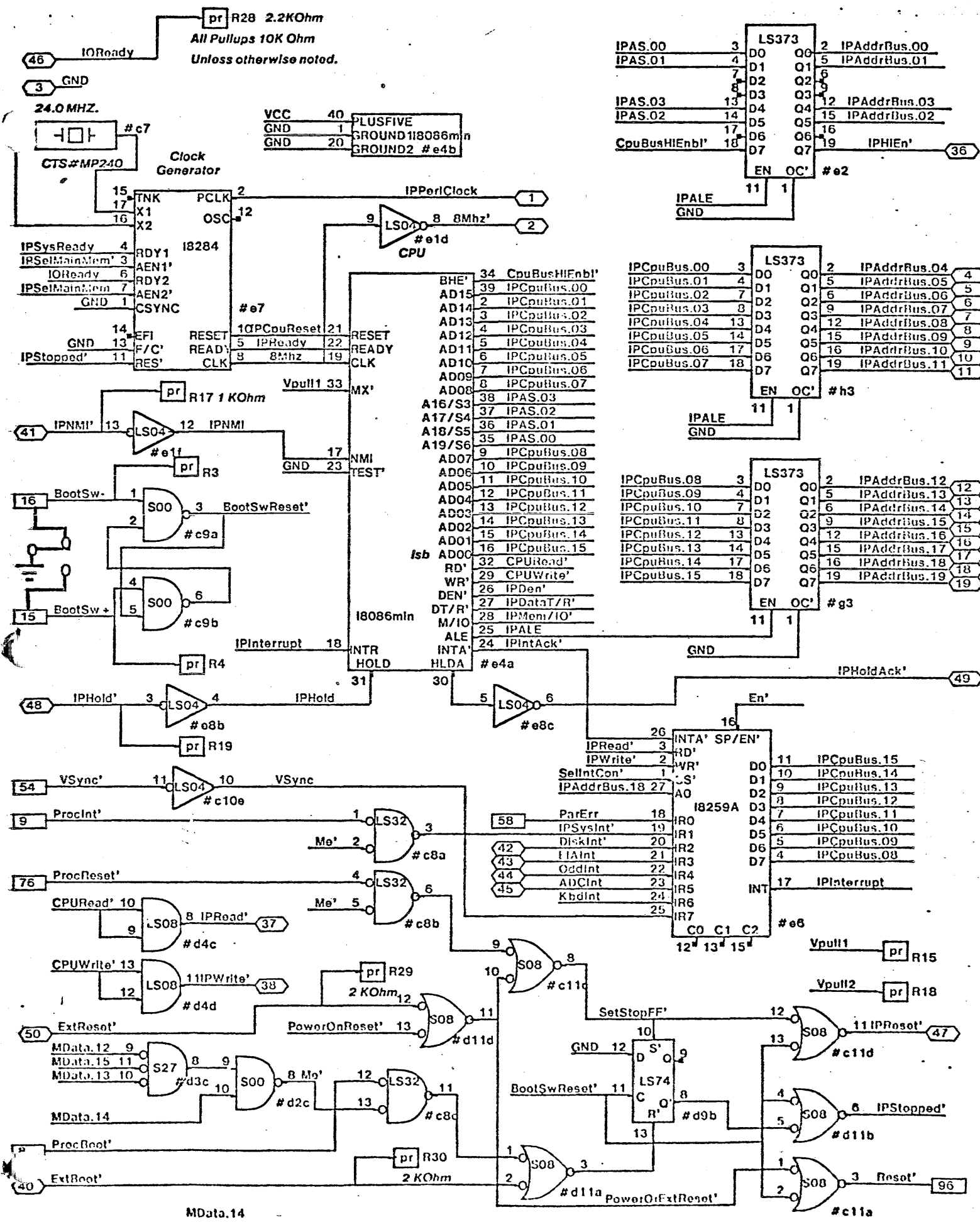
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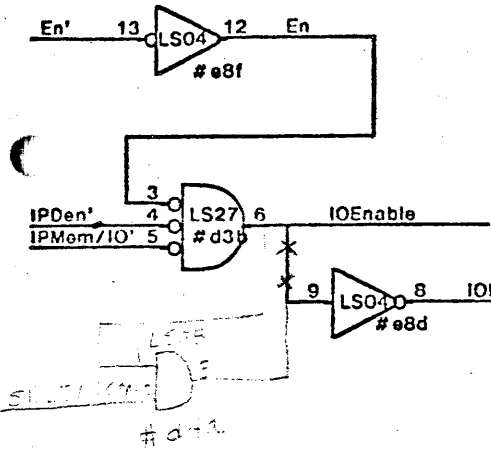
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ML	Drawing No. 217606	Rev. E
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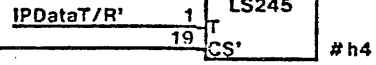
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	NTIOP8-PWASSY.SIL		Model No. NOTETAKER	Date 4-2-79	Sheet 9 of
2 1 7 6 0 6	Item No.	Drawing Title	Drawing No.	No. Req.	Remarks
	61				
ML	62	CAPACITOR, TANT, 22uF, 35V		5	C1,2,4, 5,6 SPRAGUE 196D226X9035ZA3
	63	TANT, 2.2uF, 35V		3	C3,7,9 CORNING #MZG-035-225-R-20
	64	TANT, 10uF, 15V		1	C13 CORNING #MZG-015-106-R-20
	65				
	66	CER DISC .1uF		4	C8,10,14,15 SPRAGUE #CK05BX104H
	67	CER DISC 100pF		2	C16,17 SPRAGUE #CK05BX101H
	68	CERAMIC .047uF		21	C18-32, 35-40 #DGO15C473M(AVX)
	69	CERAMIC .001uf		4	C33,34 SPRAGUE C41,42 #CK05BX102K
	70	CAPACITOR, ELECTROLYTIC 100uF		2	C11,12 ARCO #RME-E-E-100
	71				
	72				
	73				
	74				
	75	SOCKET, IC 14 PIN AUGAT #514-AG11D		27	
	76	16 PIN AUGAT #516-AG11D		9	
	77	20 PIN AUGAT #520-AG11D		15	
	78	24 PIN AUGAT #524-AG11D		3	
	79	28 PIN AUGAT #528-AG11D		1	
	80	SOCKET, IC 40 PIN AUGAT #540-AG11D		2	
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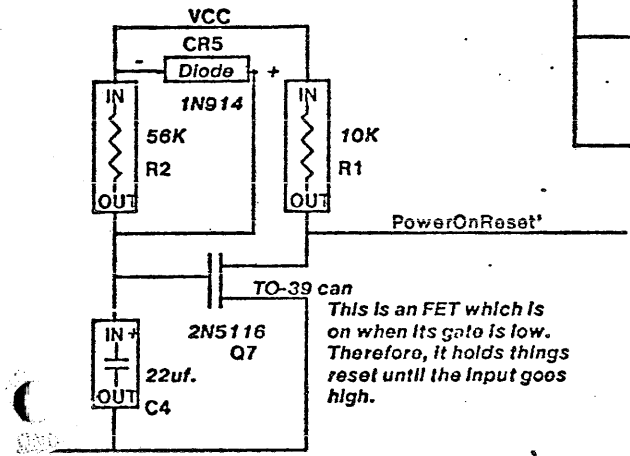
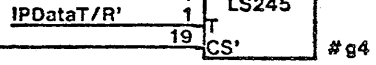
Input/Output Trceivers



IPCpuBus.00	2	A0	B0	18	IPDataBus.00	20
IPCpuBus.01	3	A1	B1	17	IPDataBus.01	21
IPCpuBus.02	4	A2	B2	16	IPDataBus.02	22
IPCpuBus.03	5	A3	B3	15	IPDataBus.03	23
IPCpuBus.04	6	A4	B4	14	IPDataBus.04	24
IPCpuBus.05	7	A5	B5	13	IPDataBus.05	25
IPCpuBus.06	8	A6	B6	12	IPDataBus.06	26
IPCpuBus.07	9	A7	B7	11	IPDataBus.07	27

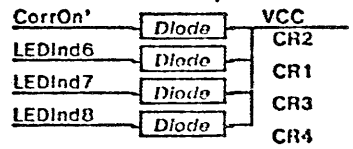
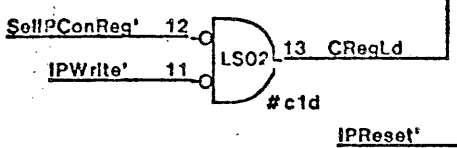


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IPCpuBus.09	3	A1	B1	17	IPDataBus.09	29
IPCpuBus.10	4	A2	B2	16	IPDataBus.10	30
IPCpuBus.11	5	A3	B3	15	IPDataBus.11	31
IPCpuBus.12	6	A4	B4	14	IPDataBus.12	32
IPCpuBus.13	7	A5	B5	13	IPDataBus.13	33
IPCpuBus.14	8	A6	B6	12	IPDataBus.14	34
IPCpuBus.15	9	A7	B7	11	IPDataBus.15	35

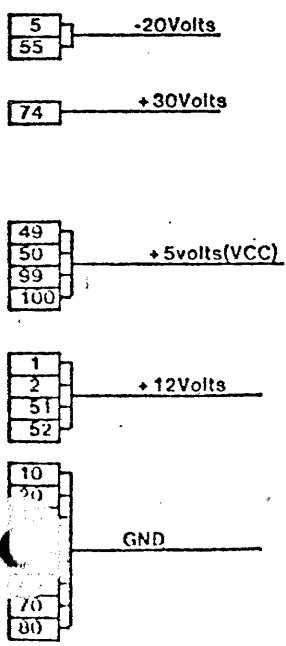


IPCpuBus.10	3	D0	Q0	2	CharCtr	17
IPCpuBus.12	4	D1	Q1	5	CorrOn'	94
IPCpuBus.14	7	D2	Q2	6	LEDInd7	
IPCpuBus.15	8	D3	Q3	9	LEDInd8	
IPCpuBus.13	13	D4	Q4	12	LEDInd6	
IPCpuBus.11	14	D5	Q5	15	DisableROM	
IPCpuBus.09	17	D6	Q6	16	ProcLock	
IPCpuBus.08	18	D7	Q7	19	BootSeqDone	

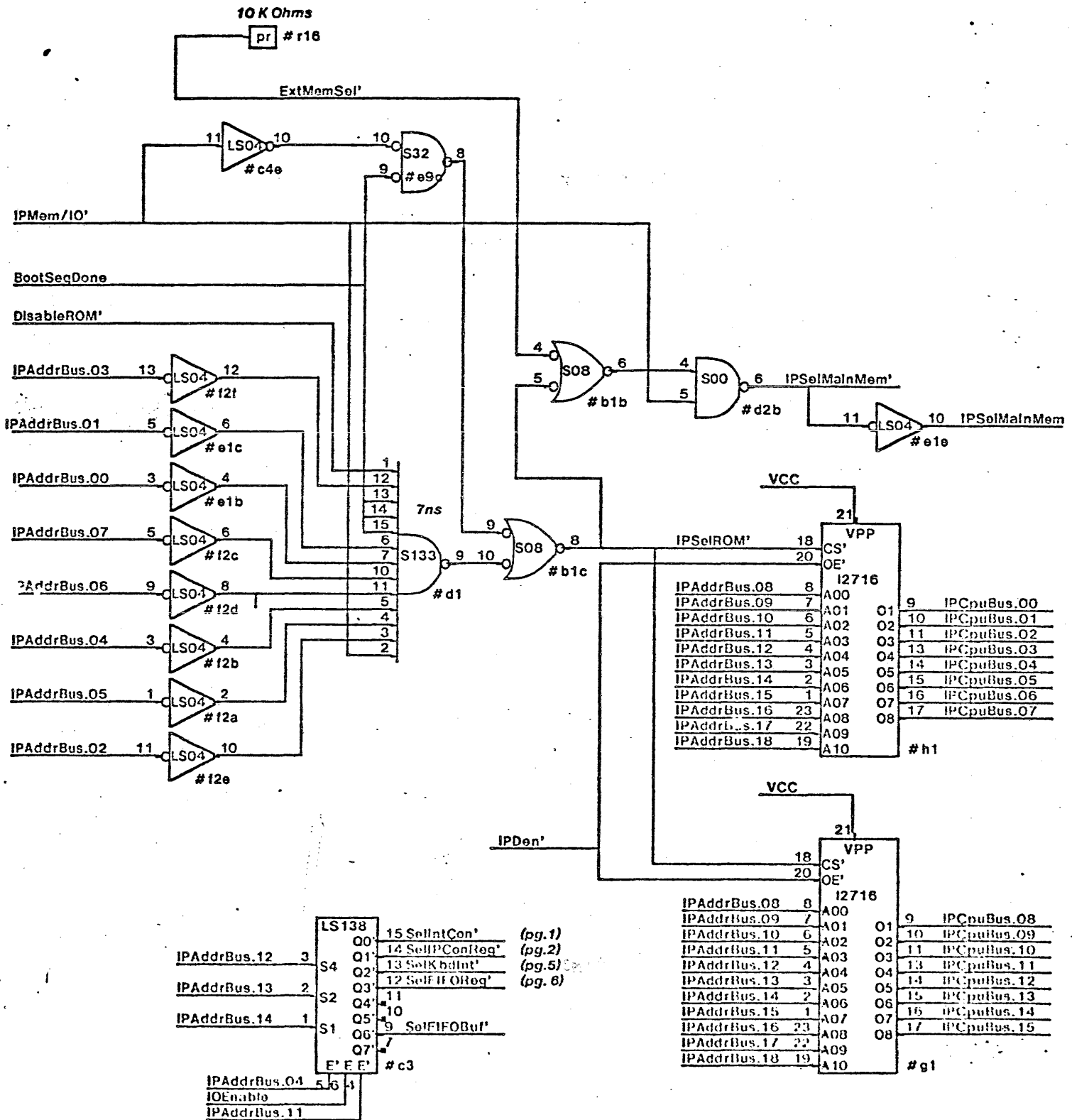
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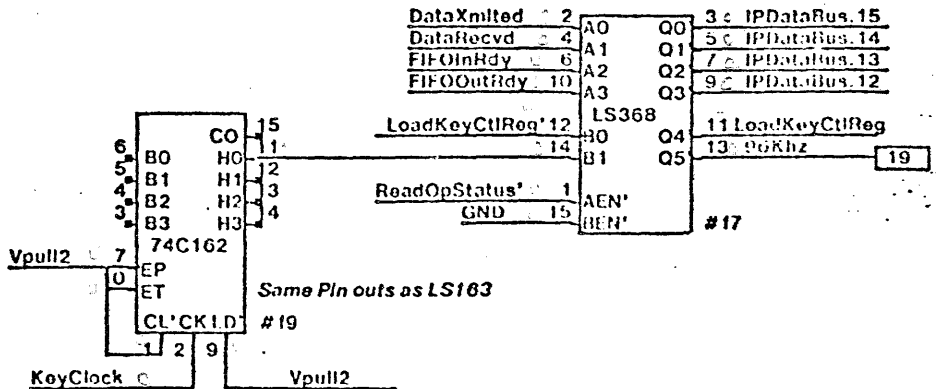
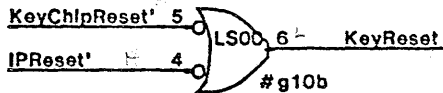
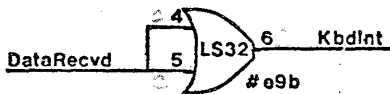
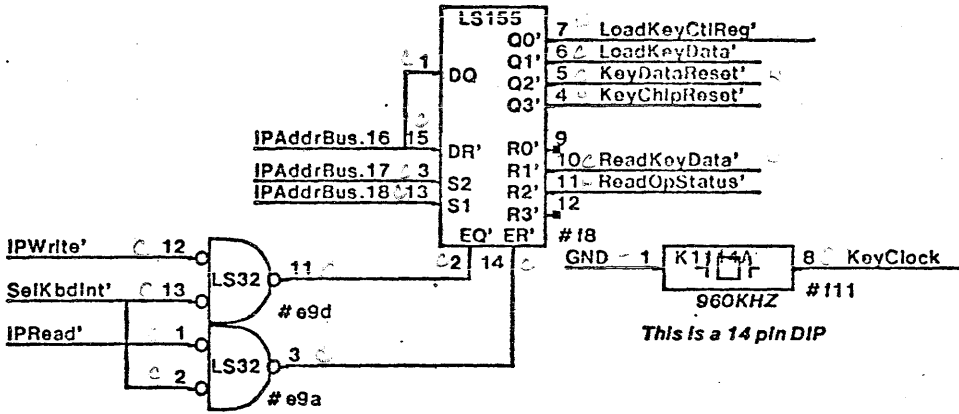
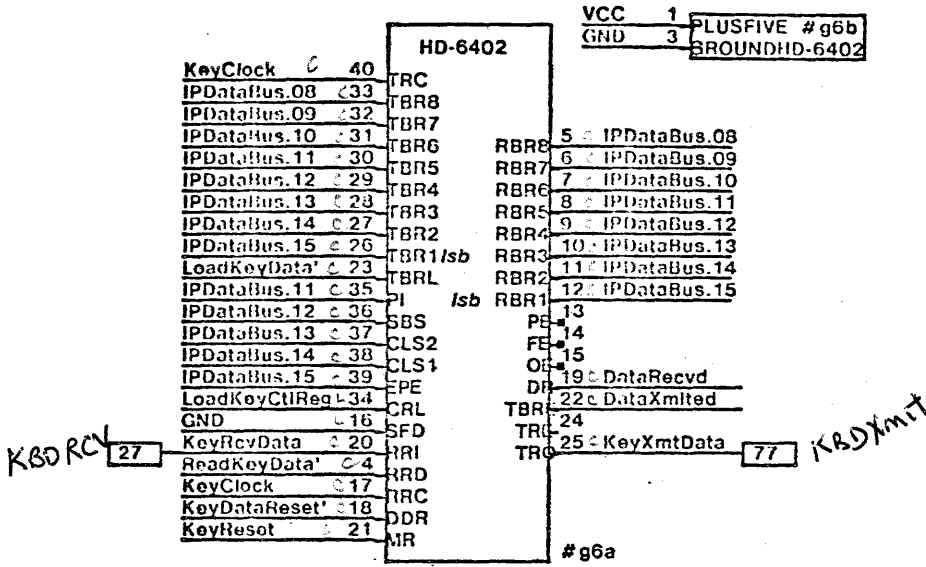
These are LEDs with integral resistors. They can be placed on 0.1" centers. The lead diameter is 0.02" and the distance between the leads can be a minimum of 0.2". HP Part # HLMP-6620

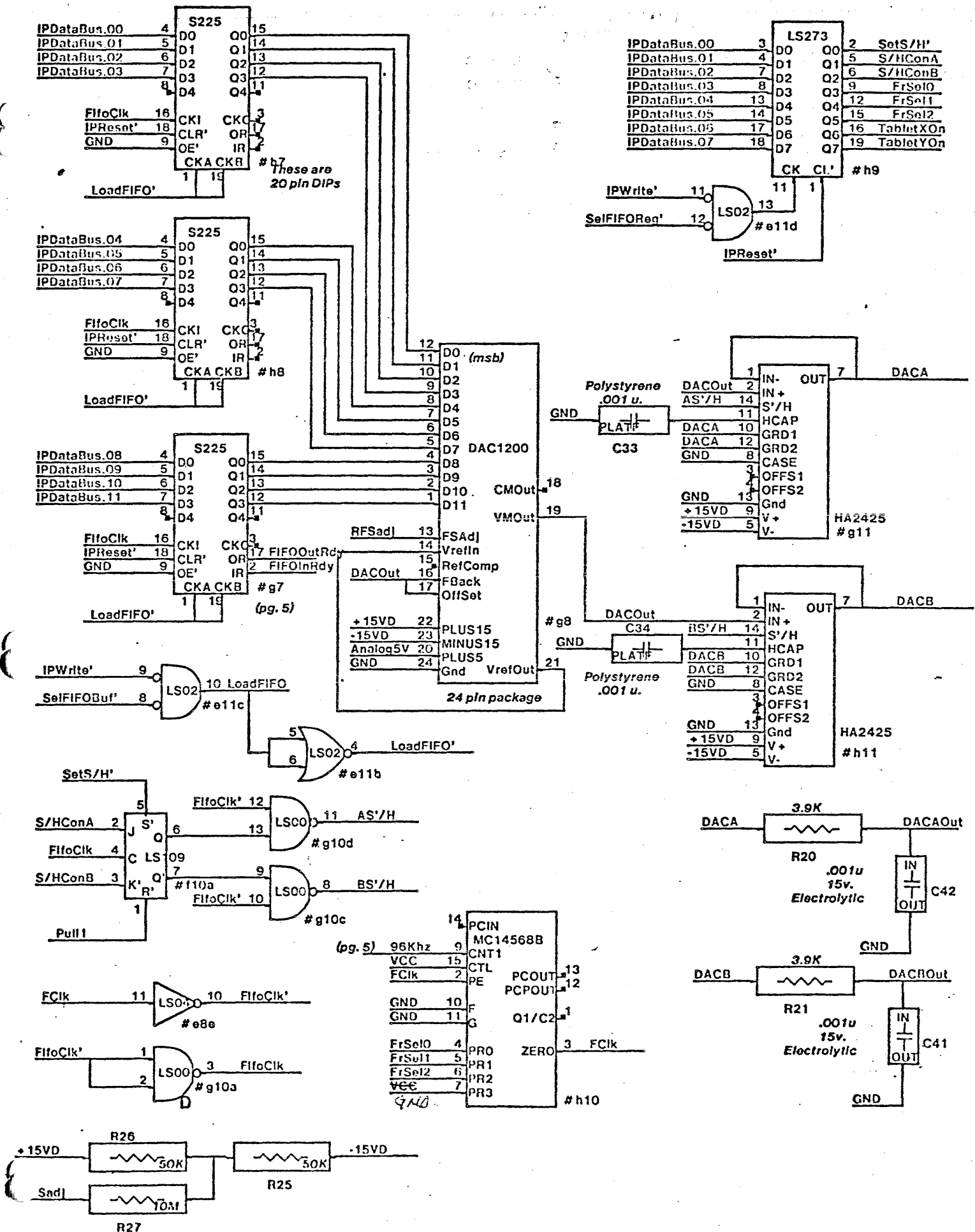


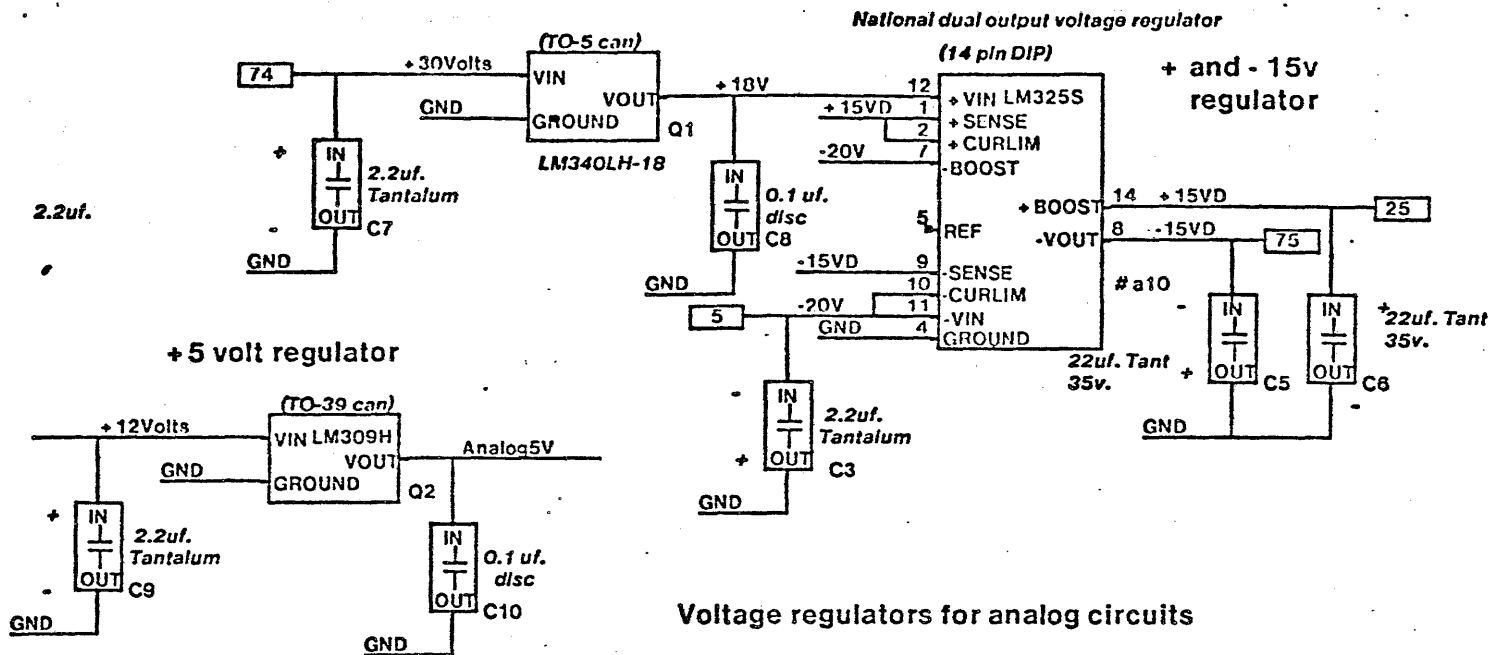
Boot addresses the high 16 cells in the ROM(ffff-ffff)
 After boot, ROM is addressed as 00000-00fff
 Note that 00fff-00fff contains the Boot code.



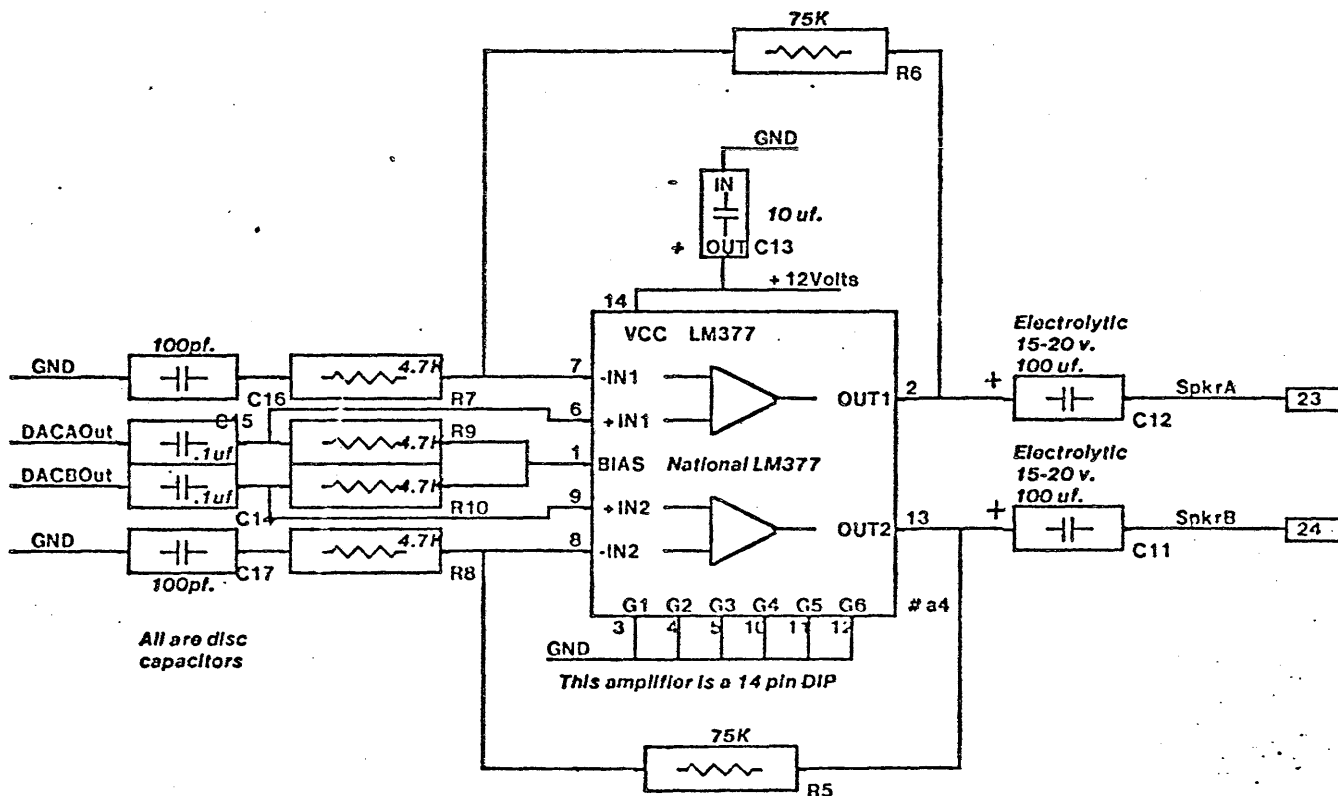
CMOS UART



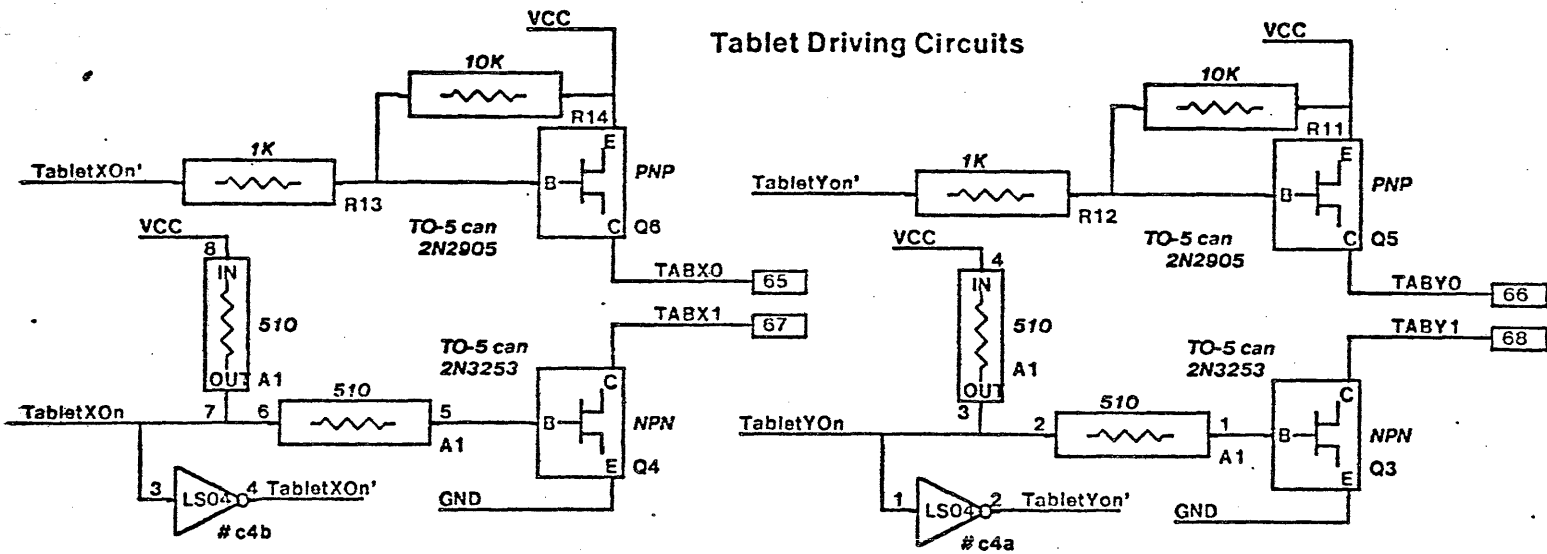




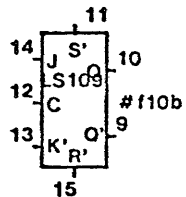
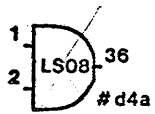
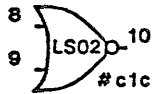
Stereo Amplifier



Tablet Driving Circuits



SPARES



XEROX SPG	Project	I/O Processor Module Tablet Interface	File	PNTIOP08.sil	Date	Dwy. No. 217606	Rev	E	Page	17
	NoteTaker		Designer	Fairbairn	4/21/78					