

Hardware Reference Designs

for SBC405GP

Wind River

Reference designs

Reference platform for preproduction design, prototyping, and evaluation

Wind River's SBC405GP reference design is a single board computer (SBC) that allows development to begin before the hardware is available. This board offers a solid platform for both hardware and software engineers to use in evaluation and prototyping designs that closely parallel their final applications.

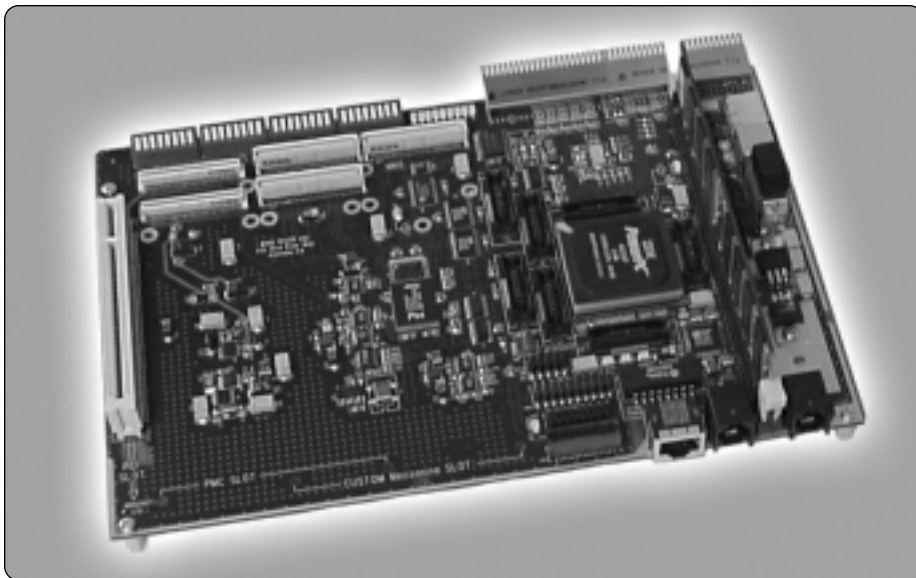
Like all Wind River reference designs, the SBC405GP comes complete with detailed schematics, board support package (BSP), visionBOOT boot loader, user documentation, and power supply.

The SBC405GP utilizes an IBM® PowerPC™ 405B3 embedded core running up to 266MHz, and features an MMU, I- and D-cache, as well as debug and execution trace logic. High performance and peripheral integration of the SBC405GP make the device ideal for wired communications, high density storage, network printers, and other pervasive computing applications.

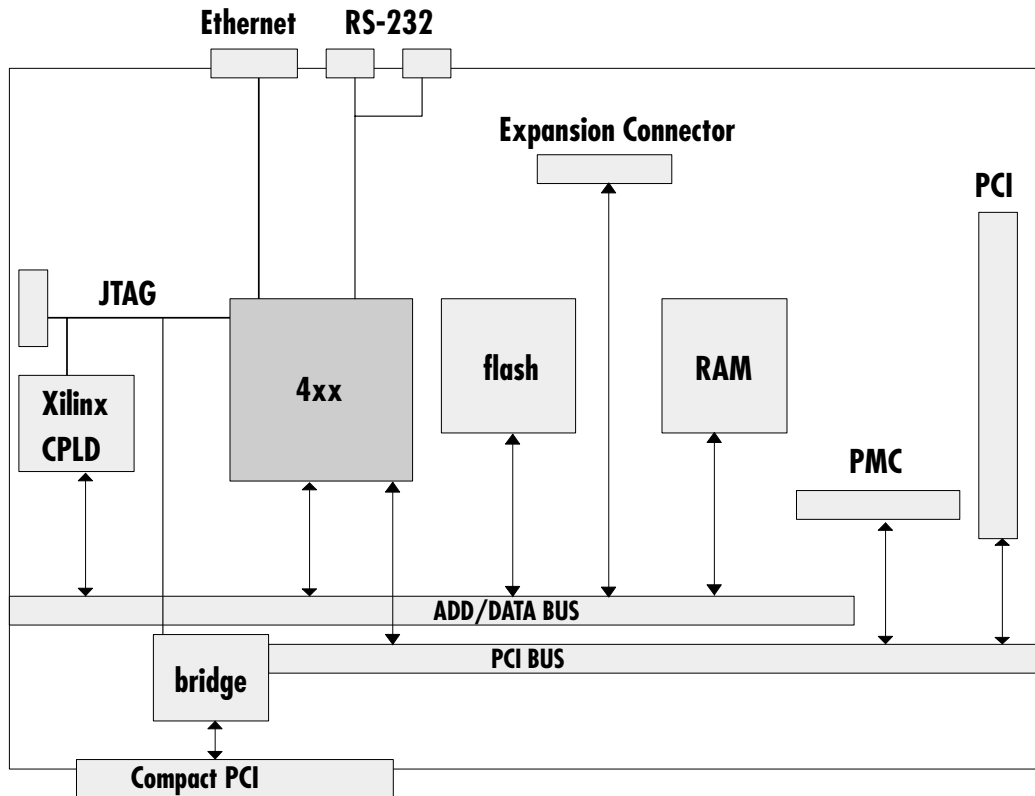
The SBC405GP comes with 64MB of SDRAM, 16MB of flash, and access to all the processor's communications channels via industry-standard headers and connectors.

Features

- 16MB Strata flash
- 64MB SDRAM (expandable up to 256MB)
- Mictor connectors for Agilent and Tektronix logic analysis support
- Two RS-232 communications ports
- 10/100BaseT Ethernet
- PCI and PMC connectors
- PCI-PCI bridge with CompactPCI connector
- Multidevice JTAG connection providing access to OCD and CPLD device programming
- visionBOOT boot loader
- Trace port access for application execution trace with visionTRACE
- VxWorks board support package



SBC405GP BLOCK DIAGRAM



Benefits

Experienced support

As a supplier of reference designs and hardware-assisted debugging tools for an array of microprocessors, Wind River has the experience to provide knowledgeable, responsive application and technical support for development projects.

Free detailed schematics

All Wind River reference designs come with free schematics in electronic format. Schematics give hardware engineers a solid design for use building their own boards.

Tools integration

Wind River reference designs and hardware-assisted debugging tools are preintegrated. Every board has its own register files, so developers can immediately begin using all of Wind River's tools.

Board support packages

Because even the best hardware design is useless without software support, Wind River provides the source code for board support packages available with VxWorks® 5.4 and VxWorks AE. Developers can fully utilize and modify the source code for current and future projects.

Accelerated early development

Firmware, hardware, and production test engineers can all take advantage of Wind River's reference designs. Firmware and hardware engineers can adapt Wind River's reference designs to their own, enabling them to reduce development time. Production test engineers can use the board and visionICE II/ visionPROBE II debugging tools to design and prototype complex and efficient production algorithms before the custom hardware is prototyped.

visionBOOT

visionBOOT provides boot loading that can use TFTP downloading of an image from a flash file system, an HTTP server, or a disk. The flash file image includes a VxWorks image and a diagnostic script that can be used to test the board hardware. visionBOOT is built with the visionWARE software development suite, which can be purchased separately.

visionWARE automatically generates visionBOOT and diagnostics and can program flash and field programmable gate arrays (FPGAs).

Emulators for turnkey development

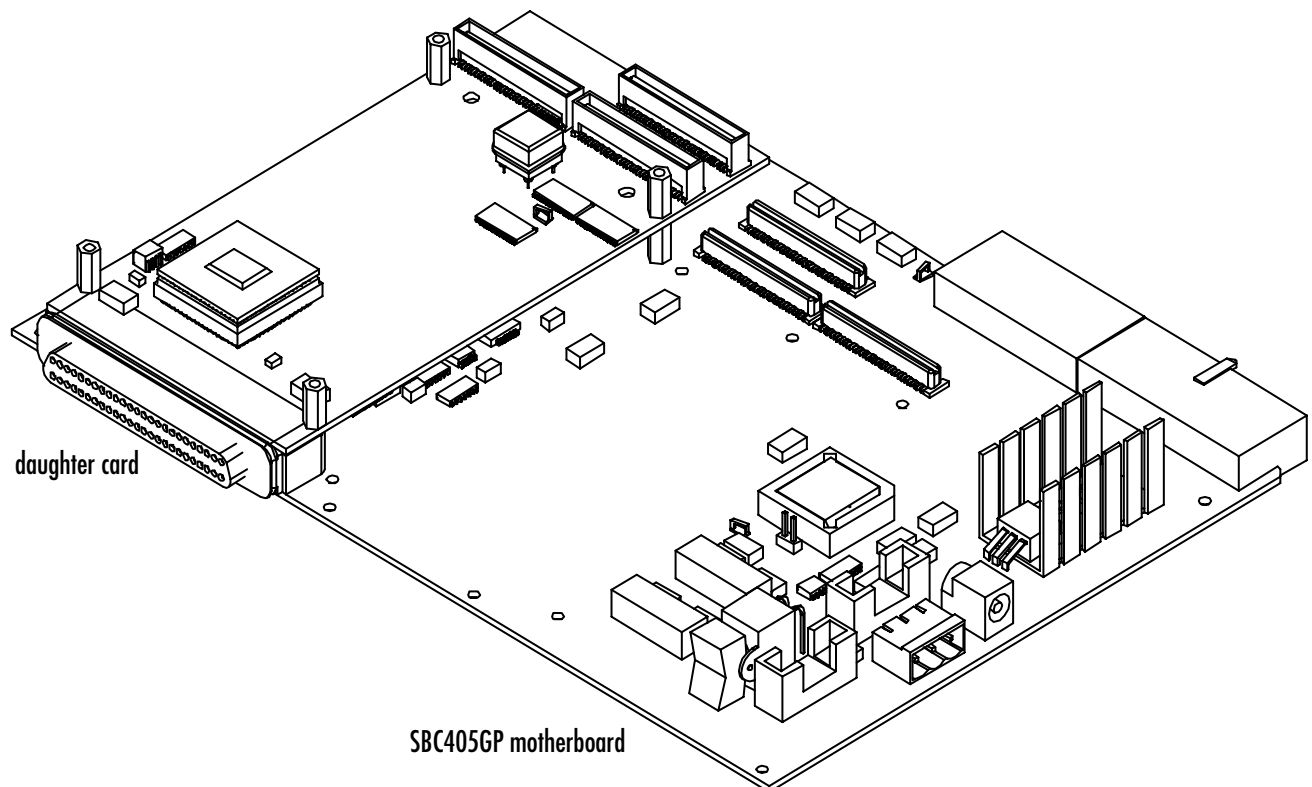
visionICE II and visionPROBE II JTAG emulators are designed to handle the full lifecycle of an embedded project. These tools offer JTAG-preconfigured hardware diagnostic tests, which are crucial for hardware bring-up phases. The features and fast download speed of visionICE II and visionPROBE II help shorten software engineers' projects.

PCI expansion connections

The SBC405GP is a robust baseline on which many different types of PCI peripherals can be integrated with the IBM405GP processor. These peripherals can be interfaced with the SBC405GP via one of three methods:

- CompactPCI via an Intel 21154 bridge chip
- PMC form factor
- 32-bit PC PCI slot

SBC405GP MOTHERBOARD WITH DAUGHTER CARD



EMBEDDED DEVELOPMENT SOLUTIONS

Fully integrated solutions

Wind River offers fully integrated hardware and software solutions designed for all phases of embedded development, from early board bring-up to debug and production/test. This tool suite includes reference designs with schematics and BSPs, software debuggers, hardware-assisted on-chip debuggers, compilers, and other software tools.

Hardware-assisted tools

Wind River's JTAG emulator tools include a high-performance debugging parallel cable for PCs (visionPROBE II) as well as a networked emulator complete with 10/100BaseT capabilities (visionICE II) for both PC and UNIX.

Hardware tools benefits

- Real-time target control via on-chip debugging
- High-speed binary downloads to target
- Built-in hardware diagnostics
- Flash memory programming
- Source-level debugging
- Statistical performance analysis
- Internal register configuration

Tornado integration

visionICE II and visionPROBE II are tightly integrated with the Tornado® development environment and enable core and optional Tornado tools to communicate with the target through JTAG. This provides board bring-up and debug support, crash diagnosis, as well as a fast download and debug channel, if Ethernet or serial channels are used.

RTOS independent

Wind River provides an extensive line of embedded development tools for all embedded real-time operating systems (RTOSs) including in-house RTOSs, Wind River RTOSs, and other commercial embedded operating systems. Published application program interfaces (APIs) support easy integration of Wind River tools with other embedded operating systems.

Source-level debuggers

Fully integrated with Wind River's hardware-assisted tools are source-level debuggers including visionCLICK, visionXD, and SingleStep™ with vision running on Windows and UNIX development hosts. In addition to point-and-click capability for major functions, these debuggers offer support for hardware, software, and complex breakpoints, a register configuration utility, built-in diagnostics, and complete run control.

Software tools benefits

- Intuitive graphical user interface
- Easy project configuration that reduces project start-up time
- RTOS API kit allows choice of third-party tools
- Tight integration with Wind River's full suite of embedded development tools
- Powerful register configuration utility to reduce register programming time
- Sophisticated compiler optimization technology with finegrained compiler control
- Kernel awareness for task-aware debugging of VxWorks, proprietary operating systems, and many third-party operating systems

The visionWARE developers kit

Wind River's visionWARE developers kit is designed for engineers developing software that interacts with and controls hardware. visionWARE extends JTAG emulator features with an application development framework that automatically generates a boot loader and a flash file system for custom boards that store and launch customer applications. It also generates a ready-to-run diagnostics program and target resident code to program advanced programmable devices, such as flash and FPGA, using a target Ethernet port.

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