## MANAGEMENT SUMMARY

The May 16, 1983 announcement of the seven new models of the V-8600 Series provides a more competitive entry for NCR into the large systems marketplace. The new models replace the previous V-8650 and V-8670 models which have been placed in a "no new sale" category. The singleprocessor V-8635 and dual-processor V-8645 are the base models of the new V-8600 Series. The larger models are made up of various configurations of the base models. Migration Path Engineering concepts which are built into the new V-8600 Series offer current users of V-8500 systems an excellent growth path and protection of their software investment. The new V-8600 Series offers a 32 percent faster central processor than the V-8500 Series, along with several other improvements including cache memory and a superior I/O System. NCR claims that the V-8635 should be comparable to the IBM 3033S and the V-8645 is comparable to the IBM 3083 in performance. The V-8600 systems use the VRX (Virtual Resource Executive) operating system exclusively.

The V-8500 Series, originally introduced April 1976, is now the seven-model Group II family of systems, and includes the uniprocessor V-8545-II, V-8555-II, V-8565-II, and the V-8665-IIE, and the dual processor V-8575-II, V-8585-II, and V-8595-II. All systems use the VRX virtual memory operating system which can also run programs written for Century systems without recompiling. The price/ performance of these systems ranges through the IBM 4300 Series product line. The V-8555-II, V-8565-II, and V-8665-IIE uniprocessors can be connected into tightly-coupled MP (multiprocessor) configurations of from two to four processors. The V-8575-II, V-8585-II, and V-8595-II, which are already tightly-coupled dyadic systems, can be connected with another system of the same type to form a fourprocessor configuration. NCR's multiprocessor systems NCR recently announced seven new models in its V-8600 family which replace the two previous V-8600 models and feature better performance. This diverse family of computers can support batch, transaction, and distributed processing environments. MODELS: V-8545-II, V-8555-II, V-8565-II, V-8565-IIE, V-8575-II, V-8585-II, V-8595-II, V-8635, V-8645, V-8655, V-8665, V-8675, V-8685, and V-8695. **CONFIGURATION:** Depending on product line, one to eight CPUs, 1 to 64 megabytes of memory, and a wide assortment of I/O devices are available. **COMPETITION: Burroughs B 1900 through** B 6900; Digital Equipment DECsystem-10 and -20; Hewlett-Packard HP 3000; Honeywell DPS 7 and DPS 8 Series; IBM System/38, and 4300 Systems; Sperry System 80, 1100/60 and 1100/80 Series. **PRICE:** Purchase prices range from \$56,9405 (V-8545-II) to \$2,995,000 (V-

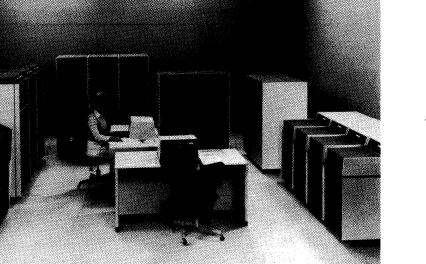
## **CHARACTERISTICS**

8695).

MANUFACTURER: NCR Corporation, Dayton, Ohio 45479. Telephone (513) 445-5000.

CURRENT MODELS: V-8545-II, V-8555-II, V-8565-II, V-8565-II, V-8565-II E, V-8575-II, V-8585-II, V-8595-II, V-8635, V-8645, V-8655, V-8665, V-8665, V-8675, V-8685, V-8695.

PRIOR MODELS: V-8415, V-8435, V-8455, V-8555M, V-8565M, V-8575M, V-8585M, V-8650, V-8670.



NCR's V-8500 Group II Series includes seven models that can have from one to four CPUs, one to eight megabytes of memory, and a wide range of I/O devices. Performance is comparable to the IBM 4300 Series.

	V-8545-II	V-8665-II				
	V-8555-II	V-8665-IIE	V-8575-11	V-8585-II	V-8595-II	V-8635
SYSTEM CHARACTERISTICS						
Date of introduction	Feb. 1982	1982 & 1983	Feb. 1982	Feb. 1982	Feb. 1982	May 1983
Number of main processors	1 to 4	1 to 4	2 to 4	2 to 4	2 to 4	1
Active Production	Yes	Yes	Yes	Yes	Yes	Yes
Relative performance level, approximate	0.75 to 1.00	1.25	1.65	2.08	2.48	3.0
System firmware	VS1, VS2	VS1, VS2	VS2	VS2	VS2	VS6
Operating systems	VRX; B1, B2,	VRX; VRX/MP,	VRX/MP,	VRX/MP,	VRX/MP,	VRX
	B3	B1, B2, B3	B3	B3	B3	
Basic system rental, per month	\$4,230 to	\$6,913 to	\$10,475	\$15,787	\$18,255	\$17,255
	\$4,675	\$8,255				
AIN PROCESSOR						
Cycle time, nanoseconds	56 to 84	56	56	56	56	38
Dynamic address translation hardware	Yes	Yes	Yes	Yes	Yes	Yes
Floating-point assist	Yes	Yes	Yes	Yes	Yes	Yes
VRX assist (instruction lookahead)	No	Yes	No	Yes	Yes	Yes
Instruction storage capacity, bytes	24K	32K	64K	64K	64K	96K
Upgradeable to	V-8555-II &		V-8585-II	V-8595-II		V-8645
	V-8565-II					
AIN STORAGE (All MOS, error correcting)						
Memory circuitry	16K, 64K chip	16K, 64K chip	16K, 64K chip	16K, 64K chip	16K, 64K chip	64K chip
Cycle time, nanoseconds	440	440	440	440	440	380
Minimum capacity, bytes	1M	2M	2M	2M	4M	4M
Maximum capacity, bytes	4M	8M	6M	6M	8M	8M
Increment size, bytes	1M	1M	1M	1 <b>M</b>	2M	4M
Interleaving	2-way	2-way	4-way	4-way	4-way	4-way
O CONTROL						
Integrated disk control	Optional	Optional	Optional	NA	NA	NA
Maximum disk subsystems per controller	4 strings of 8	3 strings of 8		NA	NA	64
· ·	drives	drives	drives			
Maximum no. of common trunks	6	8	8	8	8	2
Common trunk data rates, bytes/sec:						
Low-speed trunk	75KB	100KB	100KB	100KB	100KB	50KB
Medium-speed trunk	225KB	315KB	315KB	315KB	315KB	None
Very high-speed trunk	1200KB	1200KB	1200KB	1200KB	1200KB	1.1MB
I/O Link Controllers	Yes	Yes	Yes	Yes	Yes	2.0MB
OMMUNICATIONS CONTROL						
Integrated comm. control, max. lines	20	20	NA	NA	NA	NA
External comm. control, max. lines	256	256	256	256	256	256

### Table 1. 8500 AND 8600 SYSTEMS CHARACTERISTICS

Share a single copy of VRX/MP, the multiprocessor version of VRX. At the present time there are over 900 installations of V-8500 systems in the U.S.

NCR's V-8500 and V-8600 systems are designed to operate in batch, transaction-oriented, remote job entry (RJE), and distributed processing environments. The VRX operating system supports multiprogramming and tightly and loosely coupled multiprocessing.

## **PROCESSORS AND PERIPHERALS**

The V-8500 and V-8600 Series processors are microcode based and make extensive use of emitter-coupled logic (ECL) circuitry. The system architectures in all three product families are based on a high-speed Internal Transfer Subsystem, an internal bus with speeds of up to 72 million bytes per second. Various processor and peripheral subsystems, such as the Memory Subsystem and Common Trunk Subsystem, are connected to the internal bus for improved system flexibility. Each processor's "personality," or oper**DATE OF INTRODUCTION: See Table 1.** 

#### DATA FORMATS

BASIC UNIT: 8-bit byte. Each byte can represent 1 alphanumeric character, 1 or 2 BCD digits (in unpacked or packed format, respectively), or 8 binary bits. Four consecutive bytes form a "word."

FIXED-POINT OPERANDS: Can range from 1 to 256 bytes in length, in either decimal or binary mode. A "word binary" mode is available that takes particular advantage of the system's 4-byte adders; each 4-byte word is treated as a signed 31-bit integer.

FLOATING-POINT OPERANDS: Consist of a 7-bit exponent and a 24-bit fraction in the single-precision format and a 7-bit exponent and 56-bit fraction in the doubleprecision format.

INSTRUCTIONS: 4 or 7 bytes in length, specifying 1 or 2 memory addresses, respectively.

	V-8645	V-8655	V-8665	V-8675	V-8685	V-8695
SYSTEM CHARACTERISTICS						
Date of introduction	May 1983	May 1983	May 1983	May 1983	May 1983	May 1983
Number of main processors	2	2	3	4	6	8
Active Production	Yes	Yes	Yes	Yes	Yes	Yes
Relative performance level, approximate	5.70	5.88	8.23	10.76	16.17	21.52
System firmware	VS6	VS6	VS6	VS6	VS6	VS6
Operating systems	VRX	VRX	VRX	VRX	VRX	VRX
Basic system rental, per month	\$26,325	\$33,680	\$44,450	\$52,205	\$78,385	\$98,680
MAIN PROCESSOR						
Cycle time, nanoseconds	38	38	38	38	38	38
Dynamic address translation hardware	Yes	Yes	Yes	Yes	Yes	Yes
Floating-point assist	Yes	Yes	Yes	Yes	Yes	Yes
VRX assist (instruction lookahead)	Yes	Yes	Yes	Yes	Yes	Yes
Instruction storage capacity, bytes	192K	192K	288K	384K	576K	768K
Upgradeable to	V-8665 to	V-8665 to	V-8675 to	V-8685 to	V-8695	
	V-8695	V-8695	V-8695	V-8695		
MAIN STORAGE (All MOS, error correcting)						
Memory circuitry	64K chip	64K chip	64K chip	64K chip	64K chip	64K chip
Cycle time, nanoseconds	380	380	380	380	380	380
Minimum capacity, bytes	4M	8M	8M	8M	12M	16M
Maximum capacity, bytes	16M	16M	24M	32M	48M	64M
Increment size, bytes	4M	4M	4M	4M	4M	4M
· · ·						
Interleaving	4-way	4-way	4-way	4-way	4-way	4-way
I/O CONTROL						
Integrated disk control	NA	NA	NA	NA	NA	NA
Maximum disk subsystems per controller	64	64	64	64	64	64
Maximum no. of common trunks	2	2	2	2	2	2
Common trunk data rates, bytes/sec:						
Low-speed trunk	50KB	50KB	50KB	50KB	50KB	50KB
Medium-speed trunk	None	None	None	None	None	None
Very high-speed trunk	1.1MB	1.1MB	1.1MB	1.1MB	1.1MB	1.1MB
I/O Link Controllers	2.0MB	2.0MB	2.0MB	2.0MB	2.0MB	2.0MB
COMMUNICATIONS CONTROL						
Integrated comm. control, max. lines	NA	NA	NA	NA	NA	NA
External comm. control, max. lines	256	512	512	512	512	512

#### Table 1. 8500 AND 8600 SYSTEMS CHARACTERISTICS (continued)

➤ ating mode, is controlled by two different types of firmware: Century-emulation and Virtual. Century emulation (on "N" systems) firmware permits batch processing, multiprogramming, on-line transaction processing, and is used on V-8500 systems. Virtual (on "V" systems) firmware uses virtual memory techniques that provide greater internal processing capabilities and better system resource management. The virtual firmware is available in the V-8500 and V-8600 systems. The V-85X5-II and V-8600 systems are designed specifically to use "V" firmware.

Various processor subsystems can be attached to the common Internal Transfer Subsystem. The Memory Subsystems use either 4K, 16K, or 64K MOS memory chips. Memory cycle times range from 380 to 440 nanoseconds. The minimum memory size ranges from one megabyte on the V-8545-II and increases to 16 megabytes on the V-8695.

The maximum memory available ranges from 2 megabytes on the V-8545-II to 64 megabytes on the V-8695. All memory subsystems have single-bit error correction and double-bit error detection. A high-speed cache memory is

#### MAIN STORAGE

STORAGE TYPE: Metal oxide semiconductor (MOS). The V-85X5-II models and the V-8600 models use 64K chips.

CAPACITY: The main memory capacity of the V-8500 and V-8600 systems is stated in Table 1.

CYCLE TIME: See Table 1.

CHECKING: All data paths between the central processor and main storage are parity-checked by byte. When data is stored, an error-correcting code is substituted for the parity bits. When the data is retrieved, single-bit errors are detected and corrected automatically, and most multiple-bit errors are detected and signaled so that appropriate program action can be taken.

STORAGE PROTECTION: The segment table origin register guards against inadvertent overriding and/or unauthorized reading of data in specified blocks of storage. This register is standard in the V-8500 and V-8600 systems.

CACHE MEMORY: The V-8600 systems have a cache memory for high-speed access to the most recently stored

➤ used in all V-8600 systems. I/O devices can be attached to the system via several peripheral subsystems: 1) the Common Trunk Subsystem, used on V-8500 systems (Trunk Channel Control Processor on V-8600), 2) the Integrated Disk Subsystem, used on the V-8500 systems, 3) the I/O Link Control Subsystem, used on V-8500 systems, and 4) the I/O Subsystem, used on the V-8600 systems. These systems can accommodate data transfer rates as high as two megabytes per second.

An optional Communications Subsystem is available on all models to connect remote terminals or satellite processors.

The Service Subsystem (V-8500 Series), and the System Control Unit (V-8600 Series) perform the following functions: 1) firmware loading during start-of-day procedures, 2) peripheral subsystem control, and 3) system testing and diagnostics.

The V-8500 Group II systems replace the older V-8500M models and provide improved price/performance over their predecessors. The seven-model product line includes the entry-level V-8545-II, a one-megabyte system that has one-third more power than the V-8535-II, which is now discontinued. It can have up to two megabytes of memory. The V-8555-II has about 40 percent more performance than the V-8545-II, one megabyte of memory, and can be expanded to up to four megabytes. The V-8565-II has 25 percent more throughput than the V-8555-II. This model features the same 56-nanosecond CPU as the larger dualprocessor models, four-way memory interleaving for better performance, and a hardware-based Virtual Assist Unit that can boost virtual memory operations. The V-8565-II has two to six megabytes of memory. Each of the smaller uniprocessors can be field upgraded to the V-8565-II. The V-8565-IIE is the largest uniprocessor with a tuned firmware set and memory expansion to 8 megabytes.

At the upper end of the performance scale are three dyadic, or dual-processor systems, the V-8575-II, V-8585-II, and V-8595-II. All models feature 56-nanosecond processor cycle times and from two to eight megabytes of four-way interleaved memory. The V-8575-II has about 20 percent less power than NCR's previous dyadic system, the V-8585M, and costs less than half as much. The V-8585-II has performance comparable to the system's namesake, the V-8585M, yet costs almost 40 percent less. Both the V-8575-II and V-8585-II have two to six megabytes of memory. The largest system is the V-8595-II, which has four to eight megabytes of memory, about 20 percent more power than the V-8585-II, and costs about 15 percent less than the V-8585M. The V-8575-II and V-8585-II can be field upgraded to the V-8595-II. The V-85X5-II uniprocessor models cannot be field upgraded to the dyadic systems since a swapout of CPUs is required.

The top-end V-8600 Series models include the V-8655 which is configured with two V-8635 processors, the V-8665 which is configured with one V-8635 single- and one V-8645 dual-processor, the V-8675 which is configured with two V-8645 dual-processors, and the V-8695 which is  $\triangleright$ 

► activities in memory. The V-8600 systems use from 32K bytes to 512K bytes of cache memory.

MEMORY INTERLEAVING: This feature is provided on all systems, and allows either two or four contiguous words to be read from or written to memory without accessing any memory module continuously for more than a single cycle. It also permits multiple simultaneous memory access from several subsystems. Four-way interleaving is provided in the V-8565-II, V-8665-II E, V-8575-II, V-8585-II, V-8595-II, and all V-8600 systems. The V-8555-II has two-way interleaving, and the V-8535-II and V-8545-II do not use interleaving.

#### **CENTRAL PROCESSORS**

The Central Processing Units for the V-8500 and V-8600 series systems are built around an Internal Transfer Subsystem, or high-speed bus, onto which major system components are connected. The processors make extensive use of emitter-coupled logic (ECL) circuitry.

A Processor Subsystem is connected to the Internal Transfer Subsystem. The Processor Subsystem operates under firmware control and performs the following functions in all systems: 1) interprets and executes instructions from software; 2) manages data transfer from main memory to peripheral devices; and 3) performs console functions as requested by the operator.

These additional CPU functions are provided by the Service Subsystem (V-8500) and the System Control Unit (V-8600): 1) firmware loading during start-of-day procedure; 2) peripheral subsystem message management; and 3) system testing diagnostics, and error logging.

The System Control Unit (SCU) is the central control point of the V-8600 systems. Either one or two optional microprogrammed control processors control two independent console displays for operator communications and system diagnostics, two 1-megabyte flexible disk drives for firmware loading and error logging, an optional console printer, and channels for additional CRT/keyboard units which can operate as remote system consoles. From either of the system consoles or a remote console, both primitive level diagnostics (which test the basic hardware functions and capabilities) and virtual level diagnostics (which test the virtual machine functions and capabilities) can be run on system elements while normal operations continue.

The control processor (CP) is the primary controlling unit of the SCU. To increase system availability, a second CP is available. Each processor and its control elements operate independently when the second CP is employed which provides immediate attention for all connected devices and backup capabilities preventing any single point SCU failure. Both CPs communicate with each other which results in the most efficient use of SCU resources.

Instructions are executed using a three-stage pipeline technique. The three stages are: 1) the fetch stage, which obtains the instruction; 2) the interpret stage, which assembles all necessary operands and decodes the instruction; and 3) the execute stage, which performs the specified operation. It takes three processor cycles to perform an instruction, and all three stages are active, continuously performing their respective functions on three separate instructions. This provides an effective execution rate of one instruction per processor cycle. All of the V-8600 systems include a highspeed buffer software unit (32K bytes in the V-8635 and 512K bytes in the V-8695) for increased performance.

configured with four V-8645 dual-processors. The smallest of the V-8600 Series processors, the V-8635, has a main memory capacity of eight megabytes while the larger V-8695 has a main memory capacity of 64 megabytes. The V-8645 is tightly-coupled; however, a unique combination of both tight and loose coupling is achieved when a V-8635 and a V-8645 are configured together.

The current V-8500 and V-8600 systems can use most of the peripheral devices that have been used on the more mature Century and Criterion systems. Users have a wide variety of peripherals to select from, including four different mass storage devices ranging from 10 megabytes up to 1.1 gigabytes, five tape drives with numerous configurations, a large assortment of low-, medium- and high-speed printers, and various unit record and MICR devices.

Communications with remote terminals and remote hosts can be achieved via the Integrated Communications Subsystem (ICS) and the 621-103 Communications Multiplexer. The ICS provides up to 20 lines for on-line remote communications with terminals operating at from 50 to 9600 bps. The free-standing 621-103 Communications Multiplexer can accommodate a mix of asynchronous and synchronous lines up to 255 lines. Asynchronous lines can handle devices ranging up to 9600 bps, and synchronous lines can accommodate up to 56,000 bps speeds.

#### SOFTWARE AND SUPPORT

A choice of three basic operating systems is available for the V-8500 and V-8600 systems. To provide a migration path for existing NCR Century B1, B2, or B3 operating systems, certain V-8500 Series models can function with any of these three operating systems, provided the Centuryemulating RS1 or RS3 firmware is used. B-Series applications can be executed without the RS1 firmware if run under the VRX operating system. The Century operating systems can support single-user and multiprogrammed batch operations. Languages supported include Cobol 74, Fortran, RPG, Basic, and NEAT/3 assembler.

When using the Virtual Resource Executive (VRX), the user will effectively have 16 million bytes of storage available for each program, regardless of the real-memory capacity. VRX operates under the VS1 firmware in a uniprocessor system and the VS2 and VS6 firmware in a multiprocessor environment. The virtual-memory VRX can also run Century programs with minimal reconfigurations. The current version of VRX is Release 9.

As programs are executed under VRX, it uses special hardware called the Dynamic Address Translator to assign currently active portions of virtual storage to real memory. VRX supports various functions, such as batch, transaction processing, remote job entry (RJE), multiple RJE, direct program access, and on-line program development.

A primary feature of VRX is the data management system called the Criterion Access Method (CAM). CAM fully supports the input/output requirements of the Cobol-74

➤ The 85X5-II series offers multiple processor configurations with the dual-processor V-8575-II, V-8585-II, and the V-8595-II systems. Using the VRX/MP (multiprocessing) operating system, up to four processors can be configured on a V-8555-II, V-8565-II, V-8575-II, V-8585-II, and a V-8595-II system. These multiprocessing systems are based on a "tightly-coupled" principle, in which all processors 1) share access to all available main memory, 2) communicate with each other via data storage in memory and passing signals between each processor, and 3) operate equally under one copy of the operating system.

The 86X5 series also offers multiple processor configurations. The base models in this series include the singleprocessor 8635 and the dual-processor 8645. The remaining 8655, 8665, 8675, 8685, and 8695 models are made up of multiple configurations of the base models and are termed the "processor complex" models. The 8655 consists of two 8635 processors, the 8665 consists of one 8635 single- and one 8645 dual-processor, the 8675 consists of two 8645 dualprocessors, the 8685 consists of three 8645 dual-processors, and the 8695 consists of four 8645 dual-processors. The dual-processor 8645 is a tightly-coupled system and the 8655 is loosely-coupled; however, when a complex processor system includes both an 8635 and an 8645 system, a unique combination of both tight and loose coupling results.

Connecting two or more systems into an MP configuration requires an MP kit which contains all the necessary hardware. The MP conversion requires an Interbus Cable between each processor, a 16KB Instruction Storage Unit to hold the larger VRX/MP (also known as VS2) firmware, and a two-sided floppy disk for each processor that contains the VRX/MP firmware. Exceptions to this rule are the V-8575-II, V-8585-II, and the V-8595-II, which are dualprocessor systems operating under VRX/MP. The maximum four-processor configurations can be obtained by connecting two of the same model together via an Interbus Communications Adapter. No ISUs are needed.

INDEX REGISTERS: A separate set of 64 32-bit registers is maintained in reserved storage for each active program. The 64-word set associated with the program currently being executed by the processor is brought from memory and contained in a hardware register set.

INSTRUCTION REPERTOIRE: There are two basic system instruction sets: the V-8500 Base Virtual Machine and the VRX Virtual Machine. The Base Virtual Machine uses the 71-instruction set found in the NCR Century 300 computer. The VRX Virtual Machine instruction set contains 95 instructions in the uniprocessor (VS1) version, and 103 instructions in the multiprocessor (VS2) version. The following table summarizes the instruction sets for both the Base Virtual Machine and the VRX Virtual Machine:

	Base Virtual Machine	VRX Virtual Machine
Fixed Point Binary	11	11
Decimal Arithmetic	9	9
Move Data	3	6
Logical	8	12
Transfer	13	24
Special	15	17
Input/Output	_	4
Floating Point	12	12

The additional instructions used in the multiprocessor VS2 set are for monitor and control functions within the larger system configuration.

Subsystems	658	6530	6540	6550
Cabinets per subsystem	1	1 or 2	1 or 2	1
Disk packs/HDAs per cabinet	1	2 to 4	2 to 4	2
Capacity, megabytes	100 to 200	27 to 81	540	1,092
Average access time, milliseconds	20 to 30	30	30	25
Average rotational delay, milliseconds	8.33	8.33	8.33	8.33
Data transfer rate, bytes/second	806,000	1,200,000	1,200,000	1,200,000
	IDC			
Controller model	625-0301	6539	6549	6559
Comments:	Bit Serial			
	I/O Link			
	Controller/			
	Adapter			
	combination			
	recommended			

#### Table 2. MASS STORAGE

language and handles three different file organizations: sequential, relative, and indexed. For even greater database management requirements, NCR offers the popular TO-TAL, from Cincom Systems, and TOTAL IQL, an interactive query language.

Three compilers are available with VRX: Cobol-74, Fortran 77, and NEAT/VS. The VRX Cobol-74 compiler is an implementation of the ANSI 1974-standard language; it produces object code for the Cobol virtual machine which runs under VRX. The NEAT/VS compiler is compatible with NCR's NEAT/3, a macrooriented assembly language, and provides programming interfaces to VRX. The Fortran 77 compiler conforms to the ANSI Fortran 77 standards and requires an Extended Hardware Assist feature on all V-85X5-II Series processors.

Transaction processing and on-line program development support are key elements in NCR's product strategies. Leading the transaction processing movement is VRX TRAN-PRO, a general-purpose monitor that utilizes the speed and efficiency of VRX to its maximum benefit. Augmenting TRAN-PRO is VRX TRANQUEST, a comprehensive query language that operates either on-line with TRAN-PRO and TOTAL, or in a batch mode with TO-TAL. NCR has three systems to facilitate the transition from batch to on-line operations, including VRX Telecomm/Development, which defines the network configuration, VRX TP-Monitor, which provides greater on-line efficiencies, and the EZ-KEY Data Entry System, for improved data entry operations.

For communications users, VRX also offers a Network Description Language (NDL) to enable on-line configuration modification at execution time rather than at compilation time, thus providing more flexibility in a communications environment. Teletype-compatible de► set to process object code generated by the VRX Cobol compiler.

VRX Fortran is available on V-85X5-II (with a hardware assist option) and V-8600 systems for processing ANSI Fortran 77 object code.

The VRX Virtual Machine permits programs currently running on the Base Virtual Machine and NCR Century processors to run on systems using the VRX operating system.

INSTRUCTION STORAGE UNIT (ISU): In all NCR V-8500 and V-8600 systems, most of the firmware that directs the system to perform the required functions (as a Century system or a virtual memory system) is stored in a high-speed memory called the Instruction Storage Unit. Capacity of the ISU ranges from 24K bytes on the V-8545 to 192K bytes on the V-8645.

TIME OF DAY CLOCK: Used by the software for such functions as providing time indication for operator messages and timing program runs by logging the starting and ending times of program execution.

#### **INPUT/OUTPUT CONTROL**

Input/output control within the V-8500 systems is provided through three types of subsystems: Common Trunk I/O Subsystems, the I/O Link Controller (IOLC) used on all V-8500 systems, and the Integrated Disk Subsystem (IDS). The IDS is not used on the V-8575II, V-8585-II, and V-8595-II.

I/O control on V-8600 systems is maintained by two basic subsystems: Channel Control Processors (CCP) which can control up to 32 peripheral channels, and an optional Trunk Channel Control Processor (TCCP), both of which interface a wide variety of peripherals.

The Common Trunk Subsystem and Trunk Channel Control Processor (TCCP) subsystems all use a combination of low-, medium-, and very high-speed trunks. Each trunk type > vices, bisynchronous line disciplines, and existing Century on-line applications are supported.

Customer Operated Automatic Checkout (COACH) diagnostics, capable of isolating hardware problems to a faulty module, are also available to V-8500 users. COACH enables the user to provide advanced information to the NCR field engineer concerning the nature of the problem prior to his/her arrival at the site. A more comprehensive set of diagnostic programs is available to the NCR field engineer for in-depth fault isolation. This on-site diagnostic capability is further enhanced by the use of a remote system console that can, via telephone, connect NCR specialists to the customer's system for even greater levels of diagnosis and analysis.

#### NCR COMMUNICATIONS NETWORK ARCHITECTURE

NCR's comprehensive communications network design, NCR/CNA, is composed of software and various services that define NCR's overall network strategy for the 1980s. NCR/CNA includes NCR/Data Link Control protocol, intra-network disciplines, access methods, and other telecommunications functions. The NCR/CNA approach permits each processor to function in an independent or distributed mode, which reduces the need for a centralized host to control the network.

The network protocol, NCR/Data Link Control (NCR/ DLC), is a bit-oriented control protocol in accordance with ANSI's ADCCP and ISO's HDLC. NCR/DLC will support SDLC and the protocols of other vendors, such as BSC and TTY. For short-distance communications links, NCR/ DLC includes a proprietary, modemless technique for high-speed (48,000 bps) transmission. A Virtual Circuit Interface, based on CCITT's X.25, will also be supported, enabling the network to provide communications links with public packet-switching networks.

CNA's processor access method, NCR/Telecommunications Access Method (NCR/TAM), will provide a standard, transparent telecommunications handler for application programs. The application program interface with NCR/TAM is the ANSI Cobol 74 Message Control System. The functions performed by NCR/TAM include system and link control queue management, resource scheduling, packet header processing, error recovery and reporting, and diagnostic support.

### **COMPETITIVE POSITION**

The NCR V-8500 Series is marketed to compete with the IBM 4300 Series in the areas of price and performance. The V-8545-II has one-third more power than the V-8535-II. The V-8555-II has 40 percent more processing power than the V-8545-II, and is comparable to the IBM 4331-II. The V-8565-II has 25 percent more power than the V-8555-II, and compares with the Burroughs B 5930. The V-8575-II has about 20 percent less power than the V-8585M and is comparable to the IBM 4341-1. The V-8585-II has about the same power as the V-8585M, and about one-third more **D** 

▶ is designed to handle peripherals with specific data transfer characteristics. Each trunk can attach up to eight I/O devices. Only low-speed and very high-speed trunks can be configured on V-8600 series processors. Low-, medium-, and very high-speed trunks can be configured on V-8500 series processors.

Low-speed trunks provide for single-byte transfers to and from the CPU. The CPU performs the data transfers using reserved memory locations for control registers.

Medium-speed trunks have two major improvements over their low-speed counterparts. The control registers implemented in memory are contained in the trunk circuitry, and a 4-byte interface is used instead of the single-byte interface. The CPU performs the data transfers to and from main memory.

Very high-speed trunks are direct memory access devices that do not require any CPU activity. They include all the features of the medium-speed trunks and also have memory address generation circuitry and up to two stages of data buffering. The very high-speed trunks perform all functions necessary to transfer data to and from main memory.

The I/O Link Controller Subsystem is based on three elements: the I/O Link Controller, I/O Links, and the I/O Link Adapter. The I/O Link Controller (IOLC) can attach up to four peripheral subsystems through I/O links. Up to six IOLCs can be configured in the V-8545-II and V-8555-II and up to eight in the V-8565-II, V-8575-II, V8585-II, and V-8595-II. The I/O Link is a coaxial cable that provides a two-megabyte-per-second bit-serial data path between the IOLC and the I/O Link Adapter (IOLA). The IOLA is a buffered interface that provides the timing necessary to connect the peripheral to the IOLC subsystem. Up to four I/O Link Adapters can be attached to an I/O Link Controller.

Channel Control Processors in the V-8600 I/O Subsystem interface directly to main memory via the Internal Transfer Subsystem. All I/O management functions are performed by the CCP with no involvement of the CPU required. Two CCPs are standard, and two are optional. Each CCP has eight channels. On the 8635 and 8645, up to 32 channels connect various peripheral subsystems to the CCP through the Dynamic Channel Exchange (DCX), a switching center that provides bit-serial data paths between the peripheral and CCP. Since all CCPs connect to the DCX, the loss of a CCP does not result in the loss of I/O devices. Automatic load leveling is also provided with this arrangement. The maximum data transfer rate over each channel is two megabytes per second. In those situations where an I/O device is not compatible with the CCP, an optional Trunk Channel Control Processor (TCCP) can be used. Up to two NCR common trunks (either low- or very high-speed) can be connected to the TCCP for these devices. The configurations available include either one low-speed or two low-speed, or one lowspeed and one high-speed.

The Dynamic Channel Director (DCD) is a solid-state electronic switch and is used on the processor complex models in place of the DCX. The DCD offers additional I/O capabilities that provide the inter-system communication required for loose coupling. The basic 4 x 16 DCD module connects any of four CCPs to any of 15 channels and one System to System Adapter (SSA). Additional DCDs can be connected to a complex processor system to provide up to a 16 x 64 link. The SSA is a bit-serial link device which allows multiple hosts to indirectly communicate with each other. With the processor status (system up or system down) stored within the SSA, each processor within the complex may inquire on the condition of other processors.

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	Table 3	5. INPUT/UUTPU			
Magnetic Tape Units	Number of Tracks	Recording Density, Bits/Inch	Encoding	Tape Speed Inches/Sec.	Transfer Rate, Bytes/Sec.
6340-215/205	9	1600	PE	50	80,000
6340-219	9	800 1600	NRZI PE PE/ NRZI	50	40,000 to 80,000
6370-0401	9	1600 6250	PE GCR	75	120,000 to 468,750
6370-0601	9	1600 6250	PE GCR	125	200,000 to 781,250
6370-0801	9	1600 6250	PE GCR	200	320,000 to 1,250,000
Printers	Printing Speed	Print Positions	Horizontal Spacing, Chars./Inch	Vertical Spacing, Lines/Inch	Form Size, Inches
647-201	2,000 to 3,500 Ipm	132	10/15 cpi	6 or 8	Industry Std.
6420-0101	300 lpm	132	10/15 cpi	6 or 8	Industry Std.
6420-0201	600 lpm	132	10/15 cpi	6 or 8	Industry Std.
6420-0301	900 lpm	132	10/15 cpi	6 or 8	Industry Std.
6420-0601	1200 lpm	132	10/15 срі	6 or 8	Industry Std.
Card Equipment	Columns	Speed Cards/Min.	Input Hopper Capacity	Output Stacker Capacity	Options
6831-0201/0301	80	600	1000	1000	
MICR/OCR Reader/Sorters	Type Font	Speed, Documents/Min.	Number of Stackers	Document Size, Inches	Options
670-101	_	600	11	variable	off-line sorting
671-101	—	1200	18	_	off-line sorting,
6755-101	_	750	11	6''	endorser
6781-0101	E13B	800 to 1440	up to 34	6''	—

## Table 3. INPUT/OUTPUT UNITS

 CONFIGURATION RULES: The number of I/O links that can be included with each type of system and the data rates for each type of interface are included in Table 1 in this report. On-line operations can be connected via the Integrated Communications Subsystem, or a common trunk. NCR's > 85X5-II series can provide up to twice the performance of comparable uniprocessor configurations.

The top-of-the-line V-8695 is equal in rank with the Honeywell DPS 8/70 with a total main memory capacity of 64 megabytes. The V-8695 has four times the main storage capacity as the previous V-8670. The V-8655 has the same amount of main storage capacity (16 megabytes) as the IBM 4341 Model Group 12. The low-end V-8635, with up to 8 megatytes of maximum main storage, ranks equally with the Sperry 90/80-4.

### **ADVANTAGES AND RESTRICTIONS**

The V-8600 series hardware has many performance advantages over the V-8500 series processors. The V-8600 has a 32 percent faster cycle time than the V-8500. A high-speed cache memory which acts as a buffer between the processor and main memory exists on the V-8600 and not on the V-8500. Retrieval of data from cache memory is approximately six times faster than from main memory, allowing the processor to work more efficiently. The 32 entry Dynamic Address Translator (DAT) on the V-8600 is double the size of the V-8500 DAT, which enhances virtual storage operations by reducing the frequency of time-consuming page faults. Arithmetic functions that were performed by microcode in the V-8500 are now performed by the Arithmetic Assist Unit (AAU) hardware. This feature significantly speeds up programs written in Fortran.

The disk subsystem with the largest mass storage capacity available to the NCR V-8500 or V-8600 user is the 1.1billion-byte 6550 Disk Subsystem with a data transfer rate of 1.2 billion bytes per second. On a system that can access up to 64 megabytes of main memory, the user may find a need for larger disk capacities to accommodate large databases. IBM, whose 308X Series can also access up to 64 megabytes of main memory, offers users the 3380 Disk Subsystem with a storage capacity of 2.52 billion bytes and a data transfer rate of 3.0 megabytes per second.

PL/1, a higher-level programming language, is not supported on the NCR V-8500 or on the V-8600 Series. PL/1 is supported on comparable systems of other vendors such as Burroughs, IBM, Honeywell, and Sperry.

#### USER REACTION

Datapro received a total of 97 NCR user responses in our 1983 survey of computer users. The responses for V-8400 and V-8500 systems were combined and 87 of the respondents had one system installed while 10 other respondents each had two systems installed for a total of 107 systems. The user population consisted of a wide variety of business types, with retail/wholesale, manufacturing, and banking/ finance institutions represented most frequently. The primary applications were traditional accounting/billing and payroll/personnel operations, although many others were mentioned. The average installed time was a little more than 3 years for both the V-8400 and V-8500 systems. About 54 percent of the respondents had purchased their equipment from NCR, 22 percent rented the equipment **D**  latest disk subsystems and other associated peripherals are interfaced through a fully-buffered Bit Serial I/O Link Controller. This interface maximizes I/O throughput, lowers I/O and central processor contention, and eliminates the need for common trunk interfaces.

#### MASS STORAGE

For information on mass storage devices on the V-8500 and V-8600 systems, refer to Table 2.

#### **INPUT/OUTPUT UNITS**

Refer to Table 3 for information on tape units, printers, card equipment and MICR/OCR equipment on the NCR V-8500 and V-8600 systems.

636 CASSETTE TAPE SUBSYSTEM: Consists of a controller and one or optionally two cassette handlers. Each cassette cartridge contains approximately 280 feet of tape with two parallel recording tracks, only one of which can be accessed at a time. The capacity of each track is 2040 80character blocks or 984 256-character blocks. Recording density is 800 bits per inch in phase-encoded mode, tape speed is 7.5 inches per second, and data transfer rate is 750 characters per second.

7649 GENERAL PERIPHERAL CONTROLLER: Supports a 15-inches-per-second cassette drive, a diskette drive, and connects to the NCR common trunk interface.

#### **COMMUNICATION CONTROL**

INTEGRATED COMMUNICATIONS SUBSYSTEM: The Integrated Communications Subsystem provides up to 20 lines for on-line/real-time communications with remote devices using various transfer rates. The ICS links the computer system with remote terminals through either public or private communications networks. Integrated microprocessors (Communications Line Controllers), controlled by firmware, supervise the access, transmission, and output to and from the terminals in the system. A multiplexer or front-end processor can be added to the system to handle additional communications lines. The Integrated Communications Subsystem is available in all V-8500 systems except the V-8575-II, V-8585-II, and V-8595-II.

621-103 COMMUNICATIONS MULTIPLEXER: This system is capable of handling 15, 127, or 253 lines, using centralized character parity assembly and stripping, plus centralized block checking (CBC), cyclic redundancy checking (CRC), and function code control. A HardwareAssisted Software Queue (HASQ) feature is also available to help identify the terminals. The 621-103 connects to the Common Trunk Subsystem. The 621-103 simultaneously handles both synchronous and asynchronous devices using various transmission codes and speeds. Asynchronous devices can operate at 16 speeds ranging from 45 to 2400 bits/sec, and synchronous devices at speeds ranging from 600 to 50,000 bits/sec.

692-600 ASYNCHRONOUS LINE ADAPTER: An interface device that connects the 621-103 Communications Multiplexer to one or more terminals. It can handle halfand fullduplex transmissions at speeds ranging from 45 to 4800 bits per second. It meets both EIA RS-232-C and CCITT V.24 data communications interface standards.

693-600 SYNCHRONOUS LINE ADAPTER: An interface device that connects the 621-103 Communications Multiplexer to a data set for synchronous data transmissions. It can handle half- and full-duplex modes at speeds ranging from 600 to 50,000 bits per second. It meets both EIA RS-232-C and CCITT V.24 data communications interface standards. ▷ from NCR and third party leases averaged about 21 percent. Just about everyone used Cobol as the primary programming language. Most users developed their applications programs in-house, with the purchase of NCRdeveloped programs almost as popular. The user responses for both the V-8400 and V-8500 systems are presented in the chart below.

	Excel.	Good	Fair	Poor	<u>WA*</u>
Ease of operation	42	49	4	0	3.40
Reliability of mainframe	54	34	5	2	3.47
Reliability of peripherals	34	46	12	2	3.19
Maintenance service:					
Responsiveness	38	40	12	7	3.12
Effectiveness	33	40	18	5	3.05
Technical support:					
Trouble-shooting	9	42	28	16	2.46
Education	13	46	30	6	2.69
Documentation	7	35	37	14	2.38
Manufacturers software:					
Operating system	27	55	13	2	3.10
Compiler & assemblers	22	57	15	2	3.03
Application programs	6	43	27	9	2.54
Ease of programming	17	59	15	2	2.98
Ease of conversion	36	39	17	1	3.18
Overall satisfaction	26	55	12	4	3.06

\*Weighted Average on a scale of 4.0 for Excellent.

We also asked the NCR users two final questions: "Did the system measure up to your expectations?" and, "Would you recommend it to another user?" Better than 80 percent of the V-8400 and V-8500 users felt their systems performed as they had expected. Less than 10 percent were unsatisfied with their computers. Almost 80 percent of the V-8400 and V-8500 users said they would recommend their systems to others. About 10 percent in both user groups answered "no" to this question. While statistics tell an important story, personal observations from NCR users can be even more beneficial. Accordingly, Datapro telephoned several NCR V-8400 and V-8500 users for their comments.

We first called a banking institution in the midwest with a V-8455 installed. This user had converted from an IBM system and encountered "no more problems than expected" during the conversion and seemed pleased that programming changes were able to correct the problems that did occur. The ease of the conversion allowed things to "stay right on schedule". The bank's spokesman said that support is "better than average", but not "fantastic". The NCS operating system is used and does what it was designed to do; however, if more applications were run on the system, a virtual operating system would be sought.

A call was then placed to a jewelry distributor in the southwest who converted to a V-8555 from an N-8450. The conversion was said to have gone "very smoothly". Several applications are being used with 28 terminals on-line. At initial installation, a bad 658 disk was installed which was "fixed promptly" by NCR. Overall, the user rated support  $\triangleright$ 

➤ 7900 VISUAL DISPLAY TERMINAL: The 7900 Model 1 Terminal is a microprocessor-based CRT that operates asynchronously and can communicate with all V-8500 and V-8600 systems. The CRT has a 12-inch diagonal display with 25 lines of 80 characters each. It uses a 7 x 7 dot matrix display and features five cursor controls, blinking, reverse video, and underlining. The keyboard can generate a full 128-characater ASCII set and includes a numeric pad. The 7900 can operate in either half- or full-duplex mode, and transmits at speeds ranging from 50 to 19,200 bits per second. The terminal has an EIA/20mA current loop interface and a serial interface for printers.

#### SOFTWARE

OPERATING SYSTEM: NCR offers three operating systems for the V-8500 Series computers: NCS, VRX, and IRX. The V-8600 systems use VRX only. Each of these control programs emphasizes one of the application areas (NCR Century Emulation, virtual memory systems, or interactive processing systems) for which the processors can be optimized.

NCR CENTURY SOFTWARE: The NCS operating mode is a superset of the Century B1, B2, and B3 operating systems, and is compatible with Century software at the object-code level. Each B-series operating system consists of a Monitor, an Executive, and several other routines. The Monitor controls the sequencing, loading, and linking of programs. The Executive is a run-time supervisor that handles all I/O operations, error conditions, and program overlays.

Basic Executive (B1): All Century computers can use the basic B1 operating system. This system handles batch-mode processing of one program at a time. The B1 system consists of a Monitor, an I/O executive, and Disk Management, Log, and Display routines.

On-Line Operating Executive (B2): Usable on all Century systems with at least 32K bytes of main storage, this operating system divides main memory into two distinct areas which can be used for processing either two concurrent batch programs or one communications program plus one background batch program.

VIRTUAL RESOURCE EXECUTIVE: VRX is a group of software modules that utilize the VS1, VS2, or VS3 firmware to make up a flexible operating system with multiprocessing, virtual-machine, and virtual-storage capabilities, while remaining compatible with existing NCR Century programs. VRX supports multiple-processor systems and treats processing elements in the system as assignable resources. The current level of VRX is Release 8.

VRX uses virtual storage, allows supervisor routines to map main memory to disk, and allows executing programs to be relocated between main storage and secondary storage without directly involving the executing program itself. Using paging supervisor routines, VRX reads scheduled jobs from the page file on disk and writes changed pages back to disk as necessary. It attempts to optimize memory usage globally by allocating only enough real memory to a job to ensure efficient execution, releasing unused memory as soon as it becomes available.

In a virtual storage environment, a 16-million-byte virtual address space is available to each active job. Eight million bytes are used in common by the executive and certain software for all programs, and are referred to as the global software area. The remaining eight million bytes (local area) are used by the individual job for programs and data.  $\triangleright$  as being "satisfactory" and seems to be pleased with the system.

We then called a manufacturing company in the northeast who had upgraded from a V-8560 to a V-8565. The upgrade involved no "major problems" and NCR's support is said to be "very responsive". With 25 to 30 terminals on-line, response time is usually 30 seconds to one minute. According to the company's spokeswoman, the VRX operating system being used is "much faster and more efficient" than the B-3 operating system used on the V-8560.

Overall, users of the NCR V-8000 Series seem to be quite pleased with their installed systems. Our random telephone survey elicited almost no negative comments about the NCR equipment.  $\Box$ 

VRX monitors memory demands and performance for the entire job mix in order to detect excessive paging in or out (thrashing) and system underutilization. If it detects thrashing, the paging supervisor can reduce the number of active jobs; if it detects underutilization, it can activate new jobs and increase the system workload. Memory utilization statistics are recorded for every run and can be used to tune the system.

VRX multiprocessing (VRX/MP) enables the system to schedule and run multiple jobs at the same time by automatically allocating the peripherals, memory, and processor as needed. Each job may contain one or more related programs. Jobs are described to the system using a Job Control Language made up of Job Specification Language (JSL) statements and Monitor Control Language (MCL) statements. The Job Specification Language (MCL) statements. The Job Specification Language statements are used to define the hardware and media requirements of the job, while the Monitor Control Language statements identify the programs within each job and specify any run-time conditions for those programs. VRX permits users to assume as little or as much control over job processing as needed. Most scheduling, allocation, and processing decisions can be made by the software itself.

The VRX software, together with the virtual-storage firmware, enables the system to perform like two different machines using two different firmware instruction sets. The basic instruction set, called the Base VRX Instruction Set, makes the VRX system compatible with NCR Century systems and provides an interface for the virtual-storage software, while the optional VRX Cobol Instruction Set is designed to process VRX Cobol object code. A firmware routine automatically switches between the two firmware instruction sets as needed.

Virtual-storage firmware and software enable user programs, compilers, application software, and utility routines to run on the system without regard to the number of processors or the total amount of real memory. Only the active code of each program is in real memory during program processing.

The processor and memory space are assigned dynamically, and the operating software is also brought into memory only when needed and assigned space where available. There are no fixed processor assignments, no fixed partitions, and no fixed areas in real memory for software or program code. All inactive software and program code is stored in the Page File. Page sizes may be 1024 bytes, 2048 bytes, 4096 bytes, or 8192 bytes.

When a job is first introduced into the VRX system, the executive stores job specifications and any data cards for the

job in a card spool file on disk and then validates the specifications. Once in the system, the job progresses through three distinct phases: scheduling, execution, and output. During the scheduling phase, a job can be in any of several states. Between acceptance and specification validation, it is in an unprocessed state. Following validation, if specifications indicate that execution should be delayed until some event such as operator action or completion of another job has occurred, the system will place the job temporarily in a hold state. Otherwise, the job enters the scheduling state, where it is placed in a scheduled job queue to await execution. The order in which jobs are placed in the queue is determined by the priority given in the specifications. As memory and peripherals become available, VRX software accesses the scheduled job queue and attempts to execute the highest-priority job. If sufficient memory and peripherals are not available to execute the highest-priority job, the software scans the remaining jobs on the queue to see if any of these can be executed with the available resources.

When a job passes to the execution phase, it competes with other jobs in the execution mix for processor and shared resource time. Up to 35 jobs may be in the execution mix at one time, with resource allocation being determined by execution priorities assigned in job specifications. During execution, control and user data are supplied on demand from the card input spool file. Print file output is also normally spooled on disk or, optionally, magnetic tape. When a job completes the execution phase, the executive releases all the peripherals and memory space that were used.

The job then enters the output phase, where it remains until its spooled print files have been printed. Job printing order is likewise determined by priorities assigned in the job specifications. At the end of the output phase, job accounting information is entered into the log and the job is removed from the system.

VRX provides two separate logs: a hardware log and a system log. The hardware log contains information valuable to the field engineer for system maintenance, while the system log contains operation and statistics messages that can be used for job accounting and performance evaluation.

VRX provides several levels of error recovery systems, each designed for specific applications. These facilities include a CAM (Criterion Access Method) file error recovery system, which uses CAM utilities to restore CAM files if an error occurs. There is also a batch recovery system, called Rescue/ Restart, that enables a program to be continued from a previously defined rescue point instead of at the start.

VRX also provides for system recovery if an error condition results in the need to initialize the software again. A special Recovery Initialization system (REINIT) causes the software to save important system information such as spooled files before initialization so that currently active jobs can be started again.

The Inter Host Communication (IHC) facility is responsible for the communication between base processors in the processor complex models. The two components which make up the IHC are: 1) a task which receives messages from other hosts via the SSA and routes those messages to the appropriate task within this host, and 2) a set of "user-callable" routines to perform to other hosts. The capability for VRX and applications to communicate host to host is provided by the IHC. It is necessary to detect failure and to activate automatic recovery.  The VRX System Performance Measurement utility monitors the entire system's performance levels and produces printed reports to that effect in graphic or tabular format.

The Virtual Resource Executive incorporates facilities for handling NCR Century Series files, including sequential files, standard disk files, chained disk files, indexed sequential files, and NCR random filing system files. It uses a file management technique called the Criterion Access Method (CAM) that has been specifically designed for high performance under VRX with applications programmed in Cobol 74 and NEAT/VS. The CAM file structure minimizes reorganization and allows rapid insertion of records, eliminating many of the inefficiencies inherent in traditional random and indexed sequential accessing methods.

A high-level user programming language for file and record manipulation, file maintenance, data protection, and processing recovery is also provided. The access method has been designed to meet Cobol 74 requirements for sequential, indexed, and relative file processing. Since record and key lengths are variable and records with identical keys are permitted, CAM allows records to be designed in the most natural manner, and at the same time reduces external storage requirements.

The VRX Remote Job Entry subsystem (RJE) enables jobs to be input to the central computer system from remote locations by telephone communications lines; printer output is returned to the remote locations over the same lines. A remote terminal can also send messages to the central system or to any other terminal in the system.

VRX provides an On-line Program Development utility to generate source programs from as many as eight remote locations. The program may be compiled and run on the remote terminal. This utility also permits a user to create and run a catalogued job or disk control string from a remote location, to access spooled print files, and to communicate with executing VRX jobs.

The VRX Base System Package provides standard batch operating systems functions. Specfically, it can compile programs written in VRX Cobol-74, sort and merge various types of files, utilize NCR disk subsystems such as the 658, 6530, 6540, and 6550 with the capability of accessing greater than 64K disk sectors, perform general housekeeping functions, provide system performance data, analyze Cobol programs to maximize program efficiency, and provide improved disk storage management in a real-time environment.

The VRX Telecomm/Development Package permits the migration of a batch system to an on-line environment through user definition of the network configuration, user development of the on-line application, and direct user access to VRX print spool files and the VRX executive software.

To further enhance the VRX Telecomm/Development software, the VRX TP-Monitor Package provides transparency of the system to I/O devices, making it easier to change I/O units; improved job scheduling routines, and improved security measures.

Another tool to facilitate the change from batch to on-line is the EZ-KEY Data Entry System. Data enters the system through user-defined formats and is stored in the EZ-KEY data entry file. The system controls and validates the input data via batch totals, range checks, check digits, and arithmetic operations. Various utilities are provided to deal efficiently with batch data, and system performance statistics are available at the user's discretion. VRX TRAN-PRO is a general purpose transaction processing monitor that supports on-line applications in a real-time environment. TRAN-PRO uses the VRX virtual memory to increase overall throughput and interfaces VRX tasking and dynamic storage allocation functions to decrease application program requirements. It operates in conjunction with the VRX Message Control System (MCS) to route the remote inputs into the TRAN-PRO system. VRX TRAN-PRO can be implemented in V-8500 and V-8600 systems.

Another element in the NCR on-line transaction processing environment is VRX TRAN-QUEST, a comprehensive query language. TRAN-QUEST has three modules: the Data Dictionary, which stores the contents and structure of the data bases; a Data Manager, which manipulates the data base for desired information; and the Query Processor, which provides both formatted and free-form inquiries. VRX TRAN-QUEST operates on one to two modes: on-line associated with VRX TRAN-PRO and VRX TOTAL, or in a batch environment with VRX TOTAL.

TOTAL: This popular data base management system, developed by Cincom Systems, Inc., is marketed and supported by NCR at an initial license fee of \$31,350 per singleprocessor installation plus a monthly license fee of \$1,122. TOTAL is described in detail in Report 70E-132-01.

NCR also offers the TOTAL IQL interactive query language, a non-procedural data retrieval language designed for use by non-programmers. The retrieval language permits users to direct inquiries to nearly any data file at any time. A data dictionary language allows the data base manager to limit specific user access to data at the field level by assigning passwords or access codes. TOTAL IQL is offered with TOTAL for an initial license fee of \$15,675 plus a monthly license fee of \$561.

COMPILERS AND ASSEMBLERS: VRX processes programs from several different compilers, such as NEAT/3, NEAT/VS, Century Cobol-68, Century Cobol-74, VRX Cobol, Fortran 66, VRX Fortran 77, and NCR RPG. Certain compilers are designed to generate object code for Bseries operating systems: NEAT/3, Century Cobol-68, Century Cobol-74, Century Fortran, and NCR RPG. The remaining compilers (NEAT/VS, VRX Cobol, and VRX Fortran) are designed for the VRX virtual-storage operating system. Bseries compilers generate object code which can be loaded and run by the VRX operating system. VRX compilers generate object modules which are then processed by the VRX Linkage Editor before loading and processing.

COBOL 74: Meets both ANSI and federal requirements for Cobol 74, providing the highest level of support for most modules and medium-level support for all other modules.

FORTRAN: Several levels of implementation of Fortran are supported, up to the full ANS level plus the following extensions: mixed-mode arithmetic, an unlimited number of dimensions in an array, random READ and WRITE statements, and extensions to the CALL statement. Basic Fortran (Fortran II), Intermediate Fortran (1130 compatible), Fortran IV, and Fortran E (Educational) are available on all systems. VRX Fortran 77 is available only on V-8500 and V-8600 systems.

ASSEMBLER: NEAT/3 is NCR's assembler language. Strong emphasis is placed upon the use of macroinstructions to facilitate coding. NEAT/3 Level 1 is a subset of NEAT/3 that provides an easy-to-learn programming language and fast compilation. NEAT/VS is an enhanced version of NEAT/3 that includes all the features of the original language plus extensions to exploit the virtual memory features of the VRX-based systems. Generally, the enhancements

permit mixing of older Century programs with those written for execution under the newer operating systems. NEAT/VS can also process Century chained files, indexed-sequential files, or random files as well as the Criterion Access Method (CAM) files.

BASIC: A compiler for Basic, an algebraic language designed for time-sharing computers, can be used only on the NCR V-8500 computers. Programs are compiled as they are entered from remote teletypewriters and can be executed immediately. Diagnostic messages permit on-thespot correction of many errors. An accounting routine facilitates billing by recording the amount of computing time used by each programmer at each terminal.

#### **TELECOMMUNICATIONS**

VRX provides telecommunications software that has been designed to simplify the application programmer's task by alleviating the concern for network configurations and communications protocols. The Message Control System is a high-level interface that allows on-line programs to transmit messages using logical source/destination names with no reference to terminal characteristics. It consists of five verbs—SEND, RECEIVE, ENABLE, DISABLE, and AC-CEPT (message count)—that reference an MCS queue list. NDL statements specify terminals and communication links. These statements are used by the Network Definition Language Processor to create the tables necessary for online operation. The tables are subsequently combined with programs at load time by the Link Editor.

NCR's communications structure is called the NCR Communications Network Architecture (CNA). It is designed around the concept of logical addresses for each communicating location, without regard for local line configurations and system protocols. The network is essentially transparent to the user. Message flow is regulated by all nodes in the network, rather than a single host, resulting in better use of processing power throughout the network.

NCR/CNA uses NCR/DLC, SDLC, BSC, X.25, or TTY communications protocols. NCR/DLC is similar to ANSI/ ADCCP and ISO/HDLC protocols, and will be compatible with IBM's SDLC. The NCR Telecommunications Access Method (NCR/TAM) software provides the gateway from the field terminal to the application program, and typically uses the Cobol Message Control System (MCS).

APPLICATION PROGRAMS: NCR offers "packaged" programs to handle key applications in manufacturing, food processing, wholesale distribution, retailing, schools, financial institutions, hospitals, and local government. Among the application programs available to users are:

**Commercial Accounts Receivable** 

Retail Accounts Receivable

**Accounts Payable** 

Payroll and Personnel Management

Medical Audit Statistics System (Mass)

**Hospital Accounts Receivable** 

Hospital Clinical Analysis

**Inpatient** Accounting

**Post-Discharge** Accounts Receivable

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Stewardship and Management Accounting

Student Scheduling and Grade Reporting

**Student Test Analysis** 

**Utility Billing** 

**Department Store Sales Audit** 

**General Reporting System** 

Project Network Analysis (PNA)

**Basic Estimating Technique (BETS)** 

**Fashion Reporting** 

Stable Stock Replenishment

CIF Bank System

Linear Programming

Law Enforcement Control System

**Building Contractors System** 

Statistical Analysis

**Personal Trust Accounting** 

**Dedicated Commercial Bank Inquiry System** 

Local Government Administration System

**Bill of Materials Processor** 

Manufacturing Inventory Control System

Medics

Computerized Loan and Savings System (CLASS)

**Credit Management System** 

**Delinquent Accounts Receivable** 

**Purchase Order Management** 

Laboratory Reporting System

Pharmacy System (Hospital)

**Interactive Financial Management System** 

**Interactive Hospital Information System** 

**On-Line Order Processing/Inventory Control** 

**Bill of Material** 

**Materials Management** 

**Manufacturing Inventory Management** 

Material Requirements Planning

Routings

Standard Costings

**Manufacturing Order Processing** 

Work-in-Process

**Master Production Scheduling** 

**Capacity Requirements Planning** 

Purchasing and Receiving

#### PRICING

ENTRY LEVEL V-8545-II SYSTEM: Includes V-8545-II CPU with a CRT console, 1024K bytes of main memory, a card reader interface, and an I/O Link Control; one 162-megabyte 6540-2401 disk subsystem, one 0625-0301 disk controller, one 600-lpm 6420-3202 band printer, one 9-track 6370-0401 magnetic tape unit, and one 6379-0401 I/O link adapter. The purchase price is \$197,430, the annual maintenance cost is \$15,178, and the monthly one-year cost is \$4,249.

MID-RANGE V-8565-II SYSTEM: Includes V-8565-II uniprocessor with 2048K bytes of main memory, a CRT console, a card reader interface, and Virtual Assist Unit; one 1480-megabyte disk storage subsystem with two 200-megabyte 658 disk drives and eight 135-megabyte 6540 disk drives, one 0625-0301 disk controller, one magnetic tape subsystem with three 35 9-track 6370 tape units, one 6370-0601 I/O link adapter, one 900-lpm 6420-2301 line printer, and one 6831 600-card/minute card reader. The purchase price is \$435,030, the annual maintenance cost is \$31,590, and the monthly one-year rental cost is \$20,349.

HIGH PERFORMANCE V-8645 SYSTEM: Includes V-8645 dual-processor with 4096K bytes of main memory, 128K bytes of cache memory, two Channel Control Processors, 16 I/O channels, two CRT consoles, and one System Control Unit, with one Control Processor; an additional 4096K bytes of memory, two 1092-megabyte 6550-0101 disk drives, six 6550-0201 disk drives, one 6559-0101 I/O link adapter, four 200-megabyte 658-0401 disk drives, one 06250301 disk controller, one 6032-0707 auxiliary cabinet, eight 9-track 6370-0801 magnetic tape units, two 6379-0801 I/O link adapters, three 2000-lpm 647-0201 train printers, three 647-P001 I/O link adapters, and three 0960-0164 print trains. The purchase price is \$2,094,039, the annual maintenance cost is \$114,690, and the monthly one-year rental cost is \$70,689.

SOFTWARE: NCR continues its policy of unbundling software costs. In most cases there is a monthly licensing charge and for certain packages also an initial licensing fee. The monthly charge ranges up to slightly over \$1,000 and initial fees range up to about \$36,000.

The pricing policy for NCR applications software includes an initial license fee plus a monthly fee. The initial fee ranges from \$653 to over \$50,000. Payment of the initial fee provides for one year of use without additional monthly fees. Thereafter, the monthly license fees range from \$10 to \$700 a month.

SUPPORT: NCR systems support is billed to V-8000 Series users at the rate of \$104 per hour for a systems engineer to test and resolve problems on site. NCR has a Central Support plan in which a toll-free telephone number is supplied for telephone assistance. The Central Support Extended plan provides an on-site systems engineer if the problem cannot be resolved over the telephone.

EDUCATION: All educational services are separately priced.

CONTRACT TERMS: The standard NCR rental contract permits unlimited use of the equipment for all processor models. There are no extra-use charges. The basic maintenance charge covers maintenance of the equipment for nine consecutive hours between 7 a.m. and 6 p.m. on Monday through Friday. Charges for maintenance coverage beyond this period are calculated by adding a percentage premium to the basic rates. The percentage increases for various coverage periods are as follows:

	9 <u>hours</u>	12 hours	16 hours	20 hours	24 hours
Monday-Friday	Base	8%	10%	18%	20%
Saturday	5%	N/A	7%	10%	10%
Sunday & Holiday	7%	N/A	9%	12%	12%

## **EQUIPMENT PRICES**

		Purchase Price	Annual Maint.	One-Year Rental*			
8545 PROCESSOR AND MAIN MEMORY							
V-8545-II	Virtual Memory Processor System; includes a CRT console, 1024K bytes of memory, a card reader interface, and I/O Link Control	56,940	2,940	4,230			
Additional Memory for V-8545-II:							
AK 5600-P723	1,024K-byte increment (up to 1)	14,100	1,119	838			

\*Includes maintenance

## **EQUIPMENT PRICES**

		Purchase Price	Annual Maint.	One-Year <u>Rental*</u>
8555 PROCESSOF	R AND MAIN MEMORY			
V-8555-II	Virtual Memory Processor System; includes a CRT console, 1024K bytes of memory, a card reader interface, and I/O Link Control	61,290	4,380	4,675
AK 5600-P777	Multiprocessor Conversion kit	37,500	2,036	1,352
Additional Memory	for V-8555-II:			
AK 5600-P72X	1,024K-byte increments (up to 3)	14,100	1,119	838
8565 PROCESSOF	R AND MAIN MEMORY			
V-8565-II	Virtual Memory Processor System; includes a CRT console, 2048K bytes of memory, a card reader interface, and Virtual Assist Unit	73,500	7,128	6,913
AK 5600-P783	Multiprocessor Conversion kit	37,500	2,036	1,352
Additional Memory	for V-8565-II:			
AK 5600-P72X AK 5600-P726	1,024K-byte increments (up to 2) 2,048K-byte increment (one; 4096K-byte system required)	14,100 28,200	1,119 2,071	838 1,663
V-8565-IIE	Virtual Memory Processor System; includes a CRT console and 2048K bytes of memory	88,870	7,608	8,255
Additional Memory	for V-8565-IIE			
AK 5600-P726	2,048K-byte increment (up to 3)	28,200	2,071	1,663
8575 PROCESSOF	R AND MAIN MEMORY			
V-8575-II	Virtual Memory Multiprocessor System; includes a CRT console, 4096K bytes of memory, and a card reader interface	131,250	10,664	10,475
AK 5640-P777	Multiprocessor Conversion kit	37,500	2,036	1,352
Additional Memory	for V-8575-II:			
AK 5640-P72X AK 5640-P726	1,024K-byte increments (up to 2) 2,048K-byte increment (one; 4,096K-byte system required)	14,100 28,200	1,119 2,071	838 1,663
8585 PROCESSOF	R AND MAIN MEMORY			
V-8585-II	Virtual Memory Multiprocessor System; includes a CRT console, 4096K bytes of memory, a card reader interface, and Virtual Assist Unit	208,150	14,084	15,787
AK 5640-P777	Multiprocessor Conversion kit	37,500	2,036	1,352
Additional Memory	for V-8585-II:			
AK 5640-P72X AK 5640-P726	1,024K-byte increments (up to 2) 2,048K-byte increment (one; 4096K-byte system required)	14,100 28,200	1,119 2,071	838 1,663
8595 PROCESSOR	AND MAIN MEMORY			
V-8595-II	Virtual Memory Multiprocessor System; includes a CRT console, 4096K bytes of main memory, card reader interface, and Virtual Assist Unit	240,400	16,486	18,255
AK 5640-P777	Multiprocessor Conversion kit	37,500	2,036	1,352
Additional Memory	for V-8595-II:			
AK 5640-P72X	2,048K-byte increments (up to 2)	28,200	2,071	1,663
*Includes maintenance				

70C-656-02p Computers

\*Includes maintenance

# NCR V-8500 and V-8600 Systems

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## **EQUIPMENT PRICES**

		Purchase Price	Annual Maint.	One-Year Rental*
I/O CONTROL AND PI	ROCESSOR OPTIONS FOR 8500 SERIES PROCESSORS			
I/O Control				
AK 56XO-PX40 AK 56XO-PX41 AK 56XO-PX42 AK 56XO-PX43	Low-Speed Trunk Medium-Speed Trunk Very High-Speed Trunk I/O Link Control	3,562 5,380 8,093 3,710	159 230 330 244	133 200 299 142
Communications Lines	: (not on V-8575-II, V-8585-II, or V-8595-II)			
AK 5600-P950 through P953	First through fourth 5-line Communications Line Controller (CLC); requires ICS light display	1,795	730	140
AK 5600-P954 through P957	First through fourth 5-line CLC/MLA (multi-line adapter); requires ICS light display	4,330	1,150	273
AK 5600-P959 AK 5600-P958, P960, and P961	ICS Light Display MLA Upgrade for AK 5600-P950 through P953	2,535	420	134
Processor Performance	e Upgrade (Requires same memory configuration on both systems)			
V-8545-II to V-8555-II V-8555-II to V-8565-II V-8565-II to V-8575-II V-8575-II to V-8585-II V-8585-II to V-8595-II		10,320 12,960 22,580 82,500 49,760	1,555 1,523 480 3,694 4,784	990 1,601 1,945 6,072 3,917
5			Lice	nthly ense ee
Firmware Options				
VRX Mode for V-8545- N Mode for V-8545-II	U Contraction of the second			302 485
VRX Mode for V-8555- N Mode for V-8555-I MP Option for V-8555-I VRX Mode for V-8565- N Mode for V-8565-I MP Option for V-8565-I				403 660 683 548 858 933
VRX Mode for V-8575- MP Option for V-8575-I				712 210
VRX Mode for V-8585- MP Option for V-8585-I				096 866
VRX Mode for V-8595- MP Option for V-8595-I				315 236

# NCR V-8500 and V-8600 Systems

## **EQUIPMENT PRICES**

		Purchase Price	Annual Maint.	One-Year Rental*
8635 PROCESSOR	R AND MAIN MEMORY			
V-8635	Virtual Memory Uniprocessor System; includes 4 megabytes of main memory, 32K bytes of cache memory, 2 Channel Control Processors with 16 I/O chan- nels, dual CRT consoles, and one System Control Unit with one Control Pro- cessor.	495,000	27,500	17,255
AK 5710-P370	V-8635-V-8645 Upgrade	300,000	2,500	9,070
Additional Memory	for V-8635:			
AK 5710-P520	4096K to 8192K bytes	88,000	4,032	3,216
8645 PROCESSO	R AND MAIN MEMORY			
V-8645	Virtual Memory Dual Processor System; includes 4 megabytes of main memory, 128K bytes of cache memory, 2 Channel Control Processors with 16 I/O channels, dual CRT consoles, and a System Control Unit with one Control Pro- cessor	795,000	30,000	26,325
Additional Memory	for V-8645:			
AK 5710-P520 AK 5710-P522 AK 5710-P523	4096K to 8192K bytes 8192K to 12,288K bytes 12,288K to 16,384K bytes	88,000 88,000 88,000	4,032 4,032 4,032	3,216 3,216 3,216
8655 PROCESSO	R AND MAIN MEMORY			
V-8655	System includes two V-8635 Virtual Memory Uniprocessor Systems (see V- 8635 above for description)	995,000		33,680
Additional Memory	for V-8655:			
AK 5710-P522 AK 5710-P523	8192K to 12,288K bytes 12,288K to 16,384K bytes	88,000 88,000	4,032 4,032	3,216 3,216
8665 PROCESSO	R AND MAIN MEMORY			
V-8665	System includes one V-8635 Uniprocessor and one V-8645 Dual-Processor (see V-8635 and V-8645 above for descriptions)	1,325,000	_	44,450
Additional Memory	for V-8665:			
AK 5710-P522 AK 5710-P523	8192K to 12,288K bytes 12,288K to 16,384K bytes 4-megabyte increment (up to 2)	88,000 88,000 80,000	4,032 4,032 —	3,216 3,216 
8675 PROCESSO	R AND MAIN MEMORY			
V-8675	System includes two V-8645 Dual-Processor Systems (see V-8645 above for description)	1,595,000		52,205
Additional Memory	for V-8675:			
AK 5710-P522 AK 5710-P523	8192K to 12,288K bytes 12,288K to 16,384K bytes 4-megabyte increment (up to 4)	88,000 88,000 80,000	4,032 4,032 —	3,216 3,216 —
8685 PROCESSO	R AND MAIN MEMORY			
V-8685	System includes three V-8645 Dual-Processor Systems (see V-8645 above for description)	2,395,000	—	78,385
Additional Memory	for V-8685:			
AK 5710-P523	12,288K to 16,384K bytes 4-megabyte increments (up to 8)	88,000 80,000	4,032	3,216 
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\*Includes maintenance

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# NCR V-8500 and V-8600 Systems

# **EQUIPMENT PRICES**

		Purchase Price	Annual Maint.	One-Year Rental*
8695 PROCESSOR	AND MAIN MEMORY			
V-8695	System includes four V-8645 Dual-Processor Systems (see V-8645 above for description)	2,995,000	_	98,680
Additional Memory f	or V-8695:			
	4-megabyte increment (up to 12)	80,000	—	
I/O CONTROL AND	PROCESSOR OPTIONS FOR THE V-8600			
AK 5710-P540 AK 5710-P541 AK 5710-P542 AK 5710-P543 AK 5710-P035 AU 2600-0101 AU 6831-0203	Additional CCP with 8 I/O channels Trunk CCP with one Low Speed Trunk Low Speed Trunk Very High Speed Trunk Second SCU Control Processor Provision for Thermal Hardcopy Device on Console System Card Reader; 600 cpm	9,000 18,060 3,360 7,635 9,000 3,700 9,300	635 1,092 132 276 180 240 635	321 583 123 277 320 100 406
MASS STORAGE				
AU 0658-0401 AU 0658-0002 BU 6559-0101 AA 0958-0002 BU 0625-0301	Disk Drive; 200MB; requires 0625-0301 controller Disk Drive Conversion; 100MB to 200MB capacity I/O Link Adapter Disk Pack for 0658-0201 or 0658-0401 drives Disk Controller; controls up to 16 drives	24,625 2,000 24,235 525 21,800	1,348 492  3,564	959 323 760  1,145
AU 6530-2101 AK 6530-P401 AU 6530-1301 AU 6540-2401 AK 6530-P401 AA 6531-0101	Cartridge Disk Drive; 27MB Upgrade Kit; expands AU 6530-1201 to 81MB Cartridge Disk Drive; 81MB Cartridge Disk Subsystem; 162MB Upgrade Kit; upgrades AU 6530-2401 -0090 to 162MB Disk Cartridge, 13.5MB	16,800 3,270 13,500 27,200 3,270 199	1,146 276 1,056 2,400 276 —	680 125 574 1,150 125 —
BU 6549-K001 AU 6540-0201 AU 6540-2802	I/O Link Adapter Expansion kit Fixed Disk Drive; 135MB Fixed Disk Drive; 540MB, includes four 135MB units	3,392 10,800 41,100	60 959 1,104	101 589 1,315
BU 6559-0101 AU 6550-0101 AU 6550-0201 AU 6032-0707	I/O Link Adapter for 6550 Pack Disk Drive; 1092MB; 1st unit Pack Disk Drive; 1092MB; additional units Auxiliary Cable	24,235 60,205 48,369 1,500	492 2,203 2,138 	760 1,697 1,641 45
AU 6590-0101 AU 6591-0101 AU 6591-0201 AK 6590-P003	Disk Drive; 2 spindles 35MB Data Module 70MB Data Module Rotational Position Sensing Kit	18,290 900 2,500 700	1,211  57	731 64 110 22
MAGNETIC TAPE				
AU 6370-0401 BU 6379-0401 AU 6370-0601 BU 6379-0601 AU 6370-0801 BU 6379-0801	Magnetic Tape Unit; 75 ips, 9-track, PE/GCR, 120/470KB I/O Link Adapter for up to four 6370-0401 magnetic tape units Magnetic Tape Unit; 125 ips, 9-track, PE/GCR, 200/780KB I/O Link Adapter for up to four 6370-0601 magnetic tape units Magnetic Tape Unit; 200 ips, 9-track, PE/GCR, 320/1250KB I/O Link Adapter for up to four 6370-0801 magnetic tape units	21,935 33,900 25,160 33,900 28,165 33,900	1,361 1,568 1,698 1,568 2,760 2,568	808 1,294 928 1,294 1,096 1,294
BU 6340-0215 AU 6340-0205 AU 6340-0219	Magnetic Tape Unit with Controller; 9-track, 50 ips, 80KB Magnetic Tape Unit; 50 ips; PE Magnetic Tape Unit with Controller; 9-track, 50 ips, 80KB	16,430 11,310 17,000	770 658 2,029	565 405 1,043
BU 3250-1737 AU 7642-1301 AU 7620-1203	General Purpose Controller for 7649 Cassette Tape Diskette Drive Cassette Drive	6,280 2,050 1,395	699 255 77	279 94 55

\*Includes maintenance

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# NCR V-8500 and V-8600 Systems

## **EQUIPMENT PRICES**

		Purchase Price	Annual Maint.	One-Year <u>Rental*</u>
PRINTERS				
BU 0647-0201 AK 0647-P001	Train Printer; 2000 lpm, 132 positions, power stacker; includes controller I/O Link Adapter for BU 0647-0201	64,722 2,258	8,378 214	2,866 102
AK 0960-0152 AK 0960-0164 AK 0960-0157 AK 0960-0196	Print Train; 52 characters Print Train; 64 characters Print Train; 57 characters, OCR-A Print Train; 96 characters, UC/LC	3,950 3,950 3,950 3,950 3,950	 	114 114 114 114
AU 6420-2401 AU 6420-2101 AU 6420-3202 AU 6420-2301	Band Printer; 230 lpm, requires at least one print band Band Printer; 300 lpm, requires at least one print band Band Printer; 600 lpm, requires at least one print band Band Printer; 900 lpm, requires at least one print band, includes quietized cabi- net	8,020 9,220 13,720 21,400	886 1,083 1,984 3,572	315 381 640 1,245
AU 6420-0601	Band Printer; 1200 lpm, requires at least one print band, includes quietized cab- inet	33,500	4,666	1,566
SK 6420-P003	Criterion Interface; required for 6420-2101, 3202, and 2301 printers	2,650	756	269
AK 6420-K010 AK 6420-K019 AK 6420-K022 AK 6420-K024	Print Band; 64-character ASCII, 1403, 10 cpi Print Band; 96-character U/L case Print Band; 48-character SDCII, 1403, 10 cpi Quietized Cabinet	350 350 350 583		27 27 27 38
AU 6441-0202 RK 6441-K042 AK 6441-K020 AA 1001-A004-000	Serial Matrix Printer; 70 lpm; requires interface RS-232 Interface Upgrade Character Set for 6441 printer; 64-character Pedestal for 6441 printer	4,245 830 105 250	613 242 	190 69 5 —
AV 6442-010 AV 6442-0102	Serial Matrix Printer; 125 lpm with RS-232 interface Dual Mode Printer with RS-232 interface; switch-selectable from 125 lpm to 60	3,655 3,880	481 506	
AA 1001-A005	cps Pedestal for 6442-0101, -0102	275	—	—
PUNCHED CARD I/	O UNITS			
AU 6831-0201	Card Reader; 600 cpm	7,800	561	360
MICR I/O UNITS				
AU 0670-0101 AU 0671-0101 AF 0000-6711	MICR Sorter; 600 dpm, 11 pockets; includes 622-401 controller MICR Sorter; 1200 dpm, 18 pockets; includes 622-401 controller Endorser Feature for 671-101 MICR sorter	45,000 117,500 12,000	7,190 16,922 1,454	1,679 3,794 448
AU 6755-0101 BU 6781-0101	MICR Reader/Sorter; 750 dpm, 11 pockets MICR Document Reader/Sorter; up to 1,400 dpm, up to 34 pockets	58,850 128,700	7,800 12,484	2,250 3,782
COMMUNICATION	S			
BU 0621-0103 AK 0691-0201 AK 0621-0202 AK 0621-F200 AK 0621-F201 AK 0621-F202 AK 0691-0101 AU 0690-0103	On-Line Communications Multiplexer for up to 15 lines First Extension for 621-103 multiplexer; extends to 127 lines Second Extension for 621-103 multiplexer; extends capacity to 255 lines In-House Clock Driver for 0621-0103 multiplexer Synchronous Adapter Connection Cable Kit Wideband Interface Auxilary Cage On-Line Auxiliary Bay	19,500  2,120 475 725 7,950 8,480	1,063 — 248 30 30 130 64	382 — 86 15 22 135 177
AU 0692-0600 AK 0692-0600	Dual Asynchronous Adapter, one line disabled Dual Asynchronous Adapter, second line enabled	1,500 1,500	159 159	85 85

\*Includes maintenance

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# NCR V-8500 and V-8600 Systems

# EQUIPMENT PRICES

		Purchase Price	Annual Maint.	One-Year Rental*
AU 0692-0638	438-3 Adapter	3,180	143	89
AU 0693-0600	Dual Synchronous Adapter, one line disabled	2,250	170	115
AK 0693-0600	Dual Synchronous Adapter, second line enabled	2,250	170	115
AU 0695-0600	On-Line Auto Dialer	1,695	158	55
AU 0698-0300	Integrated Asynchronous Modem	1,000	131	38
AU 0752-0200	Free-Standing External Modem	700	150	36
TERMINALS				
BU 7900-1102	Model 7900 CRT; TTY compatible	2,000	350	104
AU 7900-2112	Model 7900 CRT; detachable keyboard; TTY compatible	2,000	324	108
AU 0260-8000	Thermal Printer; receive only	2,495	394	113
BU 7900-1303	Model 7900 CRT; asynchronous, pollable	3,500	427	176
AU 7900-1313	Model 7900 CRT; detachable keyboard; asynch., pollable	3,670	427	182
AU 0260-900	Thermal Printer; receive only	2,495	394	113

## **SOFTWARE PRICES**

	One-Time License Fee	Monthly License Fee
VS1 Basic System Software		
VRX Operating System VRX Cobol 74 Compiler VRX COBUG VRX Fortran 77 Compiler NRX NEAT/VS Compiler VRX SORT/MERGE VRX TRAN-PRO VRX TRAN-QUEST	0 0 0 0 25,475 13,000	0 132 100 158 121 637 325
VS1 On-Line System Software		
Terminal Communications Processor Network Definition Language Processor On-Line Program Development Remote Job Entry VRX Telecommunications EZ-KEY Data Entry System	0 0 2,810 0 0 1,500	60 36 61 72 22 42
VS1 Data Management System Software		
VRX Utilities VRX File Conversion Utility VRX Total—V-85XX Series	0 0 36,000	35 48 1,092
Management Sciences Application Software		
Statistics Linear Programming Project Network Analysis Vehicle Scheduling Feed Information System (FIS)	975 653 4,501 653 11,877	21 13 94 13 366

\*Includes maintenance

## **SOFTWARE PRICES**

	One-Time License Fee	Monthly License Fee
General Application Software		
General Payroll Payroll/Cost, Labor Scheduling Accounts Receivable—Commercial Accounts Receivable—Consumer Accounts Receivable—Tape Accounts Payable General Ledger with Reporting Subsystem Accounting System Interface	713 713 1,063 1,063 1,063 1,063 713 713	14 14 22 22 22 22 14 14
Manufacturing Application Software		
Inventory Material Control (IMC) Inventory Material Requirements (IMR) Bill of Materials Manufacturing Systems Inquiry Production Scheduling Inventory Requirements Planning Work in Progress Order Processing MISSION Manfacturing System—Basic Module	1,585 805 1,585 805 805 1,190 10,900 10,000 50,000	33 17 33 17 17 25 273 250 1,260
Wholesale Application Software		
Emphasis Order Billing Techinque II (ORBIT II) Order Billing Technique III (ORBIT III) SPIRIT V (not on 8600 series) SPIRIT—Sales Analysis	886 1,744 1,744 14,375 1,300	19 36 36 318 —
Medical Application Software		
Post Discharge Accounts Receivable In-Patient Records Medical Audit Statistics	1,705 1,265 1,265	50 33 33
Criterion Loan and Savings System (CLASS)		
General Ledger Savings Loans	2,000 23,000 16,000	70 825 570
Education Application Software		
Stewardship & Management Accounting SCHOLARS II Student Test Analysis School Bus Scheduling System	1,738 15,125 704 3,219	50 432 20 92
Government Application Software		
Financial Management Systems	5,000	150
Food Distribution Application Software		
Order Billing Technique I (ORBIT I)	1,579	37
Department Store Application Software		
Retail Sales Audit Fashion Reporting Pre-Edit Processing Staple Stock Replenish- ment	855 2,520 1,620 2,020	20 60 40 50
MEDICS Application Software		
MEDICS 2950 MEDICS ADT	50,000 15,000	1,315 395 ∎
*Includes maintenance		