

Specifications

IBM System/36 Assembler and Macro Processor For the IBM System/36 5360 and 5362 System Units Program Number 5727-AS1

The IBM System/36 Assembler Language is a symbolic programming language used to write programs or subroutines for the IBM System/36 5360 and 5362 System Units. Source programs written in this language are processed by the System/36 Assembler Program to produce relocatable object programs, which are subsequently converted to executable format by the System/36 System Support Program (SSP) Overlay Linkage Editor. The System/36 libraries are used to store the source statements, relocatable object programs, and executable load programs.

The IBM System/36 Assembler can also be used for assembly of relocatable subroutines for use with System/36 RPG II, COBOL, and FORTRAN. The subroutines written in the System/36 Assembler Language are coded by the user and separately assembled. The process of program linking is accomplished during compilation of the RPG II, COBOL, or FORTRAN source programs.

Some of the features provided by the System/36 Assembler are:

- Mnemonic operation codes
- Symbolic referencing of storage addresses
- · Automatic storage assignment
- Address displacement calculation
- · Bit-level data representation
- · Operand field expressions
- · Source identification-sequence field
- Assembler instructions
- Source and cross-reference listings
- · Error checking and diagnostic messages

The System/36 Macro Processor expands macroinstructions into Assembler source language statements that, in turn, are processed by the System/36 Assembler.

- System/36 macros are provided to the Assembler user for the following System Support Program functions: disk, printer, display station, binary synchronous communications, timer, EOJ, message log, program load, and the loadable sort interface.
- System/36 macros provide an interface to the scientific instruction set.
- Macros may also be written by the user using the macro definition language provided.

Specified Operating Environment

Machine Requirements

The IBM System/36 Assembler and Macro Processor (5727-AS1) executes on all models of the IBM System/36 5360 and 5362 System Units.

Programming Requirements

The IBM System/36 Assembler and Macro Processor (5727-AS1) is designed to operate with the IBM System/36 System Support Program Product (5727-SS1).

Warranty

The System/36 Assembler and Macro Processor Program Product (5727-AS1) is warranted to conform to its Licensed Program Specifications when shipped to the customer if properly used in the Specified Operating Environment.

Licensed Program Specifications may be updated from time to time and such updates may constitute a change in specifications.

Following the discontinuance of all program services, this program will be distributed on an As Is basis without warranty of any kind either express or implied.

Any other documentation with respect to this licensed program, including any documentation referenced herein, is provided for information purposes only and does not extend or modify these specifications.

International Business Machines Corporation Department 245 Highway 52 and Northwest 37th Street Rochester, Minnesota, U.S.A. 55901

GC21-7893-1

