

# Technical Reference

















JX TECHNICAL REFERENCE MANUAL



SOFTWARE PLANNING & DEVELOPMENT

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## Preface

This manual contains necessary information for the development of I/O devices and software. To understand and utilize this manual in a proper manner, basic knowledge of operation of the IBM 5510 Personal Computer is required. The CPU of this system uses an INTEL 8088 microprocessor and related knowledge of it is also required.

This manual consists of the following:

Chapter 1	Introduction to the System
Chapter 2	Base System
Chapter 3	Video Subsystem
Chapter 4	System Options
Chapter 5	Software
Chapter 6	Compatibility
Appendix A	BIOS Listing
Appendix B	Logic Diagrams
Appendix C	Character Code Table/Character Font

First Edition (December 1984)

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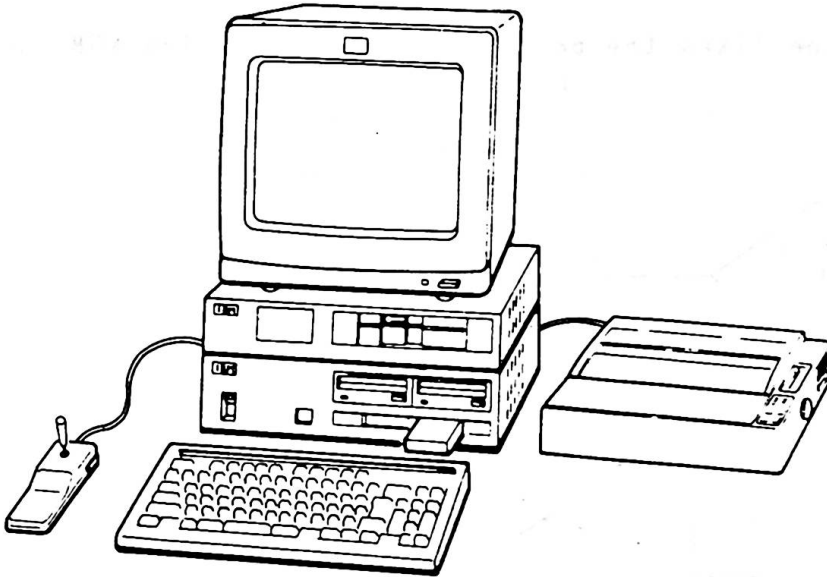
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## 1. Introduction to the System

The IBM 5510 System basically consists of a System Unit and Keyboard. By adding optional units and/or features, various system configurations can be defined. In this Chapter, these units/features are introduced.



System Configuration Sample

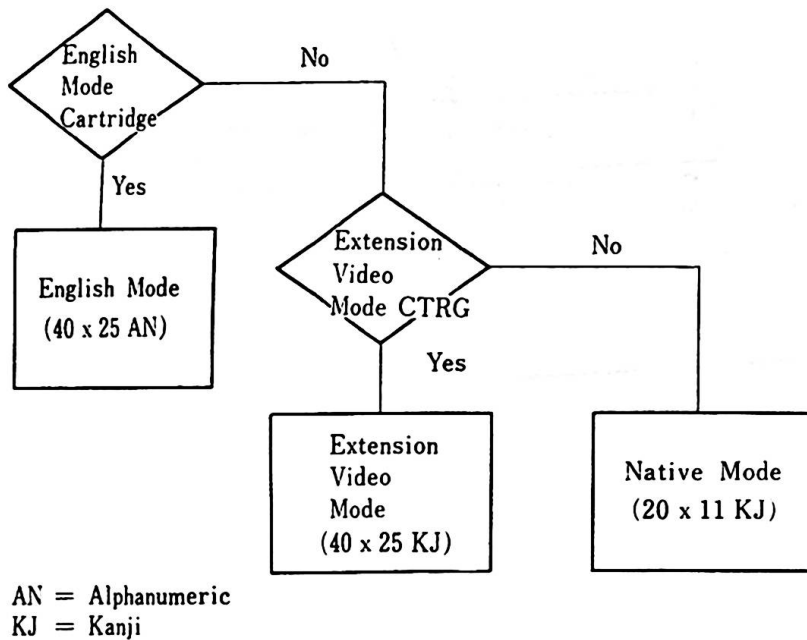
## 1. Introduction to the System

The IBM 5510 is a compact system with a single system board which holds most of the logical circuits. It has many functions and value-added options.

One of the major characteristics of the IBM 5510 is that it is designed to operate in three different modes: English Mode, Native Mode and Extension Video Mode. Switching modes can be performed only by inserting a ROM Cartridge. When no cartridge is inserted, the system operates in Native Mode.

### Mode Setting

A BIOS Routine fixes the mode depending upon which ROM Cartridge is inserted.



As shown in the above chart, mode can be selected by inserting ROM Cartridges. Users can also make their own cartridges (For instance for game programs) and can run the programs. For this purpose, there are some rules to follow in relation to the ROM Cartridge. These rules are described in Chapter 2.0 Base System - ROM Cartridge in this manual.

The IBM 5510 Video System supports a wide variety of displays ranging from a TV set normally installed in the home to a high resolution CRT display. The Video System can display up to 40 Kanji characters across and 25 lines vertically and 720 x 512 dots in two color graphic mode. In Text mode, Alphanumerics, special characters, Katakana, Hiragana and Kanji can be displayed.

The Video System functionally consists of three Video Processors (VP1, VP2, VP3). Which of these processors is active depends on the operational mode. By using Video RAM, such functions as Animation are possible. By operating VP1 and VP2, superimposing screens is possible.

The IBM 5510 Video System holds up to eight "pages" as a standard and a maximum of twelve "pages" when additional RAM is installed. VP1 supports pages 0 through 7 for ASCII code, while VP2 and VP3 support pages 8 through 11 for JIS code. Here, "page" refers to 16KB units in memory which contain data to be displayed on the screen.

The IBM 5510 system unit has the following optional features and provides interfaces for them:

#### 64KB RAM Card

increases the system memory size by 64KB to a total of 128KB and enables users to work with a higher resolution video mode in VP1.

#### 128KB RAM Card

increases the system memory size by 128KB to a total of 256KB and holds Address Decode Logic. Up to three cards can be installed when the Expansion Unit is used, making the maximum memory size 512KB.

#### Extension Video Card

makes it possible to display up to 40 Kanji characters across and 25 lines vertically. Grid Line, 720 x 512 dot two color and 320 x 512 four color graphic displays are also possible. The maximum Video RAM size is 64KB, including the 32KB provided with the base system.

#### Diskette Drive Adapter Card

controls up to three diskette drives. Diskettes are formatted to 360 KB in English mode and 720 KB in Native and Extension Video mode.

#### 3-1/2" Diskette Drive

supports a 3-1/2" 2DD Diskette.

#### 5-1/4" Diskette Drive

supports a 5-1/4" 2DD Diskette.



## 1. Introduction to the System

### RS-232C Card

is a Serial Interface Card which plugs into the IBM 5510 System Board and supports Start-Stop transmission.

### RS-232C Cable

connects the RS-232C Card with a Serial I/O Unit.

### 12" Color Display

is a medium resolution Red/Green/Blue/Intensity direct-drive display which can display a maximum of 16 colors.

### 12" Monochrome Display

is a high-resolution direct-drive display which is dual-mode and can display English, Native and Extension Video modes. Operational Frequency can be changed by changing the signal transmitted from the System Unit.

### 14" Color Display

is a high resolution Red/Green/Blue/Intensity direct-drive display which is dual-mode and can display English, Native and Extension Video modes. Operational Frequency can be changed by changing the signal transmitted from the system unit.

### Joystick

is an input device which provides the user with two dimensional positioning control. Two push buttons provide the user with additional input capability. It is center-loaded and is calibrated to 100,000 Ohms.

### TV Adapter

allows an ordinary home TV set to be connected to the IBM 5510 System. It includes an RF Modulator. When the system unit is turned on, the connection is switched from normal TV broadcasting.

### Keyboard Cable

is used to connect the keyboard to the system unit. When the keyboard cable is not used, an infrared link provides cordless communication between the keyboard and the system unit.

### CMT Cable

connects a cassette tape recorder unit with the system unit.

### 5512 Printer

is a desk-top, non-impact printer. The thermal transfer print head, consisting of 24 heating elements, makes a 24 x 24 Kanji, 12 x 24 Hankaku, 16 x 24 PICA, or 12 x 24 ELITE character font matrix.

### 5513 Printer

is a desk-top thermal/matrix printer which uses roll paper.

**Printer Cable**

connects printer units with the system unit.

**Expansion Unit**

is the same in size as the system unit and is placed on top of the system unit. It contains a Power Unit and allows the user to expand the the number of diskette drives to two or three. Through installation of an Expansion Board Kit, the number of I/O Channels and the memory size can be increased.

**Expansion Board Kit**

consists of an Expansion Board Adapter and Expansion Board. It holds five I/O Channel slots.





## 2. Base System

The IBM 5510 consists of a System Unit and Keyboard. The keyboard is provided with an infrared optical link and therefore can make key entries without a keyboard cable. It can also use a keyboard cable to connect to the system unit. The cable is available as an optional feature.

Various kinds of optional features are available for installation with the system unit. Their descriptions are in Chapter 4.0 (4.0 System Option). In this chapter, system functions are described based on the System Board, which is the fundamental part of the system unit.

Video displays are described in Chapter 3.0 (Video Subsystem).

2. Base System

Figure 2-1

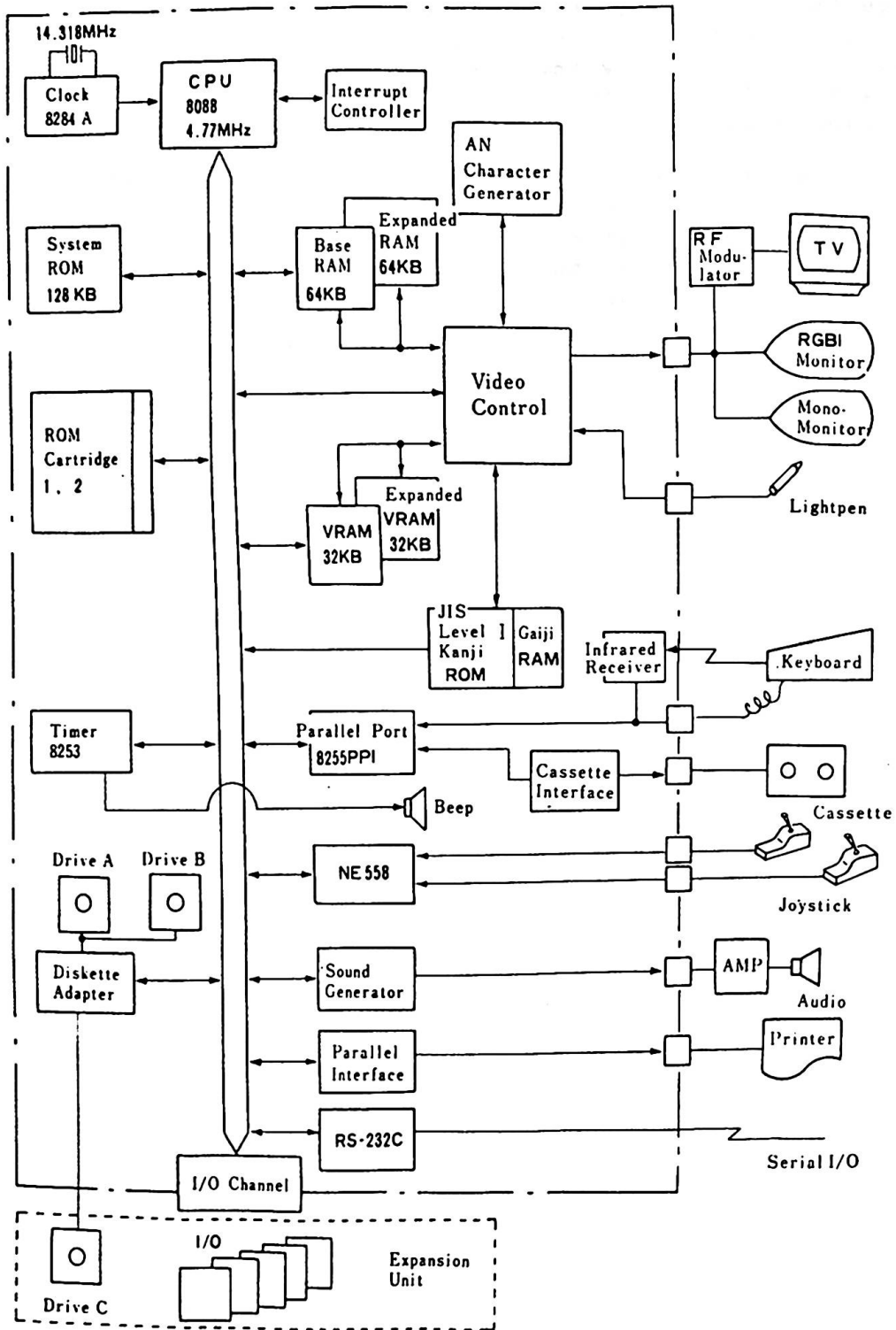


Figure 2-1 System Block Diagram

## 2.1. System Board

The IBM 5510 System Board is a single board which performs most of the system functions and is the nucleus of the system. The system board fits horizontally in the base of the system unit. It uses double-sided four layered printed circuit boards with an internal power/ground plane. It is designed with highly integrated circuits.

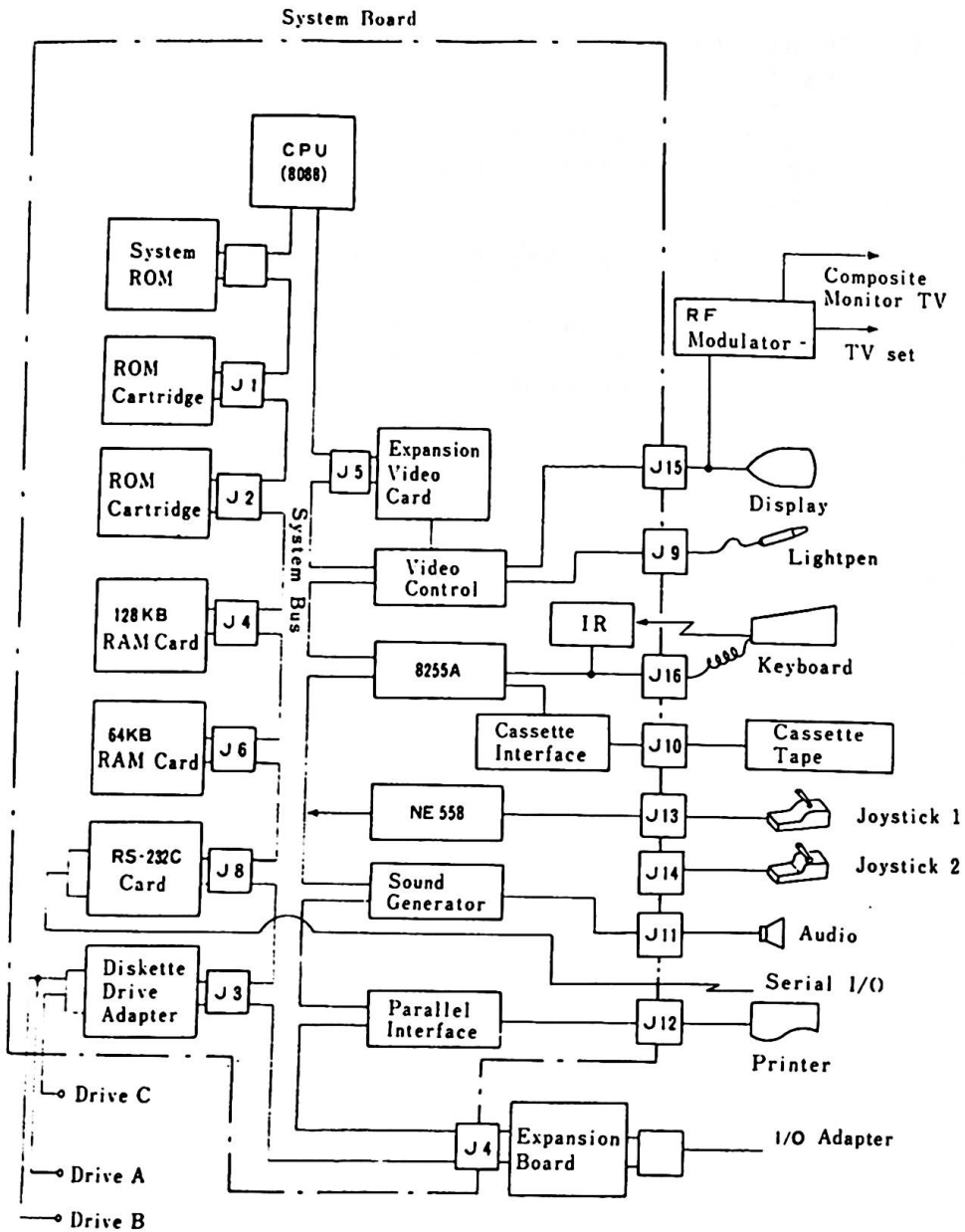
The logical circuits use 8088 family dedicated LSI, seven custom-made LSI consisting of CMOS gate arrays and various I/O LSI to make the system compact while performing many sophisticated functions. The System Board provides the following interface connectors for optional features;

- 64KB RAM Card
- 128KB RAM Card or Expansion Board Kit
- Extension Video Card
- Diskette Drive Adapter
- ROM Cartridge
- Keyboard
- RS-232C Card
- Display(3 types)
- TV Adapter
- Light Pen
- Audio Amplifier(connector only)
- Cassette Tape Cable
- Joystick
- Printer(2 types)

AC power(+5V, +12V, -12V) enters the system board through the power supply connector.

2. Base System

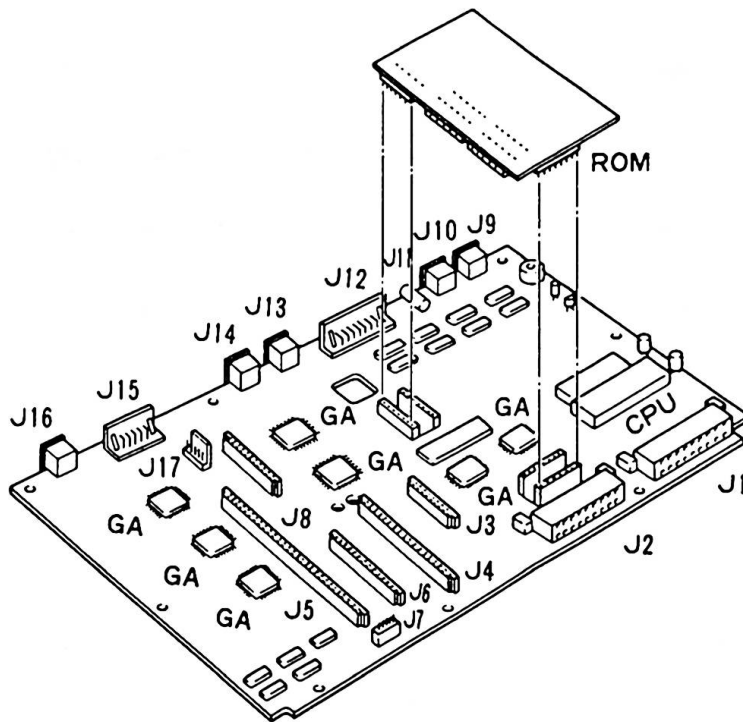
Figure 2-2



Remark) --- : Within System Board  
 JXX : Connector Number

Figure 2-2 I/O Connection Diagram





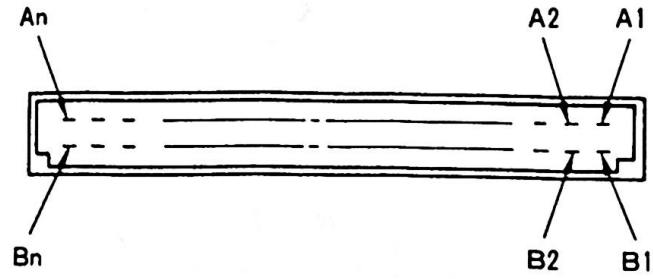
Remark) GA : Gate Array  
 ROM Board : System ROM and Kanji Character Generator (CG2) included

No.	Connecting Feature	No.	Connecting Feature
J 1	ROM Cartridge	J 10	Cassette Tape Recorder
J 2	ROM Cartridge	J 11	Audio
J 3	Diskette Drive Adapter	J 12	Printer
J 4	128KB RAM Card	J 13	Joystick (1)
J 5	Extension Video Card	J 14	Joystick (2)
J 6	64KB RAM Card	J 15	Display
J 7	Infrared Receiver	J 16	Keyboard
J 8	RS-232C Card	J 17	Power Connector
J 9	Light Pen		

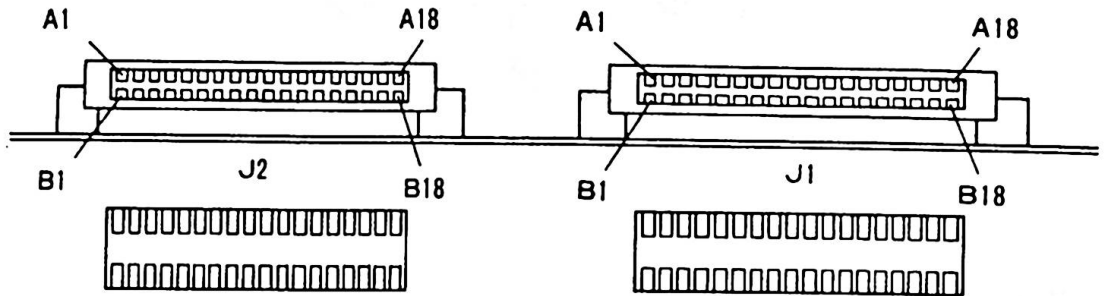
Figure 2-3 Connectors on System Board

2. Base System

Figure 2-4



Card Connector(J3, J4, J5, J6, J8)



ROM Cartridge Connector (J1, J2)

Figure 2-4 Connector. Pin Numbers

The nucleus of the system board is the Intel 8088 microprocessor. This processor (hereafter called CPU) is software-compatible with the 8086 micro-processor. The 8088 supports 16 bit operations, including multiplication and division and supports 20 bits of addressing (1 megabyte storage). It operates in the minimum mode at 4.77 MHz.

The three programmable timer/counters are provided by an Intel 8235-5 programmable interval timer and are used by the system in the following manner:

- Channel 0 : Used as a general-purpose timer to provide constant time base for implementing a time-of-day clock.
- Channel 1 : Used for deserializing the keyboard data and for time-of-day overflow during diskette operations.
- Channel 2 : Used to support the tone generation for the audio speaker and to write data to the cassette.

There are nine prioritized hardware interrupt levels and out of them, four are bussed to the system I/O channel for use by adapters. The non-maskable interrupt (NMI) of the 8088 is attached to the keyboard-interface circuits and receives an interrupt for each scan code sent by the keyboard.

The system board has space for 128K bytes by 8 bits of ROM and the ROM is aligned at the top of the 8088's address space.

The system board makes it possible to process complex screen handling and sound generation through its sound generator and video subsystem.

## 2. Base System

### 2.2. Fundamental System Function

In this Chapter, fundamental system functions and the hardware to support the functions are described.

#### 2.2.1. System Clock

The CPU operates in the minimum mode using a 4.77 MHz clock. The time of one clock cycle is 210 nsec. Normally, four clock cycles are required for a bus cycle, so that an 840 nsec ROM memory cycle time is achieved. When RAM memory is shared with video memory RAM, write and read cycles will take an average of 2 wait cycles, leading to an average of 6 clock cycles. The bus cycle time is 1.260 micro-seconds. The bus cycle time for I/O reads and writes is also 1.260 micro-seconds. The 4.77 MHz clock, whose frequency is derived from a 14.31818 MHz crystal is divided by 3 for the processor clock, and by 4 to obtain the 3.58 MHz color burst signal required for color televisions. The 1.789 MHz clock, which is divided by 8 is used as a baud rate clock of an RS-232C card.

#### 2.2.2. Interrupt Controller

Eight hardware levels of interrupts are available for the system. The highest priority interrupt is the NMI in the 8088. The NMI is followed by eight prioritized interrupt levels (0 - 7) in the 8259A Programmable Interrupt Controller. The priority of the interrupts are as follows:

<u>Priority</u>	<u>Interrupt</u>	<u>Function</u>
1	NMI	Keyboard Interrupt
2	IRQ 0	Time Clock Interrupt
3	IRQ 1	I/O Channel, Keyboard (INT 9 Software Interrupt)
4	IRQ 2	I/O Channel
5	IRQ 3	Asynchronous Port Interrupt (RS-232C)
6	IRQ 4	External I/O Channel
7	IRQ 5	Vertical Retrace Interrupt (Video)
8	IRQ 6	Diskette Interrupt
9	IRQ 7	I/O Channel and Printer

## 8259A Programming Considerations

(1) 8259A is initially set up with the following characteristics:

- Buffered Mode
- 8088/86 Mode
- Single Mode Master (No cascading is allowed)
- Edge Triggered Mode
- I/O Address is 20 (Hex)
- can issue Hardware Interrupt types Hex 8 (IRQ 0) to Hex 0F (IRQ 7)

The following is an example setup:.

```

0263      B0 13      MOV AL,13H      ; ICW1 - RESET EDGE SENSE
                                ; CIRCUIT, SET SINGLE MODE
                                ; 8259 CHIP AND ICW4 READ
0265      E6 20      OUT INTA00,AL
0267      B0 08      MOV AL,8        ; ICW2 - SET INTERRUPT
                                ; TYPE 8 - F
0269      E6 21      OUT INTA01,AL
026B      B0 09      MOV AL,9        ; ICW4 - SET BUFFERED MODE/MASTER
                                ; AND 8088 MODE
026D      E6 21      OUT INTA01,AL

```

- (2) IRQ 1,2,4 and 7 can be used by the I/O Channel. Care should be taken when IRQ1 and IRQ 7 are used, as they are shared by several I/O devices.
- (3) NMI can be masked by operation of port A0.



## 2. Base System

### 2.2.3. Parallel Port

8255 PPI (Programmable Peripheral Interface) is used as one of the I/O Interfaces and supports control of the Keyboard, Timer, and Cassette Motor. It also checks whether there is an option card or not. Bit Assignments for each Port (A, B, and C) and corresponding functions are as follows:

Port A : For Output

<u>Bit</u>	<u>Description</u>
------------	--------------------

PA0	: Reserved for Keystroke Storage
-----	----------------------------------

|

PA7	:
-----	---

Port B : For Output

<u>Bit</u>	<u>Name</u>	<u>Description</u>
------------	-------------	--------------------

PB0	Timer 2 Gate	This line is routed to the gate input of timer 2 on the 8253-5. When this bit is "low", the counter operation is halted. This bit and PB1 (Speaker Data) control operation of the 8253-5 sound source.
PB1	Speaker Data	This bit ANDs "off" the output of the 8253-5 Timer 2. It can be used to disable the 8253-5 sound source, or modify its output. When this bit is high, it enables the output. A "low" value forces the output to 0.
PB2	Text/Graphics	This bit is used to steer data from the memory into Video Processor 1. 1 --- Text Modes 0 --- Graphics Modes
PB3	Cassette Motor Control	When this bit is a 1, the cassette relay is open and the cassette motor is off. When this bit is a 0 and PB4=0, the cassette motor is on.

- PB4 Beeper & Cassette Control  
When this bit is 1, the internal beeper and cassette motor are disabled.
- PB5 Speaker Switch 0,1  
These bits steer one of 4 sound sources. The selected sound source is in the display or in the external speaker. Selected sound sources are as follows:
- PB6

PB6	PB5	Sound Sources
0	0	Timer 2
0	1	Cassette Audio
1	0	I/O Channel Audio
1	1	Sound Generator

- PB7 Open  
Reserved for future use

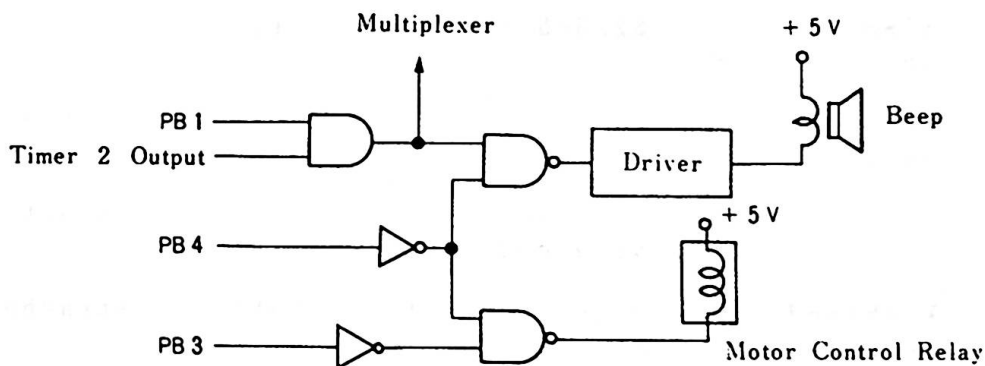


Figure 2-5 PB Bit Function

## 2. Base System

Port C : For Input

<u>Bit</u>	<u>Name</u>	<u>Description</u>
PC0	NMI Latch Input	This Input comes from a latch which is set to a one on the first leading edge of the Keyboard Data Stream.
PC1	RS-232C Card	When the RS-232C Card is installed, this bit is a 0.
PC2	Diskette Adapter	When the Diskette Adapter Card is installed, this bit is a 0.
PC3	64KB RAM Card	When the 64KB RAM Card is installed, this bit is Low Level.
PC4	Cassette Data	If the Cassette Motor Relay is "closed", and the Cassette Motor is "on", this pin will contain data which has been wave-shaped from the cassette. When the Cassette Motor Relay is off, The Timer 2 output will become PC4 input.
PC5	Timer 2 Input	8253-5 Timer 2 input.
PC6	Keyboard Data	This input contains keyboard data. The keyboard data comes from the cable, when it is attached, or from the IR Receiver Card if the cable is not attached.
PC7	Keyboard Cable	When the Keyboard Cable is attached, the bit is a 0.

## 2.2.4. Port A0 Output Description

Port output of I/O Address A0 (Hex) allows NMI interrupt and clock input selections. (Reference : 2.2.11 Keyboard Interface)

<u>Bit</u>	<u>Description</u>
Bit 7 (NMI Enable)	When this bit is a one, NMI is enabled. When this bit is a zero, NMI is disabled.
Bit 6 (IR Test ENA)	This bit enables the 8253-5 Timer 2 output into an IR diode on the IR Receiver Card. This information is then wrapped back to the the keyboard input. If the cable is connected, timer 2 should be set for 40 KHz which is the IR-modulation frequency. This feature is used only for a diagnostic test of the IR Receiver Card.
Bit 5 (Clock 1 Input Selection)	This bit selects input clocks to the 8253-5 timer 1. A zero selects a 1.1925MHz clock input (for deserializing the keyboard data). A one selects the timer 0 output to be used as the clock input to timer 1. This is used to catch timer 0 overflows during diskette drive operations, while interrupts are masked off. This is then used to update the time of day.

## 2. Base System

### 2.2.5. Memory

The IBM 5510 has two kinds of memory, ROM(Read Only Memory) and RAM(Random Access Memory). Memory is categorized functionally as follows:

- General-use Memory
- System ROM
- Video RAM
- Character Generator 1 (CG1)
- Character Generator 2 (CG2)
- Gaiji RAM

#### General-use memory

64KB of R/W memory resides on the system board. R/W Memory can be expanded to a 512KB maximum. The standard 64KB memory consists of eight 64K bit modules and has no parity bit. Sources of these memory modules include the Texas Instruments TMS4164-15 or equivalent. These are dynamic RAM with 150 ns access time. Memory size can be expanded by installing additional 64KB or 128KB cards.

Address space of 00000 - 7FFFF (Hex) is always reserved for RAM. Normally, the standard 64KB uses address space of 00000 - 0FFFF (Hex). If an additional 64 KB memory card is installed, address space of 00000 - 1FFFF (Hex) is used for the total of 128 KB space. The 64 KB system board memory is mapped to the EVEN memory address, while the 64 KB additional memory card is mapped to the ODD memory address within the 128 KB reserved space. When an additional 128KB RAM card is installed, the address space will be changed accordingly.  
(Reference : 5.5 Memory Map)

#### System ROM

The ROM subsystem is made up of 128KB of ROM aligned at E0000 - FFFFF(Hex) and has the following functions:

- Power on Self-Diagnostic test
- Initialization (ROM, RAM, I/O Port Configuration set-up)
- Basic Interpreter
- BIOS
- Diskette Boot Strap Loader
- Kanji Dictionary
- ROM Cartridge auto-link

The access time of this ROM Cartridge is 250ns and the cycle time is 375ns.

### Video RAM

The system board has 32KB Video RAM as a standard feature. The Video RAM consists of four 16K x 4 modules. These modules include the TMS 4416-15 or its equivalent and are dynamic RAM with 150ns access time.

### Character Generator (CG1)

2KB ROM space is reserved for Alphanumeric and Special character fonts used in English Mode.

### Character Generator (CG2)

128KB ROM space is provided to for Kanji, Alphanumeric, Special character, Hiragana and Katakana fonts to be used in Native and Extension Video Mode.

### Gaiji RAM

2KB static RAM is reserved for storage of up to 62 fonts (15 x 16). They can be accessed by Video Processor 2 and 3. The CPU can also read/write data. The modules are static RAM with 200ns access time.

General-Use Memory 512KB Maximum	System ROM 128KB
(Video RAM) (128KB)	Character Generator 1 2KB
	Character Generator 2 128KB
Video RAM 64KB Maximum	Gaiji RAM 2KB

Figure 2-6 Memory Classification



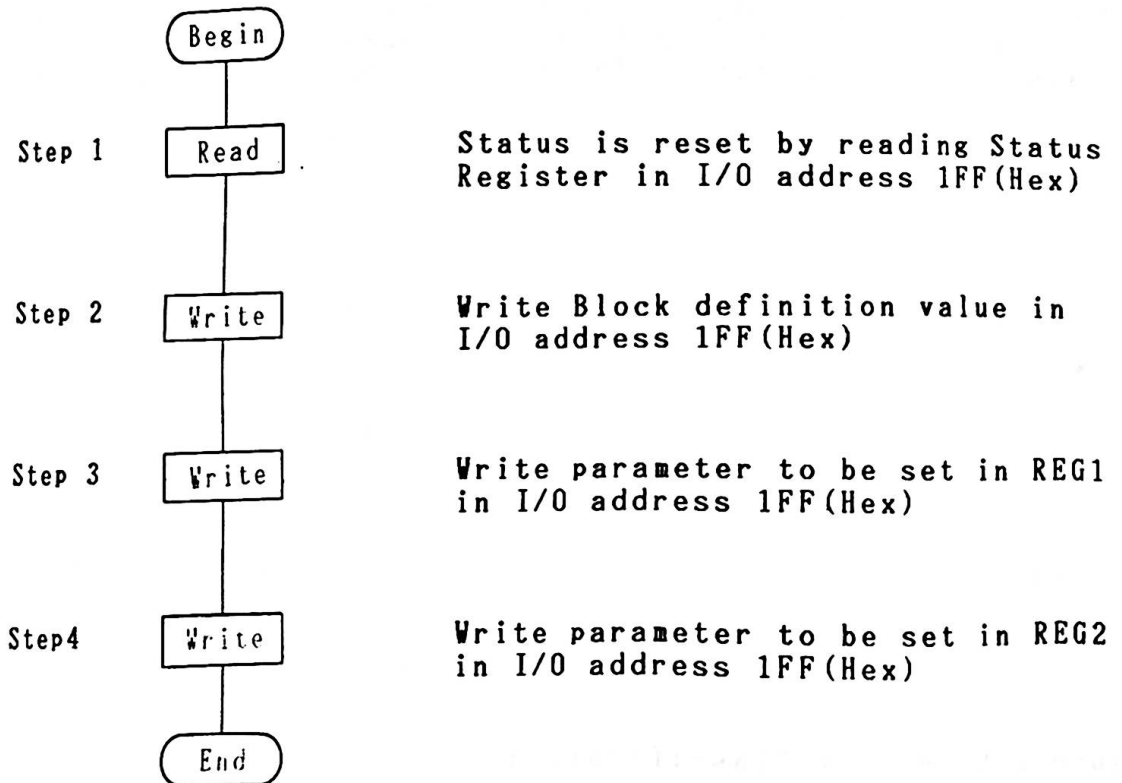
## 2. Base System

### 2.2.6. Memory Space and I/O Address Setting

The IBM 5510 is provided with memory space and dedicated custom LSI. The memory space is separated into 11 blocks and the addresses each memory block uses are defined by the software. The minimum unit of definition is 32KB. There are 19 blocks for I/O addresses. The addresses for most I/O devices can be change by software. These controls are performed by setting the appropriate parameter in I/O address 1FF (Hex). Parameters are set in two of the control registers which reside in each block. Memory space and I/O addresses are set by the following hardware elements:

- Status Register
- Block Definition
- Control Register 1 (REG1)
- Control Register 2 (REG2)

Method for setting





## 2. Base System

Block Value	I/O	Remarks	Address (Default Value)
80	8259	Interrupt Controller	020~027
81	8253	Timer	040~047
82	8255	Parallel Port	060~067
83	NMI		0A0~0A7
84	SOND	Sound Generator	0C0~0C7
85	FDC	Diskette Drive Adapter	0F0~0F7
86	JOYW	Joystick Write	200~207
87	JOYR	Joystick Read	200~207
88	PRNT	Printer	378~37F
89	8250	RS-232C Card	2F8~2FF
8A	CRTC	CRT Controller	3D0~3D7
8B	GA01	Gate Array 1	3D8
8C	GA 2 A	Gate Array 2 (VP 1)	3DA
8D	GA 2 B	Gate Array 2 (VP 2)	3DA
8E	GA03	Gate Array 3 (VP 3)	3DD
8F	LPGT	Light Pen Strobe	3DE
90	PG 2	Page Register 2	3D9
91	PG 1	Page Register 1	3DF
92	MODM	Open	3F8~3FF
93	ETSC	I/O Address in Expansion Unit	Undefined

Remark) I/O Blocks 80 - 92 are those I/O within the system unit. When one of these are selected, bus line of the Expansion Unit is separated from the system bus.

Figure 2-8 I/O Block and Definition Value

## Address Change

As the address bits of each memory block or I/O blocks are programmable, their addresses can be changed.

Block Definition Value	Block Name	ON/OFF	I/O	Mem.	RD	WR	A19	A18	A17	A16	A15
00	IROM 7	P	0	1	1	0	1	1	1	P	P
01	EROM 2	P	P	P	P	P	1	P	P	P	P
02	EROM 3	P	P	P	P	P	1	P	P	P	P
03	EROM 4	P	0	1	1	0	1	P	P	P	P
04	EROM 5	P	0	1	1	0	1	P	P	P	P
05	EROM 6	P	0	1	1	0	1	P	P	P	P
06	EROM 7	P	0	1	1	0	1	P	P	P	P
07	KJCS	P	0	P	P	P	P	P	P	P	P
08	MAIN	P	0	1	1	1	P	P	P	P	P
09	VRAM 1	P	1	1	1	P	P	P	P	P	P
0A	VRAM 2	P	0	1	1	1	P	P	P	P	P

P : Programmable

Figure 2-9 Memory Address Change

## 2. Base System

Block Definition Value	I/O Name	ON/OFF	A 9	A 8	A 7	A 6	A 5	A 4	A 3	A 2	A 1	A 0
80	8259	P	0	0	0	0	1	0	0	X	X	X
81	8253	P	0	0	0	1	0	0	0	X	X	X
82	8255	P	0	0	0	1	1	0	0	X	X	X
83	NMI	P	0	0	1	0	1	0	0	X	X	X
84	SOND	P	0	0	1	1	0	0	0	X	X	X
85	FDC	P	P	P	P	P	P	P	P	X	X	X
86	JOYW	P	1	0	0	0	0	0	0	X	X	X
87	JOYR	P	1	0	0	0	0	0	0	X	X	X
88	PRNT	P	1	1	0	1	1	1	1	X	X	X
89	8250	P	1	0	1	1	1	1	1	X	X	X
8A	CRTC	P	1	1	1	1	0	1	0	X	X	X
8B	Reserved	P	1	1	1	1	0	1	1	0	0	0
8C	GA 2A	P	1	1	1	1	0	1	1	0	1	0
8D	GA 2B	P	1	1	1	1	0	1	1	0	1	0
8E	GA 03	P	1	1	1	1	0	1	1	1	0	1
8F	LPGT	P	1	1	1	1	0	1	1	X	1	0
90	PG 2	P	1	1	1	1	0	1	1	0	0	1
91	PG 1	P	1	1	1	1	0	1	1	1	1	1
92	MODM	P	1	1	1	1	1	1	1	X	X	X
93	ETSC	P	X	X	X	X	X	X	X	X	X	X

P : Programmable

X : Neglected

0, 1 : Fixed

Remarks) In order to specify I/O address of Expansion Unit, the bit of Block Definition Value 93 "P" (on/off) should be set as 1.

Figure 2-10 I/O Address Change

## Control Register (REG1, REG2)

Each memory block or each I/O address block has one REG1 and one REG2.

## Memory Block Control Register

D7	D6	D5	D4	D3	D2	D1	D0	
ON/OFF	I/O	MEM	A 19	A 18	A 17	A 16	A 15	REG 1
	WR	RD	A 19	A 18	A 17	A 16	A 15	REG 2

REG1

- ON/OFF : "1" makes REG1 available  
 I/O : When "1", this block is allocated to I/O space  
 MEM : When "1", this block is allocated to memory space  
 A19-A15 : These correspond to address lines A19-A15 and set the first five High Storage Bits which the memory block uses with REG2 A19-A15.

REG2

- WR : When "1", writable memory  
 RD : When "1", readable memory  
 A19-A15 : REG1 specifies the bit (A19-A15) for comparison with the address bus output. When this bit is "1", comparison is not performed and when "0", comparison is done. When the comparison result is equal, these are valid addresses for this memory block. As for A14-A0, bit shown in the address bus is used as available addresses.



## 2. Base System

For instance, if REG1 is B7(Hex) and REG2 is 61(Hex), this memory block uses addresses B0000-BFFFF(Hex).

	D7	D6	D5	D4	D3	D2	D1	D0	
Address Bus →				A 19	A 18	A 17	A 16	A 15	A 14—A0
REG 1 →	1	0	1	1	0	1	1	0	X
REG 2 →	0	1	1	1	0	0	0	1	
Avail.address				1	0	1	1	X	X
				B					X X X X

### I/O Block Control Register

	D7	D6	D5	D4	D3	D2	D1	D0
ON/OFF	A 9	A 8	A 7	A 6	A 5	A 4	A 3	REG 1
	A 9	A 8	A 7	A 6	A 5	A 4	A 3	REG 2

The I/O Block Control Register and Memory Block Control Register have the same functions. I/O Address Lines compared are I/O Control REG1 and REG2 but the extent of the comparison is limited to A9 - A3.

### 2.2.7. Expansion Channel

The Expansion Channel is the means through which the CPU bus line is expanded. It can be connected with various I/O adapters which users can develop themselves.

#### Major Characteristics

- 64 Pin Connector is used
- "READY" signal is available for low-speed I/O features and slower access time memory
- Maximum load is one standard TTL
- 128KB RAM card, Expansion Board Adapter and other I/O features are connected.

The following describes connector pin(J4):

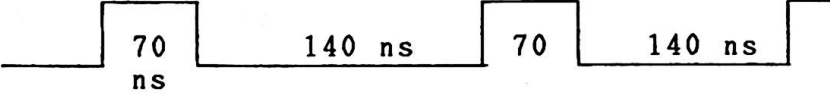
Signal Name	Pin (A)	Pin (B)	Signal Name
D0	A 1	B 1	D1
D2	2	2	D3
D4	3	3	D5
D6	4	4	D7
GND	5	5	GND
A0	6	6	A1
A2	7	7	A3
A4	8	8	A5
A6	9	9	A7
GND	10	10	GND
A8	11	11	A9
A10	12	12	A11
A12	13	13	A13
A14	14	14	A15
+ 5 V	15	15	+ 5 V
A 16	16	16	A17
A 18	17	17	A19
-IOR	18	18	-IOW
-MEMR	19	19	-MEMW
ALE	20	20	HLDA
CPU CLK	21	21	GND
IO/-M	22	22	DOT CLOCK
OPEN	23	23	OPEN
READY	24	24	RESET
-EXRD	25	25	-ETSC
IRQ 1	26	26	IRQ 2
IRQ 4	27	27	IRQ 7
-HRQ	28	28	-DEN
R/-DT	29	29	REFC
AUDIO IN	30	30	GND
AMODE	31	31	OPEN
+12V	32	32	-12V

Figure 2-11 Expansion Channel Connector(J4)

## 2. Base System

Remarks) I/O refers to the direction seen from the system board.  
Each signal is at standard TTL level except for AUDIO.

<u>Signal</u>	<u>I/O</u>	<u>Description</u>
D0 - D7	I/O	Data Lines 0 - 7: These lines provide data bus bits 0 to 7 for the processor, memory and I/O Devices. D0 is the Least Significant Bit (LSB) and D7 is the Most Significant Bit (MSB).
A0 - A19	0	Address Lines 0 - 19: These 20 address lines allow access of up to 1 megabyte of memory. A0 is the least significant bit (LSB) and A19 is the most significant bit (MSB).
-IOR	0	I/O Read Command: When this command line output is "0", it instructs an I/O device to drive its data onto the data bus. This signal may be driven by the 8088 microprocessor or by an external bus master (such as the DMA controller) after it has gained control of the bus.
-IOW	0	I/O Write Command: When this command line output is "0", it instructs an I/O device to read the data on the data bus. This signal may be driven by the CPU or by an external bus master (such as the DMA controller) after it has gained control of the bus.
-MEMR	0	Memory Read Command: This command line instructs the memory to drive its data onto the data bus.
-MEMW	0	Memory Write Command: When this command line output is "0", it instructs the memory to store the data present on the data bus.
ALE	0	ADDRESS LATCH ENABLE: This line provides the timing signal to latch the address bus.
HLDA	0	HOLD ACK : This line indicates to a bus master on the channel that -HRQ has been honored and that the 8088 has floated its bus and control lines.

<u>Signal</u>	<u>I/O</u>	<u>Description</u>
CPU CLK	0	<p>System Clock: It is a "divide-by-three" of the 14.31818 MHz oscillator and has a period of 210 ns (4.77MHz). The clock has 33 % of the duty cycle.</p> 
IO/-M	0	<p>I/O or Memory Status: This status line is used to distinguish a memory access from an I/O access. This line should be driven by a bus master after it has gained control of the bus. If this line is "high", it indicates an I/O access; if this line is "low", it allows memory access.</p>
READY	I	<p>This line, normally "high"("ready"), is pulled "low"(not ready) by a memory or I/O device to lengthen I/O or memory cycles. It allows slower devices to attach to the I/O channel with a minimum of difficulty. Any slow device requiring this line should drive it low immediately upon detecting a valid address and IO/-M signal. Machine cycles (I/O and memory) are extended by an integral number and CLK cycles (210 ns). Any bus master on the I/O channel should also honor this "ready" line. It is pulled "low" by the system board on memory and write cycles and outputting to the sound subsystem.</p>
RESET	0	<p>This line is used to reset or initialize system logic upon power-up. This line is synchronized to the trailing edge of the clock and is "active high". Its duration upon power up is 26.5 micro secs.</p>
-EXRD	0	<p>EXPANSION BUS READ CONTROL: This line is used to control the direction of data flow on the expansion channel. The read status makes it "0".</p>
-ETCS	0	<p>EXPANSION BUS Tri-state Control: It is used to control the Expansion Bus to Tri-state.</p>

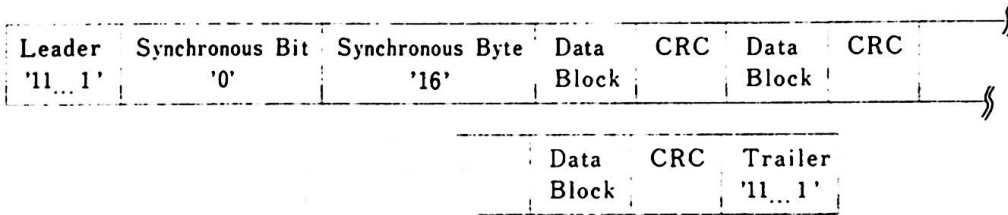
## 2. Base System

<u>Signal</u>	<u>I/O</u>	<u>Description</u>
IRQ1 IRQ2 IRQ4 IRQ7	I	Interrupt Request 1, 2, 4 and 7: These lines are used to signal the processor that an I/O device requires attention. They are prioritized with IRQ1 as the highest priority and IRQ7 as the lowest. An interrupt request is generated by raising an IRQ line from "low" to "high" and holding it high until it is acknowledged by the processor.
-HRQ	I	Hold Request: This line indicates that another bus master is requesting the I/O channel. When an I/O channel issues a -HRQ signal, the 8088 processor will respond to the -HRQ by asserting an HLDA, after it lets the data bus and address bus relinquish to an external bus master (such as the DMA controller). As a -HRQ is not an asynchronous signal, it should be synchronized to the system clock.
-DEN	O	DATA ENABLE: This signal makes the data bus available.
R/-DT	O	This signal is used to control the direction of data flow through the transceiver.
AUDIO	I	I/O devices connected to the expansion channel may provide sound sources to the multiplexer of the sound subsystem which resides on the system board. The signal level is 1V P-P.
A MODE	O	This signal is used to make the memory space which 128 KB RAM card uses fixed after the memory on the board when in English mode.
REFC	O	This line is used for dynamic RAM refresh.
DOT CLOCK	O	14.318 MHz Clock

2.2.8. Cassette Interface

The cassette interface is controlled by software. Output from the 8253 Timer 2 controls data to be written to the cassette recorder. The digital signal read from the cassette is input at PC4, 8255A PPI. PB3 and PB4 output controls the relay. When the relay is inactive (Motor is disabled), the cassette output signal has a wrap feature which connects the output to the input.

Data Format



Contents	Description
Leader	FF (Hex) of 256 bytes
Synchronous bit	1 "0" bit
Synchronous byte	"16" (Hex)
Data Block	256 byte unit Data
CRC	2 byte check Character (each Data Block)
Trailer	4 byte FF (Hex)

Figure 2-12 Cassette Data Format

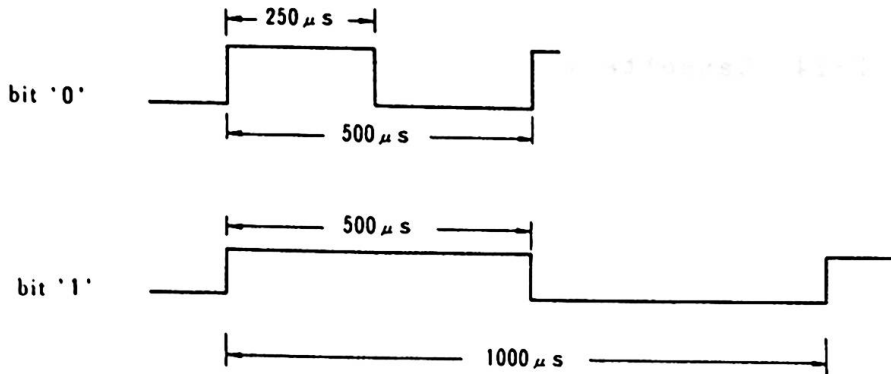


Figure 2-13 Bit Format

## 2. Base System

### Cassette Tape Write

Cassette tape write is performed from MIC DATA OUT (Timer 2 output). The cycle of Timer 2 is 0.5ms for bit 0 and is 1ms for bit 1. Data are written in units of 256-byte (Block) and when the length of data is shorter than multiplies of 256-byte, the last available data are repeatedly written to complete the block.

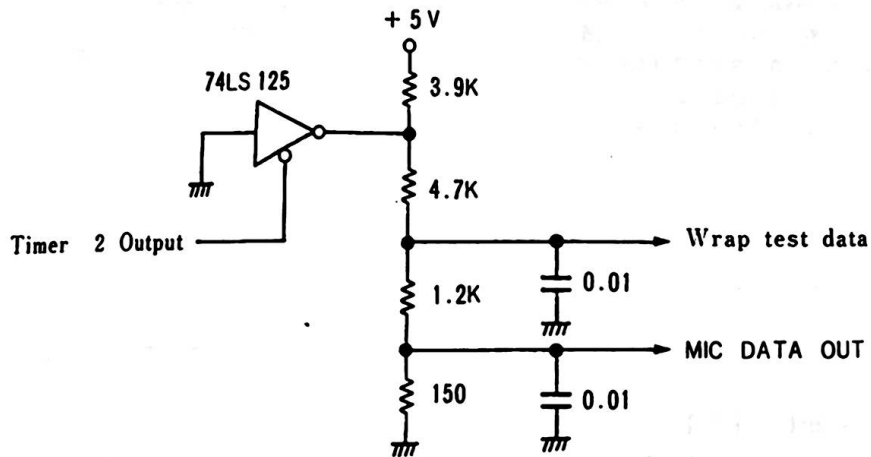


Figure 2-14 Cassette Write



## Cassette Tape Read

When more than 1/4 of leader is correctly read, the synchronous bit ("0") and synchronous byte are read. At the point where synchronous bytes are correctly read, data blocks are then read. If data are not read correctly up to a point of the synchronous bytes, the leader is searched again.

When data are read, a CRC check is performed for each block. Level 0 interrupt is not allowed while reading the cassette tape. Data read is input from CASS AUDIO to PC4.

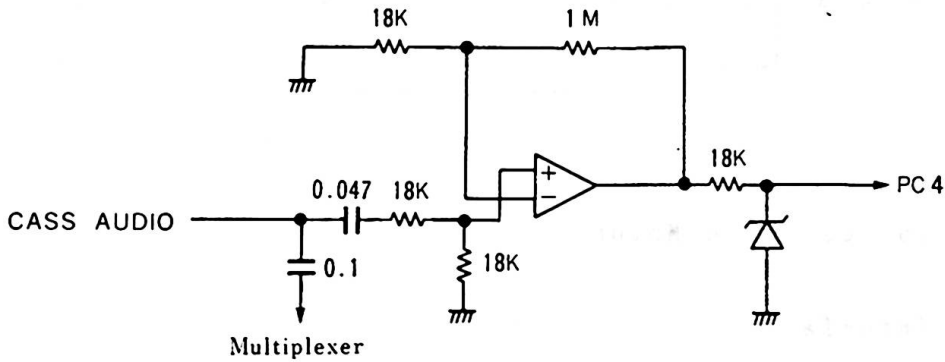


Figure 2-15 Cassette Read

## 2. Base System

### Motor Control

When both PB3 and PB4 are "0", the relay is active and the motor is turned on.

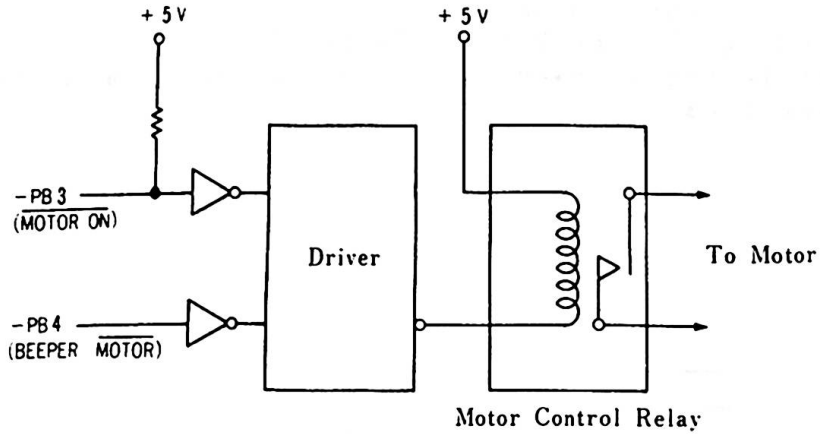
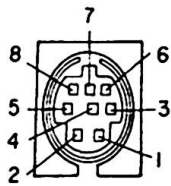


Figure 2-16 Cassette Motor Control

### Hardware Interface



(System-Side)

Pin No.	Signal Name	I/O
1	GND	—
2	OPEN	—
3	OPEN	—
4	CASS AUDIO	I
5	MIC DATA OUT	O
6	MOTOR CTL	—
7	CASS MTL CTL	—
8	OPEN	—

Figure 2-17 Interface Connector (J10)

## 2.2.9. Sound Subsystem

The nucleus of the sound subsystem is an analog multiplexer (MPX) which allows 1 to 4 different sound sources to be selected, amplified and sent to the audio output. The MPX and amplifier are configured so the amplifier's gain is unique to and consistent with each sound source. The amplifier is configured as a single-pole low pass filter with a 3dB cut-off frequency of 4.8 KHz. This filter is used to "round" off the corners of the square-wave signals. The output of the amplifier is supplied to the display interface and audio interface connector. If an external speaker is used, an external amplifier must be used to drive it. The audio output is 1V P-P alternating current and can drive a 10K Ohm or greater input impedance.

Sound source selection depends on the configuration of port bits PB5 and PB6 of 8255A PPI. Power-on selects Timer 2.

<u>PB6</u>	<u>PB5</u>	<u>Selected Sound Source</u>
0	0	Timer 2 output
0	1	Cassette Audio
1	0	I/O Channel Audio
1	1	Sound Generator

- (1) **Timer 2 Output**  
Beep or simple tone is output.
- (2) **Cassette Audio**  
Allows use of recorded sound on the cassette tape as the sound source.
- (3) **I/O Channel Audio**  
Allows the Expansion Channel to connect such I/O devices as a sound synthesizer and have an output in Expansion Channel Connector (J4) Pin A30 "Audio" for input to multiplexer.
- (4) **Sound Generator**  
SN76489A is used.

## 2. Base System

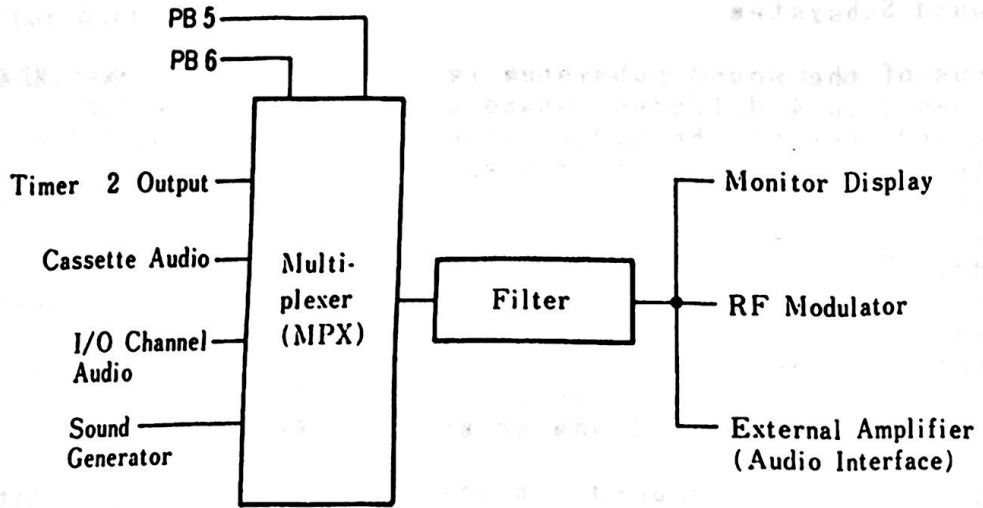


Figure 2-18 Sound Source

The RF modulator is included in the TV Adapter. It modulates the the audio signal and sends it with the screen signals to the TV set.

The IBM 5510 uses SN76489A as its sound generator. Its I/O address is C0 (Hex). This chip consists of a CPU Interface, Control Register, three tone generators, a Noise Generator, and an Audio Mixer. It is controlled by software and allows 3 simultaneous tone. A 3.579 MHz Clock Input is used.

The following is a description of each configured element:

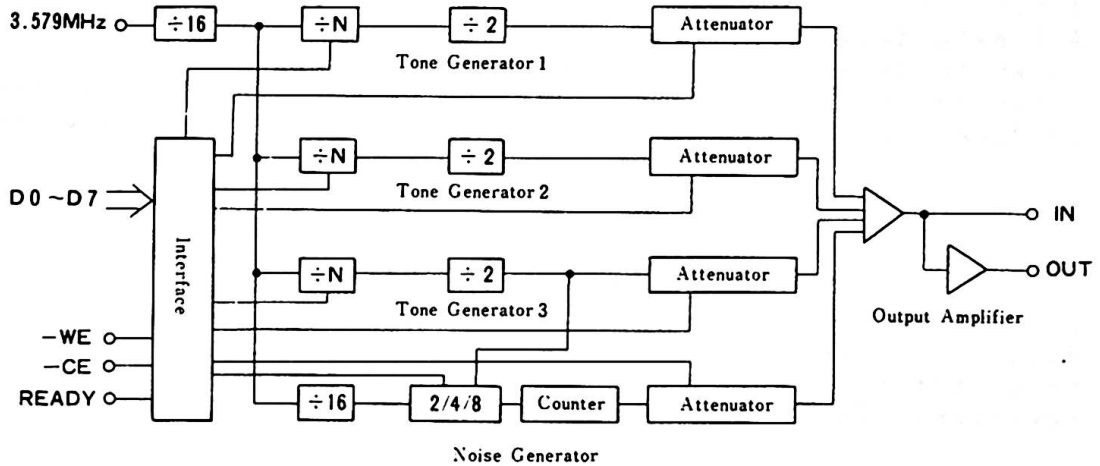


Figure 2-19 Sound Generator Block Diagram

## 2. Base System

### CPU Interface

The system microprocessor interfaces with the SN76489A by means of the 8 data lines and 3 control lines (WE, CE and READY). WE stands for Write Enable and CE for Chip Enable. The SN76489A is controlled by 1 or 2 bytes of data issued by the CPU. Each tone generator requires 10 bits of information for the frequency. The data transfer is made twice for each byte. 4 bits of information are required to select the attenuation. A 1 byte data transfer is required. It requires 32 clock cycles to write data on the register and uses a READY signal for synchronization. The READY signal becomes "Low Level" at the CE leading edge and "High Level" at the end of the data write.

### Control Register

The sound generator has 8 internal registers which are used to control the 3 tone generators and the noise source. During all data transfers to the sound generator, the first byte contains a three bit field which determines the destination control register. The register address codes are as follows:

Bit				Control Register Destination				Hex.			
MSB	R2	R1	R0								
1	0	0	0	Tone 1 Frequency				8			
1	0	0	1	Tone 1 Attenuator				9			
1	0	1	0	Tone 2 Frequency				A			
1	0	1	1	Tone 2 Attenuator				B			
1	1	0	0	Tone 3 Frequency				C			
1	1	0	1	Tone 3 Attenuator				D			
1	1	1	0	Noise Control				E			
1	1	1	1	Noise Attenuator				F			
7	6	5	4	3	2	1	0	7	6		
1	R2	R1	R0	F3	F2	F1	F0	0	X		
MSB				1st byte				LSB		2nd byte	

Figure 2-20 Control Register Destination

**Tone Generator**

Each tone generator consists of;

- Frequency synthesis section (Programmable Counter)
- Attenuation section (Programmable Attenuator)

The frequency synthesis section requires 10 bits of information (Hex F0 - F9) from 2 bytes of data issued from the CPU.

**Data Format (2 byte transfer)**

	MSB	1st Byte						LSB
		Register			Data			
1		R2	R1	R0	F3	F2	F1	F0

	MSB	2nd Byte						LSB
		Data						
0	X	F9	F8	F7	F6	F5	F4	

- (1) Register is selected by R0 - R2

R2	R1	R0	Register Assignment
0	0	0	Tone 1 Frequency
0	1	0	Tone 2 Frequency
1	0	0	Tone 3 Frequency

- (2) Frequency is set by F0 - F9 (10 bit binary number). F9 is the most significant bit and F0 is the least significant bit.

F9	F8	F7	F6	F5	F4	F3	F2	F1	F0
----	----	----	----	----	----	----	----	----	----

The frequency can be calculated by the following;

$$f = \frac{N}{32^n} \quad (\text{HZ})$$

Where N=Base clock frequency (3.579MHz)  
 n=10 bit binary number (F0 - F9)

## 2. Base System

The following is an example of obtaining a 440HZ frequency:

$$440 = \frac{3579000}{32n}$$

$$n = 254$$

Bit positions of F9 - F0 are 0 0 1 1 1 1 1 1 0.

The attenuator allows 15 stages of sound volume, ranging from 0dB to 28dB, based on the 1 byte of data issued from the CPU.

Data Format (1 byte transfer)

MSB				LSB			
Register			Data				
1	R2	R1	R0	A3	A2	A1	A0

(1) Register is selected by R0 - R3.

R2	R1	R0	Register Assignment
0	0	1	Tone 1 Attenuator
0	1	1	Tone 2 Attenuator
1	0	1	Tone 3 Attenuator

(2) Sound volume is set every 2dB each by bits A0 - A3.

dB	A3	A2	A1	A0	Hex	dB	A3	A2	A1	A0	Hex
0	0	0	0	0	0	16	1	0	0	0	0
2	0	0	0	1	1	18	1	0	0	1	9
4	0	0	1	0	2	20	1	0	1	0	A
6	0	0	1	1	3	24	1	0	1	1	B
8	0	1	0	0	4	26	1	1	0	0	C
10	0	1	0	1	5	26	1	1	0	1	D
12	0	1	1	0	6	28	1	1	1	0	E
14	0	1	1	1	7	OFF	1	1	1	1	F



## Noise Generator

The noise Generator consists of:

- Noise Source
- Attenuator

and can control the sound tone. (NF0:1, NF1:1 mode)

The noise source is a shift register with an exclusive -OR feedback-network. The feedback network has provisions to protect the shift register from being locked in the zero state (periodic noise).

Data Format ( 1 byte transfer)

MSB	LSB						
1	R2	R1	R0	x	FB	NF1	NF0

- (1) Register is selected by R0 - R2

R2	R1	R0	Register Assignment
1	1	0	Noise control
1	1	1	Noise Attenuator

- (2) The kind of noise is selected by the bit FB

<u>FB</u>	<u>Configuration</u>
0	"Periodic" Noise
1	"White" Noise

- (3) Shift rate is set by NF1 and NF0

NF1	NF0	Shift rate
0	0	N/512
0	1	N/1024
1	0	N/2048
1	1	Tone 3 frequency output

- (4) Attenuator control is the same as the Tone Generator.

## 2. Base System

### Audio Mixer

The audio mixer sums the three tone-generator outputs and the noise generator output. The output buffer will generate up to 10mA.

### Hardware Interface



Pin No.	Signal Name
1	GND
2	AUDIO

Figure 2-21 Audio Interface Connector (J11)

#### 2.2.10. Beep Subsystem

The system beeper is a small piezoelectric speaker, which can be driven from one or two sound sources. The two sound sources are as follows:

- 8255A PPI (PB4) Output
- 8253-5 Timer 2 Output (Timer clock)

The input clock for this timer is 1.19MHz.

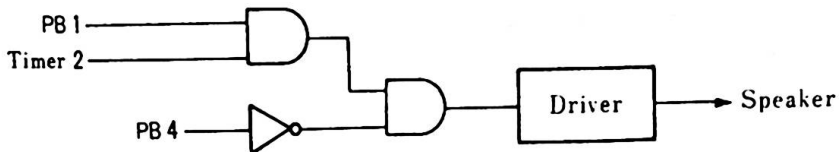


Figure 2-22 Beep Circuit

## 2.2.11. Keyboard Interface

The infrared link or keyboard cable provides communications between the keyboard and the system unit.

After the system unit reads serially encoded keyboard data from PC6, the software de-serializes them using the 8253 Timer 1.

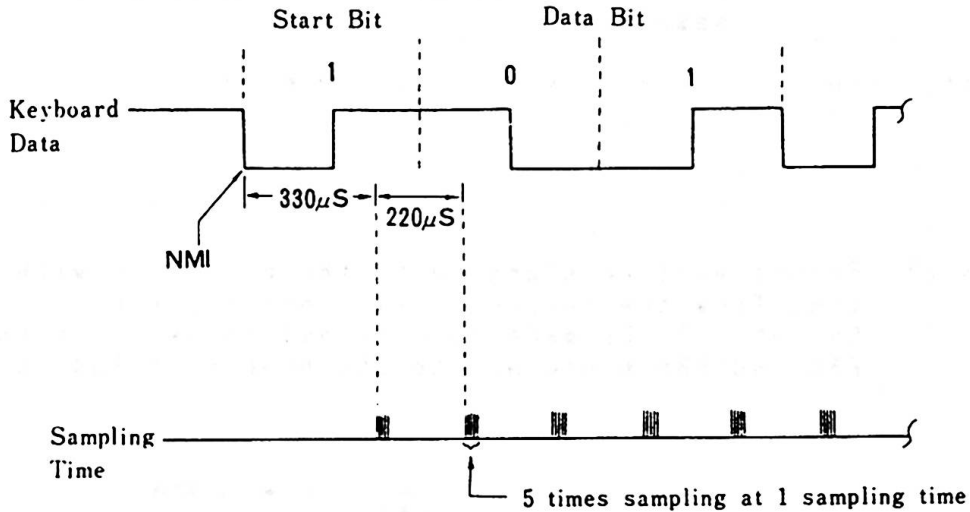


Figure 2-23 Sampling

The leading edge of the start bit will generate a non-maskable interrupt (NMI). Once the processor enters the NMI routine to handle the deserialization, the keyboard data line is sampled and the processor is waiting to sample the trailing edge of the start bit. When the trailing edge of the start bit is sampled, the processor will wait for 330 micro-sec and sample the first half of the first data bit. The processor then samples the keyboard data every half bit cell-time. The sampling interval is 220 micro-sec. The processor samples each half bit sample 5 times and will determine the logical level of the sample by majority rule. This enables the processor to discriminate against transient glitches and to filter out noise.

## 2. Base System

### Detectable Error Conditions

The software checks whether the received data has an error or not. There are two kinds of errors, Phase and Parity errors.

Error	Cause
Phase Errors	The 1st half of the bit-cell sample is not equal to the inverse of the 2nd half of the bit-cell sample.
Parity Errors	The received encoded word did not maintain odd parity.

Remark) Errors will be signaled by the processor with a short tone from the beeper or external speaker. Output will be made to external speaker, if both of PB5 and PB6 are 0 and to the beeper if PB4 is 0.

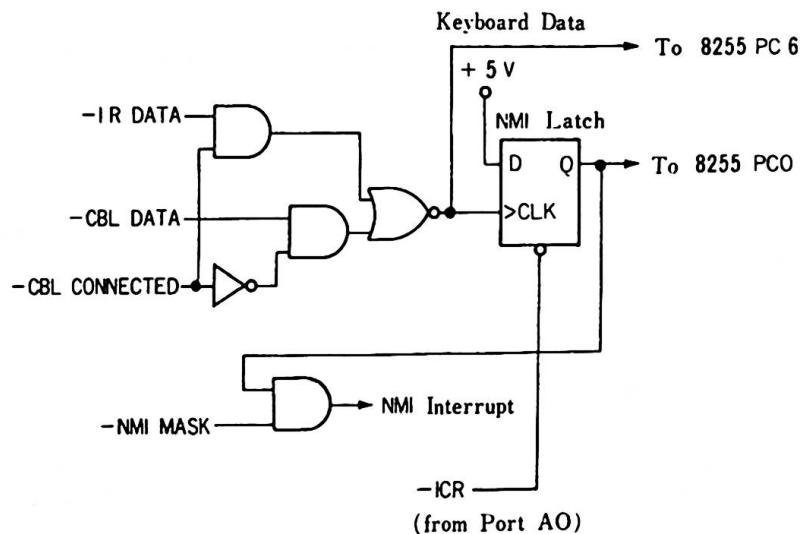


Figure 2-24 Keyboard Interface Block Diagram

## Operational Parameters

The operational distance from infrared devices to the system is 5 meters (line of sight). Operational efficiency can be impaired by outside sources. Those sources are, excessively bright lights and high voltage lines. A keyboard cable is recommended for those instances.

### Port A0

Port A0 is strongly related to the keyboard data handling and is described here. When a key is depressed, a serialized scan code is sent to the system unit. The latch causes an NMI on the first leading edge of the keyboard data if the enable NMI bit (port A0 bit D7) is on. The 8088 then reads the 8253 timer to determine when to interrogate the serial stream. Depending on whether the bit is on or off, 1 character of data is deserialized and the NMI Interrupt service routine resets the NMI latch to read the next NMI interrupt. The NMI latch is cleared by reading I/O port A0(Hex). As the latch can also be read on the 8255 PC0, the program can determine if a keystroke occurred while NMI was disabled by reading the status latch. For instance, during certain critical operations such as diskette I/O, the processor will mask off the NMI interrupt. As the keyboard input during this time cannot be serviced, the NMI latch should be checked later and the appropriate action should be taken. The system board provides for selection of keyboard data from either the cable or the IR receiver board. When the signal level -CBL CONNECTED on the keyboard cable connector is "0", data are read from the cable and when "1", from the IR receiver board. The 8088 processor uses one 8255 PPI bit (PC6) to do the software de-serialization of the keyboard.

## 2. Base System

### Infrared Receiver

The infrared receiver is located in the system unit and has an infrared sensitive device that demodulates the signal transmitted from the keyboard and sends it to the system. The infrared sensitive device is located on the front of the card and receives its input through an opening in the front of the system unit box. There is also an infrared transmitter mounted on the receiver card for diagnostic purposes.

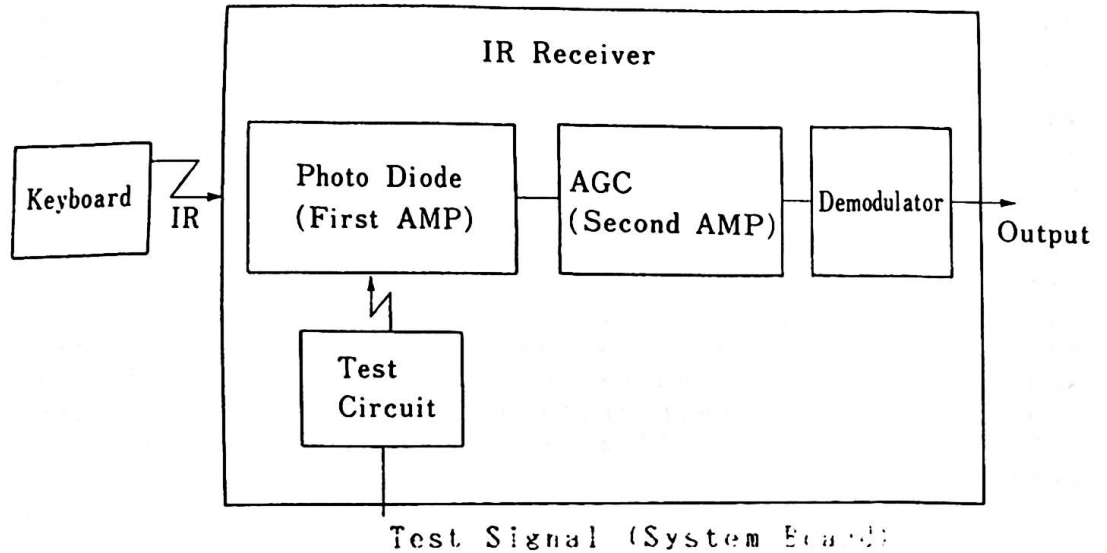


Figure 2-25 IR Block Diagram

The infrared receiver is mounted on the system board with a 5-pin connector.

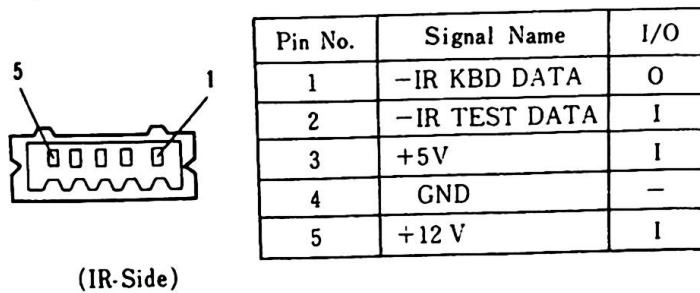


Figure 2-26 IR Connector specifications (J7)

## Keyboard Cable Interface

The keyboard cable connects to the system unit connector J16. The -CBL CONNECTED signal notifies the system unit that the cable is connected. The system unit's IR receiver circuit is "disabled" by this signal. When the keyboard cable is used, the power is supplied from the system unit.

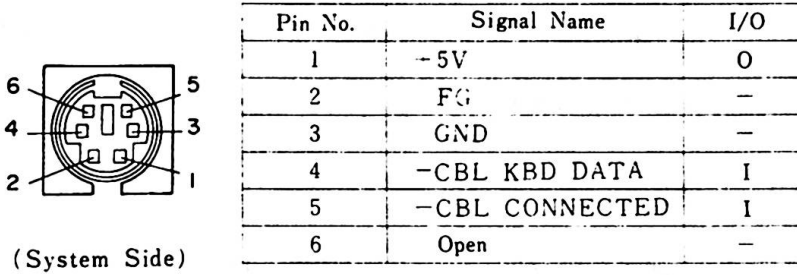


Figure 2-27 Keyboard Cable Connector (J16)

## 2. Base System

### 2.2.12. Joystick Interface

Interface connectors are provided for two joysticks. The I/O address is common and is 201 (Hex). When the data are read from the joystick, the following data are sent on the data bus:

Joystick 2		Joystick 1		Joystick 2		Joystick 1	
Switch # 2	Switch # 1	Switch # 2	Switch # 1	Coordinate Y	Coordinate X	Coordinate Y	Coordinate X
Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Switch Input				Resistor Input			

#### Switch Input

Switch input is shown in data bits 7,6 (or 5,4) when I/O address 201 (Hex) is read. Each of the four switch inputs has a 1K ohm pull up resistor connected to +5V. When the button is depressed it is read as 0 and when the button is not depressed, it is read as 1.



## Resistor Input

The joystick position is indicated by X and Y coordinates which are set by the two (for 1 joystick) variable resistors. When software writes data (dummy) in I/O address 201 (Hex), the hardware issues two kinds of pulses in accordance with the resistor value. This pulse output is shown in data bit 3,2 (or 1,0) when I/O address 201(Hex) is read. The software can determine the coordinate value based on this.

The width of the pulse is calculated by the following formula:

$$\text{Time} = 24.2 \text{ micro sec} + 0.011 \times R \text{ micro sec}$$

where R is the resistance in ohms.

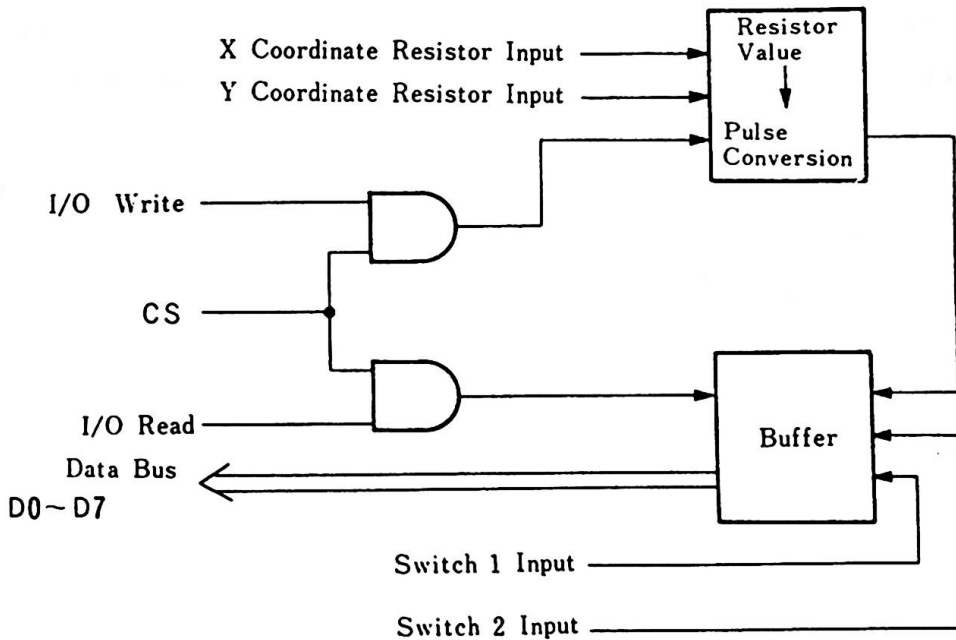
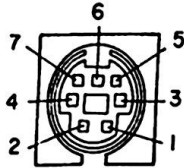


Figure 2-28 Joystick Interface Block Diagram

## 2. Base System

### Hardware Interface

The joystick interface has two connectors in the rear of the system unit to connect two joysticks.



(System Side)

Pin No.	Signal Name	I/O
1	SW 1	I
2	SW 2	I
3	RX	I
4	RY	I
5	+5V	O
6	GND	-
7	Open	-

Figure 2-29 Joystick Interface Connector (J13, J14)

## 2.2.13. ROM Cartridge

When a ROM cartridge is inserted into either one of two identical slots in the front of the machine, ROM is added to the system unit or is replaced.

Each cartridge can hold up to 96 KB ROM. Cartridge selection is accomplished by the chip selects, each of which addresses one of the high 32KB memory blocks. Each cartridge uses up to three of the six chip selects and the selection is determined based on the intended use of the cartridge.

The ROM modules used are 250 ns access time devices. Typical modules are the Mostek MK37000, TMM23256, SY23128 or equivalent.

## Cartridge ROM Storage Allocations

Address map of system ROM and ROM cartridge is as follows:

Address	System ROM		ROM Cartridge	
	Chip Select	Contents	Chip Select	Contents
D0000~D7FFF	—	—	EROM 2	Application Cartridge
D8000~DFFFF			EROM 3	
E0000~E7FFF		BASIC	EROM 4	
E8000~EFFFF	IROM 7		EROM 5	Special Cartridge
F0000~F7FFF		Kanji Dictionary	EROM 6	
F8000~FFFFF		BIOS	EROM 7	

Figure 2-30 Cartridge ROM Chip Select

- The system ROM on the board is selected by chip select IROM 7.
- System ROM address space is E0000 - FFFFF (Hex).
- Some portions of the address space for ROM cartridges will be used by the system ROM and some will not.

## 2. Base System

- Normally, application cartridges should use EROM 2 and EROM (D0000 - DFFFF).
- When EROM 4 - EROM 7 (E0000 - FFFFF) whose address space is shared by the system ROM, is used, addressing must be done carefully.
- EROM 4 and 5 are used for Language compilers which will not compete with BASIC.
- When EROM 6,7 are used for replacement of BIOS, the system characteristics can be changed. In this case, BASE 1 ROM (A04 Pin) or BASE 2 ROM (A09 Pin) should be wired to GND GND (A01 Pin) to notify the system.
- When EROM 4 - 7 chip selects which will compete with the system ROM is used, ROM space allocation of the system ROM will be as follows:
  1. F0000 - FFFFF (64KB)
  2. F8000 - FFFFF (32KB)
  3. All area deletion
- This change of ROM space allocation is made at the time of self-diagnostic test right after the system power-on or system reset.
- When two cartridges of different modes are used in combination, "Error L" will be displayed. To continue, the carriage return key should be pressed.

There are some ground rules to follow in making application cartridges. The major rules are:

### Conventions

- (1) These conventions should be followed for "Initial Program Load-able" program cartridges. These cartridges should include:

Location	Contents
0, 1	55AA (Hex) : For English Mode AA55 (Hex) : For Native and Extension Video Mode
2	Length
3,4,5	Jump to initializing code
6	00 (Hex)
Last 2 bytes	CRC Bytes

- Locations 0 and 1 are used to check whether there is a cartridge inserted during self-diagnostic test. 55AA (Hex) or AA55 (Hex) indicates that a cartridge is inserted.
- Location 2 contains a length indicator representing the entire address space taken by the ROM on the cartridge. The algorithm for determining the contents of this byte is (ROM data size/512). The contents of this byte are used by the CRC check routine to determine how much ROM to check. Address space for a ROM cartridge is on a 2048 byte boundary.
- Locations 3, 4, and 5 contain a Jump call which is used to jump during an initialization routine. For cartridge programs that are "IPL-able" (Basic or Assembler programs), this routine should set the INT 18 vector to point to their entry points. Other types of cartridges should merely return to the caller. Setting the INT 18 (Hex) vector will enable transfer of control to the cartridge program by the IPL routine.
- Location 6 should be 00.
- CRC byte: The last two locations of the address space used by the cartridge must be blank. CRC characters will be placed in these bytes when the cartridge is built. For the CRC algorithm, refer to the routine at label "CRC Check" in the BIOS listing.

2. Base System

- (2) For cartridges which include DOS command words and run under DOS, the following conventions should be followed:

Location	Contents
0, 1	55AA (Hex) : For English Mode AA55 (Hex) : For Native and Extension Video Mode
2	Length
3-5	Jump to initializing code
6	The length of the command word
Z	Command name
Y	Location of jump, when command name is typed
X	The length of the next command word, or 00(Hex) when no more command words exist.
Last 2 bytes	CRC Bytes

- Locations 0 and 1 are used to check whether there is a cartridge inserted during self-diagnostic test. 55AA (Hex) or AA55 (Hex) indicates that a cartridge is inserted.
- Location 2 contains a length indicator representing the entire address space taken by the ROM on the cartridge. The algorithm for determining the contents of this byte is (ROM data size/512). The contents of this byte are used by the CRC check routine to determine how much ROM to check. Address space for a ROM cartridge is on a 2048 byte boundary.
- Location 3 contains a Jump call which is used to jump during an initialization routine. (There might be instances where only FAR RETURN call exists.)
- Locations 6 - Y are command name pointers. If a cartridge has a routine called "Test" at location 0FB5 (Hex. offset from the start of the segment that the cartridge is in) that needs to be executed when "Test" is entered as a DOS command, the entries (in Hex) at locations 6 - Y would be:

Location 6      04 (4 byte length command name)  
 Location Z      54(T), 45(E), 53(S), 54(T) T E S T  
 Location Y      B50F (offset ; 0FB5)

Until the count field changes to 00, which occurs after the last name pointer (6-Y), the next name pointer will be set.

5. CRC byte: The last two locations of the address space used by the cartridge must be blank. CRC characters will be placed in these bytes when the cartridge is built. For the CRC algorithm, refer to the routine at label "CRC Check" in the BIOS listing.

(3) The cartridge chip selects should specify EROM2 or EROM3, when the cartridge is designed to run application programs which are written in the Basic language, because the Basic Interpreter addresses E0000 (Hex). When the Basic Interpreter is activated, a check is made to see if there is a cartridge application at D0000 (Hex). When there is a cartridge application and its format is correct, the application is loaded into RAM and run.

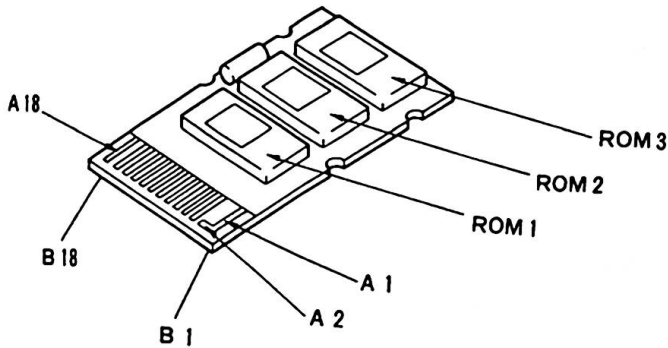
The format for interpretable BASIC code must be as follows:

Location	Contents
0, 1	55AA (Hex) : For English Mode AA55 (Hex) : For Native and Extension Video Mode
2	Length
3	CB (Hex)
4	AA (Hex)
5	55 (Hex)
6	00 (Hex)
7	FF (Hex) for unprotected Basic Program FE (Hex) for protected Basic Program
8	Starting address of Basic Source Code
n	FF (Hex). Padding to next 2048 byte boundary
Last 2 bytes	CRC Bytes

- Locations 0 and 1 are used to check whether there is a cartridge inserted during self-diagnostic test. 55AA (Hex) or AA55 (Hex) indicates that a cartridge is inserted.
- Location 2 contains a length indicator representing the entire address space taken by the ROM on the cartridge. The algorithm for determining the contents of this byte is (ROM data size/512). The contents of this byte are used by the CRC check routine to determine how much ROM to check. Address space for the ROM cartridge is on a 2048 byte boundary.

2. Base System

3. Location 3 must be CB (Hex. Far Return instruction).
4. Locations 4 and 5 contain the word AA55 (Hex). This is used by the Basic Interpreter to check for the presence of a Basic Application cartridge.
5. Location 6 must be 00.
6. Location 7 can either FF (Hex) to indicate an unprotected Basic Program, or FE (Hex) to indicate a protected program.
7. Location 8 must be the start of the Basic Application program.
8. CRC byte : The last two locations of the address space used by the cartridge must be blank. CRC characters will be placed in these bytes when the cartridge is built.



ROM No	Address	Chip Select
ROM 3	D8000~DFFFF	EROM 3
ROM 2	E0000~E7000	EROM 4
ROM 1	E8000~EFFFF	EROM 5

Figure 2-31 ROM Cartridge



## Hardware Interface

There are two slots for ROM cartridge insertion and they both have the same function. The following is a description of pin allocations and signals:

Signal Name	Pin (A)	Pin (B)	Signal Name
GND	A 1	B 1	-CTWR
-CARTRIDGE RESET	2	2	-EROM7
-EROM 5	3	3	-EROM3
-BASE 1 ROM	4	4	A14
A13	5	5	A12
A8	6	6	A7
A9	7	7	A6
A11	8	8	A5
-BASE 2 ROM	9	9	A4
A10	10	10	A3
D7	11	11	A2
D6	12	12	A1
D5	13	13	A0
D4	14	14	D0
D3	15	15	D1
-EROM2	16	16	D2
-EROM4	17	17	-EROM6
+5V	18	18	Open

Figure 2-32 ROM Cartridge Connector (J1,J2)

## 2. Base System

- Remarks) 1. I/O is referred to as "System-side" view.  
2. Each signal is at a standard TTL level.

<u>Signal</u>	<u>I/O</u>	<u>Description</u>
A0 - A14	O	Processor Address line A0 - A14
D0 - D7	I	Processor Data lines
-EROM2 - 7	O	These chip select lines are used to select ROS modules.
-BASE 1 ROM	I	When wired to A01 pin (GND), -EROM7 is made available. Addresses F8000 - FFFFF (Hex) are used by the ROM cartridge.
-BASE 2 ROM	I	When wired to A01 pin (GND), -EROM6 is made available. Addresses F0000 - F7FFF (Hex) are used by the ROM cartridge.
-CARTRIDGE RESET	I	This input when "low" causes a reset to the system. The system will remain reset until this line is brought back to "high". This tab is usually wired with an L shaped land pattern to the GND at A02 which provides a momentary "reset" when a cartridge is inserted or removed
-CTWR	O	Memory Write Signal (open collector).

2.2.14. Printer Interface

Centronics specifications for an 8 bit parallel interface are provided.

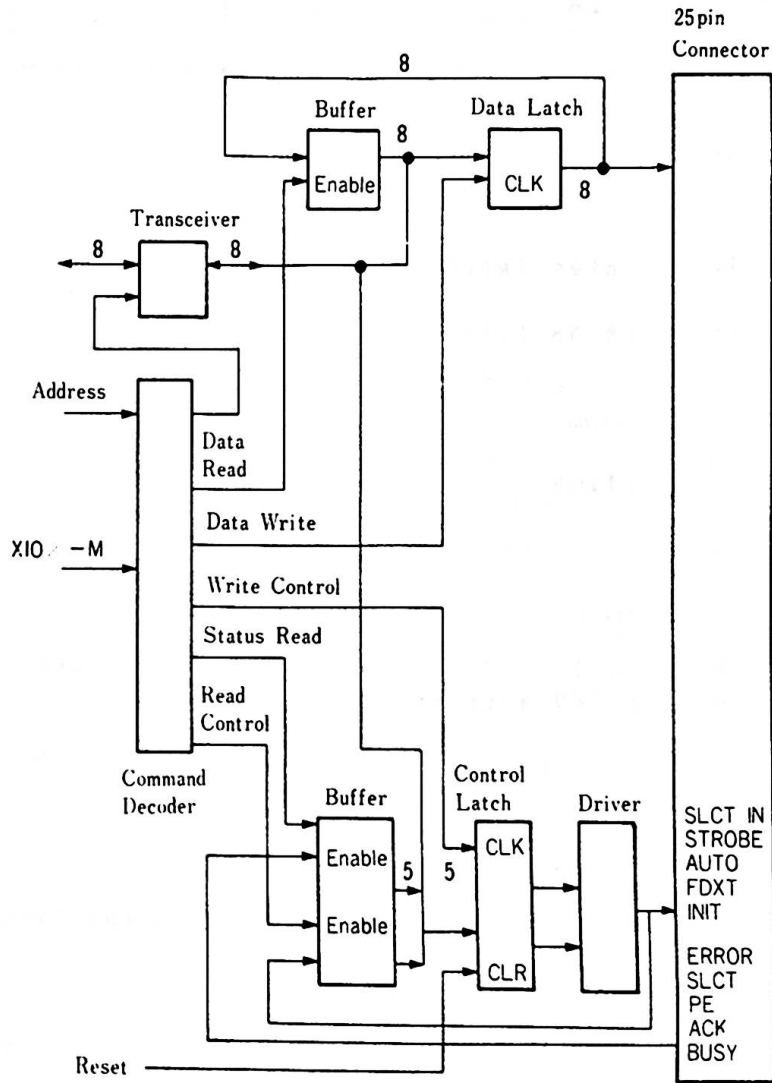


Figure 2-33 Printer Interface Block Diagram

## 2. Base System

The interrupt level is 7. To make it possible to interrupt, control latch bit 4 should be changed to 1.

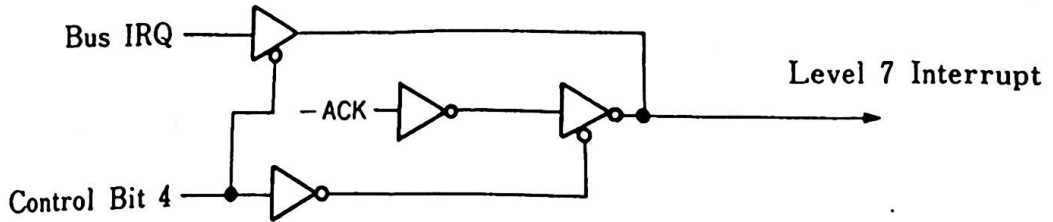


Figure 2-34 Printer Interrupt

I/O Addresses are as follows:

I/O Address (Hex)	Allocation
378.37C	Data Latch
379.37D	Status
37A.37E	Control Latch
37B.37F	Reserved

Remarks) Address line A2 is not decoded and therefore either one of two I/O addresses are to be used.

The following descriptions apply when the IBM 5513 Printer is connected with the printer interface:

Data Latch (I/O address 378 or 37C)

Printer output data are temporarily kept in the Data Latch.

Status (I/O address 379 or 37D)

Input data read at I/O address 379 or 37D contain the following printer status:

Bit 7 (BUSY): When "0", a printer cannot accept data. Following are instances where "0" is set:

1. Data Transfer
2. Printing
3. An error occurs

Bit 6 (ACK): When the printer finishes accepting data, it will return the signal -ACK to notify the system that it got ready to accept the next data.

Bit 5 (PE): When the printer paper runs out, "1" is set.

Bit 4 (SELECT): When the printer is selected, "1" is set.

Bit 3 (ERROR): When a printer error occurs, "0" is set.

Bit 2 (KJ-CD): When "1", connection to an Image Printer is assume.

Bit 1,0: Open

Control Latch (address 37A or 37E) contains the printer control signal.

Bit 7, 6, 5 : Open

Bit 4 (INTR): When "1", an interrupt is possible (Level 7).

Bit 3 (SELECT IN): When "1", it is possible to control the printer from the CPU.

Bit 2 (INIT): When "0", the printer is initialized. (More than 50 microseconds is required.)

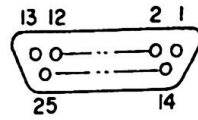
Bit 1 (AUTO FD): When "1", a line feed is performed after each line is printed.

Bit 0 (STROBE): 5 microsecond pulse (level "1") allows sending of data for this duration.

## 2. Base System

### Hardware Interface

The connector used is a D shaped connector and the signal level is at standard TTL.



(System Side)

Pin No.	Signal Name	I/O
1	-STROBE	0
2-9	DATA 0-7	0
10	-ACK	1
11	BUSY	1
12	PE	1
13	SLCT	1
14	-AUTO FD	0
15	-ERROR	1
16	-INIT	0
17	-SLCT IN	0
18	KJ-CD	1
19-25	GND	—

Figure 2-35 Printer Interface Connector (J12)

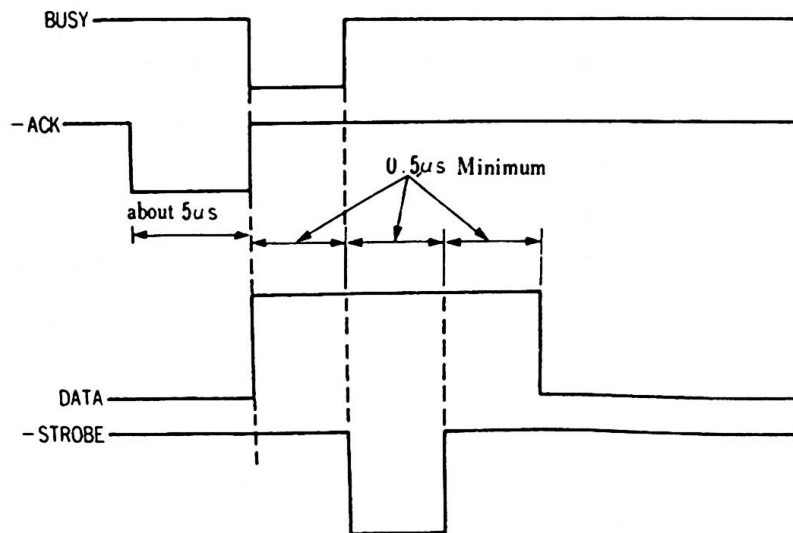


Figure 2-36 Timing Chart

2.3. Keyboard

The cordless keyboard is battery powered and interfaces to the system unit with an infrared (IR) optical link. There are two types of keyboards. One is a 83-key (Compact Keyboard) and the other is a 102-key (Full Keyboard). The keys are arranged in a standard typewriter layout with the addition of function keys and cursor keys. All keys are typamatic keys. (When a key is pressed for more than 0.6 seconds, that key code is repeatedly sent out at a speed of 11 characters per second.) The microprocessor in the keyboard is normally in a standby-power-down mode until a key is depressed. When an optional keyboard cable is used, power is supplied from the system unit and signals are transmitted via the cable.

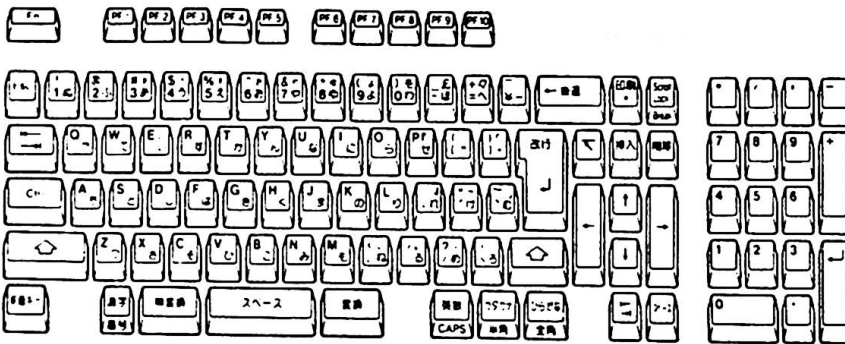


Figure 2-37 Full Keyboard

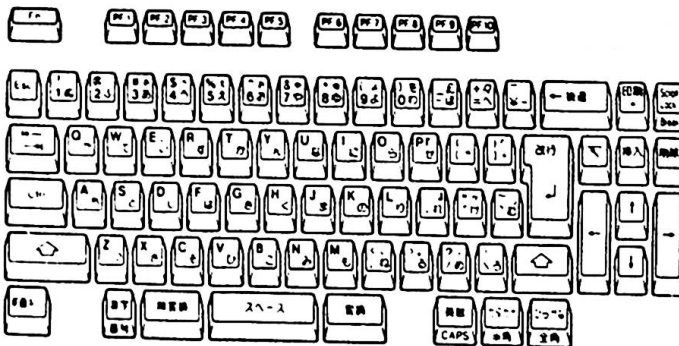


Figure 2-38 Compact Keyboard

## 2. Base System

### Transmitter

Serially encoded scan codes are transmitted to the system unit with a bit cell of 440 micro-sec. (Odd parity) After each scan code is transmitted, they are added with 11 stop bits. When infrared link is used, logical 1 contains a 40 KHz (50 % duty ratio) carrier burst signals.

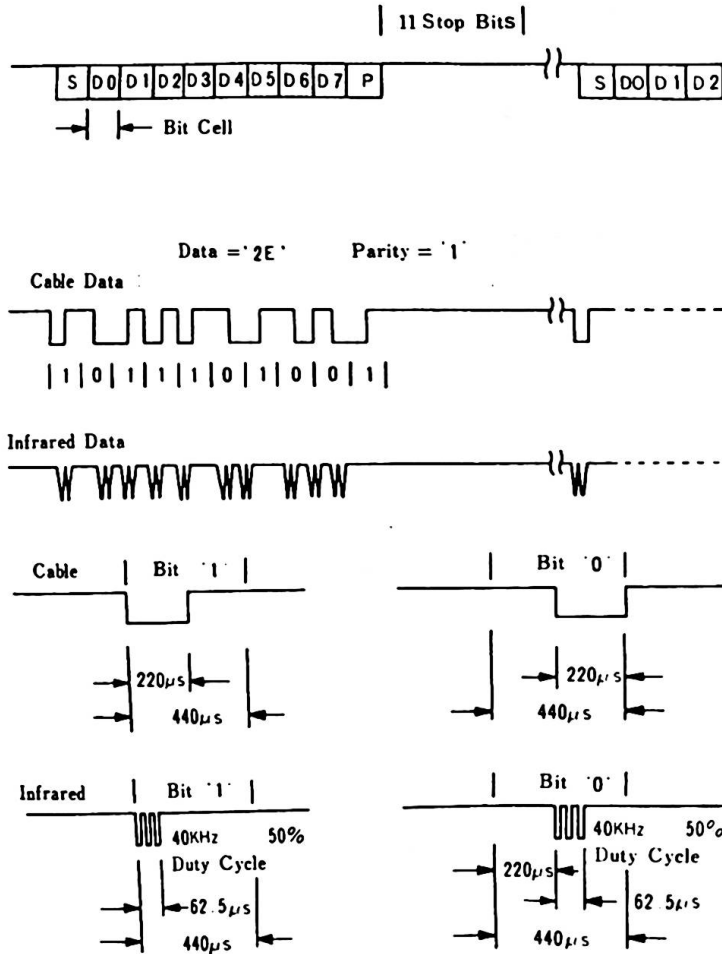


Figure 2-39 Keyboard Data Transmission Format



## Micro Processor

The keyboard contains a microprocessor and its functions are as follows:

- Suppress Chattering
- Scan Code Conversion
- Typamatic
- N Key Roll-over
- Simultaneous Keystroke check
- From parallel to serialized Scan Code Conversion
- Biphase Modulation

When keys are not pressed, the keyboard is in a standby-power down mode.

## Scan Code

Each key is assigned a scan code and when a key is pressed or released, serially encoded codes are sent to the system unit. The scan code when a key is released, is obtained by adding 80 (Hex) to the scan code when a key is pressed. For instance, the scan code of "ESC" when pressed is "01" and is "81" when released. (Reference : Appendix C)

## Error Detect

The keyboard has an N Key Roll-over function. Every combination is possible at  $N = 2$  and when at  $N = 3$ , more than 95 % will be correctly processed. When Keys of unavailable combination are pressed, scan code 55 (Hex) is sent to the system unit as a phantom key and notifies it that the keystrokes are in error. In this case nothing will appear on the display or incorrect words will be displayed.

## 2. Base System

### 2.4. Power Unit

The power unit resides in the system box and provides direct current power to a system board, a diskette drive and cooling fan. The output is +5V, +12V, -12V.

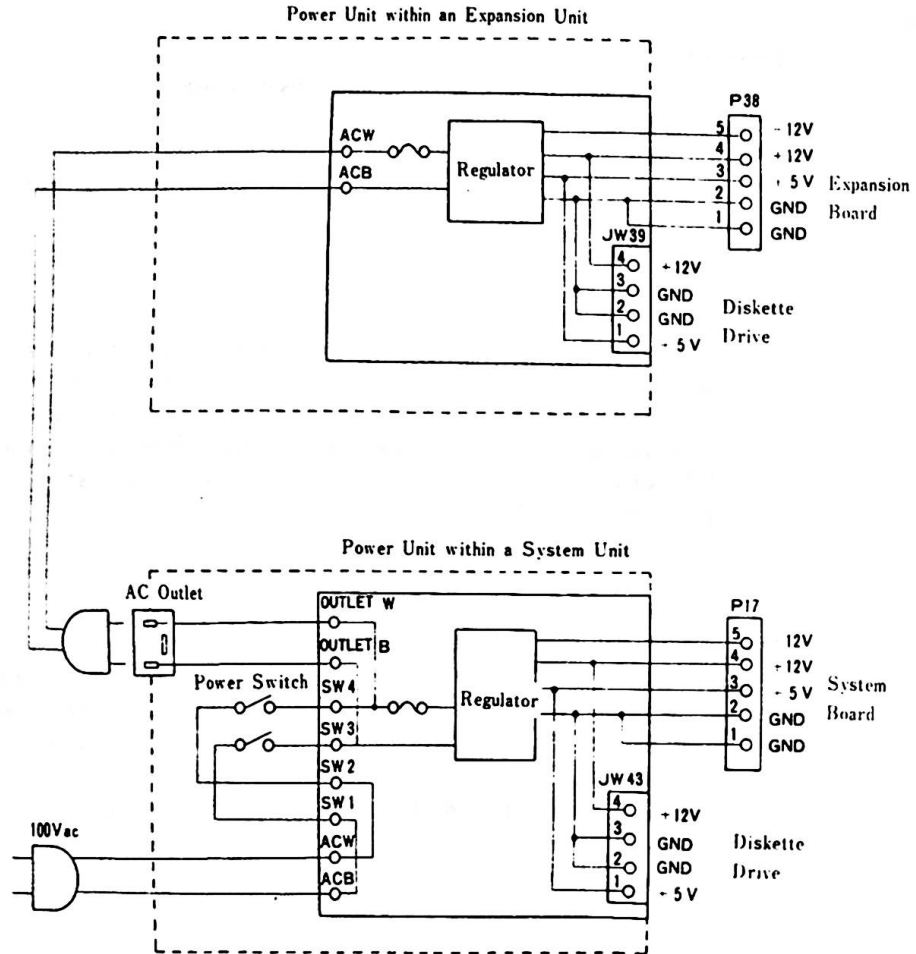


Figure 2-40 Power Unit Logic Diagram

**Power Specifications**

Voltage	Rating Current	Maximum Current	Ripple	Variance
+ 5 V	3.6 A	5 A	100mV	$\pm 5\%$
+12V	0.8 A	1.15 A	150mV	$\pm 5\%$
-12V	0.05A	0.05 A	100mV	$\pm 5\%$



### 3. Video Subsystem

The video subsystem allows the IBM color display, a wide variety of television-frequency monitors, or ordinary home TV sets to display characters and graphics. It can operate in black-and-white as well as in color mode and provides a lightpen interface. In this chapter, an overall description of the VSS is mentioned first, followed by descriptions of the functions of the three video processors (VP1, VP2 and VP3).

### 3. Video Subsystem

#### 3.1. Introduction

The video subsystem is implemented using a CRT controller and a video gate array. It contains three video processors.

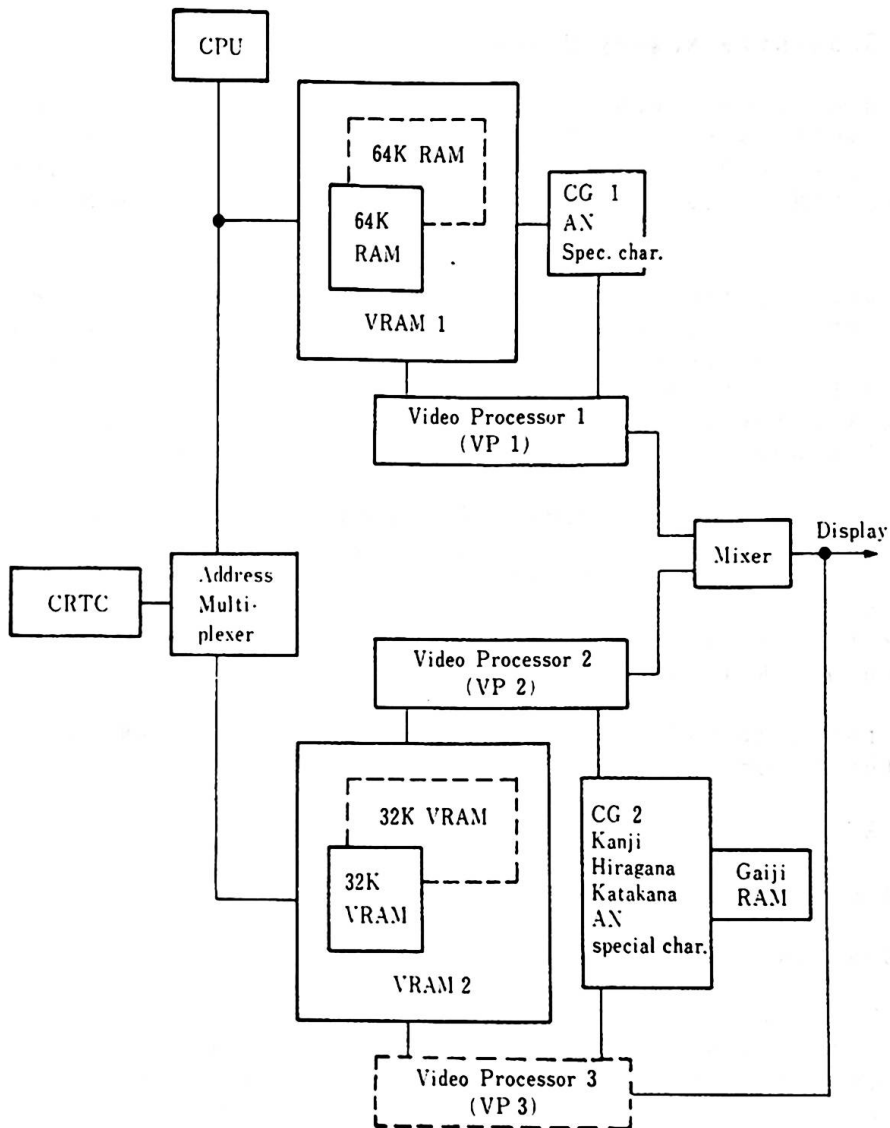
Video processor 1 (VP1) is used both in English and in Native mode. It can process alphanumerics and special characters (8 x 8 dot) which are compatible with PCjr. It can also process a maximum of 640 x 200 dot 4 color graphics.

Video processor 2 (VP2) is used in Native mode. It can process Kanji, Hiragana, full and half-size Katakana characters, alphanumerics, special characters and a maximum of 640 x 200 dot 4 color graphics.

Video processor 3 (VP3) is contained in an optional Extension Video Card and is used in Extension Video mode. It can process Kanji, Hiragana, full and half-size Katakana characters, alpha-numerics, special characters, grid lines which are compatible with the IBM Multistation 5550 and a maximum of 720 x 512 dot two color graphics.

There are four video frequencies available depending on operational mode. They are as follows:

3.5 MHz	:	160 x 200 dot
7 MHz	:	320 x 200 dot
14 MHz	:	640 x 200 dot
20 MHz	:	720 x 512 dot



- Remarks) 1. CG : Character Generator  
 2. --- : Option  
 3. AN : Alphanumerics

Figure 3-1 Video Subsystem Block Diagram

### 3. Video Subsystem

#### 3.2. Video Subsystem Memory Usage

The base video color/graphics subsystem accesses 64K bytes of system read/write memory (RAM) and 32K bytes of video RAM. When an optional RAM card (64 KB) and an Extension Video Card (32 KB video RAM included) are installed, video RAM space is expanded.

The memory used by the VSS is separated into VRAM 1 and VRAM 2. VRAM 1 consists of 64 KB memory on the system board which is provided as a standard feature and 64 KB of expanded memory. VRAM 2 is 64 KB of video RAM (32 KB of expanded memory). The reading/writing of video RAM is usually performed by first specifying the page and by using the virtual address.

If an additional 128 KB of memory is added, it can not be accessed by the VSS and cannot be used as video RAM. When 192KB of expanded memory (one 64KB RAM card and one 128KB RAM card) is added, the address space of the 128KB expansion comes first, followed by that of the 64KB on the system board and the 64KB expansion card (Reference: 5.5 Memory Map).

The memory for displaying screens by the VSS (video RAM) uses the following three concepts:

- Virtual Address
- Page
- Page Register

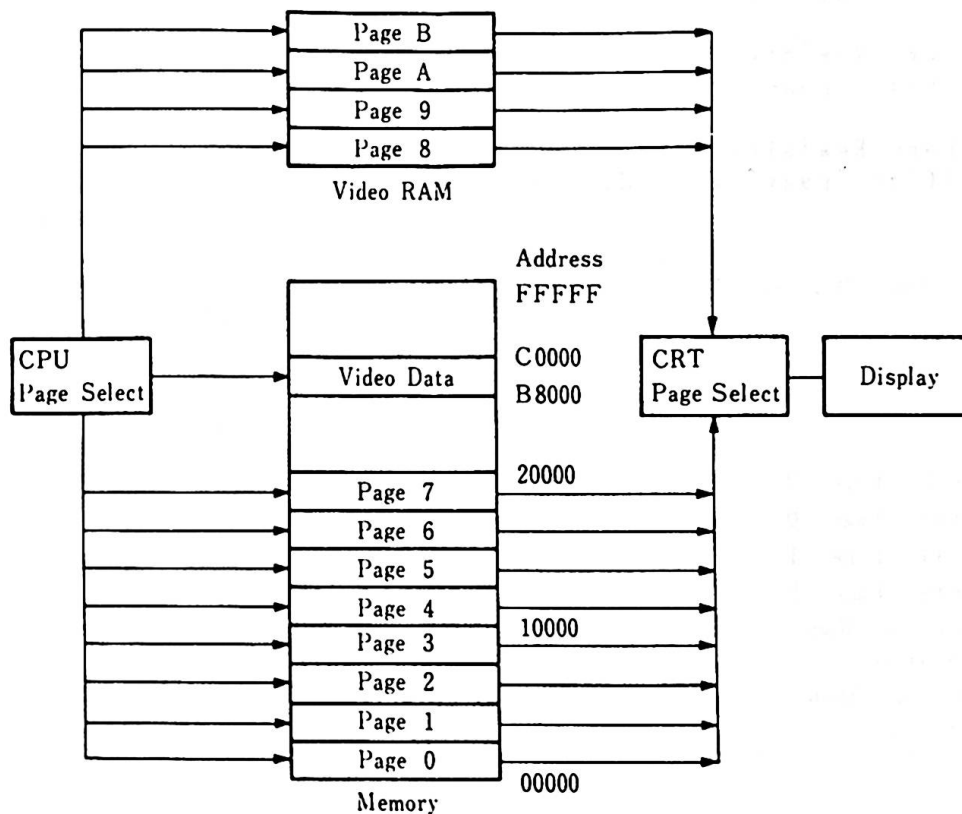
#### Virtual Addresses

B8000 - BFFFF (Hex) are virtual addresses for VP1 and VP2. A0000-AFFFF (Hex) are virtual address for VP3. The memory for these addresses does not physically exist. When a data read/write is performed from the CPU to these addresses, the read/write is performed on the pages (any of 0 - B) which are specified by the CPU page register.

#### Page

A page is a memory block separated into 16 KB units. It is part of a total of 192 KB memory which consists of 64 KB of base memory and 64 KB of expanded memory, making total of 128 KB (VRAM 1) plus 64 KB of Video RAM (VRAM 2). One screen is displayed with 1, 2 or 4 pages. The number of pages necessary to display a screen depends on the number of colors and the display resolution. Four pages are necessary to display a 720 x 512 dot 2 color or 360 x 512 dot 4 color screen.





Remarks) The memory addresses shown above are correct for the case where only 64KB of expanded memory is installed. (Reference : 5.5 Memory Map)

Figure 3-2 Video Memory Map

### 3. Video Subsystem

#### Page Register

Pages 0-7 are allocated for VRAM 1, while pages 8-B are for VRAM 2. The pages are selected by the page register. The I/O address for VRAM 1 is 3DF (Hex) and for VRAM 2 is 3D9 (Hex). Both are write-only 8 bit registers. There are two kinds of page registers, the CPU Page Register and the CRT Page Register.

- CPU Page Register specifies "page" for read/write from CPU to video RAM.
- CRT Page Register specifies "page" for display on the screen.

Page Register 1 (I/O address 3DF)			Page Register 2 (I/O address 3D9)		
Bit	Meaning	Remarks	Bit	Meaning	Remarks
0	CRT Page 0	} CRT Page 0-7 specified	0	CRT Page 0	} CRT Page 8-B specified
1	CRT Page 1		1	CRT Page 1	
2	CRT Page 2		2	Open	
3	CPU Page 0	} CPU Page 0-7 specified	3	CPU Page 0	} CPU Page 8-B specified
4	CPU Page 1		4	CPU Page 1	
5	CPU Page 2		5	Open	
6	Display Mode Selection		6	Open	
7	Display Mode Selection		7	Open	

Figure 3-3 Page Register

**Bits 0 - 2**

Which page in VRAM 1 (16 KB/page) to display is determined by these three bits in page register 1.

Which page in VRAM 2 (16 KB/page) to display is determined by these three bits in page register 2.

**Bits 3 - 5**

These three bits select the page for the data transfer in case the data reside at virtual addresses B8000-BFFFF (Hex). In Extension Video mode, bits 3 and 4 should be set to "0" and virtual addresses of A0000-AFFFF (Hex) are used.

**Bit 6, 7 (Only for VP 1)**

VP1 display mode is selected.

Bit 7, Bit 6	Meaning
0 0	All text modes
0 1	Low resolution graphics mode
1 0	Open
1 1	High resolution graphics mode

**Low Resolution Graphics Modes:**

- 160 x 200 dots 16 colors
- 320 x 200 dots 4 colors
- 640 x 200 dots 2 colors

**High Resolution Graphics Modes:**

- 320 x 200 dots 16 colors
- 640 x 200 dots 4 colors

The CPU page register allows pages other than that being displayed to be selected and to be read/written. A change of the contents of the CPU register does not affect the displaying screen, but when the contents of the CRT page register are changed, the screen being displayed will change. Page changes are performed by the CRT page register by BIOS at the time of vertical line retrace of the display raster. Because of this, you can use page changes effectively to achieve an animation effect without garbling the screen.

(Reference: 2.2.6 Memory Space and I/O Address Setting)

### 3. Video Subsystem

#### 3.3. Character Generator

The Video Subsystem has two different character generators: Character Generator 1 (CG1) and Character Generator 2 (CG2).

##### 3.3.1. Character Generator 1 (CG1)

CG1 consists of 2 KB of storage, for alphanumerics and special characters and is used only by VP1. One character consists of 8 x 8 dots. CG1 has a 350 ns access time/350 ns cycle-time. An NM2364 (or equivalent) is used.

The kinds of characters contained in CG1 are as follows:

- 16 special characters for games
- 15 characters for word processor editing
- 96 ASCII graphic characters
- 48 English characters
- 48 graphic characters for business use
- 16 symbols
- 15 scientific characters

##### 3.3.2. Character Generator 2 (CG2)

CG2, used by VP2 and VP3, uses 128 KB of ROM. The following are its characteristics:

1. The IBM 5510 uses the JIS Level 1 Kanji character set. CG2 contains Hankaku (half-size) characters of 8 x 16 dots such as alphanumerics, special characters and Katakana, and Zenkaku (full-size) characters of 16 x 16 dots such as Hiragana and JIS Level 1 character set Kanji. CG2 contains 8 x 8 alpha/numerics, special characters and Katakana which are used for 80 x 25 text mode display by VP2.
2. CG2 consists of four 32 KB ROMs of type TM23256 (or equivalent)
3. Access time and cycle-time are both 250 ns.
4. CG2 can be accessed from the VP2 and VP3 code buffer and fonts can be read through BIOS. In order to display Kanji characters in graphics mode, CG2 is read by BIOS and is written on the video RAM.

5. In order to distinguish one-byte (Hankaku) codes from two-byte (Zenkaku) code, 256KB of virtual address space are used. The codes are then compressed 128KB of Kanji ROM (CG2).
6. When a Kanji is written on video RAM, the hardware uses the Kanji code to point to a ROM Kanji address and the character at that address is displayed.
7. In memory, CG2 is allocated to 80000 - BFFFF.

### 3. Video Subsystem

#### 3.4. Display Function of VP1 and VP2

VP1 and VP2 have the following two display modes:

- Text mode
- Graphics mode

The Graphic displaying functions of VP1 and VP2 are the same, but their text displaying functions are different:

#### VP1/VP2 Displaying functions

Function	VP 1	VP 2
Text Display	40 char. × 25 lines (AN) 80 " × 25 " (AN)*	20 char. × 11 lines (Kanji) 40 " × 11 " ( " ) 40 × 25 (ANK) 80 × 25 (ANK)
Graphics Display	160 × 200 dot 16 colors 320 × 200 " 4 " 320 × 200 " 16 " * 640 × 200 " 2 " 640 × 200 " 4 " *	The same as VP1
Character display possible	Alphanumeric Special Characters (8 × 8 dot)	JIS Level 1 Kanji (16 × 16 dot) Hiragana (16 × 16 dot)  Katakana, Alphanumeric, Special Char. (16 × 16 or 8 × 16 dot)  Katakana, Alphanumeric, Special Char. (8 × 8 dot)
Super-Impose	Graphics display only	Graphics and Text display

- Remarks) 1. \* indicates that 64 KB expanded memory is required for VP1.  
2. AN stands for Alphanumeric and ANK for Alphanumeric and Katakana.

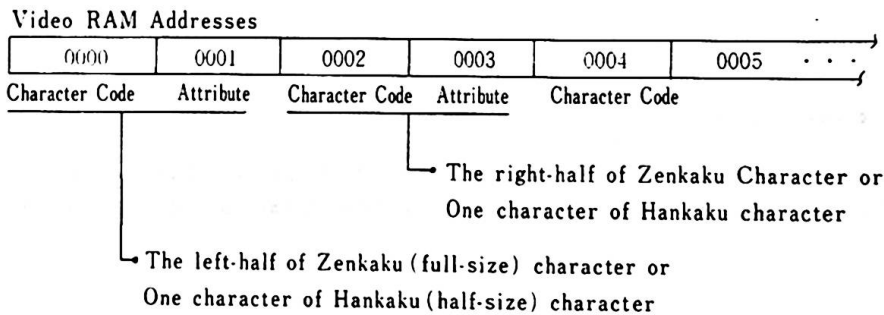
Figure 3-4 VP1 and VP2 Display Function

### 3.4.1. VP1 Text Display

In color-text mode, 16 colors are available either for the foreground or the background. The number of background colors, however, becomes 8 when "blink" is used. One of 16 colors is available for use in the text mode screen border.

The character generator contains 256 character fonts in its 2 KB ROM (CG1).

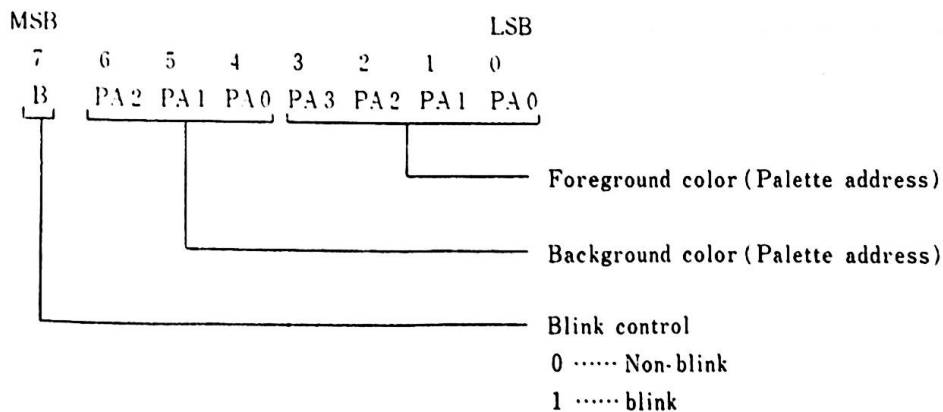
The display character codes are stored in even addresses, while their character attributes are stored in the odd addresses next to them.



### VP1 Attributes

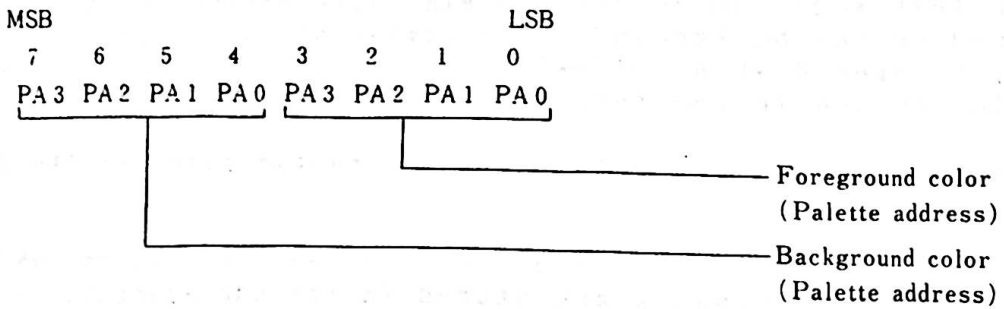
#### (1) Blink is possible

In the mode control 2 register, a "1" makes blink possible. The number of background colors, however, is reduced to a maximum of 8.



### 3. Video Subsystem

#### (2) Blink is not possible

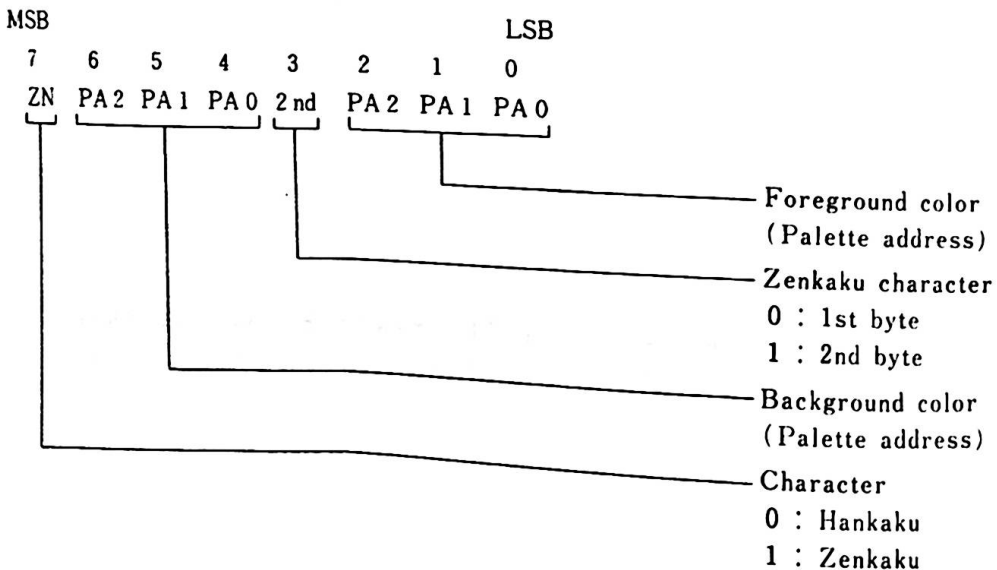


#### 3.4.2. VP2 Text Display

##### VP2 Attributes

#### (1) Blink is possible

In the VP2 mode control 1 register, if bit 6 is "0", it is possible to display alphanumeric mode characters. (8 x 8 dots to display one character. Reference: 3.4.4 Video Gate Array.) In this display mode, an attribute has the same meaning as in VP1.





### 3.4.3. VP1 & VP2 Graphics Display

VP1 and VP2 support the following six graphics modes:

#### Graphics Mode 1

Graphics mode 1 displays on an ordinary TV set, 12" color display or 14" color display and has the following characteristics:

- 160 dot horizontal and 200 dot vertical display
- 16 colors are available for each dot
- 16 KB memory per screen are necessary, as the color is specified for each dot
- 1 byte is required to specify 2 dots
- This corresponds to Screen Mode 1 in Basic.

MSB								LSB
	7	6	5	4	3	2	1	0
	<u>PA3</u>	<u>PA2</u>	<u>PA1</u>	<u>PA0</u>	<u>PA3</u>	<u>PA2</u>	<u>PA1</u>	<u>PA0</u>
	1st display dot				2nd display dot			

#### Graphics Mode 2

Graphics mode 2 displays on an ordinary TV set, 12" color display or 14" color display and has the following characteristics:

- 320 dot horizontal and 200 dot vertical display
- 4 out of 16 colors can be displayed
- 16 KB memory per screen are necessary, as the color is specified for each dot
- 1 byte is required to specify 4 dots
- This corresponds to Screen Mode 2 in Basic.

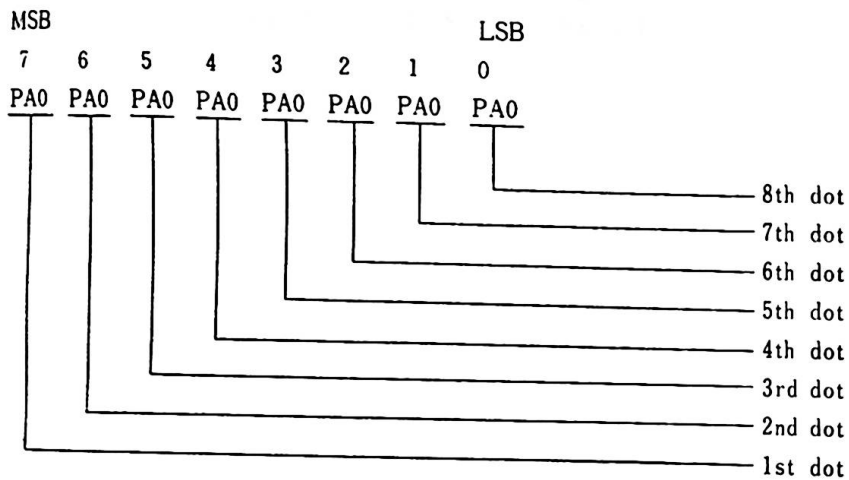
MSB								LSB
	7	6	5	4	3	2	1	0
	<u>PA1</u>	<u>PA0</u>	<u>PA1</u>	<u>PA0</u>	<u>PA1</u>	<u>PA0</u>	<u>PA1</u>	<u>PA0</u>
	1st display dot		2nd display dot		3rd display dot		4th display dot	

### 3. Video Subsystem

#### Graphics Mode 3

Graphics mode 3 displays high-resolution 2 color graphics and is possible only on high-resolution displays. This mode has the following characteristics:

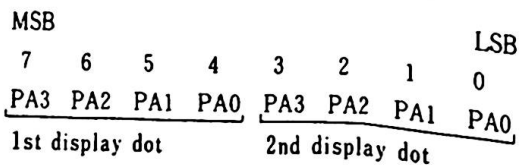
- 640 dot horizontal and 200 dot vertical display
- 2 out of 16 colors can be displayed
- 16 KB memory per screen are necessary, as the color is specified for each dot
- 1 byte is required to specify 8 dots
- This corresponds to Screen Mode 3 in Basic.



#### Graphics Mode 4

Graphics mode 4 displays on an ordinary TV set, 12" color display or 14" color display and has the following characteristics:

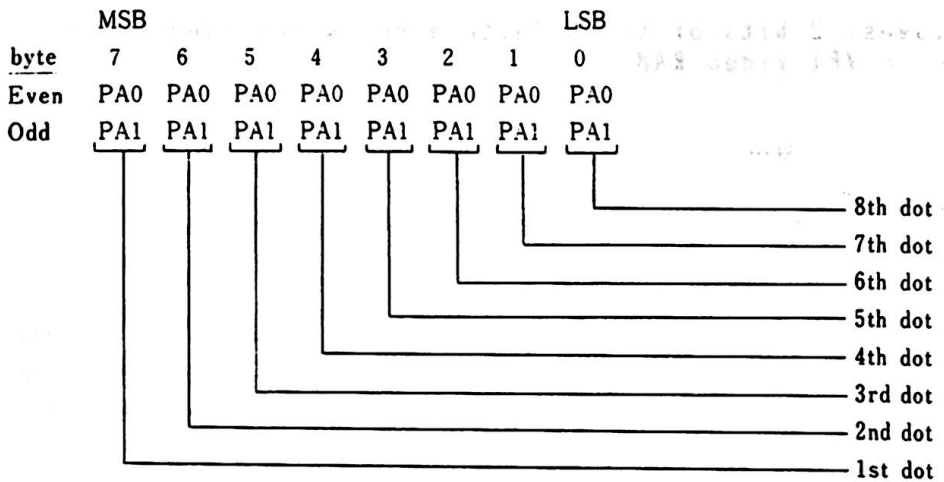
- 320 dot horizontal and 200 dot vertical display
- 16 colors are available for display
- 32 KB memory per screen are necessary, as the color is specified for each dot
- 1 byte is required to specify 2 dots
- 64 KB expanded memory is required for VP1.
- This corresponds to Screen Mode 4 in Basic.



Graphics Mode 5

Graphics mode 5 can display only on high-resolution displays and has the following characteristics:

- 640 dot horizontal and 200 dot vertical display
- 4 out of 16 colors can be displayed
- 32 KB memory per screen are necessary, as the color is specified for each dot
- 64 KB expanded memory is required for VP1
- 2 bytes are required to specify 8 dots
- This corresponds to Screen Mode 5 in Basic.



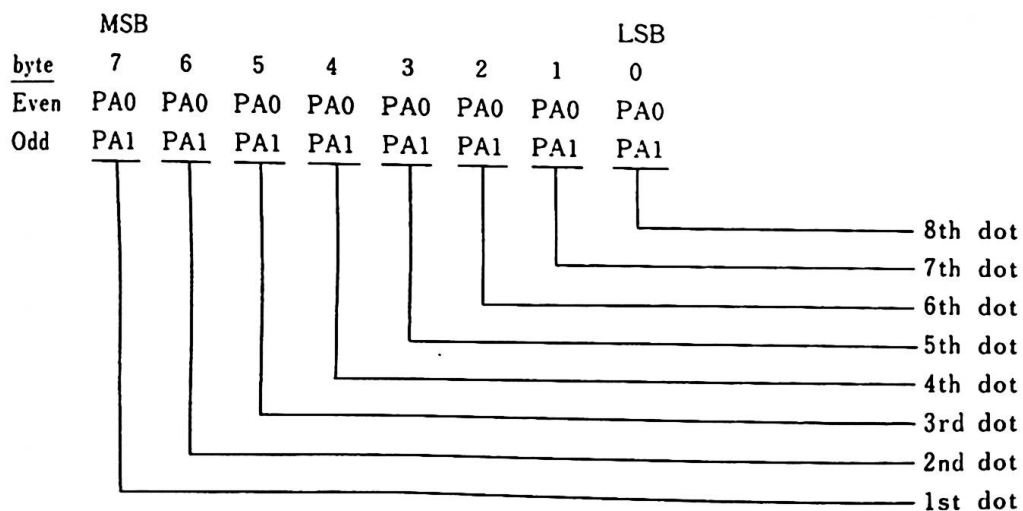
### 3. Video Subsystem

#### Graphics Mode 6

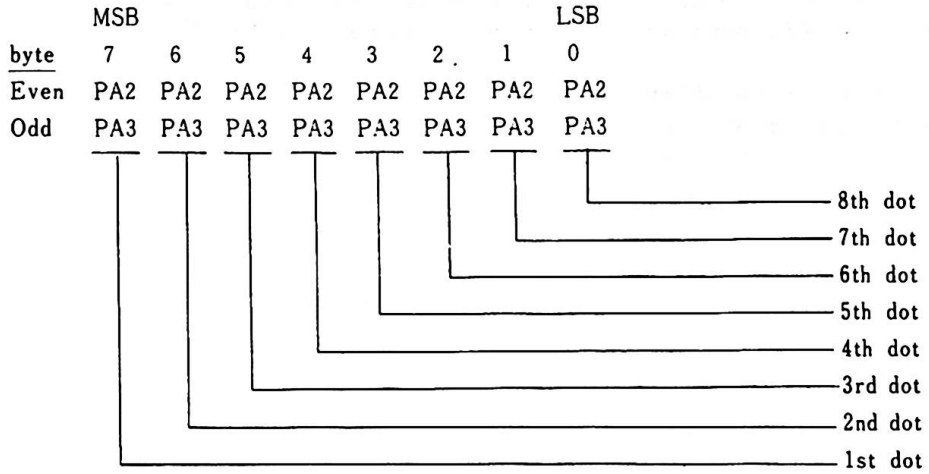
Graphics mode 6 can display only on a high-resolution display. This mode is achieved by combining two screens. One is screen mode 5 of VP1 and the other is screen mode 5 of VP2. This mode has the following characteristics:

- 640 dot horizontal and 200 dot vertical display
- 16 colors can be displayed
- 64 KB memory per screen are necessary, as the color is specified for each dot
- 64 KB expanded memory is necessary
- 4 bytes are required to specify 8 dots
- This corresponds to Screen Mode 6 in Basic.

The lowest 2 bits of the palette address are used to set the value in VP1 video RAM.



The highest 2 bits of the palette address are used to set the value in VP2 video RAM.



In graphics mode 6, methods of specifying the palette address differs from those of graphics modes 1 through 5.

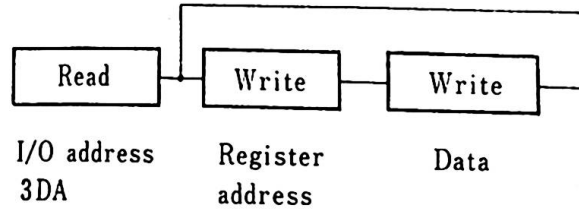
Mode 1-5 address				Mode 6 address				Hex. Value
PA3	PA2	PA1	PA0	PA3	PA2	PA1	PA0	
0	0	0	0	1	0	1	0	0/A
0	0	0	1	1	0	1	1	1/B
0	0	1	0	1	0	0	0	2/8
0	0	1	1	1	0	0	1	3/9
0	1	0	0	1	1	1	0	4/E
0	1	0	1	1	1	1	1	5/F
0	1	1	0	1	1	0	0	6/C
0	1	1	1	1	1	0	1	7/D
1	0	0	0	0	0	1	0	8/2
1	0	0	1	0	0	1	1	9/3
1	0	1	0	0	0	0	0	A/0
1	0	1	1	0	0	0	1	B/1
1	1	0	0	0	1	1	0	C/6
1	1	0	1	0	1	1	1	D/7
1	1	1	0	0	1	0	0	E/4
1	1	1	1	0	1	0	1	F/5

### 3. Video Subsystem

#### 3.4.4. Video Gate Array

The video gate array controls the video functions and the I/O address is 3DA (Hex). The gate array for VP1 and VP2 has 8 kinds of internal registers. There are internal registers for each of VP1 and VP2 and also ones used commonly by VP1 and VP2.

To control read/write to a register, a flip-flop latch is used. Each time a write is performed to an I/O register, address/data flip-flop latch will be reversed. When a read is performed, this flip-flop changes to "address status".



The following are descriptions of the registers:

#### Internal Register Address

The addresses for VP1 and VP2 are the same. When a register for VP1 is accessed, however, the "P" bit of memory block GA2A should be "1". Similarly, when a register for VP2 is accessed the "P" bit of memory block GA2B should be "1". (Reference 2.2.6 Memory Space and I/O Address Setting)

Address(Hex)	Register Name
00	Mode control 1 register (separate)
01	Palette mask register (separate)
02	Border color register (common)
03	Mode control 2 register (separate)
04	Reset register (common)
05	Transparent palette register (for VP1 only)
06	Superimpose control register (common)
10~1F	Palette register (common)

Remarks) separate --- separately used by VP1 and VP2  
common ----- commonly used by VP1 and VP2

## Mode Control 1 Register

This is a write-only 8 bit register and the address in the video gate array is 00 (Hex).

Bit	VP1 function	VP2 function
0	High to low bandwidth	High to low bandwidth
1	Graphics/Text	Graphics/Text
2	—	—
3	Video enable	Video enable
4	16 color graphics	16 color graphics
5	—	—
6	—	VRAM 2 Kanji/AN mode
7	—	Super 16 color

Bit 0 : The high video bandwidth display requires it to be "1". The following display modes require the high video bandwidth.  
An optional 64 KB expanded RAM card is required for VP1.

- 80 char. x 25 lines text mode
- 640 x 200 dot 4 color graphics
- 320 x 200 dot 16 color graphis

Bit 1 : "1" for all graphics modes, "0" for text mode.

Bit 2 : Always "0".

Bit 3 : "1" makes the video signal available. "0" makes it not available and the screen becomes border color.

Bit 4 : "1" for 160 x 200 dot 16 color graphics and 320 x 200 dot 16 color graphics.

Bit 6 : For VP2. "1" makes it possible to display Kanji characters using VRAM 2. When it is "0" and bit 1 is "0", 80 chars. x 25 lines text display (ANK) is possible using VP2.

Bit 7 : For VP2. "1" makes it possible to display 640 x 200 dot 16 color graphics (screen mode 6). In this case, as both VRAM 1 and VRAM 2 are used, superimpose is not possible.

### 3. Video Subsystem

#### Palette Mask Register

This is a write-only 8 bit register and the address in the video gate array is 01 (Hex). "0" makes it possible to mask the palette.

Bit	VP1 function	VP2 function
0	Mask bit 0	Mask bit 0
1	Mask bit 1	Mask bit 1
2	Mask bit 2	Mask bit 2
3	Mask bit 3	Mask bit 3
4	—	VRAM 1
5	—	VRAM 2
6	—	Extended video
7	—	Video bandwidth control

Bit 0 - 3 : "0" masks palette register address bit.

Bit 4 : "1" makes VP1 access VRAM 1.

Bit 5 : "1" makes VP2 access VRAM 2.

Bit 6 : "1" makes VP3 access VRAM 3.

Bit 7 : This changes the video bandwidth. "0" sets 14 MHz while "1" sets 20 MHz.

#### Border Color Register

This is a write-only 4 bit register whose address in the video gate array is 02 (Hex). The four bits correspond to R, G, B and I and the border color is fixed by their combination.

VP1, VP2	
Bit	Color
0	B (Blue)
1	G (Green)
2	R (Red)
3	I (Intensity)

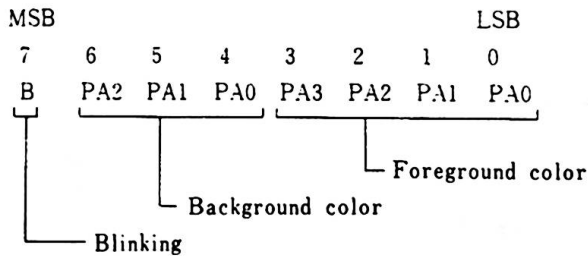


## Mode Control 2 Register

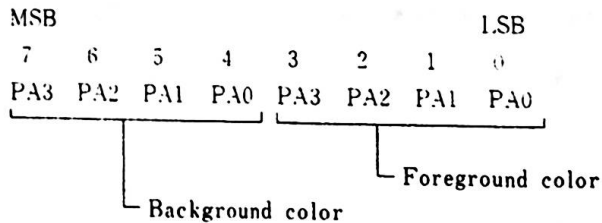
This is a write-only register whose address in the video gate array is 03 (Hex).

Bit	VP1 function	VP 2 function
0	always "0"	always "0"
1	Blinking	Blinking ("0" in Kanji)
2	always "0"	always "0"
3	2 color graphics	2 color graphics
4	—	English mode

Bit 1 : When "1", the attribute byte in text display has the following meaning:



When "0", the attribute byte in text display has the following meaning:



In graphics mode, when the control 2 register bit 1 is "1", the palette (PA3) high bit address is replaced by character the blink rate and for this, 2 colors will be displayed alternately. If the palette of the higher half and the lower half are the same, the color remains unchanged. If they are different, two colors will be displayed alternately in accordance with the blink rate. To cause this, only 8 colors are possible and bit 3 of the palette mask register becomes inactive.

### 3. Video Subsystem

- Bit 3 : "1" when in 640 x 200 2 color graphics mode.
- Bit 4 : "1" when in English mode. The memory space for the 128 KB expanded RAM card is placed after that of the 64 KB memory on the system board and the 64 KB expanded RAM. "0" when in Native and Extended Video mode. In this case, memory space for the 128 KB expanded RAM is always placed after memory address 20000 (Hex).

#### Reset Register

This is a write-only 2 bit register whose address in the gate array is 04 (Hex).

Bit	Function
0	Asynchronous Reset
1	Synchronous Reset

- Bit 0 : "1" issues an asynchronous reset in the video gate array and stops all memory cycles. All output signals are tri-stated and memory is cleared. This reset should be issued only once after power-on and then the synchronous reset should be used.
- Bit 1 : "1" issues a synchronous reset in the video gate array and stops all memory cycles. All output signals stop. When a synchronous reset is made after a memory refresh at the time of a display mode change, the contents of the gate array mode control register and the CRT control register will be changed. After the synchronous reset is released, a memory refresh is done. In this way, the memory contents will not be changed even at the time of display mode change.

#### Transparent Palette Register

This is a register for VP1 only and specifies which palette register will become transparent. The address in the gate array is 05 (Hex).

Bit	Function
0	Register Select 0
1	Register Select 1
2	Register Select 2
3	Register Select 3

### Superimpose Control Register

The address in the gate array is 06 (Hex). It controls superimposing of VRAM 1 and VRAM 2.

Bit	Function
0	Priority Control
1	Transparent Control
2	Mode Control 1
3	Mode Control 2

Bit 3, 2 = 11 --- OR  
 10 --- AND  
 01 --- XOR  
 00 --- has the following meanings depending on bits 0 and 1.

Bit 1, 0	Foreground	Background	Transparent(Y or N)
0 0	VRAM 1	VRAM 2	NO
0 1	VRAM 2	VRAM 1	NO
1 0	VRAM 1	VRAM 2	YES
1 1	VRAM 2	VRAM 1	Background color is displayed in the foreground

### 3. Video Subsystem

#### Palette Register

This is a write-only 4 bit register. The combination of the 4 bits selects a color. There are 16 registers and the addresses in the video gate array are 10 - 1F (Hex). Each address has a corresponding color code.

Color Code (Hex)	Palette Register Address (Hex)
0	10
1	11
2	12
3	13
4	14
5	15
6	16
7	17
8	18
9	19
A	1A
B	1B
C	1C
D	1D
E	1E
F	1F

Figure 3-5 Palette Register and Color Code

Palette Bit				Color
I	R	G	B	
0	0	0	0	Black
0	0	0	1	Blue
0	0	1	0	Green
0	0	1	1	Light Blue
0	1	0	0	Red
0	1	0	1	Purple
0	1	1	0	Yellow
0	1	1	1	White
1	0	0	0	Bright Black
1	0	0	1	Bright Blue
1	0	1	0	Bright Green
1	0	1	1	Bright Light Blue
1	1	0	0	Bright Red
1	1	0	1	Bright Purple
1	1	1	0	Bright Yellow
1	1	1	1	Bright White

**Figure 3-6** Palette Register and the color displayed

When the palette is loaded, the video is in a "display disable" status and the color on the screen is set depending on the contents of the register which the processor specifies. When the program finishes loading the palette, the video changes again to a "display enable" status.

### 3. Video Subsystem

#### 3.4.5. Superimpose

VP1 and VP2 operate on the same clock at 14.31818 MHz. The two video signals can be imposed using the mixer. The video mixer includes a 4 bit x 16 word palette. The mixer sets the priority of VP1 and VP2 and allows screens to be imposed using such logic operations as AND, OR, or XOR.

		VP2								
Page 0~7	Page 8~B	Kanji 20× 11	ANK 40× 25	160× 200 16C	320× 200 4C	640× 200 2C	ANK 80× 25	320× 200 16C	640× 200 4C	Kanji 40× 11
	VP1	AN 40×25	×	×	×	×	×	×	×	×
160×200 16C		○	○	○	○	○	×	×	×	×
320×200 4C		○	○	○	○	○	×	×	×	×
640×200 2C		○	○	○	○	○	×	×	×	×
AN 80×25		×	×	×	×	×	×	×	×	×
320×200 16C		×	×	×	×	×	○	○	○	○
640×200 4C		×	×	×	×	×	○	○	○	○

Remarks) ○ ---- Imposition Possible  
 × ---- Impossible  
 AN --- Alphanumeric, Special Character  
 ANK -- Alphanumeric, Special Character, Katakana  
 C ---- Color

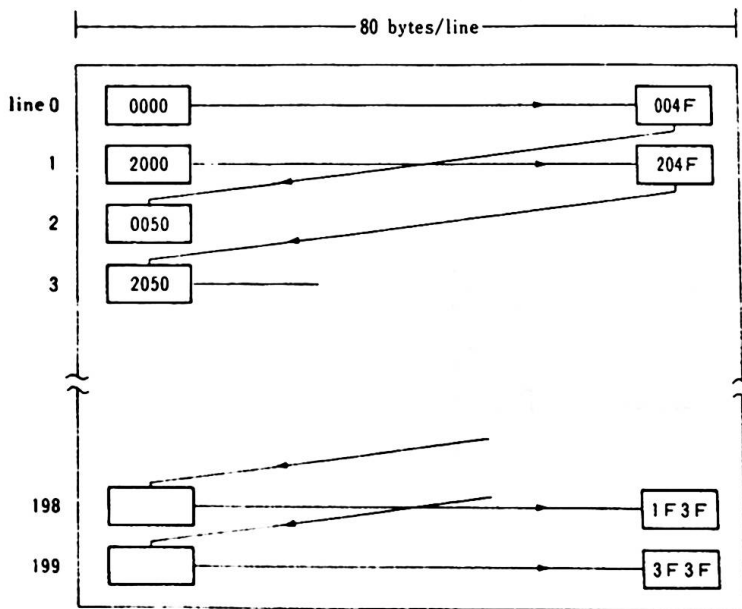
Figure 3-7 Combinations for Superimpose

3.4.6. Video RAM and Display Screen Relationships

The size of video RAM necessary to display graphics depends on what is being displayed.

The following displays require two banks of memory, each of which is 8000 bytes:

- Graphics mode 1 (160 x 200 dot 16 colors) : 2 dots/byte
- Graphics mode 2 (320 x 200 dot 4 colors) : 4 dots/byte
- Graphics mode 3 (640 x 200 dot 2 colors) : 8 dots/byte



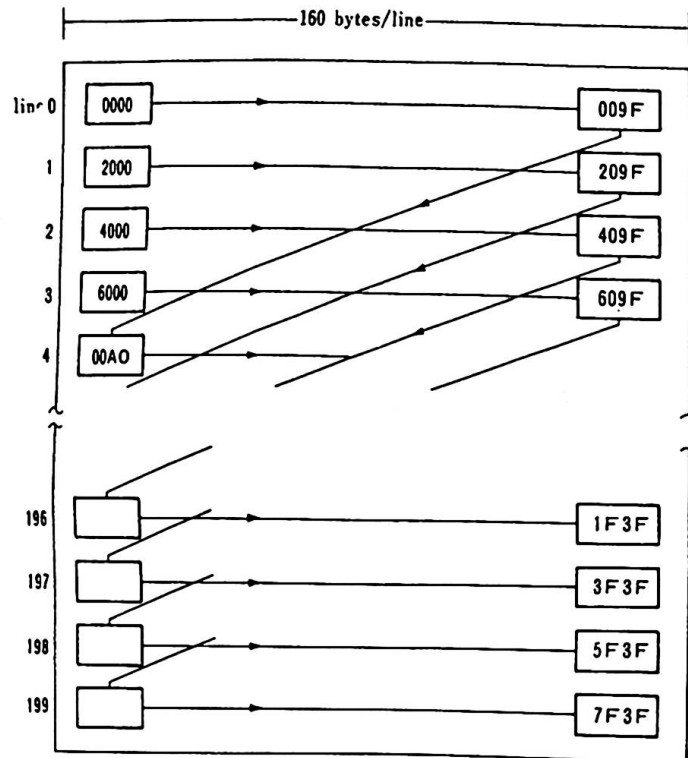
Remark) The value in the rectangle indicates the relative address (Hex) of the video RAM.

Figure 3-8 Video RAM and the screen (1 of 2)

### 3. Video Subsystem

The following displays require four banks of memory, each of which is 8000 bytes.

- Graphics mode 4 (320 x 200 dot 16 colors)
- Graphics mode 5 (640 x 200 dot 4 colors)



Remark) The value in the rectangle indicates the relative address (Hex) of the video RAM.

Figure 3-8 Video RAM and the screen (2 of 2)



### 3.5. VP3 Display Function

VP3 is a video processor for Extension Video mode and is included in an optional Extension Video card.

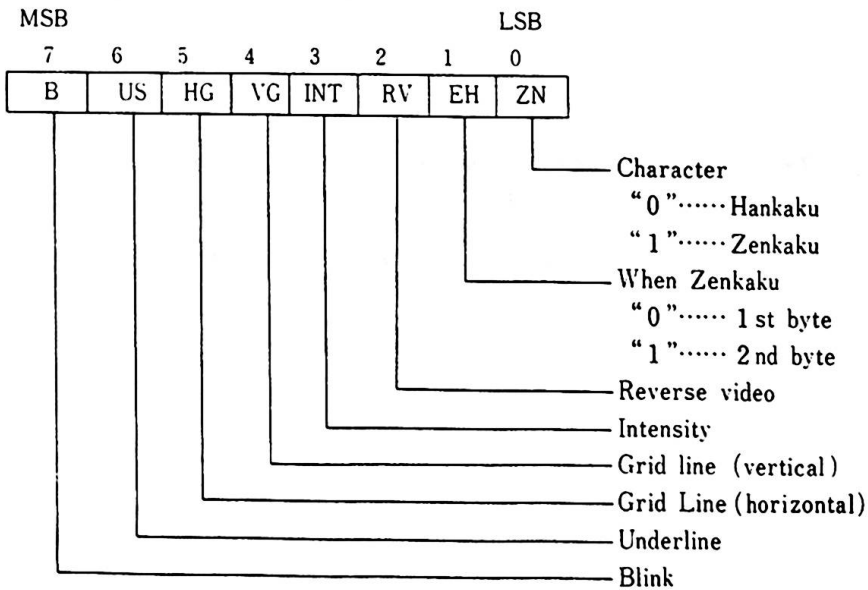
#### 3.5.1. VP3 Text Display

VP3 supports display of the following types of text :

- Kanji, Hiragana  
Zenkaku (Full-size) 16 x 16 dot, 40 characters x 25 lines
- Katakana, Alphanumerics, Special Characters  
Zenkaku (Full-size) 16 x 16 dot, 40 characters x 25 lines  
Hankaku (Half-size) 8 x 16 dot, 80 characters x 25 lines
- Vertical and Horizontal Grid lines

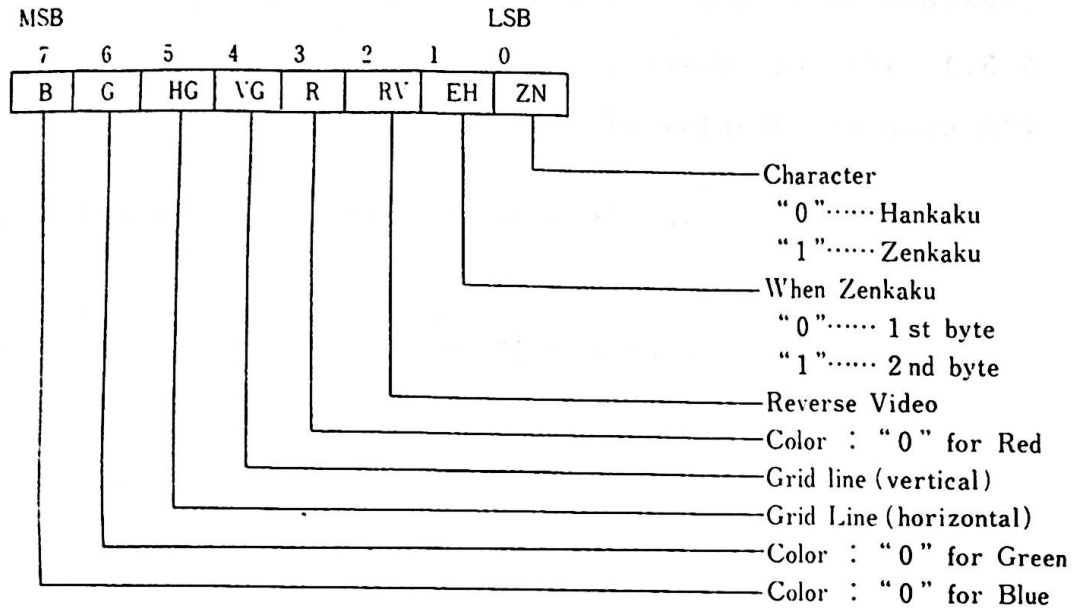
Attributes differ between monochrome and color displays.

Monochrome display attributes:



### 3. Video Subsystem

#### Color Display Attributes:



- Remarks) 1. The combination of bits 3, 6 and 7 makes it possible to display 8 colors.
2. Display is not possible when all of bits 2, 3, 6 and 7 are "1".

### 3.5.2. VP3 Graphics Display

In VP3 graphics display, the address space of A0000 - AFFFF (Hex) is allocated as the video RAM virtual addresses.

VP3 supports the following two graphics modes:

- Mode 1 : 360 x 512 dot 4 colors
- Mode 2 : 720 x 512 dot 2 colors

#### Mode 1

Mode 1 graphics display has the following characteristics:

- 360 horizontal x 512 vertical dot display screen
- 4 out of 16 colors can be displayed at one time
- 46,080 bytes of memory are required, as the color is specified for each dot.
- 1 byte specifies 4 dots.

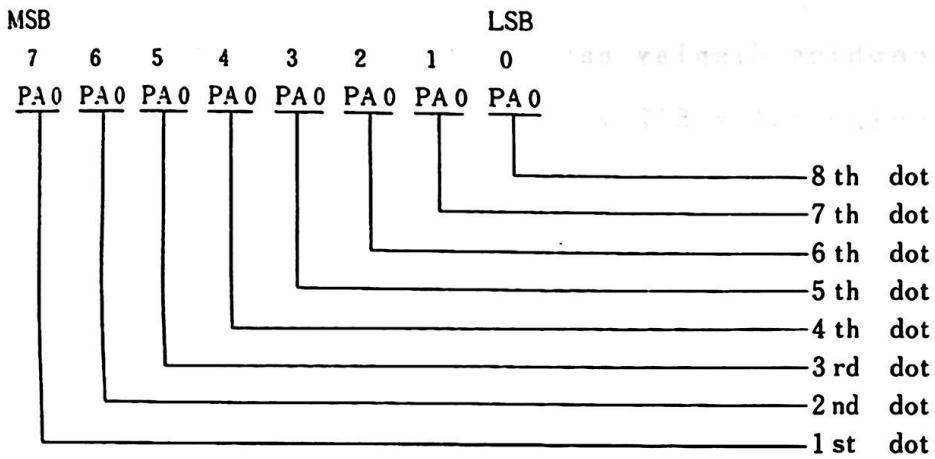
MSB						LSB	
7	6	5	4	3	2	1	0
<u>PA1</u>	<u>PA0</u>	<u>PA1</u>	<u>PA0</u>	<u>PA1</u>	<u>PA0</u>	<u>PA1</u>	<u>PA0</u>
1st		2nd		3rd		4th	
display		display		display		display	
dot		dot		dot		dot	

### 3. Video Subsystem

#### Mode 2

Mode 2 graphics display has the following characteristics:

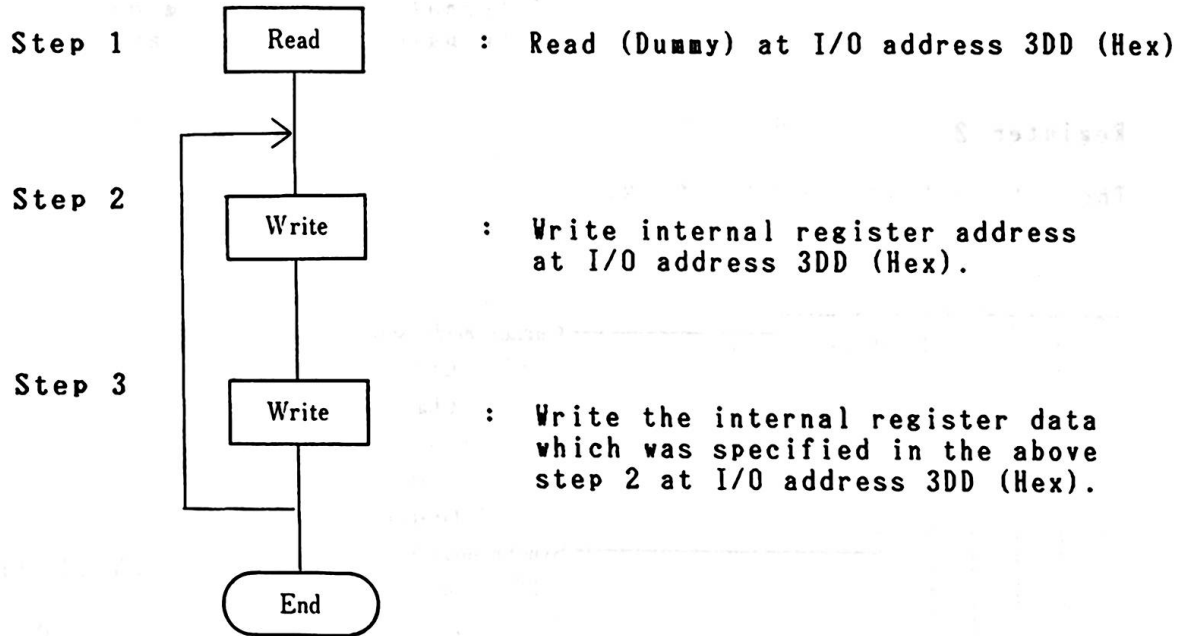
- 720 horizontal x 512 vertical dot display screen
- 2 out of 16 colors can be displayed at one time
- 46,080 bytes of memory are required, as the color is specified for each dot.
- 1 byte specifies 8 dots.



## 3.5.3. Gate Array

The VP3 gate array is contained in an optional Extension Video Card and the I/O address is 3DD (Hex). The gate array has eight internal registers whose internal addresses are 00 - 07 (Hex). The internal registers perform various controls on VP3.

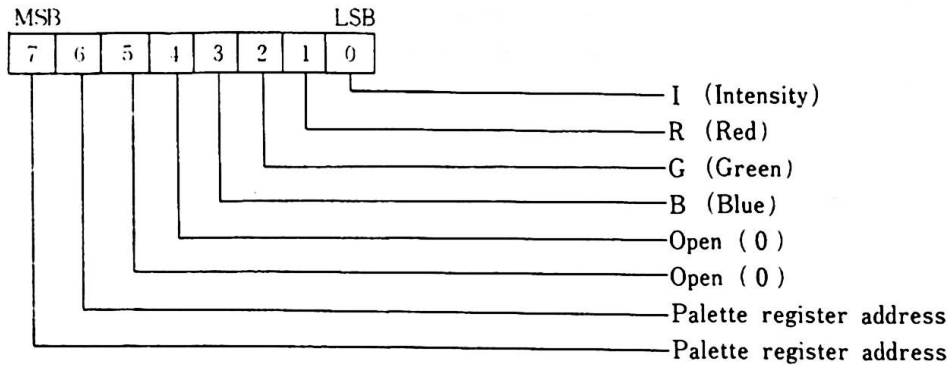
Write to internal register





**Register 3**

This register contains the information for setting the color in the palette register. The internal address is 03 (Hex).

**Bit 7, 6:**

00	Palette register 0
01	Palette register 1
10	Palette register 2
11	Palette register 3

The palette registers 0 through 3 can be used in 360 x 512 dot graphics display. The palette registers 0 and 1 can be used in 720 x 512 dot graphics display.

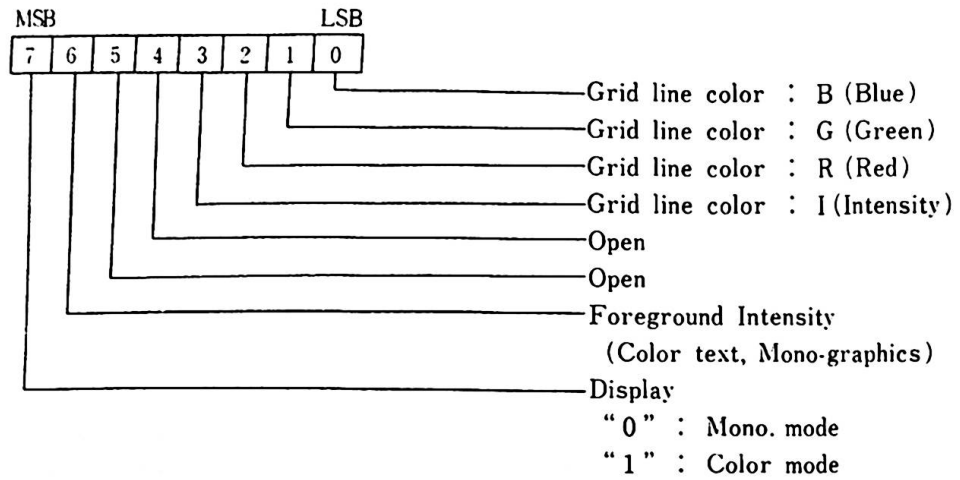
**Register 4**

The internal address is 04 (Hex). A write is performed against this register to make the clock available (any data will do).

### 3. Video Subsystem

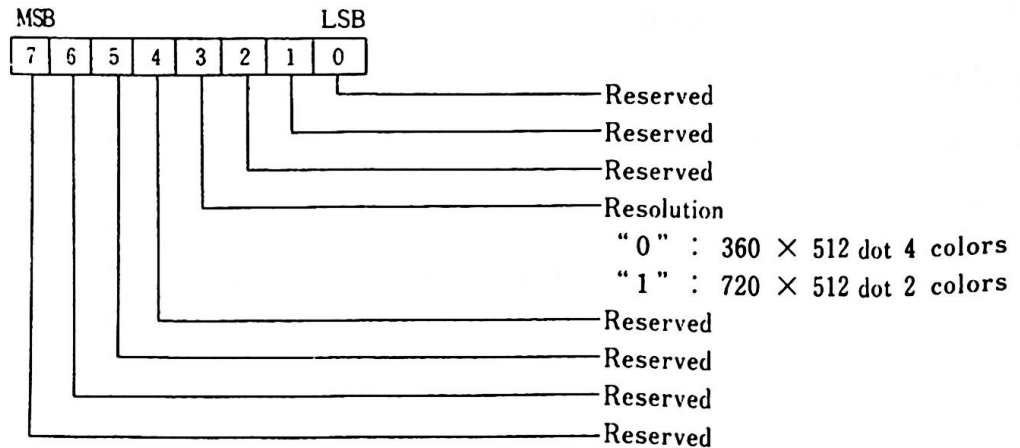
#### Register 5

The internal address is 05 (Hex).



#### Register 6

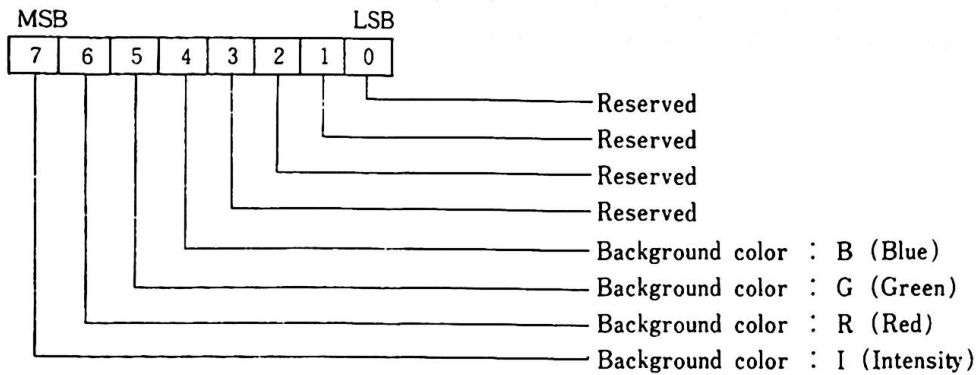
The internal address is 06 (Hex).





**Register 7**

The internal address is 07 (Hex).

**Display Mode Selection**

The VP3 display mode is set by the combination of the bits as follows:

1. Register 2, bit 1 --- X
2. Register 5, bit 7 --- Y
3. Register 6, bit 3 --- Z

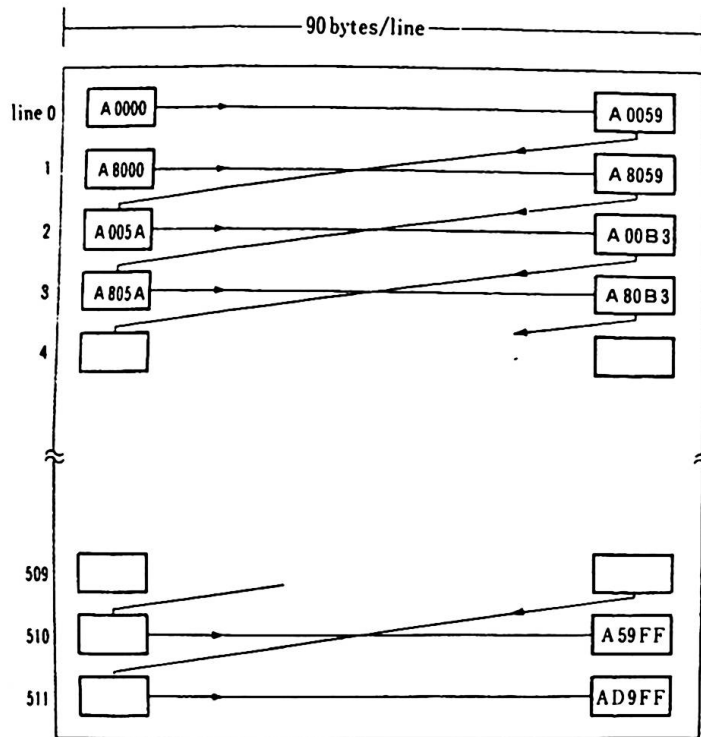
Display mode	X	Y	Z
Mono. Text	0	0	—
Color Text	0	1	—
Mono. Graphics	1	0	—
Color Graphics (mode 1)	1	1	0
Color Graphics (mode 2)	1	1	1

### 3. Video Subsystem

#### 3.5.4. Video RAM and Display Screen Relationships

Two banks of memory each of 23,040 bytes are used to display the following:

- Graphics mode 1 (360 x 512 dot, 4 colors)
- Graphics mode 2 (720 x 512 dot, 2 colors)



Remark) The value in the rectangle indicates the absolute address (Hex) of the video RAM.

Figure 3-9 Video RAM and the screen (VP3)

## 3.6. CRT Controller

The HD46505 is used as the CRT Controller (CRTC). It has 19 accessible internal registers. One of these registers, the Index register, is actually used as a pointer to the other 18 registers. It is a write-only register and the I/O address is 3D4 (Hex). In order to write data in any of the 18 registers, the Index register is first loaded with the necessary pointer. Then, the Data register is loaded with the information to be placed in the selected register. The data register is loaded from the processor by executing an OUT instruction to I/O address 3D5 (Hex).

The following are the relationships between the modes and the registers:

Register		VP 1 & VP 2						VP 3	
		Text				Graphics		Text	Graphics
		No.	Address	Kanji		AN			
20×11	40×11			40×25	80×25				
R0	0	38	71	38	71	38	71	65	38
R1	1	28	50	28	50	28	50	50	2D
R2	2	2F	5C	2F	5C	2E	5B	58	31
R3	3	06	DC	06	0C	06	0C	AC	A6
R4	4	0D	0D	1F	1F	7F	3F	1A	47
R5	5	0A	0A	06	06	06	06	02	00
R6	6	0B	0B	19	19	64	32	19	40
R7	7	0C	0C	1C	1C	70	38	19	41
R8	8	02	02	02	02	02	02	03	03
R9	9	11	11	07	07	01	03	13	06
R10	A	10	10	06	06	26	26	13	27
R11	B	11	11	07	07	07	07	14	07
R12	C	00	00	00	00	00	00	00	00
R13	D	00	00	00	00	00	00	00	00
R14	E	00	00	00	00	00	00	00	00
R15	F	00	00	00	00	00	00	00	00
R16	10	Lightpen data (High Address) : Read-only							
R17	11	Lightpen data (Low Address) : Read-only							

Remark) All register values are given in Hexadecimal.

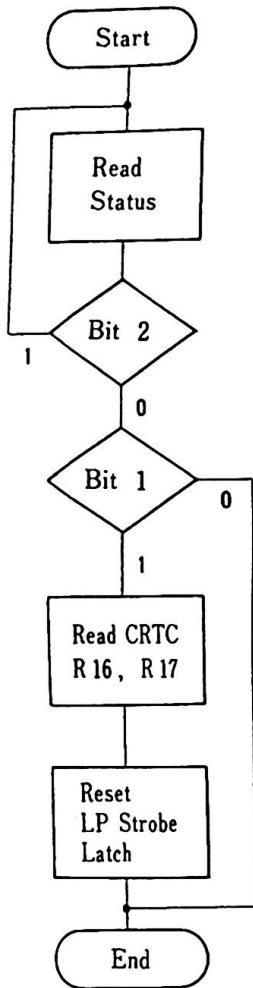
Figure 3-10 Display and CRTC Register Value

### 3. Video Subsystem

#### 3.7. Light Pen Interface

The light pen interface is designed for RGBI (Red, Green, Blue, Intensity). Due to timing differences depending on different display types, the row/column value returned from the CRT may vary. These differences must be compensated for through software.

The following show the light pen data read sequences:



Read I/O address 3DA (Hex)  
Bit 1 : LP strobe latch  
Bit 2 : -LP SW

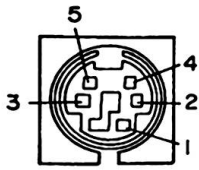
0 : Light Pen switch is on.  
1 : Light Pen switch is not pressed.

0 : Light Pen is not lighted.  
1 : Light Pen is lighted and address data are in R16 and R17 of CRTC.

Write data in I/O address 3DB (Hex).  
(Any data will do.)

### Hardware Interface

A light pen is connected to a five pin connector on the back of the system unit.



(System Side)

Pin No.	Signal	I/O
1	+12 V	O
2	-LPEN INPUT	I
3	+5 V	O
4	-LPEN SW	I
5	GND	-

Figure 3-11 Light Pen Interface Connector (J9)

### 3. Video Subsystem

#### 3.8. Video Subsystem I/O Addresses

In Native Mode, I/O addresses used by the Video Subsystem are initialized with the following but they can be changed by the software:

Address (Hex.)	A9	A8	A7	A6	A5	A4	A3	A2	A1	A0	Register
3DA	1	1	1	1	0	1	1	0	1	0	VP1/2 Gate Array
3DB	1	1	1	1	0	1	1	0	1	1	Light Pen Latch Clear
3DC	1	1	1	1	0	1	1	1	0	0	Light Pen Latch Set
3D0,3D2 3D4,3D6	1	1	1	1	0	1	0	x	x	0	CRTC Index Register
3D1,3D3 3D5,3D7	1	1	1	1	0	1	0	x	x	1	CRTC Data Register
3DF	1	1	1	1	0	1	1	1	1	1	Page Register
3DD	1	1	1	1	0	1	1	1	0	1	VP3 Gate Array

X = N/A

Figure 3-12 Video I/O Addresses

## 3.9. Hardware Interface

This interface provides power to an optional TV Adapter. The signal level is standard TTL but the audio output operates with a 1V Peak-to-Peak signal biased at 0V which can drive a 10 K Ohm or greater input impedance.

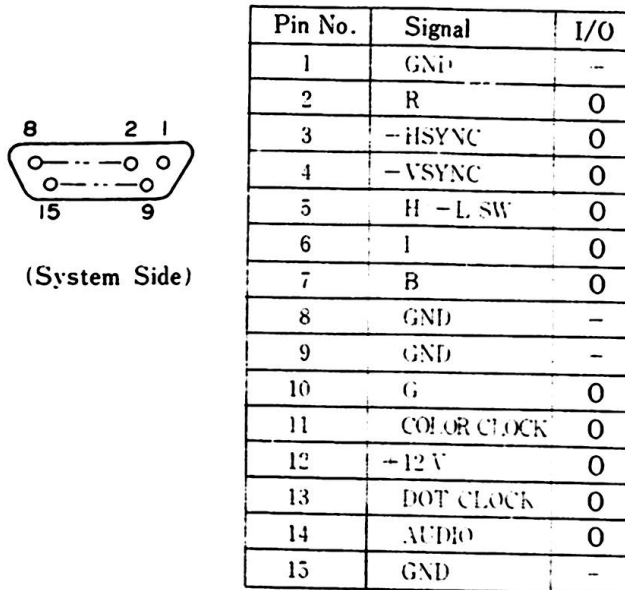


Figure 3-13 Display Interface (J15)





#### 4. System Options

The IBM 5510 system provides connectors on the system board for optional feature installation. In this Chapter, optional features and their connection to the system are described. I/O devices can be connected to the Expansion Channel connectors.

## 4. System Options

### 4.1. 64KB RAM Card

To display 80 characters x 25 lines in text mode, or 640 x 200 dots in 4 colors or 320 x 200 dots in 16 colors in graphics mode, the system RAM size should be expanded to 128KB through installation of this option. This card consists of one board and connects to a 50 pin connector on the system board.

(Only one 64KB card can be installed.)

When this card is inserted, the addressing method is changed. This memory option uses the ODD memory space, while the system memory is decoded as the EVEN memory. When an additional 128KB of RAM is installed, the addresses will again be changed.

(Reference : 5.5 Memory Map)

Memory refresh is performed on the system board logic.

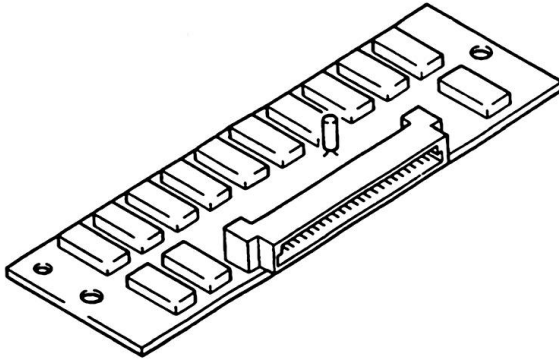


Figure 4-1 64KB RAM Card

The following are the connector specifications for the JX 64KB RAM Card.

Signal	Pin (A)	Pin (B)	Signal
D0	A1	B1	D1
D2	2	2	D3
D4	3	3	D5
D6	4	4	D7
+5V	5	5	+5V
GND	6	6	GND
A0	7	7	Open
V1A0	8	8	V1A1
V1A2	9	9	V1A3
V1A4	10	10	V1A5
V1A6	11	11	V1A7
Open	12	12	Open
-RAS1	13	13	CAS1
-WE1	14	14	SPL1
SAT1	15	15	AGPD
VIDEO MEMR	16	16	GATE
-DIS CAS0	17	17	-DIS ED
-LCG	18	18	Open
GAC0	19	19	GAC1
GAC2	20	20	GAC3
GAC4	21	21	GAC5
GAC6	22	22	GAC7
-64K CD IN	23	23	Open
Open	24	24	Open
Open	25	25	Open

Figure 4-2 64KB RAM Card Connector Specifications (J6)

#### 4. System Options

I/O refers to the view from the card side.

<u>Signal</u>	<u>I/O</u>	<u>Description</u>
-RAS	I	Row Address Strobe. This timing pulse sets the row address for the RAM modules.
A0	I	Address line A0. When this bit is "1", the 64KB RAM card is selected.
-DISABLE EDATA	I	When the expansion RAM card is installed and the microprocessor is reading an odd byte of data, the expansion card tri-states the latch for an even byte of data on the system board using this line.
SAT1	I	This signal indicates that the expansion RAM card should latch up data from the expansion RAM into the attribute latch.
GAC0 - GAC7	O	These data lines contain VP1 data from the attribute latch.
D0 - D7	I/O	Data lines D0 - D7.
V1A0 - V1A7	I	These are multiplexed address lines and contain the row, column, and CRT addresses.
VIDEO MEMR	I	This signal when high indicates that the video RAM is being accessed.
-AGDP	I	This line when low indicates that a CPU RAM cycle is occurring.
-DIS CAS0	O	This line is used to disable the system board CAS0 when a system microprocessor write is occurring in the expansion RAM.
CAS1	I	Column Address Strobe 1
-LCG	O	This line is used to instruct the system board that attributes or graphics data should be read from the expansion RAM card.
GATE	I	This line becomes the -LCG output.

<u>Signal</u>	<u>I/O</u>	<u>Description</u>
-WE1	I	This line instructs the memory that the a microprocessor write cycle is occurring.
SPL1	I	This line instructs the expansion RAM card to latch the data from the expansion RAM into the microprocessor latch.
-64KB CDIN	O	When a 64KB expansion RAM card is inserted, this line is low.

#### 4. System Options

##### 4.2. 128KB RAM Card

The 128KB RAM card expands the system's memory size by 128KB increments up to 512KB maximum. The 128KB RAM card plugs into the I/O Expansion Connector on the system board or Expansion unit. This memory is 150ns dynamic RAM. Address decode and refresh logic are included on this card. One card can be installed in the base system. When the Expansion unit is installed, a total of three cards can be installed. When installing the 128KB RAM cards, an address decoder jumper should be connected.

128 KB RAM Card	Jumper	Address Range	Address (English Mode)
1	1	00000~1FFFF	20000~3FFFF
2	2	20000~3FFFF	40000~5FFFF
3	3	40000~5FFFF	60000~7FFFF

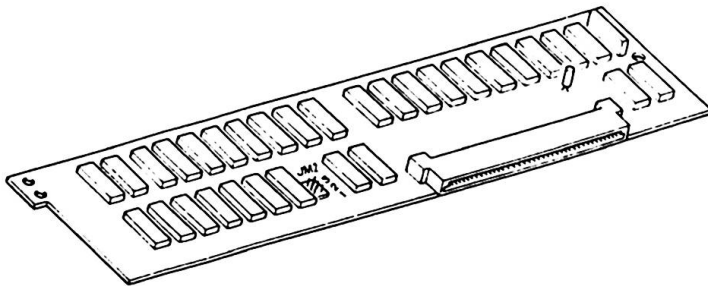


Figure 4-3 128KB RAM Card and Jumper

When the 128KB RAM card is installed with the jumper, base memory addresses are placed after the 128KB RAM addresses at the initial self-diagnostic power-on test or system reset. The base memory addresses, however, will not change in English mode.

(Reference: 2.2.7 Expansion Channel, 5.5 Memory Map)

### 4.3. Extension Video Card

The Extension Video Card consists of a gate array for Video Processor 3 (VP3), a 20MHz oscillator and a 32KB Expansion Video RAM. Enhanced video function (Extension Video mode) becomes possible when the Extension Video Card, Extension Video Mode Cartridge and the high-resolution display (12" monochrome or 14" color) are installed.

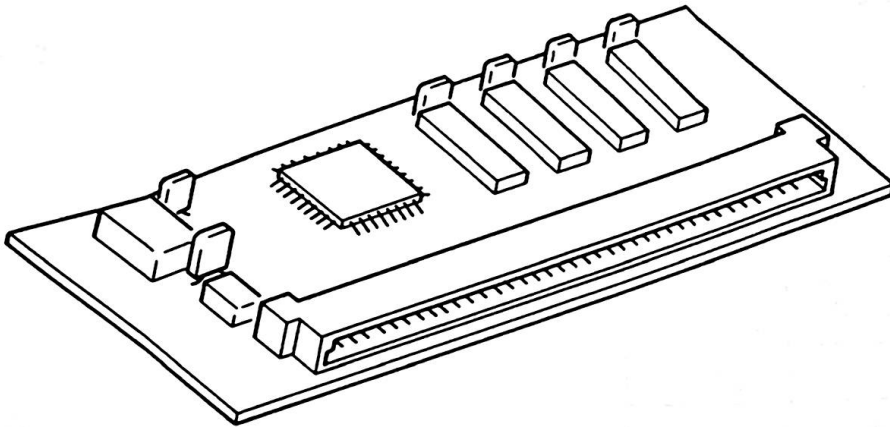


Figure 4-4 Extension Video Card

#### 4. System Options

The following are the connector pin assignments for the Extension Video Card:

Signal	Pin (A)	Pin (B)	Signal
V2A0	A1	B1	V2A1
V2A2	2	2	V2A3
V2A4	3	3	V2A5
V2A6	4	4	V2A7
-RAS2	5	5	-CAS2
-W10E	6	6	-W100
-GP10	7	7	-32K CD IN
VD0	8	8	VD1
VD2	9	9	VD3
VD4	10	10	VD5
VD6	11	11	VD7
VD8	12	12	VD9
VD10	13	13	VD11
VD12	14	14	VD13
VD14	15	15	VD15
+5V	16	16	+5V
Open	17	17	Open
GND	18	18	GND
-SX03	19	19	NCCLK3
DSPT0	20	20	CUSR
VSYC	21	21	HSYC
RA4	22	22	RA1
RA3	23	23	RA0
RA2	24	24	RPT3
ZOM	25	25	RQR3
DSPT3	26	26	RST3
GRM3	27	27	DST3
Open	28	28	OSC3
Open	29	29	Open
Open	30	30	Open
GD0	31	31	GD1
GD2	32	32	GD3
GD4	33	33	GD5
GD6	34	34	GD7
GATEE	35	35	Open
Open	36	36	-RESET
-IOW	37	37	-IOR
D0	38	38	D1
D2	39	39	D3
D4	40	40	D5
D6	41	41	D7
DUSW	42	42	Open
-B	43	43	-I
-R	44	44	-G
EHS	45	45	EVS

Figure 4-5 Connector for Extension Video Card (J5)



I/O refers to the view from the card side.

<u>Signal Name</u>	<u>I/O</u>	<u>Description</u>
V2A0 - V2A7	I	These lines are the multiplexed address of the expansion video RAM.
VDO - VD15	I	Data lines D0 - D15
-RAS 2	I	Row Address Strobe. This line instructs the Extension Video Card to latch up the address on the first MPX'd address.
-CAS 2	I	Column Address Strobe. This line instructs the expansion VRAM to latch up the address on the second MPX'd address.
-W10E	I	This line instructs the expansion VRAM to write the even addresses of the expansion VRAM.
-W10O	I	This line instructs the expansion VRAM to write the odd addresses of the expansion VRAM.
-GP10	I	This line instructs the expansion VRAM to gate out the expansion VRAM on a read option.
-32K CD IN	O	When an extension video card is inserted, this signal is low.
VDO - VD15	I/O	Video RAM I/O Data Bus line.
-SX03	I	When this signal is low, the VP3 gate array is selected.
NCCLK3	O	Inverted signal for "GATEE" (A35 pin)
DSPTO	I	Display Timing signal from CRTC. When high, the contents of VRAM read are displayed
CUSR	I	Video signal to display the cursor.
VSUNC	I	Vertical synchronous signal from CRTC
HSUNC	I	Horizontal synchronous signal from CRTC

#### 4. System Options

<u>Signal Name</u>	<u>I/O</u>	<u>Description</u>
RA0 - RA4	I	These lines are raster addresses from CRTC and are available in text mode. RA0 - RA4 are decoded and then the raster to be displayed in a character box is determined. Interlace mode is used.
PRT 3	0	This line is a timing signal to determine which of the CRTC, CPU or Refresh accesses VRAM2.
20M	0	20MHz clock output
RQR 3	0	This line is used to reset requests for use to VRAM1 and 2.
DSPT 3	0	This line is used to correct the access timing from CRTC to VRAM. This signal is derived by delaying "DSPT0" (A20 Pin) by one character.
RST 3	0	RAS Time
GRM 3	0	When VP3 is in graphics mode, this signal is high.
DST 3	0	This line is used as a timing pulse to keep the timing of memory timing data with the character generator timing.
CSC 3	0	This line is used as a chip select signal of a character generator.
GDO - GD7	I	These lines are image data signals for screen display.
-RESET	I	System Reset Signal
-IOW	I	I/O Write signal
-IOR	I	I/O Read signal
D0 - D7	I/O	Data bus D0 - D7

<u>Signal Name</u>	<u>I/O</u>	<u>Description</u>
DUSW	I	When this signal is high, VP3 operation is allowed. When low, the signal lines EHS, EVS, -R, -G, -B, -I float.
-B	O	Blue signal to CRT. When consecutive vertical lines are displayed, the frequency becomes maximum. (10MHz: Applied the same as the following -I, -R, -G)
-I	O	Intensity signal to CRT.
-R	O	Red signal to CRT.
-G	O	Green signal to CRT.
EHS	O	When the DUSW signal is high and bit 2 of VP3 gate array register 2 is "1" (synchronous), the HSYNC signal is output.
EVS	O	When the DUSW signal is high and bit 2 of VP3 gate array register 2 is "1" (synchronous), the VSYNC signal is output.

## 4. System Options

### 4.4. Diskette Drive Adapter

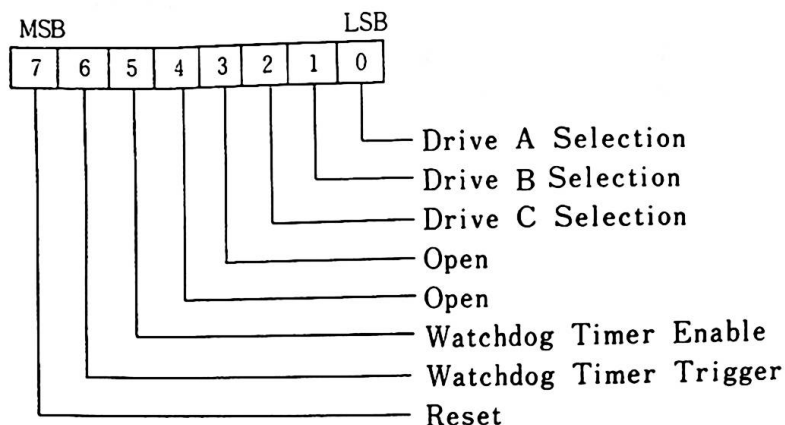
The diskette drive adapter consists of a single sub-board (card) and is connected to the system unit connector (30 pin) by a signal cable. It is attached to the three diskette drives through two kinds of signal cables. Power is supplied to each diskette drive via the diskette drive adapter connector.

The adapter uses the JPD765 or equivalent and is designed for Modified Frequency Modulation (MFM).

The following are descriptions of its components:

#### DOR (Digital Output Register)

The digital output register (DOR) is a write-only 8 bit register used to control the three diskette drive motors and selection of the diskette drives. The I/O address is F2 (Hex).



Bit 0 (Drive A Selection)  
"1" activates drive A signal path and turns on the motor.  
"0" deactivates it and turns off the motor.

Bit 1 (Drive B Selection)  
"1" activates drive B signal path and turns on the motor.  
"0" deactivates it and turns off the motor.

- Bit 2 "1" activates drive C signal path and turns on the motor.  
"0" deactivates it and turns off the motor.
- Bit 5 (WATCHDOG TIMER CONTROL)  
"1" activates functions of WATCHDOG TIMER CYCLE and allows interrupts.  
"0" deactivates functions of WATCHDOG TIMER CYCLE and rejects interrupts.
- Bit 6 (WATCHDOG TIMER TRIGGER)  
By alternating 1 and 0 being written, the timer cycle gets started.
- Bit 7 (reset)  
"1" resets diskette controller.  
"0" releases the reset status of the diskette controller.

### Watchdog Timer

The Watchdog Timer (WDT) is a one-to-three second timer connected for output to IRQ 6 (Interrupt Level 6) of the 8259A PIC. The Watchdog Timer is used for the system unit to watch the stopped status of the adapter.

### Diskette Controller

There are two kinds of registers:

Register	I/O Address
Status Register	F 4
Data Register	F 5

### Status Register

The 8-bit status register which can be read contains the status information of the Diskette Controller.

### Data Register

Data transmission between the diskette and the CPU are performed through this 8-bit data register. Commands and parameters which are transmitted between the CPU and the diskette controller are temporarily stored in this register.

#### 4. System Options

##### Programming Considerations

1. The diskette controller is initialized with the following parameters after system power up. Parameters are stored in the addresses which the interrupt vector 1E (Hex) indicates.

Contents	Parameter (Hex)	Remarks
Sector Size	02	512 Bytes/Sector
Sector Count	09	Sectors/Track (9 sector)
Head Unload	1	Always '1' (32ms)
Head Step Rate	E	4ms/step
Head Load Time	01	Minimum Head Load Time (4ms)
Format Gap	50	
Write Gap	2A	
Non-DMA Mode	01	
Fill byte for Format	19	

2. BIOS uses the following commands for the diskette drive adapter.

- SPECIFY
- SEEK
- RECALIBRATE
- SENSE INTERRUPT STATUS
- SENSE DRIVE STATUS
- READ DATA
- WRITE DATA
- FORMAT TRACK

3. Head Load

When a diskette is correctly inserted in a diskette drive and the drive is selected, the head is automatically loaded. Programming for head load is, therefore, not necessary. Read/Write commands cannot be accepted until after 0.5 seconds of head load.

4. Interrupt

The system should not allow other interrupts than interrupt level 6 (IRQ6 : used by diskette drive adapter). This is due to the strict time limitations during diskette read/write.

Figure 4-6

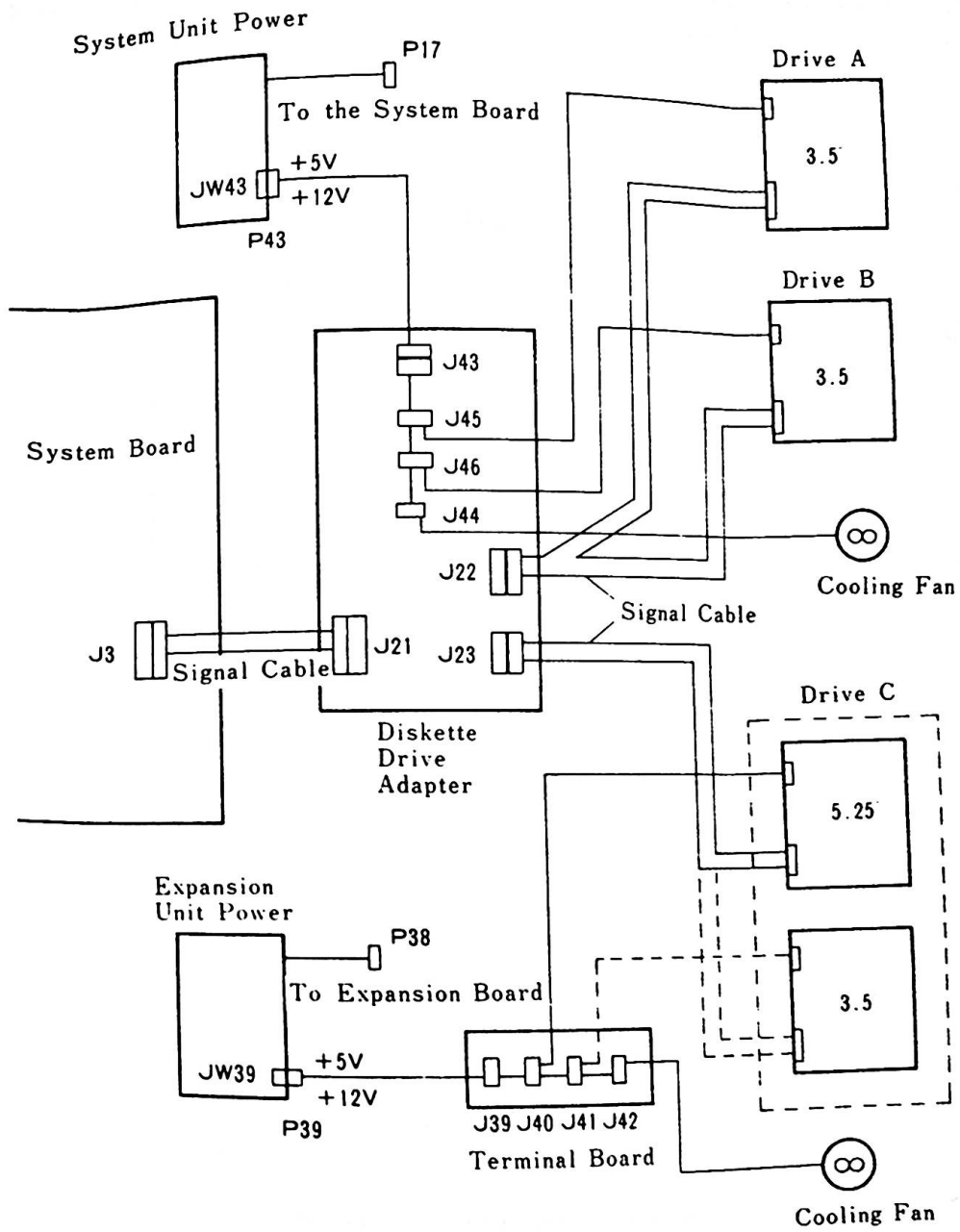
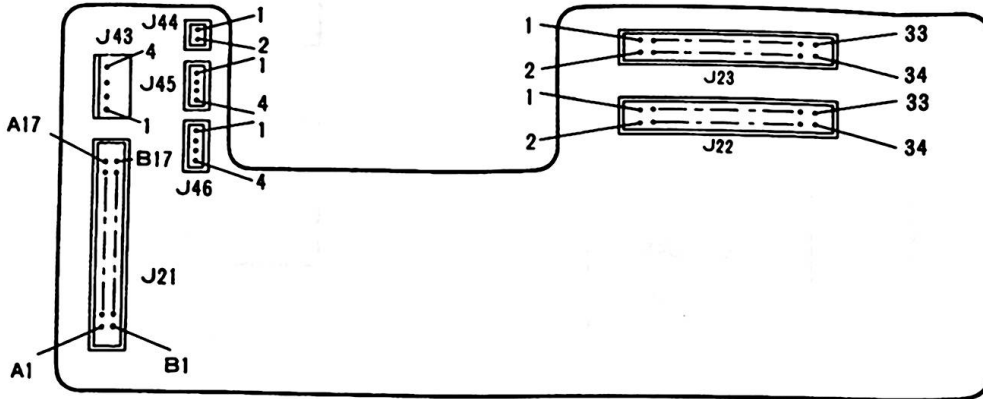


Figure 4-6 Diskette Drive Connections

#### 4. System Options

##### Interface Connector

The adapter is provided with the signal cable connector and power-supply connectors.



- J21 : Signal Cable Connector
- J22 : Signal Cable Connector (Drive A, B)
- J23 : Signal Cable Connector (Drive C)
- J43 : Power-supply Connector (From the Power Unit)
- J44 : Power-supply Connector (To the Cooling Fan)
- J45 : Power-supply Connector (To the Drive A)
- J46 : Power-supply Connector (To the Drive B)

Power for the diskette drive C is supplied from the power unit in the Expansion Unit.

Figure 4-7 The connector on the diskette drive adapter



## System Interface

The adapter is connected using the dedicated cable to the connector (J3) on the system board. Following are the pin assignments and their descriptions:

Signal	Pin (A)	Pin (B)	Signal
D0	A1	B1	D1
D2	2	2	D3
D4	3	3	D5
D6	4	4	D7
A0	5	5	A1
A2	6	6	A9
Open	7	7	Open
-IOR	8	8	-IOW
-RESET	9	9	GND
DSKT INTR	10	10	GND
-FDC CS	11	11	GND
GND	12	12	GND
+5V	13	13	GND
+5V	14	14	GND
+5V	15	15	GND
-FDC CD IN	16	16	GND

Figure 4-8 Connector for Diskette Drive Adapter (J3)

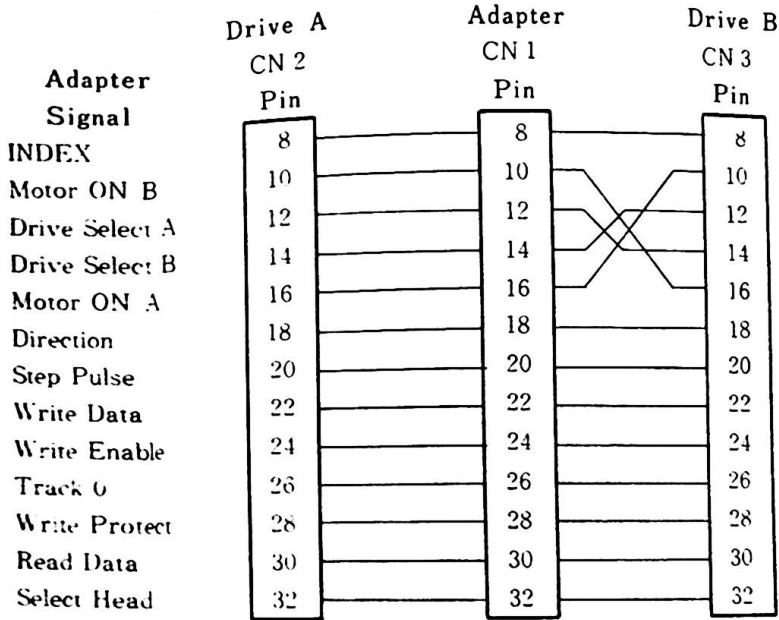
#### 4. System Options

I/O refers to the view from the card side.

<u>Signal Name</u>	<u>I/O</u>	<u>Description</u>
D7 - D0	I/O	These eight lines form a bus through which all commands, status and data are transferred.
A0 - A3	I	These lines are address lines used in register selection.
-IOW	I	Data are written in the register by the trailing edge of this signal.
-IOR	I	This line is used to read the contents of the register.
-RESET	I	This line is used to reset the controller and to clear DOR.
DSKT INTR	O	This line becomes "high" by time-out of WATCHDOG TIMER.
-FDC CS	I	This line remains "low" while the CPU performs read/write to a diskette drive adapter
A9	I	Address line A9.

## Drive Interface

Two connectors are provided on the card for connection to the diskette drive. One is for the two diskette drives in the system unit (Fig. 4 - 9, 1 of 2) and the other is for the diskette drive in an Expansion unit (Fig. 4 - 9, 2 of 2). Signals are at standard TTL levels.



- Remarks) 1. Odd number pins from 5 through 33 are GND, and pins 1 through 4 are open.  
2. CN2 is connected to drive A, while CN3 is connected to drive B.

Figure 4-9 Diskette Drive Signal Cable (1 of 2)

#### 4. System Options

Adapter Signal	Adapter CN 4 Pin	Drive C CN 5 Pin	Drive-side Signal
INDEX	8	8	INDEX
Open	10	10	Open
Drive Select C	12	12	Drive Select
Open	14	14	Open
Motor ON C	16	16	Motor ON
Direction	18	18	Direction
Step Pulse	20	20	Step Pulse
Write Data	22	22	Write Data
Write Enable	24	24	Write Enable
Track 0	26	26	Track 0
Write Protect	28	28	Write Protect
Read Data	30	30	Read Data
Select Head	32	32	Select Head

- Remarks) 1. Odd number pins from 5 through 33 are GND, and pins 1 through 4 are open.  
 2. CN5 is connected to drive C.

Figure 4-9 Diskette Drive Signal Cable (2 of 2)

I/O refers to the view from the adapter side.

<u>Signal</u> -----	<u>I/O</u>	<u>Description</u> -----
-DRIVE SELECT	0	This line is used to select the diskette drive.
-MOTOR ON	0	This line is used to turn on the drive motor.
-STEP PULSE	0	This line is used to move the head by 1 cylinder.
-DIRECTION	0	This line is used to set the moving direction of the head. When this line is low, the head moves inward.
-WRITE DATA	0	When WRITE ENABLE (pin 24) is low, the change of the signal is written on the diskette.
-SELECT HEAD	0	When "high", diskette side 1 is selected : When "low", diskette side 2 is selected.
-INDEX	I	Each time the diskette goes round, one pulse (index pulse) is issued.
-WRITE PROTECT	0	This line is used to indicate that the diskette is write-protected.
-TRACK 0	I	This line is used to indicate that the head is located at track 0.
-READ DATA	I	The selected drive must supply a pulse on this line each time when magnetic flux change is encountered on the diskette.
-WRITE ENABLE	0	When "low", a data write to the diskette is possible.

#### 4. System Options

##### Power Supply Connector

The power supply connector provides each drive with direct current from the power unit. It also provides the cooling fan with +12V.

Pin No.	Signal
1	+ 5 V
2	GND
3	GND
4	+ 12 V

Pin No.	Signal
1	GND
2	+ 12 V

J44

J43, J45, J46

Figure 4-10 Power Supply Connector on the Diskette Adapter

#### 4.5. Diskette Drive

By installing an optional diskette drive, the IBM 5510 can be expanded to include a maximum of three 3.5" diskette drives or two 3.5" and one 5.25" diskette drives. Both 3.5" and 5.25" diskettes have the same format and have the following characteristics:

- Double-sided double-density
- 80 tracks per side
- 9 sectors per track
- 512 bytes per sector
- Seek time 4ms per track
- Write-protect sensor
- Cooling fan
- Track 00 sensor
- Index sensor
- +5VDC, +12VDC used



## 4. System Options

### 4.6. TV Adapter

The TV adapter includes the RF Modulator and allows an ordinary TV set to be used as a display by getting the RGBI direct video signal through the encoder and modulating it to the frequencies of channel 1 or channel 2.

The adapter has two switches, one for changing the channel (channel 1 or 2) and the other for color mode (B/W or color). The RF modulator output is automatically directed to the TV set at the time of system power-on. When the system unit is turned off, the normal TV broadcasting signal is received. Encoder output becomes the external output as a composite video signal.

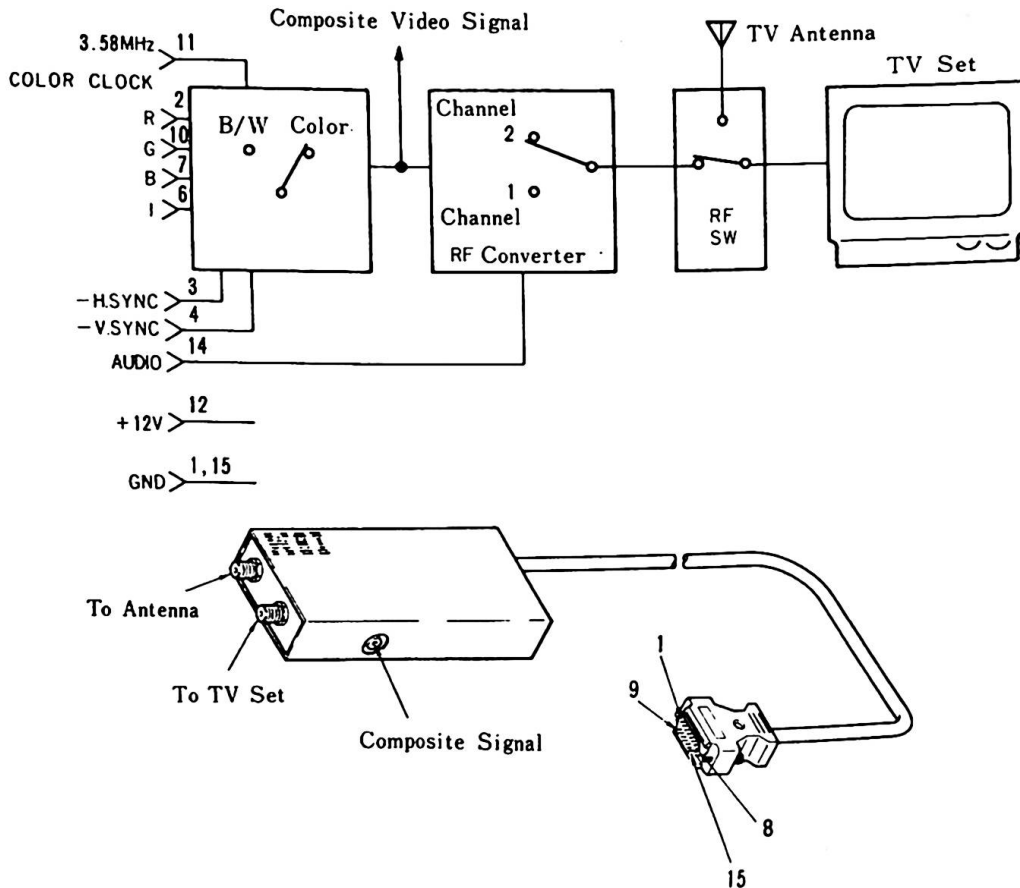


Figure 4-11 TV Adapter



## 4.7. Keyboard Cable

The IBM 5510 Cordless Keyboard can be attached to the JX using the optional Keyboard Cable. When the keyboard cable is connected, the signal level of -CBL CONNECTED becomes "low" (0 Volt) and the system unit's infrared (IR) receiver circuit is disabled, allowing keyboard input via the keyboard cable.

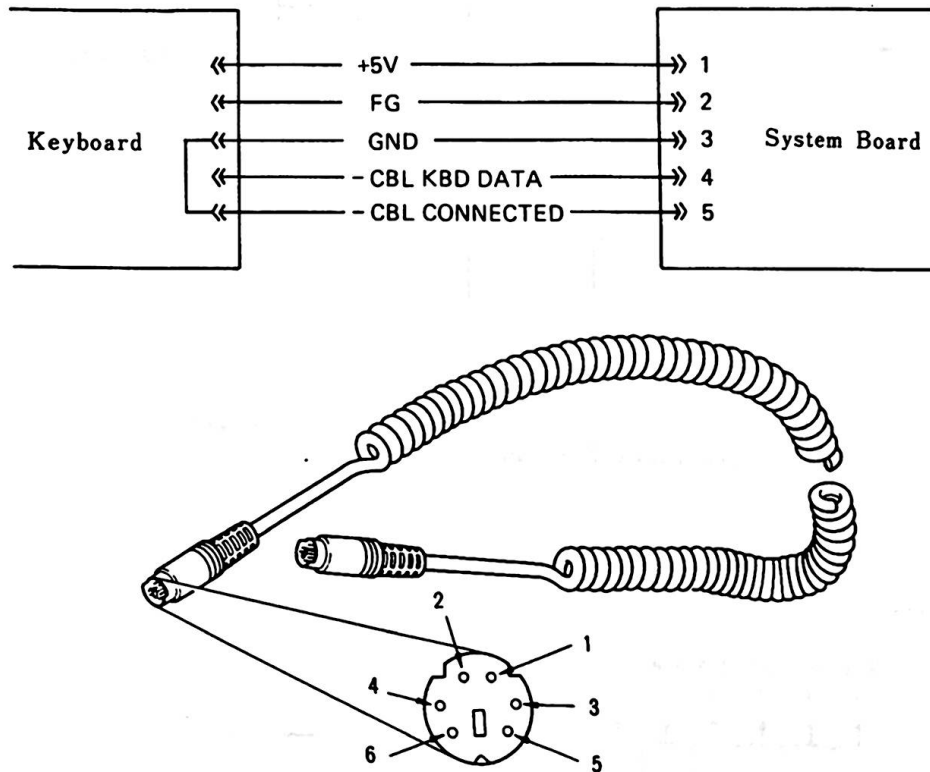


Figure 4-12 Keyboard Cable

System Options

. RS-232C Card

RS-232C card allows asynchronous communication under the program control. It contains an INS8250A LSI chip.

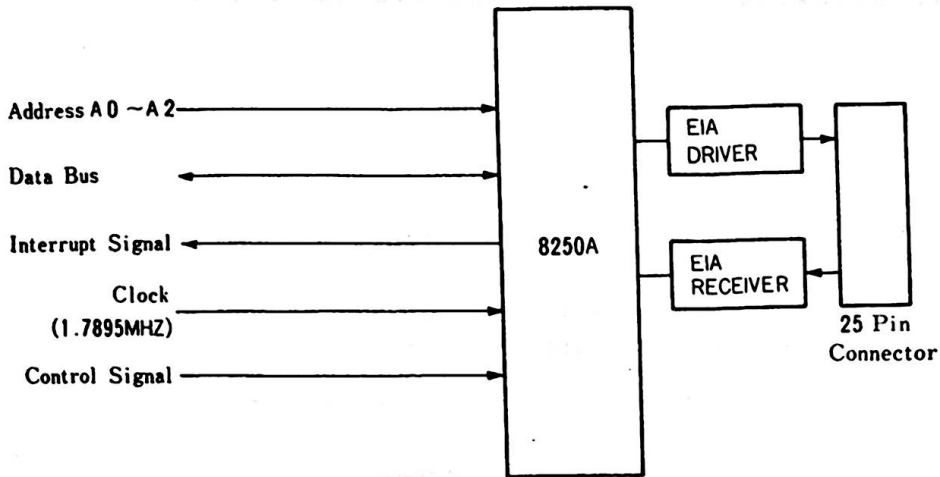


Figure 4-13 RS-232C Card Block Diagram

The following is a standard send/receive data format of asynchronous communications:

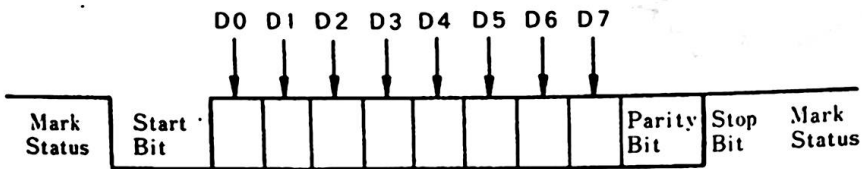


Figure 4-14 Asynchronous Communication Data Format

Following are the programming considerations and the necessary data for use with the IBM JX.

1. When a diskette read/write is performed, asynchronous communications can not be made. (Interrupts are prohibited.)
2. The speed of asynchronous communication is up to 4800 bps. A speed below 1200 bps is recommended when keyboard data are received.
3. Consecutive I/O operations to the 8250A should not be made in view of the necessary I/O time for the 8250A. An interval of more than 15 clocks is necessary.
4. The hardware interrupt level is "3".
5. 8250A pin 34 (OUT1) and pin 31 (OUT2) are not used.
6. I/O addresses are as follows:

I/O Address (Hex)	Register	DLAB Status
2F8	TX Buffer	DLAB=0 (Write)
2F8	RX Buffer	DLAB=0 (Read)
2F8	Divisor Latch(LSB)	DLAB=1
2F9	Divisor Latch(MSB)	DLAB=1
2F9	Interrupt Enable Register	DLAB=0
2FA	Interrupt Identification Registers	
2FB	Line Control Register	
2FC	Modem Control Register	
2FD	Line Status Register	
2FE	Modem Status Register	
2FF	Scratch Register	

Figure 4-15 8250 I/O Addresses

#### 4. System Options

7. The following Assembler language program initializes the 8250A. Operating conditions are:

- 1200 bps
- 8 bits
- 1 stop bit
- Odd parity

Sample program:

```
PROC    NEAR
MOV     AL,80H           ; SET DLAB=1
MOV     DX,2FBH         ; To Line Control Register
OUT     DX,AL
JMP     $+2             ; I/O Delay
MOV     DX,2F8H         ; LSB of Divisor Latch
MOV     AL,5DH          ; LSB Value
OUT     DX,AL
JMP     $+2             ; I/O Delay
MOV     DX,2F9H         ; MSB of Divisor Latch
MOV     AL,00H
OUT     DX,AL
JMP     $+2             ; I/O Delay
MOV     DX,2FBH         ; Line Control Register
MOV     AL,0BH          ; 8 Bits/Word, 1 Stop Bit,
                        ; Odd Parity, DLAB = 0
OUT     DX,AL
JMP     $+2             ; I/O Delay
MOV     DX,2F8H
IN      AL,DX
ENDP
```

8. RS-232C card connector specifications are as follows:

Signal	Pin (A)	Pin (B)	Signal
D0	A1	B1	D1
D2	2	2	D3
D4	3	3	D5
D6	4	4	D7
A0	5	5	A1
A2	6	6	A9
-SERIAL CD IN	7	7	Open
BAUD CLK	8	8	RESET
SERIAL INTR	9	9	-8250 CS
-IOR	10	10	-IOW
Open	11	11	Open
+12V	12	12	-12V
Open	13	13	Open
+5V	14	14	+5V
Open	15	15	Open
GND	16	16	GND

Figure 4-16 RS-232C Card Connector (J8)

#### 4. System Options

Signal Name	I/O	Description
D0 - D7	I/O	Data Lines D0 - D7.
A0,1,2,9	I	Address Lines A0, A1, A2, and A9.
-SERIAL CD IN	0	When an RS-232C card is inserted, this signal becomes "low".
BAUD CLOCK	I	1.7895 MHz Clock input.
RESET	I	Signal for RS-232C card reset.
SERIAL INTR	0	Interrupt request signal.
-8250 CS	I	8250 LSI Chip Select Signal.
-IOR	I	I/O Read
-IOW	I	I/O Write

9. This card provides an EIA RS-232C electrically compatible interface for connection to external devices.

Pin No.	Signal Name	Signal Level
2	TRANSMIT DATA	Valid signal levels for all pins except 1 and 7 are: + : +3V ~ +15V - : -3V ~ -15V
3	RECEIVE DATA	
4	REQUEST TO SEND	
5	CLEAR TO SEND	
6	DATA SET READY	
7	GND	
8	CARRIER DETECT	
20	DATA TERMINAL READY	
1	FG	

Figure 4-17 RS-232C External Interface

## 10. Baud Rate Generator

The baud rate generator which resides within the 8250A generates the clock signal that is the basis for data transfer speed (baud rate) by dividing the 1.7895MHz clock input by the programmable divisor.

The output clock frequency of the baud rate generator should be set with the product of 16 multiplied by the baud rate. The divisor of the baud rate generator is calculated as follows:

$$\text{Divisor} = (1.7895 \times 10^6) \div (\text{Baud Rate} \times 16)$$

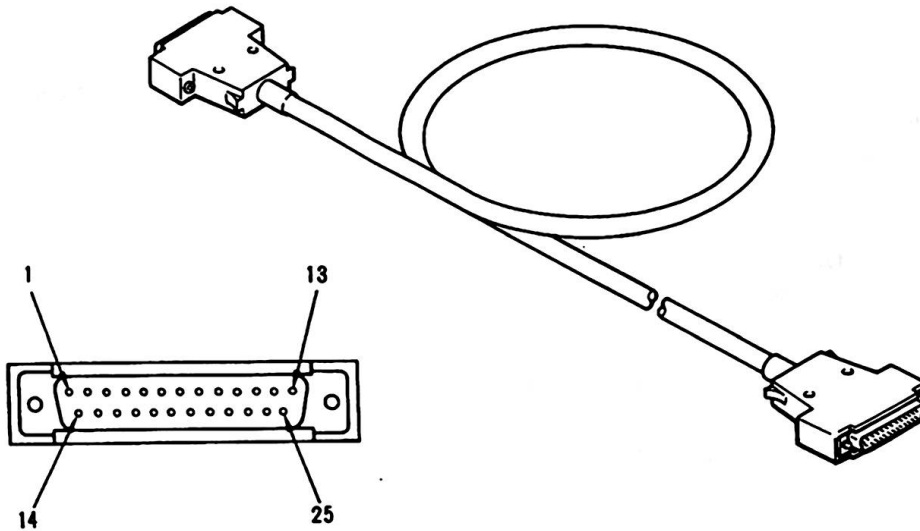
Sample :

Required Baud Rate	Divisor Value (Hex)	Variance
50	8BD	0.006
75	5D3	0.017
110	1A1	0.023
134.5	167	0.054
150	12C	0.050
300	175	0.050
600	0BA	0.218
1200	05D	0.218
1800	03E	0.218
2000	03B	0.140
2400	02F	0.855
3600	01F	0.218
4800	017	1.291

#### 4. System Options

##### 4.9. RS-232C Cable

This IBM 5510 optional feature is provided with a 25-pin, "D" shaped connector. The cable is used to connect the RS-232C card with serial and asynchronous communication devices.



(Reference : 4.8 RS-232C Card)

Figure 4-18 RS-232C Cable



## 4.10. Display

There are three types of displays:

- 12" Color Display
- 12" Monochrome Display
- 14" Color Display

The 12" Monochrome or 14" Color display is required for operation in Extension Video Mode. These are dual-scan displays and they work in all operational modes. The 12" color display can be used in Native and English modes.

Their characteristics are as follows:

Contents	12" Color	14" Color	12" Mono.
Video Frequency	14.318 MHz	20.000 MHz 14.318 MHz	20.000 MHz 14.318 MHz
Vertical Scan	59.92 Hz	76.68 Hz 59.92 Hz	76.68 Hz 59.92 Hz
Horizontal Scan	15.700 KHz	21.930 KHz 15.700 KHz	21.930 KHz 15.700 KHz
No. of Colors	16 Color	16 Color	Gray Scale 16
Dots (Graphics)	640 × 200	720 × 512 * 640 × 200	720 × 512 * 640 × 200
Dots (Text)	640 × 200	720 × 525 * 640 × 200	720 × 525 * 640 × 200
Characters (Hankaku)	80 × 11	80 × 25 80 × 11	80 × 25 80 × 11
Character Box (Hankaku)	8 × 18	9 × 21 8 × 18	9 × 21 8 × 18

Remark) \* is for Extension Video Mode.

## 4. System Options

### 4.11. CMT Cable

The CMT cable connects the system unit with a cassette recorder. Connector pin assignments are as follows:

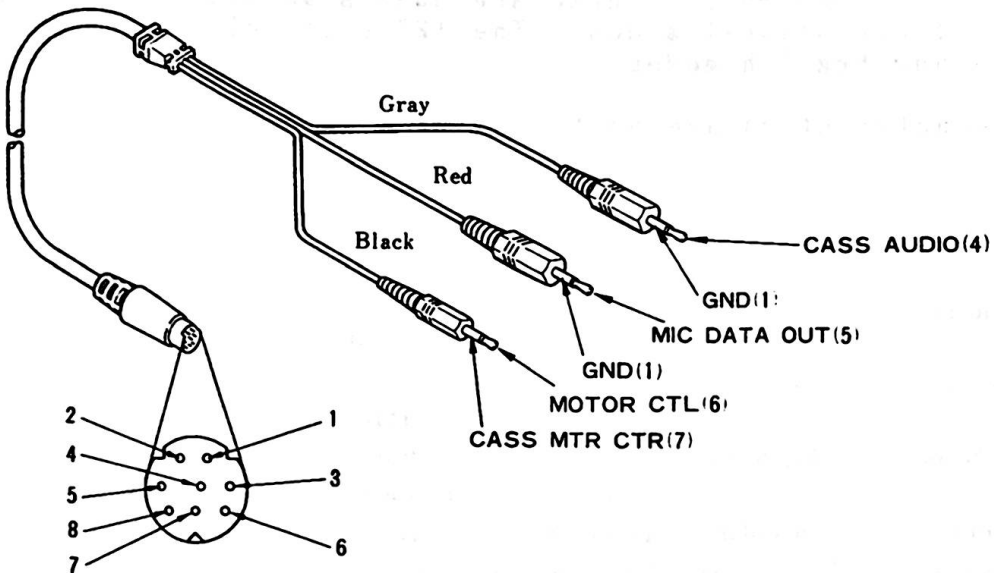


Figure 4-19 Cassette Cable Pin Assignments

## 4.12. Joystick

The joystick is an input device with the location control function indicated in X/Y coordinates. It consists of two switches and two potentiometers. By moving the operational stick vertically (Y coordinate) or horizontally (X coordinate), each potentiometer varies within a range from 0 to 100K Ohms. A maximum of two joysticks can be installed and they are connected to connector J13 (joystick 1) and J14 (joystick 2).

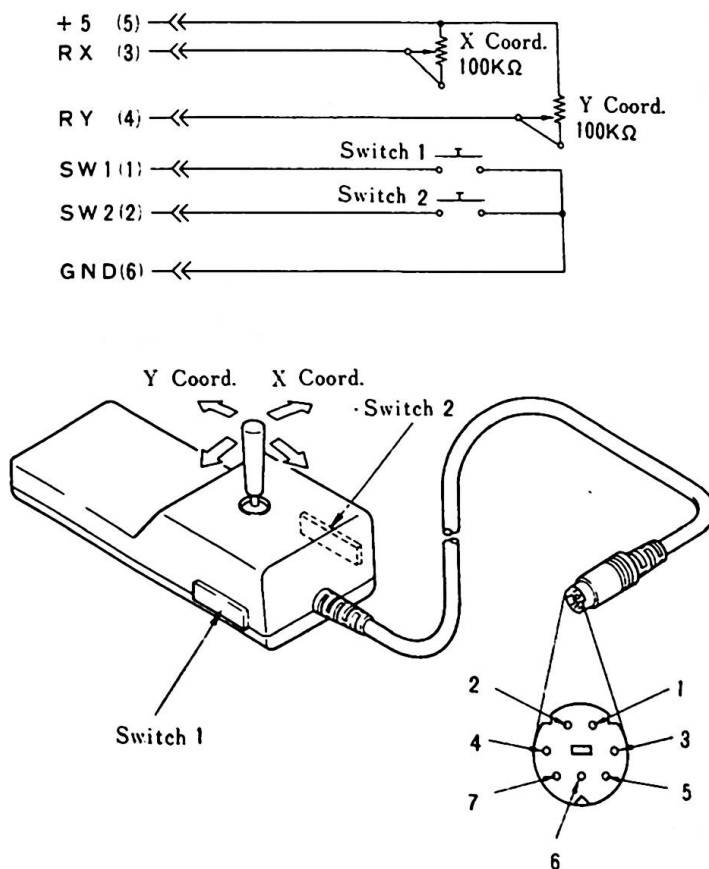
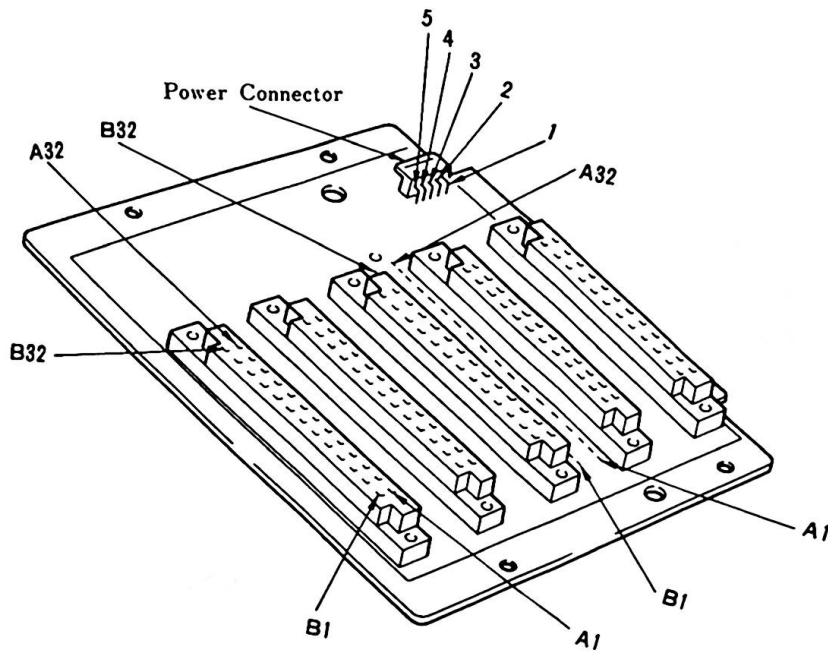


Figure 4-20 Joystick

## 4. System Options

### 4.13. Expansion Board

The Expansion Board is connected to the I/O channel via the expansion adapter. There are five 64-pin connectors on the board. The pin assignments are the same as those for the I/O channel. (Reference: 2.2.7 Expansion Channel)



Power Connector

Pin No.	Signal
1	GND
2	GND
3	+5V
4	+12V
5	-12V

Figure 4-21 Expansion Board and Pin Assignments

## 4.14. Expansion Unit

The expansion unit contains a power unit whose power capacity is the same as that of the system unit. An expansion board and diskette drive C (either 3.5" or 5.25") can be optionally installed. Alternating current is supplied from the connector on the system unit.

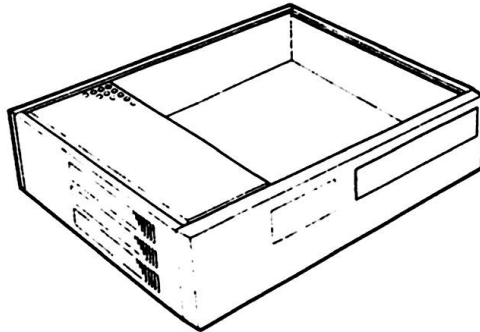


Figure 4-22 Expansion Unit



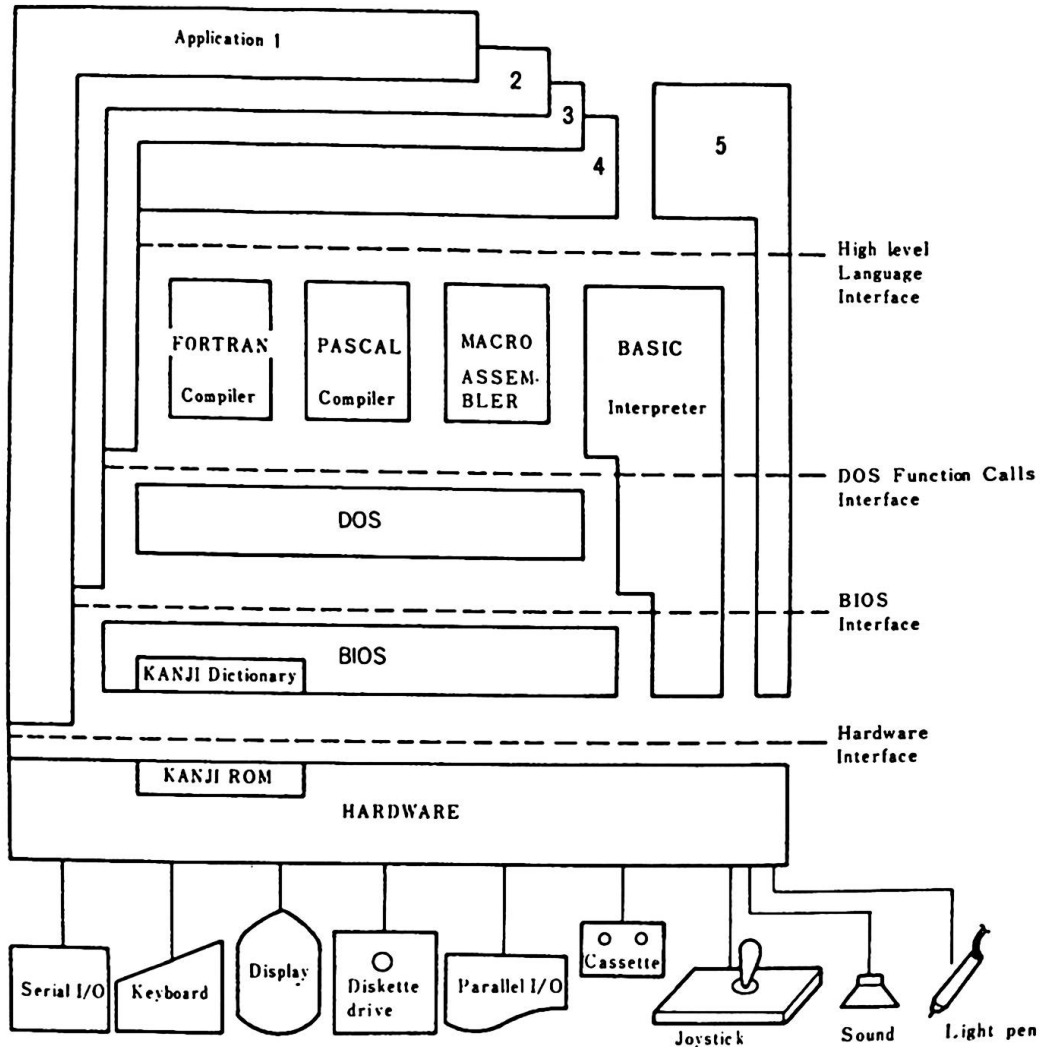
## 5. Software

This chapter contains information about system software, especially information about putting BIOS to practical use.

## 5. Software

### 5.1. Software Structure

The IBW 5510 is utilized by the FORTRAN, PASCAL, ASSEMBLER, and BASIC programming languages in Native or Extension Video mode. BIOS is the interface between software and hardware.



Application 1: by Assembly Language or High level Language using hardware interface

Application 2: by Assembly Language using BIOS interface

Application 3: by Assembly Language using DOS interface

Application 4: by High level Language

Figure 5-1 Software structure



## 5.2. System Software

The Basic Input/Output System (BIOS) of JX Native mode resides in ROM on the system board and provides device level control for the major I/O devices in the system. In Extension Video mode, the initialization routines, video I/O and keyboard I/O routines are replaced by code which resides in the optional Extension Video mode cartridge. The other BIOS routines are shared with Native mode. In English mode, the initialization routines are replaced by code which resides in the optional English mode cartridge. The value at address FFFFE indicates whether English mode or one of the other two modes is currently active.

Mode	Contents of Address FFFFE (Hex)
English mode	FD (Hex)
Native mode / Extension Video mode	ED (Hex)

Distinction between Native or Extension Video mode is made possible by reading the AL register after issuing INT 11.

Bit 5,4 in AL

1 0 : Extension video mode  
0 1 : Native mode

Figure 5-2 shows a map of system software routines in ROM.

5. Software

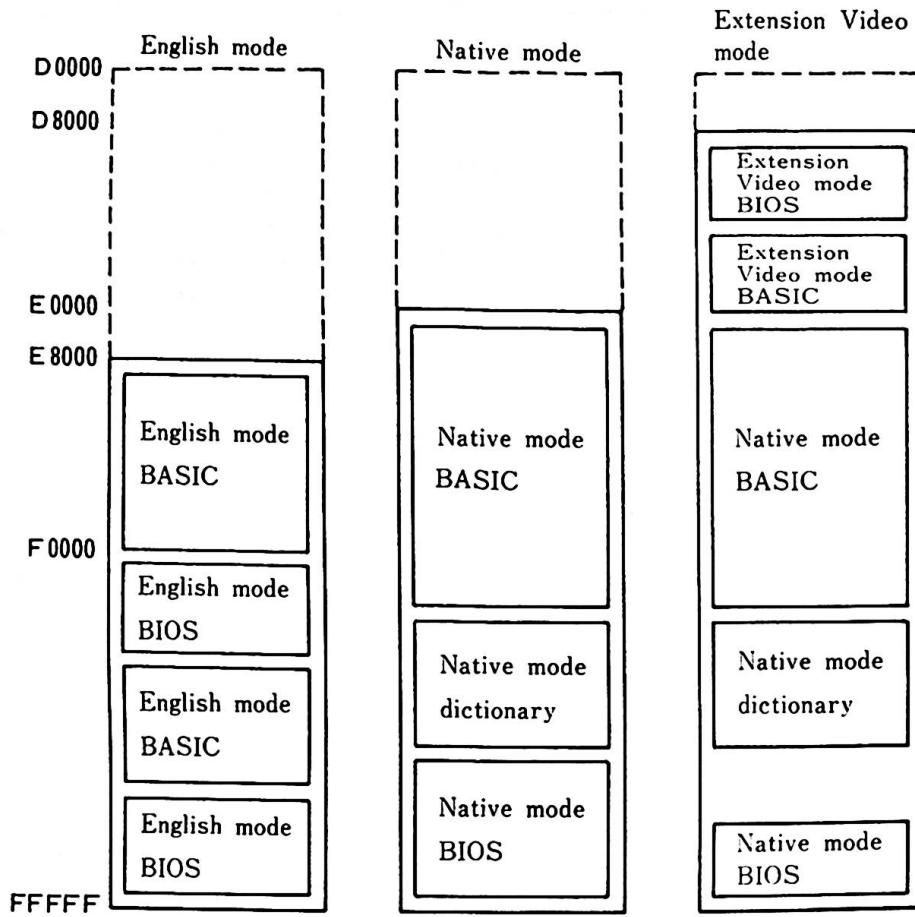


Figure 5-2 System Software Map

### 5.3. BIOS Usage

Access to BIOS is made through the software interrupts. All data and parameters passed to and from the BIOS routines go through the registers of the MPU(8088).

If a BIOS routine supports several possible functions, the AH register indicates the desired function.

For example, the following code can be used to set or to read the time-of-day.

To set time-of-day:

```

MOV    AH, 1           ; function is to set time-of-day
MOV    CX, [HIGH COUNT]
MOV    DX, [LOW COUNT]
INT    1AH            ; software interrupt

```

To read time-of-day: ( CX and DX get values of TOD )

```

MOV    AH, 0           ; function is to read time-of-day
INT    1AH            ; software interrupt

```

Generally, the BIOS routines save all registers except for AX and the flags.

#### BIOS Programming Guidelines

1. To invoke the BIOS code use Software interrupts. Do not 'hard code' BIOS addresses into applications. The internal workings and absolute addresses in BIOS are subject to change without notice.
2. When any error is detected in diskette operation, the diskette drive adapter must be reset before retrying the operation. A specified number of retries should be required on diskette "read" to insure that the problem is not due to motor start-up.
3. When altering I/O port bit values, change only those bits which are necessary to the current task. Upon completion, restore the original environment. Failure to adhere to this practice may cause incompatibility between present and future systems.

## 5. Software

The following are the BIOS interrupt vectors explained in this chapter.

Vector Address	Type of Interrupts	Function
40-43	10	Video I/O
44-47	11	System configuration
48-4B	12	Memory size definition
4C-4F	13	Diskette I/O
50-53	14	ASYNC port I/O
54-57	15	Cassette I/O
58-5B	16	Keyboard I/O
5C-5F	17	Printer I/O
60-63	18	Resident(ROM) BASIC
64-67	19	System reset
68-6B	1A	Time of Day
6C-6F	1B	Keyboard Break address
70-73	1C	Timer
74-77	1D	Video parameter
78-7B	1E	Diskette Parmeter
	49	Conversion table
	7A	Dictionary pointer

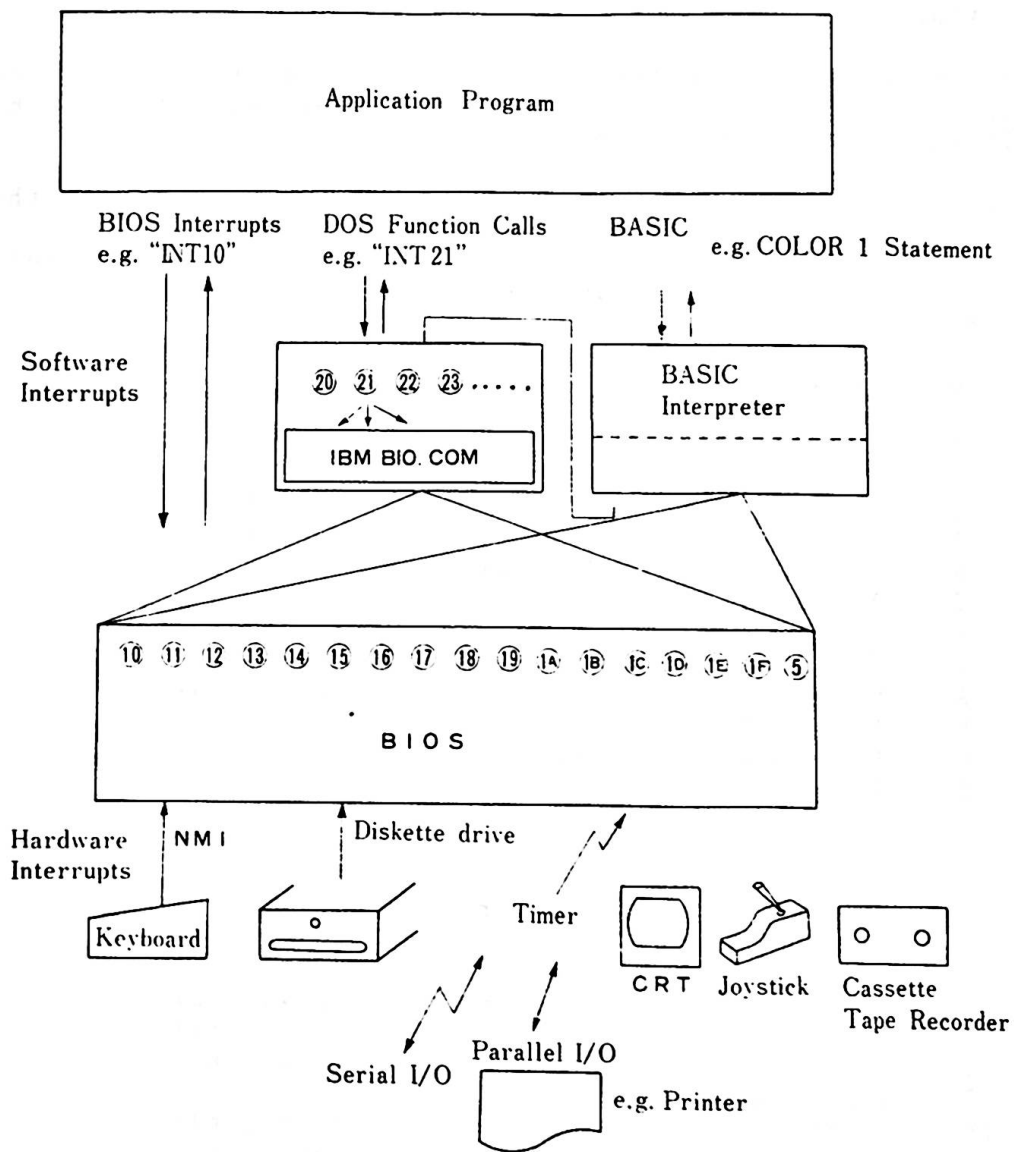


Figure 5-3 Software Interrupt Architecture

## 5. Software

### 5.3.1. Native Mode BIOS Interrupts

#### TYPE 10 Video I/O

This vector points to the the code to be executed when video I/O operation is needed. The function performed depends on the value placed in the AH register.

( AH ) = 0 : SET MODE The AL register is used to set the desired video display mode. When AL bit 7 is "1", VRAM contents are not cleared.

( AL )	MODE		
0	40X 25	16 color	ANK
1	40X 25	16 color	ANK
2	80X 25	16 color	ANK
3	80X 25	16 color	ANK
4	320X200	4 color	(40X25 ANK)
5	320X200	4 color	(40X25 ANK)
6	640X200	2 color	(80X25 ANK)
7	Not used		
8	160X200	16 color	(20X25 ANK)
9	320X200	16 color	(40X25 ANK)
A	640X200	4 color	(80X25 ANK)
B	Not used		
C	Not used		
D	Not used		
E	Not used		
F	Not used		
10	20X 11	8 color	KJ
11	20X 11	8 color	KJ
12	40X 11	8 color	KJ
13	40X 11	8 color	KJ
14	320X200	4 color	(20X11 KJ)
15	320X200	4 color	(20X11 KJ)
16	640X200	2 color	(40X11 KJ)
17	Not used		
18	160X200	16 color	(10X11 KJ)
19	320X200	16 color	(20X11 KJ)
1A	640X200	4 color	(40X11 KJ)
1B	640X200	16 color	(40X11 KJ)

#### REMARKS )

ANK : Alphanumeric, Special character, Katakana  
KJ : Kanji

- ( AH )= 1      Set cursor type. The cursor type is specified by the following bits in the CX register:
- |                |                             |
|----------------|-----------------------------|
| Bit 14,13 = 00 | Non-Blink                   |
| = 01           | Non-Display                 |
| = 10           | Blink at 4 times per second |
| = 11           | Blink at 2 times per second |
- Blinking is not supported in graphics mode.
- |          |   |
|----------|---|
| Bit 12-8 | Start line of the cursor in a character box |
| Bit 4-0  | End line of the cursor in a character box   |
- ( AH )= 2      Set Cursor position. The cursor position (row,col) is specified by values in ( DH,DL ). A sub page (16 KB page is further separated into 1 KB or 2 KB units) is specified in (BH). In graphics mode, a sub page is set to 00 in (BH). (0,0) indicates the home (upper left) position.
- ( AH )= 3      Read cursor position. When sub-page number is specified in (BH), the current cursor position (DH=row,DL=col) and the cursor type (CH,CL) are read in.
- ( AH )= 4      Read Light pen position.
- |         |  |
|---------|--|
| (AL)=0  | : Light pen switch is not pressed.       |
| (AL)=1  | : Valid light pen value is in registers. |
| (DH,DL) | : light-pen position (row,col)           |
| (CH)    | : raster value (0-199)                   |
| (BH)    | : column value (0-319,0-639)             |

## 5. Software

- ( AH )= 5      Select active page.  
(AL)=00-0F : sub-page ( 16 KB page is further separated into 1 KB or 2 KB units ) is specified.  
(AL)=80 : Read CRT/CPU page-registers  
(AL)=81 : Write the contents of (BL) to CPU page-register. CPU page mode is specified in (CL).  
(AL)=82 : Write the contents of (BH) to CRT page-register  
(AL)=83 : Write the contents of (BL) and (BH) to CPU and CRT page-registers. CPU page mode is specified in (CL).
- ( AH )= 6      Scroll display upward. (CH,CL) specifies the upper left corner of the portion to be scrolled (CH=row,CL=column). (DH,DL) is the lower right corner (DH=row,DL=column). AL is the number of lines to be scrolled. BH holds the attributes for the space left.  
AL=0 clears the area defined by CX and DX.
- ( AH )= 7      Scrolls display downward. Same as ( AH )= 6.
- ( AH )= 8      Read a character ( into AL ) and its attributes (into AH ) at the current cursor position. A sub page is specified in (BH). Full size characters return the following attributes:  
  
1st byte 1XXX 0XXX (left half of a character )  
2nd byte 1XXX 1XXX (right half of a character)
- The attributes returned are the same as those written when specifying ( AH )= 9.
- All the attributes are supported in graphics mode.
- In color graphics mode, the color attribute bits of AH have no affect on the color of the characters.



( AH ) = 9      Writes one or more copies of the character in AL and its attributes in BL starting at the current cursor position. CX contains a count of the number of characters to be written. The attributes of full size characters change after the 2nd byte is written.

The meanings of the attribute bits are explained in Chapter 3, " 3.4 Display function of VP1 and VP2 "

( AH ) = A      Write characters only. Same as (AH)=9 , except that attributes are not written.

5. Software

( AH )= B

Set a color in the palette.  
 Specify in BL the color number to be assigned to  
 the specified palette number.  
 (BH)=0 : Set color number for background  
 (BH)=1 : Select the palette number to be used

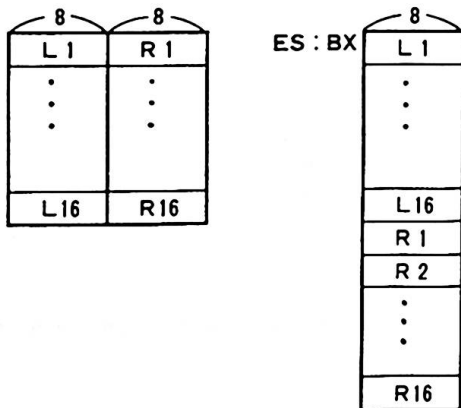
color number	2-color		4-color		8-color	16-color
	BL=0	BL=1	BL=0	BL=1		
1	white	black	green	light blue	blue	blue
2			red	purple	green	green
3			yellow	white	light blue	light blue
4					red	red
5					purple	purple
6					yellow	yellow
7					white	white
8						gray
9						bright blue
10						bright green
11						bright light blue
12						bright red
13						bright purple
14						bright yellow
15						bright white

Remarks) Color number 0 specifies border color in 40X11 or  
 80X11 character mode.  
 In graphics mode, color number 0 specifies border and  
 background color.

- ( AH )= C     Write a dot. (DX,CX) specifies the bit position (row,column) where the dot is to be written. (0,0) represents the home position on the display. Both the row and the column numbers are in units of dots, not characters. In color mode, AL specifies the palette number for the color dot to be written. If the eighth bit (bit 7) in AL is specified as 0, the value of AL will be written directly. If the value in bit 7 is set to 1, however, the current value of the dot will be XOR'ed (exclusive OR) with bit 0 and the result will be written. This function works only in graphics mode.
- ( AH )= D     Read a dot. (DX,CX) specifies the (row,col) of the dot position to be read ( both the row and column number are in units of dots, not characters ). The dot value (on/off) of a dot is read into AL. The function only works in graphics mode.
- ( AH )= E     ASCII teletype routine for output. Writes a character in the cursor position and advances the cursor. If the cursor is already in the rightmost position in line 10, the screen is scrolled upward.
- The character to be written is specified in AL.
- In graphics mode, color is specified in (BL). The status/mode symbols line is located outside the screen scroll area.
- If the cursor is at the last position in a row and a full size character is to be written, a space is entered at the last position, and the character is written at the first position in the next line.
- This function also works in graphics mode.
- ( AH )= F     Get the current display status. AL receives the current mode, AH receives the number of character columns displayed and BH receives the number of the sub-page.

5. Software

- ( AH )=10      Set Palette Register.      Set the parameter in (AL).
- (AL)=0      : Specifies the number (00H-0FH) of the palette register in (BL), and the color number in (BH).
- (AL)=1      : Sets contents of (BH) in the border color register.
- (AL)=2      : Sets the palette register and the border color register. ES:DX points to a 17 byte list. Bytes 0-15 are written to palette registers 0-15. Byte 16 is written to the border color register.
- ( AH )=11-12      Reserved
- ( AH )=13      Request a character font.      Return the character font for the specified character in the user-designated memory location. CX must hold the internal code of the requested character. CH must be 0 for half size characters. AL must be 0. The font will be placed in the memory location designated by ( ES:BX ).



( AH )=14

## Superimpose

- (AL)= 0 : Mode is specified in (BH). VRAM is not cleared if bit 7 of (BH) is 1.
- (BH)=0-3 not used
  - (BH)=4 320X200 4 colors : 40X25 ANK
  - (BH)=5 320X200 4 colors : 40X25 ANK
  - (BH)=6 640X200 2 colors : 80X25 ANK
  - (BH)=7 not used
  - (BH)=8 160X200 16 colors: 20X25 ANK
  - (BH)=9 320X200 16 colors: 40X25 ANK
  - (BH)=A 640X200 4 colors
  - (BH)=B-13 not used
  - (BH)=14 320X200 4 colors : 20X11 KANJI
  - (BH)=15 320X200 4 colors : 20X11 KANJI
  - (BH)=16 640X200 2 colors : 40X11 KANJI
  - (BH)=17 not used
  - (BH)=18 160X200 16 colors: 10X11 KANJI
  - (BH)=19 320X200 16 colors: 20X11 KANJI
  - (BH)=1A 640X200 4 colors : 40X11 KANJI
- (AL)= 1 : (BH)=1 Superimpose is set to enable  
(BH)=0 Superimpose is set to disable
- (AL)= 2 : (BH)=0 VRAM 1 is set for a foreground page.
- (AL)= 3 : Set a transparent color to the palette register specified in (BH).
- (AL)= 4 : Set superimpose mode through (BH).
- (BH)=0 priority
  - (BH)=1 XOR
  - (BH)=2 AND
  - (BH)=3 OR

## 5. Software

### TYPE 11 Device Configuration Status

The device configuration status is returned in AX.  
The information specified by the bits in AX is listed below:

Bit 15,14	Number of printers (usually 1 is returned)
Bit 13	Serial printer is connected when 1
Bit 12	Game I/O is connected when 1
Bit 11-9	Number of ASYNC communication ports connected
Bit 8	Hard-disk unit is connected
Bit 7,6	Number of diskette drives (besides A)
Bit 5,4	Video mode initialized ( always 01 )*
Bit 3,2	RAM size on board ( always 11 )
Bit 1	Reserved
Bit 0	IPLed from a diskette drive

\* Native or Extension Video mode is identified by a 1  
in bit 4. In Native mode it is always 1.

### TYPE 12 Get Memory Size

Memory size is set in AX in 1K-byte units.

### TYPE 13 Diskette I/O

Executes different functions based on the value in AH.

( AH )= 0      Reset the diskette system.    Bits 5-0 in DL must  
                 be set to the drive number.  
                 Set bit 7 to 0 when diskette drive units are  
                 connected, and set bit 6 to 0 (40 tracks) or 1 (80  
                 tracks).    Set bit 7 to 1 when a hard disk unit  
                 is connected.

( AH )= 1      Read the system status. The DL setting is the same as for (AH)=0. The status from the last operation, returned in AL, is described below.

Value(Hex)	Operation status
80	Time out
40	Bad seek operation
30	No hard disk (only when bit 7 of DL is 1)
20	Device out of order
10	CRC (Cyclic Redundancy Check) error found in reading a diskette
04	Desired sectors not found
03	Attempt to write to a write-protected disk
02	Address mark not found
01	Incorrect command

( AH )= 2-7      Set registers as follows:

(AL) Number of sectors      ( value unchecked, not used by FORMAT )  
 (CH) track number      (0-79, value unchecked)  
 (CL) sector number      (value unchecked, not used by FORMAT)  
 (DH) head number      (0-1, value unchecked)  
 (DL) drive number      (0-3, value checked)  
                          bit 7 =0      identifies a diskette drive,  
                          bit 6 =1      identifies double track access.

(ES:BX) buffer location ( not required for checking)

( AH )=2      Read sectors  
 ( AH )=3      Write sectors  
 ( AH )=4      Check sectors

## 5. Software

- ( AH )= 5      Format sectors.      The buffer pointer (ES:BX) must point to the set of address marks of a track. Each address mark consists of 4 bytes (C,H,R,N), where C stands for track, H for head number, R for sector number, and N for number of bytes ( 00=128, 01=256, 02=512, 03=1024) within a sector. Each sector in a track must have a corresponding address mark, which helps locate the desired sector in a read/write operation.
- ( AH )= 6-7      Reserved

If the transfer of data succeeds, the carry flag (CF)=0 will be returned. If not, (CF)=1 is returned, and the status is returned in (AH) as when (AH)=1.

For read, write, and check operations, only AX and the carry flag value will be altered. The number of sectors actually read will be returned in AL, but this value is meaningless when a time-out occurs or the system is reset with (AH)= 0. The contents of AH cannot be guaranteed in the latter case.

When an error message is received, retry after resetting the diskette adapter. More than 10 retries are required.



## TYPE 14 ASYNC Communication Port Input/Output

This routine provides byte stream I/O to the communication ports as designated by the following parameters. Port number (0-1) is specified in DX.

( AH )= 0        Initializes the communications port as specified in AL :

	<u>baud rate</u>	<u>parity</u>	<u>stop bits</u>	<u>character length</u>
BIT :	7 6 5	4 3	2	1 0
	0 0 0 -110	X 0 -none	0 -1 bit	1 0 -7 bits
	0 0 1 -150	0 1 -odd	1 -2 bits	1 1 -8 bits
	0 1 0 -300	1 1 -even		
	0 1 1 -600			
	1 0 0 -1200			
	1 0 1 -2400			
	1 1 0 -4800			
	1 1 1 -9800			

The DTR signal will be ON upon completion of initialization. When the routine exits, the AL value will be set in a call for communications status (AH=3).

( AH )= 1        Send the character in AL over the communications line. The contents of AL are preserved.

If the character can not be transmitted, bit 7 of AH is set to 1. Otherwise, the current line status will be returned by the remaining bits as when (AH)=3.

( AH )= 2        Receive a character from the communications line into AL, before returning to the caller. On exit AH has the current line status, as set by the status routine (AH=3), except that the only bits left on are the error bits (7,4,3,2,1).

## 5. Software

( AH )= 3

Returns port status in AX.

AH will contain the communications line status as shown below:

bit 7	time out
bit 6	transmitter shift register is empty
bit 5	transmitter holding register is empty
bit 4	break detect
bit 3	framing error
bit 2	parity error
bit 1	overrun error
bit 0	data ready

AL will contain the modem status as follows:

bit 7	data carrier detect
bit 6	ring indicator
bit 5	data set ready
bit 4	clear to send
bit 3	data carrier detect status changed
bit 2	ring indicator end
bit 1	data set ready changed
bit 0	clear to send changed

## TYPE 15 Cassette Input/Output

The function is specified in AH as follows:

- ( AH )= 0 Turn Cassette motor on
- ( AH )= 1 Turn Cassette motor off
- ( AH )= 2 Read data from the cassette tape unit.

(ES,BX) contains the pointer to the data buffer.  
(CX) contains the number of data to be read.

The registers and the values returned are as follows:

ES:BX : buffer address of the last byte read  
plus 1  
DX : the number of actual bytes read  
AH : =1 when CRC error is detected  
=2 when no signal is detected  
=4 when no leader is detected  
CY : carry flag =0 no error is detected  
carry flag =1 some error is detected

- ( AH )= 3 Write data to the cassette tape unit.

(ES:BX) contains the pointer to the data buffer.  
(CX) contains the number of data to be written.

The registers and the values returned are as follows:

ES:BX : buffer address of the last byte written  
plus 1  
AH : Any other than the above values causes  
(CY)=1 and (AH)= 80 to be returned.

5. Software

TYPE 16 Keyboard Input/Output

Executes one of the following functions as indicated by AH:

( AH )= 0 Reads next character. Reads a character from the keyboard and puts the following codes into AH and AL.

Data type	AH	AL
1-byte character	scan code	ASCII code
2-byte char. 1st-byte	scan code	1st-byte
2-byte char. 2nd-byte	scan code	2nd-byte
function key, etc.	pseudo scan code	00
input JIS 8 bit code with ALT key pressed	00	pseudo scan code
Kanji 1st-byte	FF	1st-byte
2nd-byte	FF	2nd-byte
by Kana-Kan conversion		

If more data remain in the buffer, the initial data are returned. Otherwise, the routine stays active for the new data input.

( AH )= 1 Indicate if a character is available to be read. ZF (zero flag) will be set as follows, to indicate whether data have been transmitted into the buffer or not:

(ZF)=1 no character is in the buffer for reading  
(ZF)=0 character is in the buffer for reading

When (ZF)=0, the next character will be sent to AX. The character remains unchanged in the buffer until a call is made with (AH)=0 to read the next character.

( AH )= 2 Reads shift status. Current shift status is sent to AL and AH as follows:

## AL register

bit 7	= 1	Insert mode
bit 6	= 1	CAPS Lock pressed
bit 5		Unused
bit 4	= 1	Scroll Lock pressed
bit 3	= 1	ALT key pressed
bit 2	= 1	Control key pressed
bit 1-0	= 01	Right-shift key pressed
bit 1-0	= 10	Left-shift key pressed

## AH register

bit 7-3		Unused
bit 2-1	= 00	Alphanumeric shift
	= 01	Katakana shift
	= 10	Hiragana shift
bit 0	= 1	Full size mode
	= 0	Half size mode

( AH )= 3 Sets typamatic rate

AL=0	Return to default values
AL=1	Increase initial delay
AL=2	Slow typamatic rate by one half
AL=3	Combine AL=1 and AL=2
AL=4	Disable typamatic

( AH )= 4

AL=0	Turn keyboard click off
AL=1	Turn keyboard click on

5. Software

( AH )= 5 Alters keyboard status or mode. Sets desired status or mode in AL.

Status/Mode symbols displayed are altered.

AL register

bit 7-6	=00	Shift out Kanji-mode
	=01	Shift in Kanji-mode
	=10	Shift Kanji-mode in or out
	=11	Not switched
bit 5-4	=00	CAPS Lock off
	=01	CAPS Lock on
	=10	Switch CAPS Lock on or off
	=11	Not swithed
bit 3-2	=00	Alphanumeric shift
	=01	Katakana shift
	=10	Hiragana shift
	=11	Not changed
bit 1-0	=00	Half size mode
	=01	Full size mode
	=10	Switch Half/Full-size mode
	=11	Not switched

( AH )= 6 Reserved

( AH )= 7 Status/symbol line and Kana-Kanji conversion possible or impossible.

AL

Bit 0=	0	Kana-Kanji conversion is possible ( default ).
	= 1	Kana-Kanji conversion is impossible
Bit 1=	0	Access to the indicator line is possible. ( default )
	= 1	Access to the indicator line is impossible.

When the screen mode is reset ,these parameters will revert to the default values.

( AH )= 85 Same as (AH)=5, except status/mode symbols displayed are not altered.

## TYPE 17 Printer I/O

The printer BIOS supports the IBM 5512 Thermal Transfer Printer ( hereafter called PT-2 ) and the IBM 5513 Thermal Paper Printer ( hereafter called PT-1 ). When the printer is called by (AH)=0, the entire contents of AL are printed to the PT-2 printer.

The situation may not be the same as PT-1.

Descriptions of PT-1 output when called by (AH)=0 are provided followed by a description of the BIOS common to both printers.

When PT-1 is connected and BIOS is called by AH=0, the contents of AL determines whether they are character code, control code or data. As for the multiple number of bytes of control code, the control code sequence should strictly be followed at the time of BIOS process.

Character codes for sending one byte (ANK) or two byte characters should be IBM internal codes.

The character font output to the printer is an image of what was obtained by a request for a Video BIOS character font.

The characters use Hankaku 7 X 16 dot and Zenkaku for 15 X 16 font patterns.

The printer output is performed in units of one line. The data are stored in the BIOS character buffer until the printing commands such as LF, FF are received.

At the time when BIOS receives printing commands, character patterns for one line are output to the printer as image data. For this reason, characters and image data can not reside within the same line. ( In this case, a line is automatically fed.)

The PT-1 uses an 8 dot print head. 16 dot vertical printing requires the head to move twice.

Printed characters can be large or small characters. A change from one size to the other must be made at the beginning of a line.

Small characters are the default.

## 5. Software

Control codes used are those for the IBM 5553/5557 printers and some of these differ from those of the PT-1. BIOS converts them to PT-1 codes. Due to the PT-1 hardware limitations, those codes which BIOS can not convert are replaced by blanks.

Out of the IBM 5553/5557 control codes, the following are converted by BIOS:

- 1) CAN : Cancellation  
After the image buffer or code buffer within BIOS are cleared, the CAN code is output to the printer.
- 2) CR : Carriage Return  
Because the PT-1 automatically issues an LF, the CR code is neglected within BIOS.
- 3) LF : Line Feed  
An LF is taken as a print command and after data in the buffer are output to the printer, the LF is performed.  
The PT-1 automatically issues an LF.
- 4) FF : Page Change  
FF is taken as a print command and after data in the buffer are output to the printer, the page change is performed.
- 5) SP : Space  
One Hankaku character space corresponds to one space output.
- 6) ESC % 1 : Single length image data transfer  
( graphics image handling code )  
This is used when graphics image data are sent.  
Conversion is made as follows: ( 2 byte code data transfer mode should be followed. )

ESC % 1 N1 N2 D1 D2 D3 D4 .....D(2 X N1N2)

is converted to

ESC L N1 N2 D1 D3 D5 ....D(2 X N1N2)-1  
+LF  
+CR  
+ESC L N1 N2 D2 D4 D6 ....D(2 X N1N2)

MSB	1	3	5	Image data (ODD)	
LSB					
MSB	2	4	6	Image data (EVEN)	
LSB					



7) ESC % 2 : Double length image transfer ( graphics image handling code )

This is used in graphics image data transfer as follows:  
(2 byte data transfer mode should be followed.)

ESC % 2 N1 N2 D1 D2 D3 D4 ....D(2 X N1N2)

is converted to

ESC L N1' N2' D1 D1 D3 D3..D(2 X N1N2)-1 D(2 X N1N2)-1  
+LF  
+CR  
+ESC L N1' N2' D2 D2 D4 D4..D(2 X N1N2) D(2 X N1N2)

MSB	1	1	3	3	Image data (ODD)
LSB					
MSB	2	2	4	4	Image data (EVEN)
LSB					

5. Software

- 8) ESC % 3 : Horizontal skip ( graphics image handling code )  
This is a command to skip the dots specified and is converted as follows:

ESC % 3 N1 N2

is converted to

ESC 1 N1 N2 00 00 00 00 ..... 00 00

N1N2

- 9) ESC % 5 : Vertical skip ( graphics image handling code )

This is a command to feed the paper vertically for the number of dots specified. As the PT-1 is unable to process dots, the conversion is made as follows:

ESC % 5 N1 N2

is converted to

ESC 0 ( N1N2 / 13 )

The actual paper feed is made in units of 1/9 inch (2.82 mm), N1N2 / 13 times (rounded).  
( 2 inch maximum ( 50.8mm ) )

Where N1N2 / 13 is less than 2, the value is rounded up to 2.

- 10) ESC % 6 : Set CR point ( graphics image handling code )

This is a command to move the print-begin-position the dots specified and is converted as follows:

ESC % 6 N1 N2

is converted to

CR ( carriage return )  
+ESC L N1N2 00 00 00 00 00 00 00 00

N1N2

## 11) ESC % 9 : Set Line Space

The number of line spaces when LF is received is set by the value of N1N2 as follows:

$0 \leq N1N2 < 13$	.....1/9 inch	LF 2 Times
$13 \leq N1N2 < 26$	.....1/9 inch	LF 3 Times
$26 \leq N1N2 < 39$	.....1/9 inch	LF 4 Times
$39 \leq N1N2 < 52$	.....1/9 inch	LF 5 Times
$52 \leq N1N2 < 65$	.....1/9 inch	LF 6 Times
$65 \leq N1N2 < 78$	.....1/9 inch	LF 7 Times
$78 \leq N1N2 < 91$	.....1/9 inch	LF 8 Times
$91 \leq N1N2 < 104$	.....1/9 inch	LF 9 Times
$104 \leq N1N2 < \dots\dots\dots$	.....1/9 inch	LF 10 Times

## 12) ESC F : Set page length

This is a command to set the length of a page with 6 LPI for small characters and 3 LPI for large characters. Conversion is made as follows:

ESC F N1 N2

is converted to

ESC C N1 N2 ( N1 N2 < 126 (SMALL), 63 (LARGE) )

When N1N2 is greater than 126 or 63, the value is forced to 126 or 63.

## 13) ESC £ : Set ANK Enlargement

When this command is received, a double size character will be printed.

## 14) ESC | : Release ANK Enlargement

When this command is received, ESC £ is released and the normal size characters will be printed thereafter.

5. Software

15) FS : Fixed length image transfer ( graphics image handling code )

When this command is received, the same image transfer commands ( ESC % 1 , ESC % 2 ) as those most recently issued and the image data transfer using data numbers will be initiated.

16) Other control codes

The following control codes do not have meaning to the PT-1 and are neglected by BIOS. In this case, commands which consist of single or multiple bytes neglect all the bytes which the command takes as valid. The printer waits for the next control code to be presented.

Control Code overlooked		No. of bytes for Command generation
CR	CARRIAGE RETURN	(1)
BS	BACK SPACE	(1)
DC 1	SELECT	(1)
DC 3	DESELECT	(1)
ESC % 4 N 1 N 2	HORIZONTAL REVERSE SKIP	(5)
ESC % 8 N 1 N 2	VERTICAL REVERSE INDEX	(5)
ESC % B	BIDIRECTIONAL PRINT	(3)
ESC % U	NORMAL PRINT	(3)
ESC S	SHEET FEED	(2)
ESC V	SHEET EJECT	(2)
ESC O	HIGH SPEED PRINT START	(2)
ESC P	HIGH SPEED PRINT RELEASE	(2)
ESC (	3 BYTE DATA TRANSFER	(2)
ESC )	2 BYTE DATA TRANSFER	(2)

REMARKS) 1. As ESC % 1, ESC % 2, ESC % 3, ESC % 5, ESC % 6, FS are operated within BIOS as the graphics image handling codes, another LF is added when they are used together with character data.

2. When ESC % 1, ESC % 2, ESC % 3, ESC % 6 as used, the total of N1N2 within a line should not exceed 1120 dots, larger values are ignored.

3. When multiple ESC commands are received, the first ESC is valid and other ESC are ignored until a code other than ESC is issued.

The value in AH indicates to BIOS which of the following functions to execute.

PT-1 and PT-2 have the same function unless otherwise stated.

When returning to the calling program, DX must be 0. AH will contain status values, while other registers remain unchanged.

( AH ) = 0      Prints the character specified in AL.  
 A hex 7F (DEL) prints a special character.  
 Also sends control codes to the printer through AL.  
 In processing output data, there are differences between PT-1 and PT-2, as stated before.

( AH ) = 1      Initializes the printer. Initializes the hardware, resets the software status, and then sets the initial control values, as shown below.  
 PT-1 values are in parentheses.

- Alphanumeric      10 (12) characters per inch
- Kanji              5 (6) characters per inch
- Line feed          6 (4.5) lines per inch
- Page length        66 (49.5) lines per page
- Speed              normal
- Print character    SMALL character

( AH ) = 2      Reads status-1. Reads printer status into AH as follows:

- bit 7 =0      In use.
- bit 6          Reserved
- bit 5 =1      Out of paper (EOF) or paper jam in the automatic sheet feed.
- bit 4 =1      Printer ready.
- bit 3 =1      An error such as EOF, printer disconnected, CANCEL key pressed or time-out has occurred.
- bit 2,1=1     Reserved
- bit 0 =1      Time out. The printer is taking an abnormally long period of time to print characters.

5. Software

( AH )= 3 Reads status-2. Reads the printer status into AH as follows:

```

bit 7      Always "1"
bit 6,5    Unused
bit 4,3    =11    7.5 LPI
           10     6   LPI
           01     5   LPI
           00     4   LPI
bit 2,1    =11    7.5/15 CPI
           =10    6.7/13.3 CPI
           =01    6/12 CPI
           =00    5/10 CPI
bit 0      =1     PT-1 is connected
           =0     PT-2 is connected
    
```

( AH )= 4 Prints contents of AL register directly.

( AH )= 5 Prints double wide. Same as in (AH)=0 except that the horizontal size of the character is doubled.

( AH )= 6-A Unused

( AH )= B Prints a line (including associated attributes) in Extension Video mode. ( PT-2 only )  
 Prints the character string in the character buffer indicated by ES:DI. The nth character in the character buffer will be printed with the nth attribute in the attribute buffer. The length of the buffer is specified in CX.

The attributes for this function are one or two byte values indicating how characters are printed. The meaning of each bit in an attribute byte are as follows:

bit	Meaning
7	Reserved. Must be set to 0.
6	Underline
5	Reserved.
4	Reserved. Must be set to 0.
3,2	Vertical grid line
=00	None
=01	Single solid line
=10	Heavy solid line
=11	Single dotted line
1,0	Horizontal grid line
	The values are the same as for a vertical grid line.

A one-byte attribute is needed for a half-size character; a 2-byte attribute is needed for a full-size character. Horizontal lines are printed above the characters involved; vertical lines are printed to the left of the characters involved.

After the number of bytes specified in the CX register is printed, BIOS will automatically print a carriage return character and a line feed character unless the character buffer ends with a carriage return character or a line-feed character, or both. Thus the character buffer must contain all the characters to be printed in one line. The control characters that can be included in the character buffer are limited to the carriage return, line-feed, ESC [and ESC] that are used at the end of the character buffer.

( AH ) = C

Sets printer control values.

The control values are specified in AL as follows.

(AL) Specified control value

0 Reset printer default values. See (AH)=1.

1 Change character pitch

(BH)=90	5	Full size characters/inch
(BH)=78	6	Full size characters/inch
(BH)=6C	6.7	Full size characters/inch
(BH)=60	7.5	Full size characters/inch

The value for half size characters is double that of full size characters.

2 Change line feed pitch

(BH)=1E	4	(3) lines per inch
(BH)=18	5	(3) lines per inch
(BH)=14	6	(3) lines per inch
(BH)=10	7.5	(4.5) lines per inch

3 Change page length.

(BX)	number of lines per page
	(based on 6 lines/inch)

4 Set double/normal speed mode.

(BH)=0	set double speed mode
(BH)=1	set normal speed mode

5. Software

- 5 Set unidirectional/bidirectional printing mode  
( PT-2 only )  
(BH)=0 set unidirectional printing mode  
(BH)=1 set bidirectional printing mode
- 6 Change printing character ( PT-1 only )  
(BH)= 0 SMALL character  
(BH)= 1 LARGE character

TYPE 18 ROM BASIC  
This interrupt executes a BASIC program.

TYPE 19 System reset

DOS is restarted by the BOOT program, or by issuing  
INT 18.

TYPE 1A Timer Support. Reads or sets the time as specified  
in AH.

( AH )= 0 Reads the current value of the time-of-day counter  
and returns the following:

CX most significant word of count  
DX least significant word of count  
AL=0 count has not passed 24 hours since the  
last time it was read  
AL<>0 24 hours have passed

( AH )= 1 Set time-of-day counter to the value specified in  
the CX and DX registers.

CX most significant word of count  
DX least significant word of count  
note: Counting rate is 1193180/65536 per sec, i.e.,  
there are 18.2 counts per sec.



## 5.3.2. Extension Video Mode BIOS Interrupts

INT10, INT11, and INT16 have different meanings in Extension Video mode from in Native mode. An explanation of these three interrupts in Extension Video mode are as follows. Refer to chapter 3 "3.5 VP3 Display Function", for more information on Extension Video mode.

## TYPE 10 Display Input/Output

Execute the following functions as specified in AH.

( AH )= 0      Set mode. Sets display mode as specified in AL.

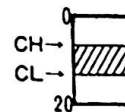
(AL)	Mode
0-7	Reserved
8	80X25 mono character mode (initial mode)
9	720X512 mono graphics mode 80X25 characters displayed
10	Reserved
11	360X512 4 color graphics - 40X25 characters displayed - double size character width - palette 11 used for character color - palette 00 used for background color - normal size characters printed except for screen print. - All palettes, except palette 0 are printed. Refer to(AH)=11, "set palette color".
12-13	Reserved
14	80X25 color characters

( AH )= 1      Set cursor-type. The cursor type is specified by bits in the CX register.

bit 14,13 =00	non-blink
=01	non-display
=10	blink at 4 times per second
=11	blink at 2 times per second

Blinking is not supported in graphics mode.

bit 12-8	start line of the cursor in a character box
bit 4-0	end line of cursor in a character box



5. Software

- ( AH )= 2      Set cursor position.      The cursor position (row,col) is specified by values in ( DH,DL ). (0,0) indicates the home (upper left) position.
- ( AH )= 3      Read cursor position.      (DH,DL) contains the current cursor position ( DH=row,DL=col ). (CH,CL) contain the cursor type.
- ( AH )= 4      Reserved
- ( AH )= 5      Reserved
- ( AH )= 6      Scrolls display upward.      (CH,CL) specifies the upper left corner of the position to be scrolled (CH=row,CL=column), (DH,DL) the lower right corner (DH=row,DL=column), AL the number of lines to be scrolled, and BH the attributes for the space left. AL=0 clears the area defined by CX and DX.
- ( AH )= 7      Scrolls display downward.      Same as ( AH )=6.
- ( AH )= 8      Reads the character at the current cursor position into AL and its attributes into AH.

Full-size characters return the following attributes.

1st-byte      XXXX XX01  
2nd-byte      XXXX XX11

The attributes returned are the same as those written by ( AH )=9.

All attributes are supported in graphics mode.

In color graphics mode the color attribute bits of AH have no affect on the color of the characters.

( AH )= 9      Writes cursor position attributes and characters. Characters to write in AL and their attributes, with the number of characters indicated units of half-size characters, should be specified in CX. For full-size characters, the attributes change after the 2nd byte is written.

Bit 1 or 0 of the attribute is set depending on the contents of the AL register.

Half-size character	XXXX XXX0
Full-size character	
1st byte	XXXX XX01
2nd byte	XXXX XX11

All the attributes except high-intensity and blink are supported in graphics mode.

In color graphics mode, the color attribute bits of BL have no affect on the color of the characters.

( AH )= A      Write character only. Same as (AH)=9, except that attributes are not written ( nothing specified in BL ).

( AH )= B      Set a color in the palette.      Specify in BH the palette number to be set (0-3). Specify in BL the color number ( 0-15 ) to be assigned to the specified palette number.

( AH )= C      Write a dot. (DX,CX) specifies the bit position (row,column) where the dot is to be written, (0,0) represents the home position on the display. Both the row and the column number are in units of dots, not characters.  
 In color mode, AL specifies the palette number (0-3) for the color dot to be written.  
 In monochrome graphics mode, bit 0 or 1 is specified in AL. If the eighth bit (bit 7) in AL is specified as 0, the value in bit 0 (or bit 0.1) of AL will be written directly.  
 If the value in bit 7 is set to 1, however, the XOR (exclusive OR) value of the current value and the value of bit 0 in AL will be written.

This function works only in graphics mode.

5. Software

( AH )= D Read a dot. (DX,CX) specify the (row,column) of the dot position to be read ( both the row and column number are in units of dots, not characters). The dot value (on /off) is read into bit 0 (or 0,1) of AL.

This function only works in graphics mode.

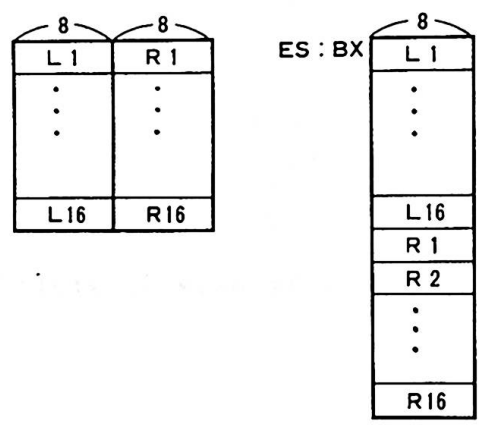
( AH )= E ASCII teletype routine for output. Writes a character at the cursor position and advances the cursor. If the cursor is already in the rightmost position in line 24, the screen is scrolled upward. The character to be written is specified in AL.

If the cursor is at the last position in a row and a full-size character is to be written, a space is placed at this position and at the 1st position in the next row the full-size character is written.

This function also works in graphics mode.

( AH )= F Get the current display status. AL receives the current mode and AH receives the number of character columns displayed.

( AH )=10 Request a font pattern. Returns the font pattern for the specified character in the user-designated memory location. CX must hold the internal code of the requested character. CH must be 0 for half-size characters. AL must be 0. Fonts will be placed in the memory location designated by (EX:BX), as follows:



( AH )=11        Sets the display attributes.        The display attributes are specified in BH as follows:

(BH)            Display attributes

bit 6 =1    display in high-intensity  
bit 3-0      grid line color (0-15). The colors are the same as those described in "Setting the palette".

#### TYPE 11    Device Configuration Status

The device configuration status is returned in AX.  
The information, specified by the bits in AX, is listed below:

bit 15,14	Number of printers (usually 1 is returned)
bit 13 =1	Reserved
bit 12	Reserved
bit 11-9	Number of ASYNC communication ports connected
bit 8 =1	Reserved
bit 7,6	Number of diskette drives
bit 5,4	Video mode initialized ( always =10 )*
bit 3,2	Type of display
	=00 12 inch monochrome display( always =00 )
bit 1	Reserved
bit 0	Reserved (=1 diskette drive is connected)

\* Extension mode is identified by checking bits 5 and 4 for values of 1 and 0, respectively.

RAM addresses 700-701(Hex) are reserved for system use in Extension Video mode.

#### TYPE 16    Keyboard Input/Output

One of the following functions is executed depending on the value in AH:

( AH )= 0        Reads next character. Reads a character from the keyboard and puts the following codes into AH and AL.

5. Software

Data type	AH	AL
1 byte code character	scan code	ASCII code
2 byte code 1st byte	scan code	1st byte
2 byte code 2nd byte	scan code	2nd byte
Function key, etc. JIS code Using ALT key	pseudo scan code	00
JIS-8 bit code input	00	pseudo scan code
Kanji 1st byte by kana-kan 2nd byte conversion	FF FF	1st byte 2nd byte

If more data remain in the buffer, the initial data are returned. Otherwise, the routine stays active for the next data input.

( AH ) = 1      Indicates if a JIS-8 bit code character is available to be read. ZF (zero flag) will be set as follows, to indicate whether data have been transmitted into the buffer or not:

(ZF)=1      No character is in the buffer for reading  
 (ZF)=0      Character is in the buffer for reading

When (ZF)=0, the next character will be sent to AX. The character remains unchanged in the buffer until a call is made with (AH)=0 to read the next character.

( AH ) = 2      Reads shift status. Current shift status is sent to AL and AH as follows:

AL register

bit 7 = 1      insert mode  
 bit 6 = 1      CAPS Lock pressed  
 bit 5            unused  
 bit 4 = 1      Scroll Lock pressed  
 bit 3 = 1      ALT key pressed  
 bit 2 = 1      Control key pressed  
 bit 1,0 = 01    right shift key pressed  
              = 10    left shift key pressed

## AH register

bit 7-3	Unused
bit 2,1 =00	Alphanumeric shift
=01	Katakana shift
=10	Hiragana shift
bit 0 =1	Full-size mode
=0	Half-size mode

( AH )= 3      Clicker on : Causes the speaker to generate sound with frequencies of 31-32767 Hz as specified in CX.

( AH )= 4      Clicker off : Turns keyboard click off.

( AH )= 5      Alters keyboard status or mode. Sets desired status or mode in AL.

## AL register

bit 7-6 =00	Shift out Kanji-mode
=01	Shift in Kanji-mode
=10	Shift Kanji-mode in or out
=11	Do not switch
bit 5-4 =00	CAPS Lock off
=01	CAPS Lock on
=10	Switch CAPS Lock on or off
=11	Do not switch
bit 3-2 =00	Alphanumeric shift
=01	Katakana shift
=10	Hiragana shift
=11	Do not change shift status
bit 1-0 =00	Half-size mode
=01	Full-size mode
=10	Switch Half/Full size mode
=11	Do not switch

Status/Mode symbols displayed are altered.

( AH )= 6      Reserved

( AH )= 7      Kana-to-Kanji conversion and status/mode symbol line possible or impossible.

## 5. Software

AL  
Bit 0 =0      Kana-to-Kanji conversion is enabled  
                  (default)  
              =1      Kana-to-Kanji conversion is disabled  
Bit 1 =0      Indicator line is enabled (default)  
              =1      Indicator line is disabled

When display mode is reset, these parameter are reset to default values.

( AH )=85      Same as (AH)=5, except that status/mode symbols displayed are not altered.



## 5.3.3. Interrupt Routines For Special Use

The following are descriptions of the BIOS routines for special use:

INT 5 : Screen print  
 When screen prints, either in Native mode or Extension Video mode are required the screen mode (character or graphics mode) is automatically set for the printer, and the printing is done in the correct mode. The printing direction for character mode is the same as that displayed. For the graphics mode, the printing line is rotated 90 degrees.

INT 1B : Keyboard Break Address  
 This vector points to the code to be executed when Break is pressed on the keyboard. The vector is invoked while responding to the keyboard interrupt, and control should be returned through an IRET instruction. The POWER-ON routines initialize this vector to an IRET instruction, so that nothing occurs when Break is pressed unless the application program sets a different value.

Control may be retained by this routine, with the following problem. The 'Break' may have occurred during interrupt processing, so that one or more 'End of Interrupt' commands must be issued in case an operation was underway at the time.

INT 1C : Timer  
 This vector points to the code to be executed on every system-clock tick. This vector is invoked while responding to the 'timer' interrupt, and control should be returned through an IRET instruction. The POWER-ON routines initialize this vector to point to an IRET instruction, so that nothing occurs unless the application modifies the pointer. It is the responsibility of the application to save and restore all registers that are modified.

INT 1D : Video Parameter  
 This vector points to a data region containing the parameters required for the initialization of the CRT Controller. Note that there are six separate tables, and all six must be reproduced if all modes of operation ( ANK, Graphics, Kanji ) are supported. The POWER-ON routines initialize this vector to point to the parameters contained in the ROM video-routines. It is recommended that if a programmer wishes to use a different parameter table, that the table contained in ROM be copied to RAM and just modify the values needed for the application.

## 5. Software

- INT 1E** : Diskette Parameter  
This vector points to a data region containing the parameters required for the diskette drive. The POWER-ON routines initialize the vector to point to the parameters contained in the ROM DISKETTE-routine. It is recommended that if a programmer wishes to use a different parameter table, that the table contained in ROM be copied to RAM and just modify the values needed for the application. The motor start-up-time parameter (parameter 10) is overridden by BIOS to force a 500-ms delay (value 04) if the parameter value is less than 04.
- INT 1F** : RESERVED
- INT 49** : Conversion Table  
This interrupt contains the address of a table used to translate non-keyboard scan-codes (scan codes from 56(Hex) to 69(Hex).) If Interrupt hex 48 detects a scan code between 56 - 69 (Hex) it translates it using the table pointed to by Interrupt Hex 49. The address that Interrupt Hex 49 points to can be changed by users to point to their own table if different translations are required.
- INT 7A** : Pointer To Dictionary  
This routine includes pointers to the dictionary for Kana-to-Kanji conversion.

## 5.4. Keyboard Scan Codes

Scan codes are used to transfer keyboard data to the system unit. A different scan code is generated by pushing or releasing a key. Functions not represented by a single keystroke can be achieved by pressing two or more keys simultaneously. The BIOS keyboard routine converts them and returns one single character code. BIOS sends scan codes and converted character codes to the CPU.

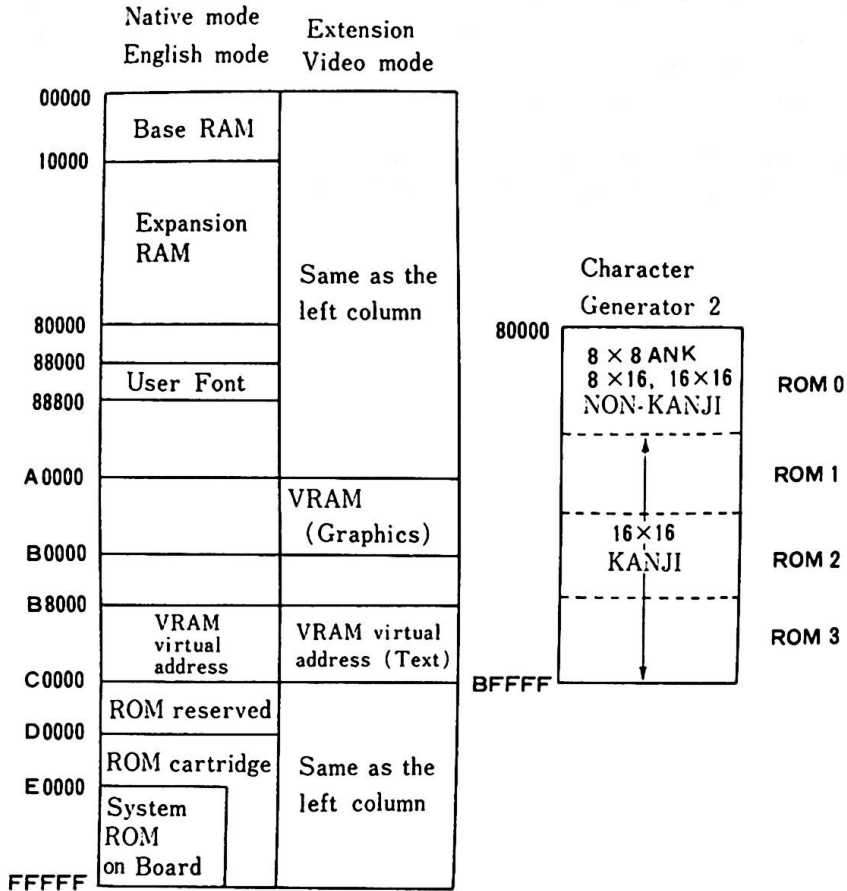
The BIOS routines for processing keyboard data are INT2, INT48, INT49, INT16, INT9, INT78, INT79, and INT7A.



5. Software

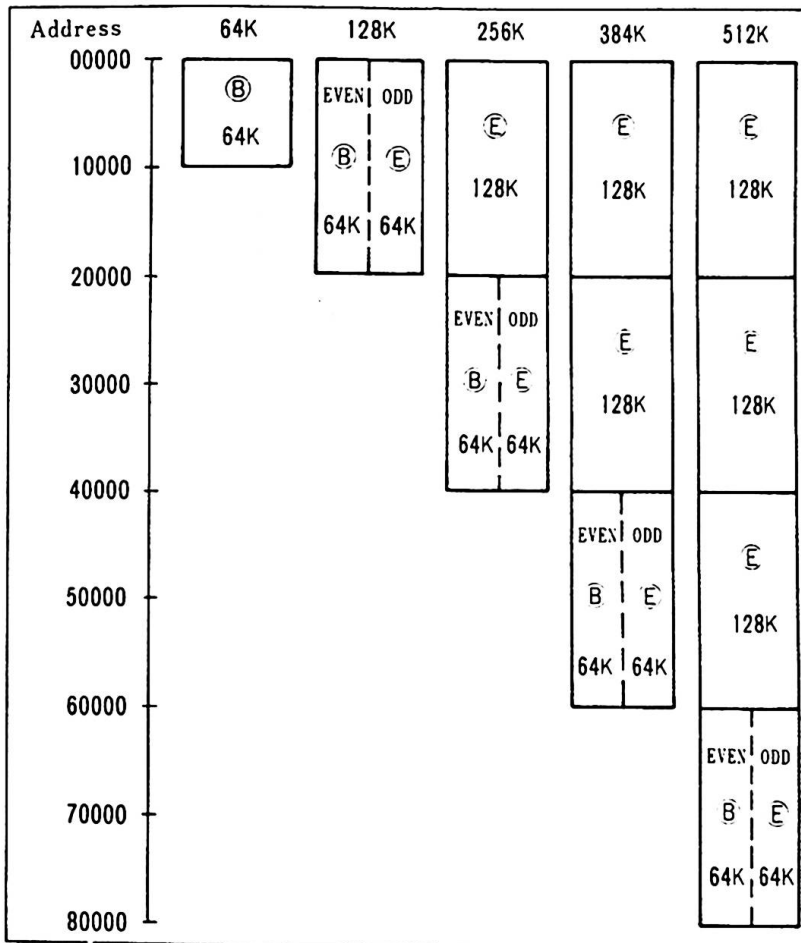
5.5. Memory Map

RAM for general use can be expanded to a 512KB maximum. The memory space allocation is referred to in Chapter 2, "2.2.6 Memory Space and I/O address Setting."



Remarks ) The map of addresses 00000-80000 (Hex) changes depending on whether or not a 128KB RAM card is installed. Refer to Figure 5-5 and 5-6.

Figure 5-4 Memory Map



Meaning of abbreviations :

(B) 64 K : 64KB Base Memory

(E) 64 K : 64KB RAM Card

(E) 128 K : 128KB RAM Card

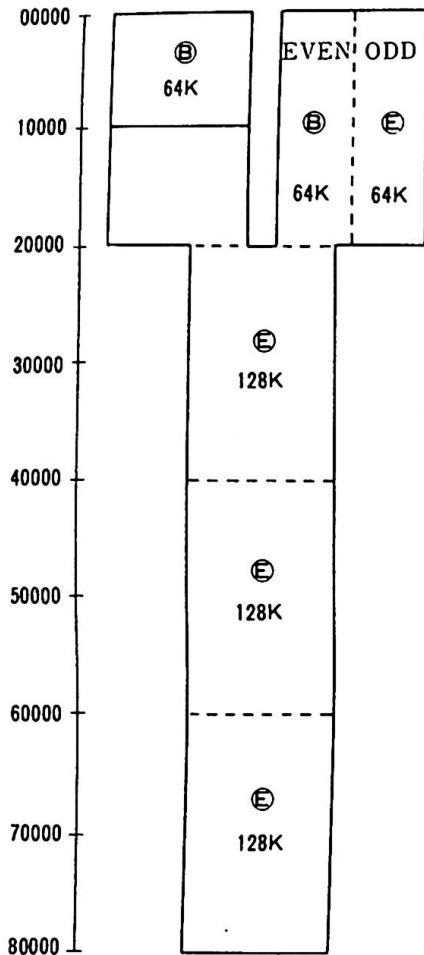
EVEN : Even address used

ODD : Odd address used

Figure 5—5 Memory Space (Native and Extension Video mode)

5. Software

In English mode the base memory and 64K bytes of expansion memory are always located at addresses 00000-FFFFF(Hex).



- Meaning of abbreviations :
- ⓑ 64 K : 64KB Base Memory
  - ⓐ 64 K : 64KB RAM Card
  - ⓐ 128K : 128KB RAM Card
  - EVEN : Even address used
  - ODD : Odd address used

Figure 5-6 Memory Space (English mode)

## 5.6. I/O Map

I/O addresses are initialized as follows. Addresses can be changed for some I/O. The relocation of I/O addresses is referred to in Chapter 2, "2.2.6 Memory Space and I/O Address Setting".

Address	I/O Name
1X	Reserved
20	8259 PIC
21	8259 PIC
40	8253 Timer 1
41	8253 Timer 2
42	8253 Timer 3
43	8253 Mode
60	8255 Port A
61	8255 Port B
62	8255 Port C
63	8255 Control
A0	NMI Control
C0	76489A Sound generator
F2	Diskette controller
F4	Diskette status register
F5	Diskette data register
1FF	Gate Array-08
201	Joystick
278	Reserved
279	Reserved
27A	Reserved
2F8-2FF	8250 register addresses
32X	Reserved
378, 37C	Parallel interface
379, 37D	Parallel interface (status)
37A, 37E	Parallel interface (command)

Figure 5-7 I/O Addresses (1 of 2)

5. Software

Address	Type of I/O
3D0, 2, 4, 6	CRTC address registers
3D1, 3, 5, 7	CRTC data registers
3D8	Reserved
3D9	Page register 2
3DA	Video Gate Array VP1, VP2
3DB	Clear light pen latch
3DC	Set light pen latch
3DD	Video gate array VP3
3DE	Light pen gate
3DF	Page register 1
3F8-3FF	Reserved

Figure 5-7 I/O Addresses (2 of 2)



## 6. Compatibility

This chapter describes points to keep in mind to maintain compatibility among the IBM 5510, IBM 5550 and PCjr systems. The differences among these systems are also described.

To have compatibility between the IBM 5510 and PCjr, it is necessary to operate the IBM 5510 in English mode. In order to be compatible with the IBM Multistation 5550, the 5510 must be operated in Extension Video mode. ROM cartridges are available for changing the mode of operation.

It is recommended that an application program use only the BIOS and DOS interrupt interfaces in order to achieve compatibility with the PCjr and IBM 5550, since absolute addresses vary among the three machines.

There are several factors to keep in mind to maintain compatibility. They are:

1. Unequal Configurations
2. Hardware Differences
3. Diskette Compatibility

Discussions of these topics follow.

## 6. Compatibility

### 6.1. Unequal Configurations

From the configuration/hardware point of view, there exist some functions which the IBM 5550 has, while the IBM 5510 does not have. Application programs which call for those functions might not work on the IBM 5510. The following are functions unique to the IBM 5550:

#### 1. Hardware

- 5550 ROM Function
- DMA
- 1024 x 768 dot display function
- IBM 5550 unique key-tops
- IBM 5550 unique printer function

#### 2. BIOS

- 1024 x 768 dot graphics mode
- Hard Disk

#### 3. DOS

- User fonts of 63 or more characters
- Hard disk support commands (SWITCH, BACKUP, RESTORE)

## 6.2. Hardware Differences

The IBM 5510, PCjr, and the IBM 5550 differ in hardware design and there might be instances where application program compatibility is not maintained because of the differences in the hardware. Figure 6-1 shows the hardware features of the IBM 5550 and PCjr compared with those of the IBM 5510.

Due to different shapes in connectors, some hardware might need modification before being used, but when the hardware is functionally compatible, it is classified as compatible.

Hardware/Function	Comparison	
	5550	PC-jr
User Memory	○ *	○ *
Compact Keyboard	× *	○ *
Full Keyboard	× *	N/A
Diskette Drive	○ *	× *
Printer Interface	× *	○
RS-232C Interface	○ *	○
Joystick Interface	N/A	○
Cassette Interface	N/A	○
Color Graphics	○ *	○
Light Pen	N/A	○
8253 Timer	○ *	○
Interrupt Controller	○ *	○
Beep Subsystem	○ *	○
Sound Generator	N/A	○ *
ROM Cartridge Interface	N/A	○

Remarks) ○ --- Compatible  
 X --- Incompatible  
 \* --- With Conditions (Mentioned hereafter)  
 N/A - Not Applicable

Figure 6-1 Hardware Configuration Comparison

## 6. Compatibility

### Hardware Differences from PCjr

1. User RAM  
PCjr always shares the user RAM with the video RAM, while the IBM 5510 has dedicated video RAM and only utilizes user RAM as video RAM in special cases. When a program uses video pages 0 through 7 on the 5510 system, user memory of 16 KB per page is required. Pages 8 through B (Hex) are areas for the dedicated video RAM and when these are accessed, user RAM is not used.
2. Diskette Drive
  - IBM 5510 : 80 tracks/side or  
40 tracks/side, 3.5" or 5.25"
  - PCjr : 40 tracks/side, 5.25"

The size difference between the 3.5" and 5.25" diskettes makes physical compatibility impossible, but the diskette formats are compatible.
3. Keyboard  
The IBM 5510 keyboard has added keys for Kanji handling but other keys are compatible with the PCjr keyboard.
4. Sound Generator  
The IBM 5510 and the PCjr use compatible chips.
5. RS-232C Card  
This is provided as a standard feature on the PCjr, while on the IBM 5510, it is an optional feature. English mode applications using either COM1 or COM2 mode should have this optional feature to run on the IBM 5510.

## Hardware Differences from IBM 5550

1. User RAM  
The IBM 5510 has the concept of multiple pages and can have up to 12 pages, while the IBM 5550 does not (one page is assumed). The memory maps for the two systems are different. However, if an application program accesses the hardware through BIOS calls, the memory map differences should be unimportant. Memory size should also be taken into account when considering compatibility because the minimum memory size of the IBM 5550 is larger than that of the 5510.
2. Diskette Operation  
The IBM 5510 does not have DMA capability, while the IBM 5550 has. The IBM 5510 uses a level 6 hardware interrupt. When diskette I/O takes place, the entire system is masked and other I/O devices are inactivated (operator keystrokes and RS-232C etc.).
3. Keyboard  
The IBM 5510 keyboard scan codes differ from those of the IBM 5550. Compatibility is maintained by using interrupt type 16 (keystroke read). The keyboard operation, however, differs due to differences in the number and the functions of the keytops.
4. Video Display Function  
There is quite a high degree of compatibility between the IBM 5550 and Extension Video mode, but the font sizes of the two systems differ. The IBM 5550 has a wrap function for displaying screens, while the IBM 5510 does not.
5. Kanji Font  
The IBM 5510 requires 32 bytes/character, while the IBM 5550 requires 72 bytes/character.
6. Dictionary  
The IBM 5510 dictionary resides in ROM, while the IBM 5550 dictionary resides on a diskette and is loaded into RAM on demand.
7. RS-232C Interface  
The IBM 5550 supports up to 9600 BPS communications through use of DMA, while the IBM 5510 supports up to 4800 BPS communications through use of a level 3 interrupt. When diskette I/O takes place, interrupts other than level 6 are rejected and therefore, RS-232C interface I/O will not occur.
8. Timer (8253) Input  
The IBM 5510 clock is 1.19 MHz, while that of the IBM 5550 is 2.0 MHz.

6. Compability

9. Interrupt Controller

Both systems use a 8259A PIC, but as the interrupt level assignments for the two systems differ, there might be instances where compatibility is not maintained.

10. Beep

As there is a difference in the frequency of the Timer Input clock between the two systems, their beep sounds are slightly different.

11. Timing

When an IBM 5550 application program is dependent on the processing speed or timing, it might not run on the IBM 5510. In this case, the program should be modified in accordance with the IBM 5510 specifications. In developing application programs, the following points should be taken into consideration:

- The processing speed differs with the size and type of memory in which an application program is running.

<u>Processing Speed</u>	<u>Memory for the program</u>
Fast	ROM
↓	128 KB RAM Card
	64 KB memory on the system board & 64 KB expanded memory
	64 KB memory on the board when it is functioning as video RAM and 64 KB of expanded memory
Slow	

- The highest degree of application compatibility can be achieved by using a common high level language and accessing the system only through BIOS and DOS interrupts.

## 6.3. Diskette Compatibility

When a 5.25" diskette drive is installed in an Expansion Unit, there will be instances where the PCjr or the IBM 5550 is completely compatible with the IBM 5510. When only 3.5" diskette drives are provided, the physical shape and size of the diskettes makes it impossible to have compatibility. 3.5" and 5.25" diskettes are, however, the same in format and logically they are compatible. The following chart shows compatibility when a 5.25" diskette drive is installed in an IBM 5510 system:

		SYSTEM B				
		PC-jr	JX Operation Mode			IBM5550
			English	Native	Ext.Video	
S Y S T E M A	PC-jr	↓	↓	↓	↓	×
	JX	English	↓	*↓	*↓	*↓
		Native	↓	↓*	↓	↓
		Ext.Video	↓	↓*	↓	↓
IBM5550	×	↓*	↓	↓	↓	

Remarks) A <----> B : Perfectly compatible  
(both systems can read/write)

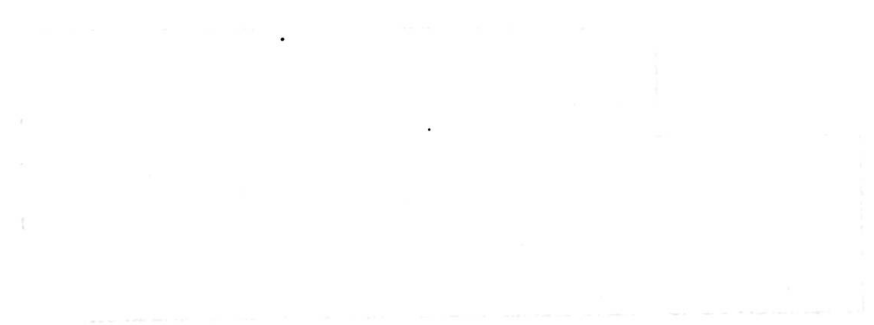
-----> : Reading of a diskette which has been  
written by the other systems is  
possible. (The arrow points to the  
system which reads.)

\* : Diskettes formatted with 40 tracks can  
be read. Ones with 80 tracks cannot.

X : No Compatibility

Figure 6-2 Diskette Compatibility

Dear Mr. [Name],



I am sorry that I cannot provide you with the information you requested. The data is currently unavailable.

Thank you for your interest in our services. We will contact you again when the information is available.





# Appendix A. BIOS

Appendix A.

<u>Absolute Address</u>	<u>PUBLIC Name</u> (EQUATES & DATA AREA)	<u>Module</u>	<u>Relative Address</u>	<u>Ref. Page</u>
-----	(EQUATES & DATA AREA)	-	----	A- 2
F800:0000	-----	1	0000	3
:0030	-----	2	0000	15
:0076	VIDEO_IO	2	0048	19
:01CD	VIDEO_PARMS	2	019D	21
:2030	EQUIPMENT	3	0000	93
:203C	MEMORY_SIZE_DETERMINE	3	000C	93
:2048	DISKETTE_IO	3	0018	94
:248D	SEEK	3	045D	103
:2539	DISK_BASE	3	0509	104
:2544	DISK_INT	3	0514	104
:25B5	RS232_IO	3	0585	105
:2696	CASSETTE_IO	3	0666	107
:27AC	READ_HALF_BIT	3	077C	110
:2900	KBDNHI	4	0000	113
:2A27	EXTAB	4	0127	115
:2A50	KEY62_INT	4	0150	115
:2C9E	KEYBOARD_IO	4	039E	119
:2ED5	KB_INT	4	05D5	122
:385D	BUFFER_QUEUEING	4	0F5D	133
:3A00	-----	5	0000	135
:3AA0	PRINT_SCREEN	5	00A0	136
:3DC0	-----	6	0000	141
:3E20	PRINTER_IO	6	0060	141
:4E00	BOOT_STRAP	7	0000	163
:4F00	KKKFDW	8	0000	169
:6700	DDS	9	0000	214
:6708	TIME_OF_DAY	9	0008	214
:6752	READ_TIME	9	0052	214
:677D	KB_NOISE	9	007D	215
:679E	BAS_ENT	9	009E	215
:67B3	TIMER_INT	9	00B3	215
:6A00	POST	10	0000	218
:6A6B	RESET	10	006B	220
:6F99	D11	10	0599	229
:6FC0	DUMMY_RETURN	10	05C0	229
:79A1	PRT_HEX	10	0FA1	246
:79C0	BEEP	10	0FC0	247
:7FC0	VECTOR_TABLE	10	15C0	255
:7FFF	POST_END	10	15FF	256

Remark) Refer to the table shown above, when you find the character "E" for the operand in the BIOS listing.

```

; <CAVEAT EMPTOR>;
;
; THE BIOS ROUTINES ARE MEANT TO BE ACCESSED THROUGH
; SOFTWARE INTERRUPTS ONLY. ANY ADDRESSES PRESENT IN
; THE LISTINGS ARE INCLUDED ONLY FOR COMPLETENESS,
; NOT FOR REFERENCE. APPLICATIONS WHICH REFERENCE
; ABSOLUTE ADDRESSES WITHIN THIS CODE VIOLATE THE
; STRUCTURE AND DESIGN OF BIOS.
;

```

```

;-----
; SYSTEM ID
;-----
PJSYSID EQU 0FDH ; PCjr SYSTEM ID
IXSYSID EQU 0EDH ; JX SYSTEM ID
;-----

```

```

;-----
; EQUATES
;-----
MFG_PORT EQU 10H ; MFG TESTER PORT
PORT_A EQU 60H ; 8255 PORT A ADDR
CPUREG EQU 38H ; MASK FOR CPU REG BITS
CRTREG EQU 7 ; MASK FOR CRT REG BITS
PORT_B EQU 61H ; 8255 PORT B ADDR
PORT_C EQU 62H ; 8255 PORT C ADDR
CMD_PORT EQU 63H
MODE_8255 EQU 10001001B
KBPORT EQU 60H ; KEYBOARD PORT
INTA00 EQU 20H ; 8259 PORT
INTA01 EQU 21H ; 8259 PORT
EOI EQU 20H
TIMER EQU 40H
TIM_CTL EQU 43H ; 8253 TIMER CONTROL PORT ADDR
TIMER0 EQU 40H ; 8253 TIMER/CHTR 0 PORT ADDR
NMI_PORT EQU 0A0H ; NMI CONTROL PORT
SND_CTL EQU 0C0H ; SOUND GENERATOR CONTROL PORT
S8STATUS EQU 1FFH ; S8 STATUS REGISTER
VRAM2IN EQU 80H ; 32K VRAM CARD IN
EROM6IN EQU 40H ; EXT. ROM 6 (F0000-) IN
EROM7IN EQU 20H ; EXT. ROM 7 (F8000-) IN
JOY_PORT EQU 201H ; JOYSTICK CONTROL PORT
RS232_1_PORT EQU 3F8H ; RS232C-1 PORT
RS232_2_PORT EQU 2F8H ; RS232C-2 PORT (ON BASE BOARD)
PARAL_PORT EQU 378H ; PARALLEL PORT
CRT_CTL EQU 3D4H ; CRT CONTROLLER CTRL PORT
VGA_CTL EQU 3DAH ; VIDEO GATE ARRAY CTRL PORT
VGA_CTL_E EQU 3DDH ; VIDEO GATE ARRAY CTRL PORT
; FOR EXTENSION MODE
PAGREG EQU 3DFH ; CRT/CPU PAGE REGISTER
PAGREG2 EQU 3D9H ; CRT/CPU PAGE REGISTER 2
;-----

```

```

;-----
; INITIALIZE EQUATES
;-----
MINI EQU 2000H ; FUTURE USE
KKOFF EQU 7*256+KANAKAN_OFF+INDICATOR_OFF ;
INIT_CODE EQU 0000H ; BOOT INITIA;L JUMP CODE
BOOT_LOCN1 EQU 0003H ; IB AREA
BOOT_LOCN2 EQU 0005H ; M AREA
KKMINIT EQU 0FF20H ; KANA KANJI CONVERSION INITIALIZE
DICT_ADDR EQU 3A00H ; DICTIONARY ADDRESS
;-----

```

```

;-----
; DISKETTE EQUATES
;-----
NEC_CTL EQU 0F2H ; CONTROL PORT FOR THE DISKETTE
FDC_RESET EQU 80H ; RESETS THE NEC (FLOPPY DISK
; CONTROLLER), 0 RESETS,
; 1 RELEASES THE RESET
WD_ENABLE EQU 20H ; ENABLES WATCH DOG TIMER IN NEC
WD_STROBE EQU 40H ; STROBES WATCHDOG TIMER
DRIVE_ENABLE EQU 01H ; SELECTS AND ENABLES DRIVE
NEC_STAT EQU 0F4H ; STATUS REGISTER FOR THE NEC
BUSY_BIT EQU 20H ; BIT = 0 AT END OF EXECUTION PHASE
DIO EQU 40H ; INDICATES DIRECTION OF TRANSFER
RQM EQU 80H ; REQUEST FOR MASTER
NEC_DATA EQU 0F5H ; DATA PORT FOR THE NEC
DRV_SUPPORT EQU 0FH ; 4 BIT SUPPORT UP TO FOUR DRIVE
DRV_RANGE EQU 03H ; RANGE CHECK 0 TO 3
OFF_DBL_TRK EQU 3FH ; J PARAMETER : DOUBLE TRACK SUPPORT
;-----

```

```

;-----
; 8088 INTERRUPT LOCATIONS
;-----
INT_10 EQU 10H ; VIDEO_IO
INT_11 EQU 11H ; EQUIPMENT
INT_12 EQU 12H ; MEMORY_SIZE_DETERMINE
INT_13 EQU 13H ; DISKETTE_IO
INT_14 EQU 14H ; RS232C_IO
INT_15 EQU 15H ; CASSETTE_IO
INT_16 EQU 16H ; KEYBOARD_IO
INT_17 EQU 17H ; PRINTER_IO
INT_18 EQU 18H ; RESIDENT BASIC
INT_19 EQU 19H ; BOOT_STRAP
INT_1A EQU 1AH ; TIME_OF_DAY
INT_1B EQU 1BH ; DUMMY_RETURN
INT_1C EQU 1CH ; DUMMY_RETURN
INT_1D EQU 1DH ; VIDEO_PARMS
INT_1E EQU 1EH ; DISK_BASE
INT_1F EQU 1FH ; CRT_CHARH
INT_80 EQU 80H ; DIAGNOSTICS
;-----

```

```

0000 ABS0 SEGMENT AT 0
0008 ORG 2*4
000A NMI_PTR LABEL WORD
000C ORG 3*4
000E INT3_PTR LABEL WORD

```

= 00FD  
= 00ED

= 0010  
= 0060  
= 0038  
= 0007  
= 0061  
= 0062  
= 0063  
= 0089  
= 0060  
= 0020  
= 0021  
= 0020  
= 0040  
= 0043  
= 0040  
= 00A0  
= 00C0  
= 01FF  
= 0080  
= 0040  
= 0020  
= 0201  
= 03F8  
= 02F8  
= 0378  
= 03D4  
= 03DA  
= 03DD

= 03DF  
= 03D9

= 2000  
= 0703  
= 0000  
= 0003  
= 0005  
= FF20  
= 3A00

= 00F2  
= 0080

= 0020  
= 0040  
= 0001  
= 00F4  
= 0020  
= 0040  
= 0080  
= 00F5  
= 000F  
= 0003  
= 003F

= 0010  
= 0011  
= 0012  
= 0013  
= 0014  
= 0015  
= 0016  
= 0017  
= 0018  
= 0019  
= 001A  
= 001B  
= 001C  
= 001D  
= 001E  
= 001F  
= 0080

0000  
0008  
000A  
000C  
000E

Appendix A.

```

0014          ORG      5H4          LABEL  WORD
0014          INT5_PTR  ORG      8H4          LABEL  DWORD
0020          INT_PTR   ORG     10H4         LABEL  DWORD
0040          VIDED_INT  ORG     1CH4         LABEL  WORD
0040          INT1C_PTR  ORG     1DH4         LABEL  WORD
0070          PARM_PTR   ORG     1EH4         LABEL  DWORD
0074          BASIC_PTR  ORG     1FH4         LABEL  WORD
0078          DISK_POINTER ORG     20H4        LABEL  DWORD
007C          EXT_PTR    ORG     21H4         LABEL  DWORD
0110          CSET_PTR   ORG     22H4         LABEL  DWORD
0120          KEY62_PTR  ORG     23H4         LABEL  WORD
0124          EXST      ORG     24H4         LABEL  WORD
0400          DATA_AREA ORG     25H4         LABEL  BYTE
0400          DATA_WORD ORG     26H4         LABEL  WORD
7C00          BOOT_LOCK ORG     27H4         LABEL  FAR
7C00          ABS0      ENDS

;-----
;          STACK -- USED DURING INITIALIZATION ONLY
;-----
0000          STACK   SEGMENT AT 30H
0000          DW      128 DUP(?)

0100          TOS     LABEL  WORD
0100          STACK   ENDS

;-----
;          ROM BIOS DATA AREAS
;-----
0000          DATA  SEGMENT AT 40H
0000          RS232_BASE DW      4 DUP(?) ; ADDRESSES OF RS232 ADAPTERS

0008          PRINTER_BASE DW      4 DUP(?) ; ADDRESSES OF PRINTERS

0010          EQUIP_FLAG DW      ? ; INSTALLED HARDWARE
0012          KBD_ERR   DB      ? ; COUNT OF KEYBOARD TRANSMIT ERRORS
0013          MEMORY_SIZE DW     ? ; USABLE MEMORY SIZE IN K BYTES
0015          TRUE_MEM  DW      ? ; REAL MEMORY SIZE IN K BYTES

;-----
;          KEYBOARD DATA AREAS
;-----
0017          KB_FLAG   DB      ?

;          SHIFT FLAG EQUATES WITHIN KB_FLAG
;          CAPS_STATE EQU     40H ; CAPS LOCK STATE HAS BEEN TOGGLED
;          NUM_STATE  EQU     20H ; NUM LOCK STATE HAS BEEN TOGGLED
;          ALT_SHIFT  EQU     08H ; ALTERNATE SHIFT KEY DEPRESSED
;          CTL_SHIFT  EQU     04H ; CONTROL SHIFT KEY DEPRESSED
;          LEFT_SHIFT EQU     02H ; LEFT SHIFT KEY DEPRESSED
;          RIGHT_SHIFT EQU    01H ; RIGHT SHIFT KEY DEPRESSED
0018          KB_FLAG_1 DB      ? ; SECOND BYTE OF KEYBOARD STATUS
;          INS_SHIFT  EQU     80H ; INSERT KEY IS DEPRESSED
;          CAPS_SHIFT EQU     40H ; CAPS LOCK KEY IS DEPRESSED
;          NUM_SHIFT  EQU     20H ; NUM LOCK KEY IS DEPRESSED
;          SCROLL_SHIFT EQU    10H ; SCROLL LOCK KEY IS DEPRESSED
;          HOLD_STATE EQU     08H ; SUSPEND KEY HAS BEEN TOGGLED
;          CLICK_ON   EQU     04H ; INDICATES THAT AUDIO FEEDBACK IS
;                               ; ENABLED
;          CLICK_SEQUENCE EQU    02H ; OCCURRNCE OF ALT-CTRL-CAPSLOCK HAS
;                               ; OCCURED
0019          ALT_INPUT DB      ? ; STORAGE FOR ALTERNATE KEYPAD
;                               ; ENTRY
001A          BUFFER_HEAD DW     ? ; POINTER TO HEAD OF KEYBOARD BUFF
001C          BUFFER_TAIL DW     ? ; POINTER TO TAIL OF KEYBOARD BUFF
001E          KB_BUFFER DW     16 DUP(?) ; ROOM FOR 15 ENTRIES

;          HEAD = TAIL INDICATES THAT THE BUFFER IS EMPTY
;          NUM_KEY     EQU     69 ; SCAN CODE FOR NUMBER LOCK
;          SCROLL_KEY  EQU     70 ; SCROLL LOCK KEY
;          ALT_KEY     EQU     56 ; ALTERNATE SHIFT KEY SCAN CODE
;          CTL_KEY     EQU     29 ; SCAN CODE FOR CONTROL KEY
;          CAPS_KEY    EQU     58 ; SCAN CODE FOR SHIFT LOCK
;          LEFT_KEY    EQU     42 ; SCAN CODE FOR LEFT SHIFT
;          RIGHT_KEY   EQU     54 ; SCAN CODE FOR RIGHT SHIFT
;          INS_KEY     EQU     82 ; SCAN CODE FOR INSERT KEY
;          DEL_KEY     EQU     83 ; SCAN CODE FOR DELETE KEY

;-----
;          DISKETTE DATA AREAS
;-----
003E          SEEK_STATUS DB     ? ; DRIVE RECALIBRATION STATUS
;                               ; BIT 0 = DRIVE NEEDS RECAL BEFORE
003F          MOTOR_STATUS DB     ? ; NEXT SEEK IF BIT IS = 0
;                               ; MOTOR STATUS
0040          MOTOR_COUNT DB     ? ; BIT 0 = DRIVE 0 IS CURRENTLY
;                               ; RUNNING
;                               ; TIME OUT COUNTER FOR DRIVE

```

```

= 0025
0041 ??
= 0080
= 0040
= 0020
= 0010
= 0009
= 0008
= 0004
= 0003
= 0002
= 0001
0042 07 [ ?? ]

MOTOR_WAIT EQU 37 ; TURN OFF
; 2 SECS OF COUNTS FOR MOTOR
; TURN OFF
DISKETTE_STATUS DB ? ; RETURN CODE STATUS BYTE
TIME_OUT EQU 80H ; ATTACHMENT FAILED TO RESPOND
BAD_SEEK EQU 40H ; SEEK OPERATION FAILED
BAD_NEC EQU 20H ; NEC CONTROLLER HAS FAILED
BAD_CRC EQU 10H ; BAD CRC ON DISKETTE READ
DMA_BOUNDARY EQU 09H ; ATTEMPT TO DMA ACROSS 64K
; BOUNDARY
BAD_DMA EQU 08H ; DMA OVERRUN ON OPERATION
RECORD_NOT_FND EQU 04H ; REQUESTED SECTOR NOT FOUND
WRITE_PROTECT EQU 03H ; WRITE ATTEMPTED ON WRITE
; PROTECTED DISK
BAD_ADDR_MARK EQU 02H ; ADDRESS MARK NOT FOUND
BAD_CMD EQU 01H ; BAD COMMAND GIVEN TO DISKETTE I/O
NEC_STATUS DB 7 DUP(?) ; STATUS BYTES FROM NEC

= 0020
= 012C
= 00AF
= 0003
= 0019
= 0004
SEEK_END EQU 20H
THRESHOLD EQU 300 ; NUMBER OF TIMER-0 TICKS TILL
; ENABLE
PARM0 EQU 0AFH ; PARAMETER 0 IN THE DISK_PARM
; TABLE
PARM1 EQU 3 ; PARAMETER 1
PARM9 EQU 25 ; PARAMETER 9
PARM10 EQU 4 ; PARAMETER 10

;-----
; VIDEO DISPLAY DATA AREA
;-----
0049 ??
004A ????
004C ????
004E ????
0050 08 [ ???? ]

CRT_MODE DB ? ; CURRENT CRT MODE
CRT_COLS DW ? ; NUMBER OF COLUMNS ON SCREEN
CRT_LEN DW ? ; LENGTH OF REGEN IN BYTES
CRT_START DW ? ; STARTING ADDRESS IN REGEN BUFFER
; CURSOR_POSH IS DEFINED LATER
; 8 DUP(?)

0060 ????
0062 ??
0063 ????
0065 ??
0066 ??
CURSOR_MODE DW ? ; CURRENT CURSOR MODE SETTING
ACTIVE_PAGE DB ? ; CURRENT PAGE BEING DISPLAYED
ADDR_6845 DW ? ; BASE ADDRESS FOR ACTIVE DISPLAY
; CARD
CRT_MODE_SET DB ? ; CURRENT SETTING OF THE
; CRT MODE REGISTER
CRT_PALLETTE DB ? ; CURRENT PALLETTE MASK SETTING

;-----
; CASSETTE DATA AREA
;-----
0067 ????
0069 ????
006B ??
EDGE_CNT DW ? ; TIME COUNT AT DATA EDGE
CRC_REG DW ? ; CRC REGISTER
LAST_VAL DB ? ; LAST INPUT VALUE

;-----
; TIMER DATA AREA
;-----
006C ????
006E ????
0070 ??
TIMER_LOW DW ? ; LOW WORD OF TIMER COUNT
TIMER_HIGH DW ? ; HIGH WORD OF TIMER COUNT
TIMER_OFL DB ? ; TIMER HAS ROLLED OVER SINCE LAST
; READ

;-----
; SYSTEM DATA AREA
;-----
0071 ??
0072 ????
BIOS_BREAK DB ? ; BIT 7=1 IF BREAK KEY HAS BEEN HIT
RESET_FLAG DW ? ; WORD=1234H IF KEYBOARD RESET
; UNDERWAY

;-----
; EXTRA DISKETTE DATA AREAS
;-----
0074 ??
0075 ??
0076 ??
0077 ??
TRACK0 DB ?
TRACK1 DB ?
TRACK2 DB ?

;-----
; PRINTER AND RS232 TIME-OUT VARIABLES
;-----
0078 04 [ ?? ]
PRINT_TIM_OUT DB 4 DUP(?)

007C 04 [ ?? ]
RS232_TIM_OUT DB 4 DUP(?)

;-----
; ADDITIONAL KEYBOARD DATA AREA
;-----
0080 ????
0082 ????
0084 ??
BUFFER_START DW ?
BUFFER_END DW ?
INTR_FLAG DB ? ; FLAG TO INDICATE AN INTERRUPT
; HAPPENED

;-----
; 62 KEY KEYBOARD DATA AREA
;-----
0085 ??
0086 ??
CUR_CHAR DB ? ; CURRENT CHARACTER FOR TYPAMATIC
VAR_DELAY DB ? ; DETERMINES WHEN INITIAL DELAY IS
; OVER
= 000F
0087 ??
0088 ??
= 0004
DELAY_RATE EQU 0FH ; INCREASES INITIAL DELAY
CUR_FUNC DB ? ; CURRENT FUNCTION
KB_FLAG_2 DB ? ; 3RD BYTE OF KEYBOARD FLAGS
RANGE EQU 4 ; NUMBER OF POSITIONS TO SHIFT
; DISPLAY

;-----
; BIT ASSIGNMENTS FOR KB_FLAG_2
;-----

```

Appendix A.

```

= 0060 FN_FLAG EQU 80H
= 0040 FN_BREAK EQU 40H
= 0020 FN_PENDING EQU 20H
= 0010 FN_LOCK EQU 10H
= 0008 TYPE_OFF EQU 08H
= 0004 HALF_RATE EQU 04H
= 0002 INIT_DELAY EQU 02H
= 0001 PUTCHAR EQU 01H
0069 ?? HORZ_POS DB ? ; CURRENT VALUE OF HORIZONTAL
; START PARM
; IMAGE OF DATA WRITTEN TO PAGREG

008A ?? PAGDAT DB ?
;-----
; KANJI DOS WORK AREA
;-----
00F0 02 [ 0000 ] GATJI_ADDR ORG 0F0H 2 DUP(0) ; RESERVED FOR KANJI DOS
00F0 DW

;-----
; CONTROL FLAGS
;-----
0300 J_EXT_STATUS ORG 300H ? ; EXTENSION CARTRIDGE CHARACTERISTICS
0300 DW ? ; EQUIPMENT FLAG EXTENSION
0302 DW ?

;-----
; JAPAN KEYBOARD DATA AREA
;-----
; KEYBOARD BUFFER
006B KANJI_KEY EQU 107 ; SCAN CODE FOR KANJI KEY
006C MUHEN_KEY EQU 108 ; SCAN CODE FOR MUHENKAN KEY
006D HENKAN_KEY EQU 109 ; SCAN CODE FOR HENKAN KEY
003A ALPHA_KEY EQU 58 ; SCAN CODE FOR ALPHA STATE KEY
006E KATAKANA_KEY EQU 110 ; SCAN CODE FOR KATAKANA STATE KEY
006F HIRAGANA_KEY EQU 111 ; SCAN CODE FOR HIRAGANA STATE KEY

; BIT ASSIGNMETS FOR JKB_FLAG
0004 HIRAGANA_STATE EQU 04H ; HIRAGANA SHIFT ACTIVE
0002 KATAKANA_STATE EQU 02H ; KATAKANA SHIFT ACTIVE
0001 ZENKAKU_STATE EQU 01H ; ZENKAKU MODE
0006 NOT_ALPHA_STATE EQU 06H ;
00F9 ALPHA_STATE EQU 0F9H ; ALPHA_STATE MASK
0005 ZENKAKU_CHAR EQU 05H ; ZENKAKU CHARACTER

; BIT ASSIGNMETS FOR JKB_FLAG_1
0010 HANKAKU_SHIFT EQU 10H ; HANKAKU SHIFT KEY DEPRESSED
0008 ZENKAKU_SHIFT EQU 08H ; ZENKAKU SHIFT KEY DEPRESSED
0004 HIRAGANA_SHIFT EQU 04H ; HIRAGANA SHIFT KEY DEPRESSED
0002 KATAKANA_SHIFT EQU 02H ; KATAKANA SHIFT KEY DEPRESSED
0001 ALPHA_SHIFT EQU 01H ; ALPHA SHIFT KEY DEPRESSED

; BIT ASSIGNMETS FOR JKB_FLAG_2
0080 KANJI_SHIFT EQU 80H ; KANJI SHIFT KEY DEPRESSED
0040 KNUM_SHIFT EQU 40H ; KNUMBER SHIFT KEY DEPRESSED
0020 MUHEN_SHIFT EQU 20H ; MUHEN SHIFT KEY DEPRESSED
0010 HENKAN_SHIFT EQU 10H ; HENK SHIFT KEY DEPRESSED
0004 NMI_FLG EQU 04H ; NMI ACTIVE FLAG
0002 INDICATOR_OFF EQU 02H ; INDICATOR ON/OFF SWITCH
0001 KANAKAN_OFF EQU 01H ; KANAKAN ON/OFF SWITCH
0304 19 [ 0000 ] KB_BUFFER_J DW 25 DUP(?) ; ROOM FOR 24 ENTRIES

0336 ?? JKB_FLAG DB ? ; 4TH BYTE OF KEYBOARD FLAGS
0337 ?? JKB_FLAG_1 DB ? ; 5TH BYTE OF KEYBOARD FLAGS
0338 ?? JKB_FLAG_2 DB ? ; 6TH BYTE OF KEYBOARD FLAGS
0339 0000 FIRST_PTR DW ? ; USED BY BUFFER QUEING (INT 41H)

;-----
; NEW VIDEO DATA AREA
;-----
= 8800 REGEN_START EQU 08800H ; SEGMENT ADDRESS OF REGEN
= 0000 DEBUG EQU 0 ; DEBUG FLAG
= 0010 NO_ACT_PAGE EQU 16 ; NUMBER OF ACTIVE PAGE
0338 ?? CRT_MODE2 DB ? ; CURRENT CRT MODE OF VIDEO PROCESSOR 2
033C ?? PAGDAT2 DB ? ; IMAGE OF DATA WRITTEN TO PAGREG 2
033D ?? CRT_MODE_SET2 DB ?
033E 0000 K_1ST_CHAR DW ? ; 1ST CHAR. CODE/ATTR. AT WRITE A/C IN KANA-KAN
0340 0000 W_1ST_CHAR DW ? ; 1ST CHAR. CODE/ATTR. AT WRITE A/C
0342 0000 TTY_1ST_CHAR DW ? ; 1ST CHAR AT WRITE TTY
0344 0000 K_TTY_1ST_CHAR DW ? ; 1ST CHAR AT WRITE RRY IN KANA-KAN
0346 ?? CRT_ROWS DB ? ; CURRENT CRT COLUMN SIZE
0347 ?? SUPIPCR DB ? ; LAST VALUE OF SUPERIMPOSE CONTROL REGISTER
0348 ?? AC_PRESENT DB ? ; ALTERNATE CURSOR PRESENT
0349 ?? GC_PRESENT DB ? ; GRAPHICS CURSOR PRESENT
034A 0000 ALT_CURSOR_POSH DW ? ; ALTERNATE CURSOR POSITION
034C ?? CPU_PAGE DB ? ; ACTIVE CPU PAGE
034D ?? CRT_PAGE DB ? ; ACTIVE CRT PAGE
034E 0000 GC_CURSOR_MODE DW ? ; GRAPHICS CURSOR MODE
0350 0000 AC_CURSOR_MODE DW ? ; ALTERNATE CURSOR MODE
0352 ?? PALETTE_MASK DB ? ; VALUE OF PALETTE MASK REGISTER
0353 ?? KJROM_STAT DB ? ; KANJI ROM STATUS (0:OFF, 1:ON)
0354 ?? VG_STAT DB ? ; VIDEO GENERATER STATUS (0:VG2, 1:VG1)
0355 0000 IEP_CTRL DW ? ; INTERRUPT ENABLE PROHIBIT FLAG
0357 ?? VSTACKL DB ? ; USED LEVEL OF VIDEO STACK
0358 0000 SS_SAVE DW ? ; SS SAVE AREA
035A 0000 SP_SAVE DW ? ; SP SAVE AREA
035C 10 [ 0000 ] CURSOR_POSH DW 16 DUP(?);

03F0 10 [ 0000 ] BASIC_WORK ORG 3F0H
03F0 DW 16 DUP(?); TEMPORARY RESERVED FOR BASIC

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03FB          ORG      3FBH
03FB ??      VIO_PROCESS DB ? ; VIDEO I/O IS PROCESSING
03FC ??      SUPPRESS_PAL DB ? ; SUPPRESS PALETTE SET DURING MODE SET
03FD          DATA   ENDS
;-----
;          EXTRA DATA AREA
;-----
0000          XXDATA SEGMENT AT 50H
0000 ??      STATUS_BYTE DB ?
;
;          THE FOLLOWING AREA IS USED ONLY ; DURING DIAGNOSTICS
0001 ??      DCP_MENU_PAGE DB ? ; TO CURRENT PAGE FOR DIAG. MENU
0002 ?????   DCP_ROW_COL DW ? ; CURRENT ROW/COLUMN COORDINATES
;
0004 ??      WRAP_FLAG DB ? ; FOR DIAG MENU
;
0005 ??      MFG_TST DB ? ; INTERNAL/EXTERNAL 8250 WRAP
0006 ?????   MEM_TOT DW ? ; INDICATOR
;
0008 ?????   MEM_DONES DW ? ; INITIALIZATION FLAG
;
000A ?????   MEM_DONE0 DW ? ; WORD EQUIV. TO HIGHEST SEGMENT IN
;
000C ?????   INT1C0 DW ? ; MEMORY
;
000E ?????   INT1C5 DW ? ; CURRENT SEGMENT VALUE FOR
0010 ??      MENU_UP DB ? ; BACKGROUND MEM TEST
;
0011 ??      DONE128 DB ? ; CURRENT OFFSET VALUE FOR
;
0012 ?????   KBDONE DW ? ; BACKGROUND MEM TEST
;
;          POST DATA AREA
;-----
0014 ?????   IO_ROM_INIT DW ? ; POINTR TO OPTIONAL I/O ROM INIT
;
0016 ?????   IO_ROM_SEG DW ? ; ROUTINE
0018 ??      POST_ERR DB ? ; POINTER TO IO ROM SEGMENT
;
0019 09 [    MODEM_BUFFER DB 9 DUP(?) ; FLAG TO INDICATE ERROR OCCURRED
;
;          ; DURING POST
;          ; MODEM RESPONSE BUFFER
;
;
;          ; (MAX 9 CHARS)
0022 ?????   MFG_RTH DW ? ; POINTER TO MFG. OUTPUT ROUTINE
0024 ?????   DW ?
;
;-----
;          SERIAL PRINTER DATA
;-----
0026 ?????   SP_FLAG DW ?
0028 ??      SP_CHAR DB ?
;
;          ; THE FOLLOWING SIX ENTRIES ARE
;          ; DATA PERTAINING TO NEW STICK
0029 ?????   NEW_STICK_DATA DW ? ; RIGHT STICK DELAY
002B ?????   DW ? ; RIGHT BUTTON A DELAY
002D ?????   DW ? ; RIGHT BUTTON B DELAY
002F ?????   DW ? ; LEFT STICK DELAY
0031 ?????   DW ? ; LEFT BUTTON A DELAY
0033 ?????   DW ? ; LEFT BUTTON B DELAY
0035 ?????   DW ? ; RIGHT STICK LOCATION
0037 ?????   DW ? ; UNUSED
0039 ?????   DW ? ; UNUSED
003B ?????   DW ? ; LEFT STICK POSITION
;
003D          XXDATA ENDS
;-----
;          BIOS LOCAL STACK AREA
;-----
0000          VSTACK SEGMENT AT 1A0H
0000 0600 [   DB 1536 DUP(?)
;
;
0600          VSTACK_TOP LABEL WORD
0600          VSTACK ENDS

```

Appendix A.

```

PAGE ,121
SEGMENT AT 120H
DSEGM
-----
; DATA SEGMENT FOR INT 17 , INT 5
;
;   XXX PORT ASSIGN XXX
PR_DATA_PORT EQU 0378H ;PRINTER I/O PORT
PR_STATUS_PORT EQU 0379H
PR_CMD_PORT EQU 037AH
;
;   XXX STATUS XXX
PR_BUSY EQU 80H ;PRINTER STATUS
PR_PE EQU 20H
PR_SELECT EQU 10H
PR_ERROR EQU 08H
PR_ATTACH EQU 04H
PR_TIMEOUT EQU 01H
;
;   XXX PRINTER ID XXX
NO_PRINTER EQU 0 ;PRINTER TYPE-I
PRT1 EQU 1 ;PRINTER TYPE-II
PRT2 EQU 2
;
;   XXX PRINT MODE XXX
EVEN_PR_FLG EQU 1 ;CHARACTER LINE MODE
LOW_PR_FLG EQU 2 ;GRAPHIC IMAGE LINE MODE
;
;   XXX CHARACTER SIZE XXX
HOR EQU 0 ;NORMAL SIZE
BAI EQU 1 ;DOUBLE SIZE
;
;   XXX FLAG 1 XXX
TWO_BYTE_FLG EQU 80H ;TWO BYTES CHARACTER CODE INDICATION
PRT_CHK_FLG EQU 10H ;PRINTER-ID CHECK FLAG
F_FLG EQU 04H ;ESC-F PROCESS FLAG
X_FLG EQU 02H ;ESC-X PROCESS FLAG
ESC_FLG EQU 01H ;ESC PROCESS FLAG
;
;   XXX FLAG 2 XXX
IGM_FLG EQU 40H ;COMMAND IGNORE INDICATION FLAG
X9_FLG EQU 20H ;ESC-X9 PROCESS FLAG
X6_FLG EQU 10H ;ESC-X6 PROCESS FLAG
X5_FLG EQU 08H ;ESC-X5 PROCESS FLAG
X3_FLG EQU 04H ;ESC-X3 PROCESS FLAG
X2_FLG EQU 02H ;ESC-X2 PROCESS FLAG
X1_FLG EQU 01H ;ESC-X1 PROCESS FLAG
;
;   XXX FLAG 3 XXX
CHG_LPI_FLG EQU 80H ;TEMPORARY LPI CHANGE INDICATION
BAI_FUL_FLG EQU 20H ;BAIKAKU SLICE FULL INDICATION
SL_FUL_FLG EQU 10H ;SLICE FULL INDICATION
;
;   XXX MAX SLICE VALUE XXX
MAX EQU 1120 ;MAX NUMBER OF SLICES
-----
; WORK AREA FOR INT 17
;
0000 01 [ 0001 ] PRINTER_ID DW 1 DUP (1) ;1:PTR1 2:PTR2
;
0002 01 [ 5A ] RETURN_CODE DB 1 DUP ('Z') ;PRINTER STATUS
;
0003 01 [ 5A ] CPI DB 1 DUP ('Z') ;CHAR / INCH
;
0004 01 [ 5A ] LPI DB 1 DUP ('Z') ;LINE / INCH
;
0005 01 [ 5A5A ] CPL DW 1 DUP ('ZZ') ;CHAR / LINE
;
0007 01 [ 5A5A ] LPP DW 1 DUP ('ZZ') ;LINE / PAGE
;
0009 01 [ 5A ] PRINT_MODE DB 1 DUP ('Z') ;0:ELSE 1:CHAR 2:IMAGE
;
000A 01 [ 005A ] CHAR_TYPE DW 1 DUP ('Z') ;1:LARGE 2:SMALL
;
000C 01 [ 5A ] SIZE_ESC DB 1 DUP ('Z') ;0:NORMAL 1:DOUBLE (ESC+[,])
;
000D 01 [ 5A ] SIZE_AH DB 1 DUP ('Z') ;0:NORMAL 1:DOUBLE (AH=0/5)
;
000E 01 [ 5A5A ] CCP DW 1 DUP ('ZZ') ;CURRENT CHAR POSITION
;
0010 01 [ 5A5A ] CSP DW 1 DUP ('ZZ') ;CURRENT SLICE POSITION
;
0012 01 [ 5A5A ] CSPMAX DW 1 DUP ('ZZ') ;CURRENT SLICE MAXIMUM POSITION

```



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0014 01 [ 5A5A ] SPSAVE DW 1 DUP ('ZZ') ;SP SAVE AREA
0016 01 [ 5A5A ] PR_TIME1 DW 1 DUP ('ZZ') ;BEEP TIMER 1
0018 01 [ 5A5A ] PR_TIME2 DW 1 DUP ('ZZ') ;BEEP TIMER 2
001A 01 [ 5A5A ] PR_TIME3 DW 1 DUP ('ZZ') ;WAIT TIMER
001C 01 [ 5A5A ] CPIMASK DW 1 DUP ('ZZ') ;13.5 CPI ADJUST MASK
001E 01 [ 5A ] STATUS17 DB 1 DUP ('Z') ;0:NOT PROGRESS 1:IN PROGRESS
001F 01 [ 5A ] SYSTEM_ID DB 1 DUP ('Z') ;00:NATIVE 20:EXTENSION
0020 01 [ 5A5A ] CODEN DW 1 DUP ('ZZ') ;THE NUMBER OF CODE
0022 01 [ 5A ] N1 DB 1 DUP ('Z') ;ESCAPE PARM N1
0023 01 [ 5A ] N2 DB 1 DUP ('Z') ;ESCAPE PARM N2
0024 01 [ 5A5A ] N1N2 DW 1 DUP ('ZZ') ;ESCAPE PARM N1N2
0026 01 [ 5A ] LF_CT DB 1 DUP ('Z') ;LINE FEED COUNT
0027 01 [ 5A ] SP_VALUE DB 1 DUP ('Z') ;SPACING VALUE
0028 01 [ 5A ] IM_MOD DB 1 DUP ('Z') ;IMAGE MODE
0029 01 [ 5A5A ] FS_N DW 1 DUP ('ZZ') ;FONT SLICE NUMBER
002B 01 [ 5A ] BYTE_ONE DB 1 DUP ('Z') ;FIRST BYTE OF TWO-BYTED CODE
002C 01 [ 5A ] FL01 DB 1 DUP ('Z') ;
002D 01 [ 5A ] FL02 DB 1 DUP ('Z') ;
002E 01 [ 5A ] FL03 DB 1 DUP ('Z') ;
002F 01 [ 5A5A ] CSIZE DW 1 DUP ('ZZ') ;CHARACTER SIZE WORK
0031 0F [ 5A ] DB 15 DUP ('Z') ; - RESERVED -
0040 1B [ 5A ] GVALUE DB 24 DUP ('Z') ;GRID LINE CONTROL VALUE

= 0001 V_SOLID EQU 1 ; VERTICAL SOLID LINE
= 0004 V_DASH EQU 4 ; VERTICAL DASHED LINE
= 0007 V_UNDER EQU 7 ; UNDERSCORE IMAGE
= 0008 V_SIZE EQU 8 ; VERTICAL SKIP SIZE
= 0012 H_KEY EQU 18 ; KEY OF HORIZONTAL VALUE
= 0013 H_DASH EQU 19 ; UPPER VERTICAL SOLID LINE
= 0016 H_SIZE EQU 22 ; HORIZONTAL SKIP SIZE
= 0017 H_SPACE EQU 23 ; SPACE BETWEEN CHARACTERS
0058 02 [ 5A5A ] TVALUE DW 2 DUP ('ZZ') ;CHAR TYPE CONTROL VALUE

= 0000 UPPER EQU 0 ; UPPER IMAGE FLAG
= 0002 LOWER EQU 2 ; LOWER IMAGE FLAG

```

Appendix A.

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005C 04 [ 5A ] DB 4 DUP ('Z') ; - RESERVED -
;-----;
; WORK AREA FOR INT 5 ;
;-----;
0060 01 [ S45A ] HSIZE DW 1 DUP ('ZZ') ;HORIZONTAL DOT SIZE
0062 01 [ S45A ] VSIZE DW 1 DUP ('ZZ') ;VERTICAL DOT SIZE
0064 01 [ S45A ] HRATIO DW 1 DUP ('ZZ') ;HORIZONTAL ENLARGE RATIO
0066 01 [ S45A ] VRATIO DW 1 DUP ('ZZ') ;VERTICAL ENLARGE RATIO
0068 01 [ 5A ] PAGENO DB 1 DUP ('Z') ;ACTIVE PAGE
0069 01 [ S45A ] SPSAVES DW 1 DUP ('ZZ') ;SP SAVE AREA FOR INT 5
006B 01 [ S45A ] COLORTB DW 1 DUP ('ZZ') ;COLOR TABLE OFFSET
006D 01 [ 5A ] VIDEO_MODE DB 1 DUP ('Z') ;>0:GRAPHIC <0:CHARACTER
006E 01 [ S45A ] SLICE DW 1 DUP ('ZZ') ;SLICE SIZE (16 OR 24)
0070 01 [ 5A ] SHIFT DB 1 DUP ('Z') ;SHIFT VALUE
0071 0F [ 5A ] DB 15 DUP ('Z') ; - RESERVED -
0080 18 [ 5A ] DOT DB 24 DUP ('Z') ;COLOR DOT PATTERN AREA
0098 08 [ 5A ] DB 8 DUP ('Z') ; - RESERVED -
;-----;
; BUFFER ;
;-----;
00A0 F0 [ 5A ] CODE_BUFFER DB 240 DUP ('Z') ;CODE SAVE AREA
0190 F0 [ 5A ] ATTR_BUFFER DB 240 DUP ('Z') ;ATTRIBUTE SAVE AREA
0280 20 [ 5A ] FONT DB 32 DUP ('Z') ;FONT IMAGE WORK AREA
02A0 36 [ 5A ] GRID DB 54 DUP ('Z') ;GRID LINE IMAGE AREA
02D6 FE [ 5A ] DB 254 DUP ('Z') ; - RESERVED -
03D4 A0 [ 5A ] CODE_INT5 DB 160 DUP ('Z') ;CODE AREA FOR INT 5
0474 A0 [ 5A ] ATTR_INT5 DB 160 DUP ('Z') ;ATTRIBUTE AREA FOR INT 5
00A0 0474 [ 5A ] IMAGE_BUFFER ORG OFFSET CODE_BUFFER
00A0 DB 1140 DUP ('Z') ;EVEN DOT SAVE AREA
0514 DSEGM ENDS
;-----;
; EXTRA SEGMENT ;
;-----;
0000 XXDATA SEGMENT AT 50H
0000 00 STATUS_BYTE DB 0
0001 XXDATA ENDS

```

[Faint, illegible text, possibly bleed-through from the reverse side of the page. Some words like "RECEIVED" and "DATE" are barely visible.]

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Appendix A.

```
#####  
#####  
#  
# MODULE 1 #  
#  
#####  
#####
```

0000

= 0000

```
0000 35 36 30 31 4A 46  
      42 20 43 4F 50 52  
      2E 20 49 42 4D 20  
      31 39 38 34
```

```
0016  
0030  
0030
```

```
-----  
: ROM RESIDENT CODE :  
-----  
CODE SEGMENT PUBLIC  
.LIST  
ASSUME CS:CODE,DS:ABS0,ES:NOTHING,SS:STACK  
BEGIN = $  
DB '5601JFB COPR. IBM 1984' ; COPYRIGHT NOTICE  
  
ORG $ ; REAL SIZE  
ORG BEGIN+30H  
CODE ENDS
```

\*\*\*\*\*  
\* MODULE 2 \*  
\*\*\*\*\*

INT 10

VIDEO\_IO

THESE ROUTINES PROVIDE THE CRT INTERFACE  
THE FOLLOWING FUNCTIONS ARE PROVIDED:

(AH)=0 SET MODE (AL) CONTAINS MODE VALUE  
(AL)= 0 40X25 COLOR ANK  
(AL)= 1 40X25 COLOR ANK  
(AL)= 2 80X25 COLOR ANK  
(AL)= 3 80X25 COLOR ANK  
GRAPHICS MODES  
(AL)= 4 320X200 4 COLOR (40X25 ANK)  
(AL)= 5 320X200 4 COLOR (40X25 ANK)  
(AL)= 6 640X200 2 COLOR (80X25 ANK)  
(AL)= 7 NOT VALID

\*\*\*\* EXTENDED MODES \*\*\*\*  
(AL)= 8 160X200 16 COLOR (20X25 ANK)  
(AL)= 9 320X200 16 COLOR (40X25 ANK)  
(AL)= A 640X200 4 COLOR (80X25 ANK)  
(AL)= B NOT VALID  
(AL)= C NOT VALID  
(AL)= D NOT VALID  
(AL)= E NOT VALID  
(AL)= F NOT VALID

\*\*\*\* KANJI EXTENSION MODE \*\*\*\*  
(AL)=10 40X11 COLOR  
(AL)=11 40X11 COLOR  
(AL)=12 80X11 COLOR  
(AL)=13 80X11 COLOR  
(AL)=14 320X200 4 COLOR (20X11 KJ)  
(AL)=15 320X200 4 COLOR (20X11 KJ)  
(AL)=16 640X200 2 COLOR (40X11 KJ)  
(AL)=17 NOT VALID  
(AL)=18 160X200 16 COLOR (10X11 KJ)  
(AL)=19 320X200 16 COLOR (20X11 KJ)  
(AL)=1A 640X200 4 COLOR (40X11 KJ)  
(AL)=1B 640X200 16 COLOR (40X11 KJ)

\*\*\* NOTE IF HIGH ORDER BIT IN AL IS SET, THE REGEN  
BUFFER IS NOT CLEARED.

(AH)=1 SET CURSOR TYPE  
(CH) = BITS 4-0 = START LINE FOR CURSOR  
\*\* HARDWARE WILL ALWAYS CAUSE BLINK  
\*\* SETTING BIT 5 OR 6 WILL CAUSE ERRATIC  
BLINKING OR NO CURSOR AT ALL  
\*\* IN ANK GRAPHICS MODES, BIT 5 IS FORCED ON TO  
DISABLE THE CURSOR  
(CL) = BITS 4-0 = END LINE FOR CURSOR

(AH)=2 SET CURSOR POSITION  
(DH,DL) = ROW,COLUMN (0,0) IS UPPER LEFT  
(BH) = PAGE NUMBER (MUST BE 0 FOR GRAPHICS MODES)

(AH)=3 READ CURSOR POSITION  
(BH) = PAGE NUMBER (MUST BE 0 FOR GRAPHICS MODES)  
ON EXIT (DH,DL) = ROW,COLUMN OF CURRENT CURSOR  
(CH,CL) = CURSOR MODE CURRENTLY SET

(AH)=4 READ LIGHT PEN POSITION  
ON EXIT:  
(AH) = 0 -- LIGHT PEN SWITCH NOT DOWN/NOT TRIGGERED  
(AH) = 1 -- VALID LIGHT PEN VALUE IN REGISTERS  
(DH,DL) = ROW,COLUMN OF CHARACTER LP POSM  
(CH) = RASTER LINE (0-199)  
(BX) = PIXEL COLUMN (0-319.639)

(AH)=5 SELECT ACTIVE DISPLAY PAGE (VALID ONLY FOR ALPHA MODES)  
(AL)=NEW PAGE VALUE (0- 7 FOR MODES 041 , 0-3 FOR MODES 283  
0-15 FOR MODES 10411, 0-7 FOR MODES 12413)

IF BIT 7 (80H) OF AL=1  
READ/WRITE CRT/CPU PAGE REGISTERS  
(AL) = 80H READ CRT/CPU PAGE REGISTERS  
(AL) = 81H SET CPU PAGE REGISTER  
(BL) = VALUE TO SET  
(CL) = CRT MODE TO SET  
(AL) = 82H SET CRT PAGE REGISTER  
(BH) = VALUE TO SET  
(AL) = 83H SET BOTH CRT AND CPU PAGE REGISTERS  
(BH) = VALUE TO SET IN CRT PAGE REGISTER  
(BL) = VALUE TO SET IN CPU PAGE REGISTER  
(CL) = CRT MODE TO SET  
IF BIT 7 (80H) OF AL=1  
ALWAYS RETURNS (BH) = CONTENTS OF CRT PAGE REG  
(BL) = CONTENTS OF CPU PAGE REG  
\*\*\* NOTE CRT/CPU PAGE 0-8 MEANS MAIN RAM, AND  
9-A MEANS VIDEO RAM.

(AH)=6 SCROLL ACTIVE PAGE UP  
(AL) = NUMBER OF LINES, INPUT LINES BLANKED AT  
BOTTOM OF WINDOW, AL = 0 MEANS BLANK  
ENTIRE WINDOW  
(CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL  
(DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL  
(BH) = ATTRIBUTE TO BE USED ON BLANK LINE

(AH)=7 SCROLL ACTIVE PAGE DOWN  
(AL) = NUMBER OF LINES, INPUT LINES BLANKED AT TOP  
OF WINDOW, AL=0 MEANS BLANK ENTIRE WINDOW



```

; ASCII TELETYPE ROUTINE FOR OUTPUT
; (AH) = 14 (0EH) WRITE TELETYPE TO ACTIVE PAGE
; (AL) = CHAR TO WRITE
; (BL) = FOREGROUND COLOR IN GRAPHICS MODE
; NOTE -- SCREEN WIDTH IS CONTROLLED BY PREVIOUS
; MODE SET
;
; (AH) = 15 (0FH) CURRENT VIDEO STATE
; RETURNS THE CURRENT VIDEO STATE
; (AL) = MODE CURRENTLY SET (SEE AH=0 FOR EXPLANATION)
; (AH) = NUMBER OF CHARACTER COLUMNS ON SCREEN
; (BH) = CURRENT ACTIVE DISPLAY PAGE
;
; (AH) = 16 (10H) SET PALETTE REGISTERS
; (AL) = 0 SET PALETTE REGISTER
; (BL) = PALETTE REGISTER TO SET (00H - 0FH)
; (BH) = VALUE TO SET
; (AL) = 1 SET BORDER COLOR REGISTER
; (BH) = VALUE TO SET
; (AL) = 2 SET ALL PALETTE REGISTERS AND BORDER
; REGISTER
; ES:DX POINTS TO A 17 BYTE LIST
; BYTES 0 THRU 15 ARE VALUES FOR PALETTE
; REGISTERS 0 THRU 15
; BYTE 16 IS THE VALUE FOR THE BORDER
; REGISTER
;
; (AH) = 17 (11H) RESERVED
; (AH) = 18 (12H) RESERVED
;
; (AH) = 19 (13H) REQUEST FONT PATTERN
; RETURNS THE REQUESTED FONT PATTERN IN USER AREA
; (AL)=0 REQUEST BASE-FONT
; (AL)=80H REQUEST BASE-FONT WITH FULL CHARACTER BOX
;
; (AL)=40H WRITE FONT PATTERN FROM USER AREA TO GAIJI RAM
; (CX) = INTERNAL CODE FOR REQUESTED FONT
; FOR HANKAKU-FONT (CH)=0
; (ES:BX) = DATA AREA FOR FONT
; NORMAL-BOX FULL-BOX
; -16X16 ; 32 BYTE 36 BYTE
; -16X8 ; 16 BYTE 18 BYTE
;
; (AH) = 20 (14H) SUPERIMPOSE SCREEN
; (AL) = 0 SET MODE
; (BH)= 0-3 NOT VALID
; (BH)= 4 320X200 4 COLOR (40X25 ANK)
; (BH)= 5 320X200 4 COLOR (40X25 ANK)
; (BH)= 6 640X200 2 COLOR (80X25 ANK)
; (BH)= 7 NOT VALID
; (BH)= 8 160X200 16 COLOR (20X25 ANK)
; (BH)= 9 320X200 16 COLOR (40X25 ANK)
; (BH)= A 640X200 4 COLOR (80X25 ANK)
; (BH)= B-13 NOT VALID
; (BH)=14 320X200 4 COLOR (20X11 KJ)
; (BH)=15 320X200 4 COLOR (20X11 KJ)
; (BH)=16 640X200 2 COLOR (40X11 KJ)
; (BH)=17 NOT VALID
; (BH)=18 160X200 16 COLOR (10X11 KJ)
; (BH)=19 320X200 16 COLOR (20X11 KJ)
; (BH)=1A 640X200 4 COLOR (40X11 KJ)
;
; *** NOTE IF HIGH ORDER BIT IN AL IS SET, THE REGEN
; BUFFER IS NOT CLEARED.
;
; (AL) = 1 SET SUPERIMPOSE
; (BH)= 0 OFF
; (BH)= 1 ON
; (AL) = 2 SET FORGROUD PAGE
; (BH)= 0 VRAM-1
; (AL) = 3 SET TRANSPARENT PALETTE
; (BH) = PALETTE REGISTER NUMBER
; (AL) = 4 SET SUPERIMPOSE MODE
; (BH) = 0 PRI (PRIORITY)
; (BH) = 1 XOR
; (BH) = 2 AND
; (BH) = 3 OR
;
; CS,SS,DS,ES,BP,DI,SI,BX,CX,DX PRESERVED DURING CALL
; AX IS DESTROYED
;-----
; VIDEO GATE ARRAY REGISTERS
;
; PORT JDA OUTPUT
;
; * VIDEO PROCESSOR 1 (MAIN RAM:VRAM1) * VIDEO PROCESSOR 2 (VIDEO RAM:VRAM2)
;
; REG 0 MODE CONTROL 1 REGISTER MODE CONTROL 1 REGISTER
; 01H +HI BANDWIDTH/-LOW BANDWIDTH
; 02H +GRAPHICS/--ALPHA +GRAPHICS/--ALPHA
; 04H RESERVED RESERVED
; 08H +VIDEO ENABLE +VIDEO ENABLE
; 10H +16 COLOR GRAPHICS +16 COLOR GRAPHICS
; 20H RESERVED RESERVED
; 40H RESERVED +KANJI MODE
; 80H RESERVED +640X200 16 COLOR GRAPHICS
;
; REG 1 PALETTE MASK REISTER PALETTE MASK REISTER
; 01H PALETTE MASK 0 PALETTE MASK 0
; 02H PALETTE MASK 1 PALETTE MASK 1
; 04H PALETTE MASK 2 PALETTE MASK 2
; 08H PALETTE MASK 3 PALETTE MASK 3
; 10H RESERVED +VRAM-1 ENABLE

```

Appendix A.

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;
; 20H RESERVED +VRAM-2 ENABLE
; 40H RESERVED +HIGH RESOLUTION ENABLE
; 80H RESERVED +HIGH/--LOW FREQUENCY DISPLAY
;
; REG 2 BORDER COLOR REGISTER BORDER COLOR REGISTER
; 01H BLUE
; 02H GREEN
; 04H RED
; 08H INTENSITY
;
; REG 3 MODE CONTROL 2 REGISTER MODE CONTROL 2 REGISTER
; 01H RESERVED -- MUST BE ZERO RESERVED -- MUST BE ZERO
; 02H +ENABLE BLINK +ENABLE BLINK
; 04H RESERVED -- MUST BE ZERO RESERVED -- MUST BE ZERO
; 08H +2 COLOR GRAPHICS +2 COLOR GRAPHICS
; (640X200 2 COLOR ONLY) (640X200 2 COLOR ONLY)
; 10H RESERVED RESERVED
; 20H RESERVED RESERVED
; 40H RESERVED RESERVED
; 80H RESERVED RESERVED
;
; REG 4 RESET REGISTER RESET REGISTER
; 01H +ASYNCHRONOUS RESET
; 02H +SYNCHRONOUS RESET
;
; REG 5 TRANSPARENT PALETTE
; 01H SELECT 0 RESERVED
; 02H SELECT 1 RESERVED
; 04H SELECT 2 RESERVED
; 05H SELECT 3 RESERVED
;
; REG 6 SUPERIMPOSE CONTROL REGISTER
; 01H +FORE=V-RAM,BACK=MAIN-RAM RESERVED
; 02H +TRANSPARENT ON RESERVED
; 04H MODE CONTROL 1 RESERVED
; 08H MODE CONTROL 2 RESERVED
;
; REGS 10 TO 1F PALETTE REGISTERS PALETTE REGISTERS
; 01H BLUE
; 02H GREEN
; 04H RED
; 08H INTENSITY
;
; VIDEO GATE ARRAY STATUS
; PORT 3DA INPUT
; 01H +DISPLAY ENABLE
; 02H +LIGHT PEN TRIGGER SET
; 04H -LIGHT PEN SWITCH MADE
; 08H +VERTICAL RETRACE
; 10H +VIDEO DOTS
;

```

```

-----
= 000D CR EQU 0DH ; CARIDGE RETURN
= 000A LF EQU 0AH ; LINE FEED
= 0007 BELL EQU 7 ; BEEP
= 0008 BS EQU 8 ; BACK SPACE
= 00A0 HALFTONE EQU 0A0H ; CODE OF HALF TONE
= 2A55 HT_FONT EQU 0AA55H AND 7F7FH ; FONT PATTERN OF HALF TONE
= FFFF TRUE EQU 0FFFFH
= 0000 FALSE EQU 0
= 0004 GRAPHICS EQU 4 ; GRAPHICS MODE
= 0014 KJGRAPH EQU 14H ; KANJI GRAPHICS MODE
= 0010 KJ_MODE EQU 10H ; KANJI MODE
= 0010 VIDEO EQU 10H ; VIDEO INTERRUPT
= 0016 KEYBOARD EQU 16H ; KEYBOARD INTERRUPT
= DEFAULT_MODE EQU KJ_MODE ; DEFAULT MODE

= 0004 PORT_B_ALPHA EQU 04H ; PORT_B ALPHA MODE
= 0008 EXP64K EQU 08H ; 64K MAIN RAM EXPANTION CARD INSTALLED

= 0000 PCMODE1 EQU 00H ; PC-J MODE CONTROL 1
= 0008 VIDEOMB EQU 08H ; VIDEO ENABLE

= 0001 PCPALETM EQU 01H ; PC-J PALETTE MASK
= 0002 PCBORD EQU 02H ; PC-J BORDER COLOR
= 0008 INTSEL EQU 08H ; INTENSITY BIT FOR PALETTE
= 0003 PCMODE2 EQU 03H ; PC-J MODE CONTROL 2
= 0004 PCRESET EQU 04H ; PC-J RESET
= 0002 SYNCRST EQU 02H ; SYNCHRONOUSE RESET
= 0005 PCYRPALT EQU 05H ; PC-J TRANSPARENT PALETTE
= 0006 PCSUPER EQU 06H ; PC-J SUPERIMPOSE REGISTER
= 0010 PCPALET EQU 10H ; PC-J PALETTE REGISTER

= 0001 FOREVRAM EQU 01H ; V-RAM IS FOREGROUND
= 0002 TRANSHN EQU 02H ; TRANSPARENT ON

= 0000 SX2STAT EQU 00H ; PC-J SX-02 STATUS REGISTER
= 0008 VERTRET EQU 08H ; VERTICAL RETRACE
= 0004 LPENSW EQU 04H ; -LIGHT PEN SWITCH MODE
= 0002 LPENTRG EQU 02H ; LIGHT PEN TRIGGER SET

= 0000 IXMODE1 EQU 00H ; PC-J MODE CONTROL 1
= 0001 IXPALETM EQU 01H ; PC-J PALETTE MASK
= 0010 VRAM1ENB EQU 10H ; VRAM 1 ENABLE 03/05
= 0002 IXBORD EQU 02H ; PC-J BORDER COLOR
= 0003 IXMODE2 EQU 03H ; PC-J MODE CONTROL 2
= 0004 IXRESET EQU 04H ; PC-J RESET
= 0010 IXPALET EQU 10H ; PC-J PALETTE REGISTER

= 0007 SBKJROM EQU 07H ; SX-08 KANJI ROM AND GAIJI RAM
= 0009 SBVRAM1 EQU 09H ; SX-08 VIDEO RAM (SHARED)
= 000A SBVRAM2 EQU 0AH ; SX-08 VIDEO RAM (SEPARATED)

```



```

= 008C      S85X02A      EQU      8CH      ; SX-08 SX-02A PC-J VIDEO
= 008D      S85X02B      EQU      8DH      ; SX-08 SX-02B JX VIDEO

= 0080      ON          EQU      80H      ; ENABLE BIT
= 0020      MEM          EQU      20H      ; MEMORY BIT
= 8000      AKJROM      EQU      08000H    ; KANJI ROM ADDRESS
= 8800      AVRAM1      EQU      08800H    ; VIDEO RAM 1 ADDRESS
= 8800      AVRAM2      EQU      08800H    ; VIDEO RAM 2 ADDRESS

= 0000      M32K        EQU      NOT 1FH AND 1FH ; 32K MEMORY RANGE SELECT
= 0001      M64K        EQU      NOT 1EH AND 1FH ; 64K MEMORY RANGE SELECT
= 0003      M128K       EQU      NOT 1CH AND 1FH ; 128K MEMORY RANGE SELECT
= 0007      M256K       EQU      NOT 18H AND 1FH ; 256K MEMORY RANGE SELECT

= 0040      WR          EQU      40H      ; WRITE ENABLE BIT
= 0020      RD          EQU      20H      ; READ ENABLE BIT
= 0000      FULL        EQU      7FH AND 0; FULL DECODE

= 03D4      A6845       EQU      03D4H    ; I/O ADDRESS OF 6845
= 01FF      SX08BASE    EQU      01FFH    ; I/O ADDRESS OF I/O ADDRESS CONTROLLER
= 03D8      APOCON      EQU      03D8H    ; I/O ADDRESS OF PALETTE AND SUPERIMPOSE CONT.
= 03DA      S2ABASE     EQU      03DAH    ; I/O ADDRESS OF VIDEO PROCESSOR 1
= 03DA      S2BBASE     EQU      03DAH    ; I/O ADDRESS OF VIDEO PROCESSOR 2

= 0030      KJROM_OFF   EQU      MEM OR AKJROM/800H ; TURN OFF KANJI ROM
= 0080      KJROM_ON    EQU      ON OR KJROM_OFF ; TURN ON KANJI ROM
= 0037      VRAM1_OFF   EQU      MEM OR AVRAM1/800H ; TURN OFF VIDEO RAM 1
= 0087      VRAM1_ON    EQU      ON OR VRAM1_OFF ; TURN ON VIDEO RAM 1
= 0037      VRAM2_OFF   EQU      MEM OR AVRAM2/800H ; TURN OFF VIDEO RAM 2
= 0087      VRAM2_ON    EQU      ON OR VRAM2_OFF ; TURN ON VIDEO RAM 2

= 007B      SX02A_OFF   EQU      SX02A/8 ; TURN OFF SX-02 A
= 00FB      SX02A_ON    EQU      ON OR SX02A_OFF ; TURN ON SX-02 A
= 007B      SX02B_OFF   EQU      S2BBASE/8 ; TURN OFF SX-02 B
= 00FB      SX02B_ON    EQU      ON OR SX02B_OFF ; TURN ON SX-02 B

= 007F      KJMASKL     EQU      007FH    ; MASK PATTERN FOR LEFT PART OF 16X16 FONT
= 80E8      WHITE_BOX EQU (9874H * 2) AND 3FFFH OR 8000H ;KJ-ROM SEG.ADDRESS OF WHITE BOX CHA

= 8140      JIS1_L      EQU      08140H    ; LOW BOUND OF JIS 1 CHARACTER
= 9873      JIS1_H      EQU      09872H+1; HIGH BOUND OF JIS 1 CHARACTER
; +1 FOR SPECIAL CHARACTER FOR KANA-KANJI CONV.
= 8440      ROSS_L_LOW   EQU      8440H    ; LOW BOUND OF ROSSIAN LOWER CHARACTER
= 8460      ROSS_L_HIGH EQU      8460H    ; HIGH BOUND OF ROSSIAN LOWER CHARACTER
= 8470      ROSS_U_LOW   EQU      8470H    ; LOW BOUND OF ROSSIAN UPPER CHARACTER
= 8491      ROSS_U_HIGH EQU      8491H    ; HIGH BOUND OF ROSSIAN UPPER CHARACTER

= 8100      RROSS_L_LOW EQU      8100H    ; LOW BOUND OF ROSSIAN LOWER REGEN CODE
= 8120      RROSS_L_HIGH EQU      8120H    ; HIGH BOUND OF ROSSIAN LOWER REGEN CODE
= 8200      RROSS_U_LOW   EQU      8200H    ; LOW BOUND OF ROSSIAN UPPER REGEN CODE
= 8221      RROSS_U_HIGH EQU      8221H    ; HIGH BOUND OF ROSSIAN UPPER REGEN CODE

= 0800      S_RAM       EQU      2048     ; SIZE OF STATIC RAM (FOR EXTERNAL CHARACTER)
= F040      EXT_CHAR_L   EQU      0F040H    ; LOW BOUND OF EXTERNAL CHARACTER
= F07E      EXT_CHAR_H   EQU      EXT_CHAR_L+S_RAM/32-2 ; HIGH BOUND OF EXTERNAL CHARACTER

= 0080      ZENBIT      EQU      80H      ; ZENKAKU BIT
= 0008      ZEN2BIT     EQU      08H      ; ZENKAKU 2ND BIT
= 0077      HAN_MASK    EQU      (NOT ZENBIT) AND (NOT ZEN2BIT) AND 0FFH ; HANKAKU MASK
= 0088      ZEN2_MASK   EQU      ZENBIT OR ZEN2BIT ; MASK OF 2ND BYTE OF ZENKAKU
= 0080      ZEROCOL     EQU      80H      ; ZERO COLUMN FLAG USED IN W_1ST_CHAR
; BIT 8 OF 1ST BYTE OF 2 BYTE CODE IS ALWAYS 1

= 0080      XOR_BIT     EQU      80H      ; X'OR WRITE IN GRAPHICS WRITE
= 0702      KKN_OFF     EQU      0702H    ; KANA-KAN DISABLE
= 0703      KKN1_OFF    EQU      0703H    ; KANA-KAN 1 INDICATOR DISABLE
= 0700      KKN1_ON     EQU      0700H    ; KANA-KAN 1 INDICATOR ENABLE
= 0701      INDICATOR_ON EQU      0701H    ; INDICATOR ENABLE
= 853F      KKN_TERM    EQU      853FH    ; KANA-KAN TERMINATE

= 0010      DISABLE_NMI EQU      10H      ; DISABLE NMI
= 0080      ENABLE_NMI  EQU      80H      ; ENABLE NMI

= 0008      VRAM2_PAGE  EQU      8        ; PAGE NUMBER OF V-RAM 2
= 07FF      PCB_MASK    EQU      07FFH    ; MASK FOR PESUDO CODE BUFFER POINTER

= 0009      ROW_KJ      EQU      11-1-1 ; ROW NUMBER OF KJ MODE
= 0018      ROW_ANK     EQU      25 - 1 ; ROW NUMBER OF AN MODE

= 0012      CBOX_ROW    EQU      18      ; ROW NUMBER OF KANJI CHARACTER BOX

= 0011      BLOCK_CURSOR EQU      CBOX_ROW-1 ; BLOCK CURSOR
= 1011      UNDER_CURSOR EQU      (CBOX_ROW-2)*256+(CBOX_ROW-1) ; UNDER CURSOR

= 0004      F_BASE      EQU      4        ; STACK FRAME BASE FOR SET RETURN PARAMETER
= 0004      F_DI        EQU      F_BASE + 0 ; FRAME OFFSET OF DI
= 0006      F_SI        EQU      F_BASE + 2 ; FRAME OFFSET OF SI
= 0008      F_BX        EQU      F_BASE + 4 ; FRAME OFFSET OF BX
= 000A      F_CX        EQU      F_BASE + 6 ; FRAME OFFSET OF CX
= 000C      F_DX        EQU      F_BASE + 8 ; FRAME OFFSET OF DX
= 000E      F_DS        EQU      F_BASE +10 ; FRAME OFFSET OF DS
= 0010      F_ES        EQU      F_BASE +12 ; FRAME OFFSET OF ES

= 000A      VIO_LOCAL   EQU      10      ; LOCAL AREA SIZE OF VIDEO IO
= 0000      VACT_PG     EQU      0        ; ACTIVE PAGE
= 0002      VCRS_POS    EQU      2        ; CURRENT CURSOR POSITION
= 0004      VKK_FLAG    EQU      4        ; KANA KANJI CONV. FLAG
= 0005      VKJ_STAT    EQU      5        ; KJ-ROM STATUS (0:OFF, 1:ON)
= 0006      VVG_STAT    EQU      6        ; VIDEO GENETATER STATUS(0:VG2, 1:VG1)
= 0007      VGC_ON      EQU      7        ; GRAPHICS CURSOR STATUS
= 0008      VVIO_ON     EQU      8        ; VIDEO I/O STATUS

```

Appendix A.

```

= 0006 SUP_LOCAL EQU 6 ; LOCAL AREA SIZE OF SCROLL UP
= 0000 SUC_MODE EQU 0 ; CRT MODE
= 0001 SU_ULR EQU 1 ; ROW OF UPPER LEFT
= 0002 SU_TSR EQU 2 ; TOP OF SOURCE ROW
= 0004 SU_ES1 EQU 4 ; SEGMENT ADDRESS OF VRAM-1

= 0006 SDN_LOCAL EQU 6 ; LOCAL AREA SIZE OF SCROLL DOWN
= 0000 SDC_MODE EQU 0 ; CRT MODE
= 0001 SD_LRR EQU 1 ; ROW OF LOWER RIGHT
= 0002 SD_BSR EQU 2 ; BOTTOM OF SOURCE ROW
= 0004 SD_ES1 EQU 4 ; SEGMENT ADDRESS OF VRAM-1

= 0010 RAC_LOCAL EQU 16 ; LOCAL AREA SIZE OF READ AC CURRENT

= 002E W_LOCAL EQU 46 ; LOCAL AREA SIZE OF WRITE AC/C CURRENT
= 0012 GW_LOCAL EQU 18 ; LOCAL AREA SIZE OF GRAPHICS WRITE
= 001C SAC_LOCAL EQU 28 ; LOCAL AREA SIZE OF SET ALT CURSOR
= 0000 WPOSN EQU 0 ; GRAPHICS WRITE POSITION
= 0002 WPOSN EQU 2 ; WRITE POSITION
= 0004 W2CODE EQU 4 ; 2ND BYTE CODE
= 0005 W2ATTR EQU 5 ; 2ND BYTE ATTRIBUTE
= 0006 WR_SEG EQU 6 ; REGEN SEGMENT
= 0008 WGMODE EQU 8 ; GRAPHICS MODE FLAG
= 0009 WC_MODE EQU 9 ; CRT MODE
= 000A WFONT EQU 10 ; FONT PATTERN

= 000F KJ_OFF EQU 00FH ; MASK FOR CRT MODE
= 0020 CURSOR_DISABLE EQU 20H ; CURSOR DISABLE BIT
= 3FFF G_CURSOR_MASK EQU 3FFFH ; MASK FOR GRAPHICS CURSOR

= 0001 VG1_ON EQU 1 ; VIDEO GENERATER 1 IS ON
= 0000 VG2_ON EQU 0 ; VIDEO GENERATER 2 IS ON
= 0002 VG12_ON EQU 2 ; VIDEO GENERATER 1 AND 2 ARE ON

= 0001 PS_PROCESS EQU 1 ; PRINT SCREEN IS PROCESSING

= 8000 REGEN_SIZE EQU 08000H ; SIZE OF REGEN

= 00A0 P_CODE_START EQU 000A0H ; SEGMENT ADDRESS OF PESUDO REGEN
= 0800 P_CODE_SIZE EQU 2048 ; SIZE OF PESUDO REGEN

```

```

----- VIDEO RAM -----
0000 8000 [ VIDEO_RAM SEGMENT AT REGEN_START
          DB REGEN_SIZE DUP(?)
          ?? ]

```

```

8000 VIDEO_RAM ENDS
;----- PESUDO CODE BUFFER -----

```

```

0000 0800 [ P_CODE_BUFFER SEGMENT AT P_CODE_START
          DB P_CODE_SIZE DUP(?)
          ?? ]

```

```

0800 P_CODE_BUFFER ENDS
;----- INDETERMINATE DS, ES -----

```

```

0000 INDETERMINATE SEGMENT
0000 INDETERMINATE ENDS

```

ASSUME CS:CODE, DS:INDETERMINATE, ES:INDETERMINATE

```

0000 VF_TABLE LABEL WORD ; TABLE OF ROUTINES WITHIN VIDEO I/O
0000 02D1 R DW OFFSET SET_MODE ; AH = 0
0002 05CC R DW OFFSET SET_CTYPE ; AH = 1
0004 0689 R DW OFFSET SET_CPOS ; AH = 2
0006 06E6 R DW OFFSET READ_CURSOR ; AH = 3
0008 0716 R DW OFFSET READ_LPEN ; AH = 4
000A 07FF R DW OFFSET ACT_DISP_PAGE ; AH = 5
000C 0978 R DW OFFSET SCROLL_UP ; AH = 6
000E 0870 R DW OFFSET SCROLL_DOWN ; AH = 7
0010 0D8D R DW OFFSET READ_AC_CURRENT ; AH = 8
0012 0F32 R DW OFFSET WRITE_AC_CURRENT ; AH = 9
0014 0F56 R DW OFFSET WRITE_C_CURRENT ; AH = A
0016 16C1 R DW OFFSET SET_COLOR ; AH = B
0018 174D R DW OFFSET WRITE_DOT ; AH = C
001A 1858 R DW OFFSET READ_DOT ; AH = D
001C 1880 R DW OFFSET WRITE_TTY ; AH = E
001E 19CA R DW OFFSET VIDEO_STATE ; AH = F
0020 19DD R DW OFFSET SET_PALETTE ; AH = 10
0022 1A52 R DW OFFSET NO_OPERATION ; AH = 11
0024 1A52 R DW OFFSET NO_OPERATION ; AH = 12
0026 1A53 R DW OFFSET NO_OPERATION ; AH = 13
0028 1C00 R DW OFFSET FONT_PATTERN ; AH = 14

002A 1E2A R DW OFFSET SET_ALT_CTYPE ; AH = 81
002C 1E62 R DW OFFSET SET_ALT_CPOS ; AH = 82
002E 1ED8 R DW OFFSET READ_ALT_CURSOR ; AH = 83
0030 1A52 R DW OFFSET NO_OPERATION ; AH = 84
0032 1A52 R DW OFFSET NO_OPERATION ; AH = 85
0034 1A52 R DW OFFSET NO_OPERATION ; AH = 86
0036 1A52 R DW OFFSET NO_OPERATION ; AH = 87
0038 0D8D R DW OFFSET NO_OPERATION ; AH = 88
003A 1EEB R DW OFFSET READ_AC_CURRENT ; AH = 89
003C 1EFD R DW OFFSET K_WRITE_AC_CURRENT ; AH = 8A
003E 1A52 R DW OFFSET K_WRITE_C_CURRENT ; AH = 8B
0040 1A52 R DW OFFSET NO_OPERATION ; AH = 8C
0042 1A52 R DW OFFSET NO_OPERATION ; AH = 8D
0044 1F0F R DW OFFSET NO_OPERATION ; AH = 8E
          DW OFFSET K_WRITE_TTY ; AH = 8E

```

```

= 0046          VFT_END EQU      0-VF_TABLE
0046          VIDEO_IO          PROC   NEAR
0046 FB          STI              ; INTERRUPTS BACK ON
0047 FC          CLD              ; SET DIRECTION FORWARD
0048 55          PUSH            BP   ; SAVE BP
0049 1E          PUSH            DS   ; SAVE DS
004A E8 0000 E   CALL            DDS   ; POINT BIOS DATA AREA
                                ASSUME DS:DATA
004D E8 1BE4 R   CALL            DISABLE_INT ; DISABLE INTERRUPT
0050 F6 06 0357 R FF TEST           VSTACKL,TRUE ; VSTACK IS ALREADY USED ?
0055 75 12          JNZ            VIO0 ; YES
0057 8C D5          MOV            BP,SS ; SAVE STACK SEGMENT
0059 8C 16 0358 R  MOV            SS,SS ;
005D 89 26 035A R  MOV            SP,SP ; SAVE STACK POINTER
0061 8D ---- R    MOV            BP,VSTACK ; SETUP NEW STACK SEGMENT
0064 8E D5          MOV            SS,SS ;
0066 8C 0600 R    MOV            SP,OFFSET VSTACK_TOP ; SETUP NEW STACK POINTER
0069          VIO0:
0069 FE 06 0357 R  INC            BYTE PTR VSTACKL ; INCREMENT VIDED STACK LEVEL
006D E8 1BEC R    CALL            ENABLE_INT ; ENABLE INTERRUPT
0070 83 EC 0A      SUB            SP,VIO_LOCAL ; ALLOCATE LOCAL WORK AREA
0073 8B EC          MOV            BP,SP ; ASSIGN BP AS FRAME POINTER
0075 06          PUSH            ES ;
0076 1E          PUSH            DS ; SAVE SEGMENT REGISTERS
0077 52          PUSH            DX ;
0078 51          PUSH            CX ;
0079 53          PUSH            BX ;
007A 56          PUSH            SI ;
007B 57          PUSH            DI ;
007C 50          PUSH            AX ; SAVE AX VALUE
007D 50          PUSH            AX ;
007E C6 46 04 00  MOV            BYTE PTR [BP+VKK_FLAG],FALSE ; CLEAR KANA KANJI FUNCTION FLAG
0082 A0 0353 R    MOV            AL,KJROM_STAT ;
0085 88 46 05      MOV            [BP+VKJ_STAT],AL ; SET CURRENT KJROM STATUS
0088 A0 0354 R    MOV            AL,VG_STAT ;
008B 88 46 06      MOV            [BP+VVG_STAT],AL ; SET CURRENT VG STATUS
008E A0 03FB R    MOV            AL,VIO_PROCESS ;
0091 88 46 08      MOV            [BP+VVIO_ON],AL ; SET CURRENT VIDEO I/O STATUS
0094 80 0E 03FB R FF OR             VIO_PROCESS,TRUE ; SET VIDEO I/O PROCESSING FLAG
0099 58          POP             AX
009A 80 FC 81      CMP            AH,81H ; FUNCTION FOR KANA-KANJI CONVERSION ?
009D 72 3F          JB             V12 ; NO
009F 80 EC 6C      SUB            AH,81H-14H-1 ; --- SET DATA FOR KANA-KANJI CONVERSION
                                ; ADJUST FOR JUMP TABLE
00A2 80 FC 18      CMP            AH,84H - (81H-14H-1) ; ALTERNATE CURSOR FUNCTION ?
00A5 72 33          JB             V11 ; YES, SKIP WORK SWAP
00A7 80 FC 22      CMP            AH,8EH - (81H-14H-1) ; WRITE TTY FUNCTION ?
00AA 74 2E          JE             V11 ; YES, SKIP WORK SWAP
00AC 50          PUSH            AX ; VIDEO FUNCTION IS WRITE AC/C
00AD 53          PUSH            BX ; SAVE AX
                                ; SAVE BX
00AE 32 FF          XOR            BH,BH ;
00B0 8A 1E 0062 R  MOV            BL,ACTIVE_PAGE ; SET CURRENT ACTIVE PAGE TO BX
00B4 D1 E3          SAL            BX,1 ; TIMES 2 FOR WORD OFFSET
00B6 89 5E 00      MOV            [BP+VACT_PG],BX ; SAVE ACTIVE PAGE * 2
00B9 A1 034A R    MOV            AX,ALT_CURSOR_POSN ; SET CURSOR POSITION TO AX
00BC 87 87 035C R  XCHG          AX,[BX+OFFSET CURSOR_POSN] ; SWAP CURSOR POSITION
00C0 89 46 02      MOV            [BP+VCRS_POS],AX ; SAVE IT
00C3 A1 0340 R    MOV            AX,W_1ST_CHAR ; GET 1ST BYTE CHARACTER AT WRITE A/C
00C4 87 06 033E R  XCHG          AX,K_1ST_CHAR ; SWAP 1ST BYTE CHAR FOR KANA-KAN
00CA A3 0340 R    MOV            W_1ST_CHAR,AX ;
00CD 8A 26 0349 R  MOV            AH,GC_PRESENT ; SET GRAPHICS CURSOR FLAG
00D1 88 66 07      MOV            [BP+VGC_ON],AH ;
00D4 C6 46 04 FF    MOV            BYTE PTR [BP+VKK_FLAG],TRUE ; SET KANA-KANJI FUNCTION FLAG
00D8 5B          POP             BX ; RESTORE BX
00D9 58          POP             AX ; RESTORE AX
00DA 8A 3E 0062 R  MOV            BH,ACTIVE_PAGE ; SET ACTIVE PAGE TO BH
00DE 8A C4          MOV            AL,AH ; GET INTO LOW BYTE
00E0 32 E4          XOR            AH,AH ; ZERO TO HIGH BYTE
00E2 D1 E0          SAL            AX,1 ; *2 FOR TABLE LOOKUP
00E4 8B F0          MOV            SI,AX ; PUT INTO SI FOR BRANCH
00E6 3D 0046        CMP            AX,VFT_END ; TEST FOR WITHIN RANGE
00E9 72 03          JB             V13 ; BRANCH AROUND BRANCH

```

Appendix A.

```

00EB 58          POP      AX          ; THROW AWAY THE PARAMETER
00EC EB 44      JMP      SHORT VIDEO_RETURN ; DO NOTHING IF NOT IN RANGE

00EE          ;--- DETERMINE SEGMENT ADDRESS OF REGEN
00EE 8A 26 0049 R  MOV     AH,CRT_MODE      ; GET CURRENT CRT MODE
00F2 80 E4 0F   AND     AH,KJ_OFF        ; MASK KJ-BIT OFF
00F5 80 FC 09   CMP     AH,9             ; IN MODE USING 32K REGEN ?
00F8 88 B800   MOV     AX,REGEN_START   ; SEGMENT FOR COLOR CARD
00FB 72 0C     JB     V131             ; NO,JUMP

00FD 80 3E 034C R 08  CMP     CPU_PAGE,VRAM2_PAGE ; IN MODE USING V-RAM1 ?
0102 73 18     JAE    V14              ; NO

;---- SETUP SEGMENT VALUE OF 32K REGEN ACCORDING TO MEMORY SIZE
; ASSUME AL = 0
; GET DIRECT SEGMENT OF MAIN RAM
0104 EB 1D95 R   CALL    GET_DIRSEG
0107 EB 16     JMP     SHORT V14

0109          ; GRAPHICS MODE ?
0109 80 FC 04   CMP     AH,GRAPHICS     ; GRAPHICS MODE ?
010C 72 11     JB     V14              ; NO
010E 80 3E 034C R 08  CMP     CPU_PAGE,VRAM2_PAGE ; IN MODE USING V-RAM2 ?
0113 72 0A     JB     V14              ; NO

0115 F6 06 034C R 01  TEST    CPU_PAGE,1      ; PAGE 9 OR B ?
011A 74 03     JZ     V14              ; NO

011C 05 0400   ADD     AX,400H         ; SET BIAS FOR ACCESS HIGH 16K OF REGEN

011F          ; SET UP TO POINT AT VIDEO RAM AREA
011F 8E C0     MOV     ES,AX           ASSUME ES: VIDEO_RAM

0121 58          POP      AX          ; RECOVER VALUE
0122 8A 26 0049 R  MOV     AH,CRT_MODE      ; GET CURRENT MODE INTO AH
0126 80 E4 0F   AND     AH,KJ_OFF        ; MASK KANJI MODE FLAG OFF

0129 BF 0132 R   MOV     DI,OFFSET VIDEO_RETURN ; GET RETURN ADDRESS
012C 57          DI          ; STACK IT
012D 2E: FF A4 0000 R  JMP     WORD PTR CS:[SI+OFFSET VF_TABLE]; JUMP TO VIDEO ROUTINE

; ASSUME DS:INDETERMINATE, ES:INDETERMINATE
0132          VIDEO_RETURN:
0132 F6 46 05 FF   TEST    BYTE PTR [BP+VKJ_STAT],TRUE; KJ-ROM ENABLED AT ENTERANCE ?
0136 74 03     JZ     V15              ; NO

0138 E8 1B88 R   CALL    ENABLE_KJROM    ; ENABLE KJ-ROM

0138          ; VG-1 ENABLED AT ENTERANCE ?
0138 F6 46 06 FF   TEST    BYTE PTR [BP+VVG_STAT],TRUE; VG-1 ENABLED AT ENTERANCE ?
013F 74 0C     JZ     V16              ; NO

0141 E8 1DB0 R   CALL    ENABLE_VG1      ; ENABLE VIDEO GENERATER 1
0144 80 7E 06 02  CMP     BYTE PTR [BP+VVG_STAT],VG12_ON ; VG-1&2 ENABLED ?
0148 75 03     JNE    V16              ; NO

014A E8 1DFC R   CALL    ENABLE_VG12     ; ENABLE VIDEO GENERATER 1&2
014D          ; POINT BIOS DATA SEGMENT
014D E8 0000 E   CALL    DDS             ASSUME DS:DATA

0150 F6 46 04 FF   TEST    BYTE PTR [BP+VKK_FLAG],TRUE; FUNCTION OF KANA-KANJI CONVERSION?
0154 74 1D     JZ     V17              ; NO

0156 8B 4E 02   MOV     CX,[BP+VCRS_POS] ; --- RESTORE THE SAVED PARAMETER
0159 8B 5E 00   MOV     BX,[BP+VACT_PG]  ; GET SAVED CURSOR POSITION
015C 89 8F 035C R  MOV     [BX+OFFSET CURSOR_POSN],CX ; GET ACTIVE PAGE
; RESTORE IT

0160 8B 0E 0340 R  MOV     CX,W_1ST_CHAR    ; GET 1ST BYTE CHAR IN KANA-KAN
0164 87 0E 033E R  XCHG   CX,K_1ST_CHAR     ; SWAP CX AND K_1ST_CHAR
0168 89 0E 0340 R  MOV     W_1ST_CHAR,CX    ; SET W_1ST_CHAR

016C 8A 7E 07   MOV     BH,[BP+VGC_ON]   ;
016F 88 3E 0349 R  CMP     GC_PRESENT,BH    ; RESTORE GRAPHICS CURSOR FLAG
0173          ; RESTORE VIDEO I/O PROCESSING FLAG
0173 8A 5E 08   MOV     BL,[BP+VVIO_ON] ; RESTORE VIDEO I/O PROCESSING FLAG
0176 8E 1E 03FB R  MOV     VIO_PROCESS,BL  ;

017A 5F          POP      DI          ; RECOVER SEGMENTS
017B 5E          POP      SI
017C 5B          POP      BX
017D 59          POP      CX
017E 5A          POP      DX
017F 1F          POP      DS
0180 07          POP      ES

0181 83 C4 0A   ADD     SP,VIO_LOCAL     ; DEALLOCATE LOCAL WORK AREA

0184 E8 1BE4 R   CALL    DISABLE_INT     ; DISABLE INTERRUPT

0187 FE 0E 0357 R  DEC     BYTE PTR VSTACKL ; DECREMENT STACK LEVEL = 0 ?
0188 75 0A     JNZ    V101            ; NO

018D 8B 2E 0358 R  MOV     BP,SS_SAVE       ; RESTORE OLD STACK SEGMENT
0191 8E D5     MOV     SS,BP           ;
0193 8B 26 035A R  MOV     SP,SP_SAVE       ; RESTORE OLD STACK POINTER
0197          ; ENABLE INTERRUPT
0197 E8 1BEC R   CALL    ENABLE_INT

019A 1F          POP      DS          ; RESTORE DS
019B 5D          POP      BP          ; RESTORE BP

019C CF          IRET             ; ALL DONE

```

019D

```

VIDEO_IO      ENDP
;-----
;
;   SET_MODE          INT 10H, AH = 0
;-----
;   THIS ROUTINE INITIALIZES THE ATTACHMENT TO
;   THE SELECTED MODE.  THE SCREEN IS BLANKED.
;
;   INPUT  AL = MODE SELECTED
;
;   OUTPUT NONE
;
;   VOLATILE          AX,BX,CX,DX,SI,DI,ES
;-----
;   ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM
;-----

```

TABLES FOR USE IN SETTING OF CRT MODE

019D

```

VIDEO_PARMS LABEL BYTE ;--- INIT_TABLE FOR 6845
;---0--1--2--3--4--5--6--7--8--9--10--11--12..15

```

```

019D 38 28 2F 06 1F 06
      19 1C 02 07 06 07
      00 00 00 00
= 0010

```

```

DB 38H,28H,2FH,06H,1FH, 6H,19H,1CH, 2H, 7H, 6H, 7H,0,0,0,0 ; SETUP FOR 40X25

```

```

01AD 71 50 5C 0C 1F 06
      19 1C 02 07 06 07
      00 00 00 00

```

```

VPARML EQU $-VIDEO_PARMS

```

```

DB 71H,50H,5CH,0CH,1FH, 6H,19H,1CH, 2H, 7H, 6H, 7H,0,0,0,0 ; 80X25

```

```

01BD 38 28 2E 06 7F 06
      64 70 02 01 26 07
      00 00 00 00

```

```

DB 38H,28H,2EH,06H,7FH, 6H,64H,70H, 2H, 1H,26H, 7H,0,0,0,0 ; GRAPHICS

```

```

01CD 71 50 5B 0C 3F 06
      32 38 02 03 26 07
      00 00 00 00

```

```

DB 71H,50H,5BH,0CH,3FH, 6H,32H,38H, 2H, 3H,26H, 7H,0,0,0,0 ; GRAPHICS 32K REGEN

```

```

01DD 38 28 2F 06 0D 0A
      0B 0C 02 11 10 11
      00 00 00 00

```

```

VIDEO_PARMS_RAS LABEL BYTE
DB 38H,28H,2FH,06H,0DH,0AH,0BH,0CH, 2H,11H,10H,11H,0,0,0,0 ; 40x11

```

```

01ED 71 50 5C 0C 0D 0A
      0B 0C 02 11 10 11
      00 00 00 00

```

```

DB 71H,50H,5CH,0CH,0DH,0AH,0BH,0CH, 2H,11H,10H,11H,0,0,0,0 ; 80x11

```

```

01FD 0800
01FF 0800
0201 1000
0203 1000
0205 4000
0207 4000
0209 4000
020B 0000
020D 4000
020F 8000
0211 8000
0213 0000
0215 0000
0217 0000
0219 0000
021B 0000
021D 0400
021F 0400
0221 0800
0223 0800
0225 4000
0227 4000
0229 4000
022B 0000
022D 4000
022F 8000
0231 8000
0233 8000

```

```

REGEN_L LABEL WORD ;--- TABLE OF REGEN LENGTHS
DW 2048 ; MODE 0 40X25 (BW)
DW 2048 ; MODE 1 40X25 COLOR
DW 4096 ; MODE 2 80X25 (BW)
DW 4096 ; MODE 3 80X25 COLOR
DW 16384 ; MODE 4 320X200 4 COLOR
DW 16384 ; MODE 5 320X200 4 (SHADE)
DW 16384 ; MODE 6 640X200 2 (SHADE)
DW 0 ; MODE 7 INVALID
DW 16384 ; MODE 8 160X200 16 COLOR
DW 32768 ; MODE 9 320X200 16 COLOR
DW 32768 ; MODE A 640X200 4 COLOR
DW 0 ; MODE B INVALID
DW 0 ; MODE C INVALID
DW 0 ; MODE D INVALID
DW 0 ; MODE E INVALID
DW 0 ; MODE F INVALID
DW 1024 ; MODE 10 40X11 (BW)
DW 1024 ; MODE 11 40X11 COLOR
DW 2048 ; MODE 12 80X11 (BW)
DW 2048 ; MODE 13 80X11 COLOR
DW 16384 ; MODE 14 320X200 4 COLOR
DW 16384 ; MODE 15 320X200 4 (SHADE)
DW 16384 ; MODE 16 640X200 2 (SHADE)
DW 0 ; MODE 17 INVALID
DW 16384 ; MODE 18 160X200 16 COLOR
DW 32768 ; MODE 19 320X200 16 COLOR
DW 32768 ; MODE 1A 640X200 4 COLOR
DW 32768 ; MODE 1B 640X200 16 COLOR

```

```

0235 28 28 50 50 28 28
      50 00 14 28 50 50

```

```

COL_L LABEL BYTE ;--- COLUMNS
; MODE--0--1--2--3--4--5--6--7--8--9--A--B--C--D--E--F
DB 40,40,80,80,40,40,80, 0,20,40,80,80; 0, 0, 0, 0

```

```

0241 0C 2F 00 02
= 0004
0245 08 2F 00 02
0249 0D 2F 00 02
024D 09 2F 00 02
0251 0A 23 00 00
0255 0E 23 00 00
0259 0E 21 00 08
325D 00 00 00 00
0261 1A 2F 00 00
0265 1B 2F 00 00
0269 0B 23 00 00
026D 00 00 00 00
0271 00 00 00 00
0275 00 00 00 00
0279 00 00 00 00
027D 00 00 00 00

```

```

GAPARM LABEL BYTE ;--- TABLE OF GATE ARRAY PARAMETERS FOR MODE SETTING
GAPARML DB 0CH,2FH,0,2 ;----- SET UP FOR 40X25 (BW) MODE 0
;----- $-GAPARM EQU
DB 08H,2FH,0,2 ;----- SET UP FOR 40X25 COLOR MODE 1
DB 0DH,2FH,0,2 ;----- SET UP FOR 80X25 (BW) MODE 2
DB 09H,2FH,0,2 ;----- SET UP FOR 80X25 COLOR MODE 3
DB 0AH,23H,0,0 ;----- SET UP FOR 320X200 4 COLOR MODE 4
DB 0EH,23H,0,0 ;----- SET UP FOR 320X200 4 (SHADE) MODE 5
DB 0EH,21H,0,8 ;----- SET UP FOR 640X200 2 (SHADE) MODE 6
DB 00H,00H,0,0 ;----- INVALID MODE 7
DB 1AH,2FH,0,0 ;----- SET UP FOR 160X200 16 COLOR MODE 8
DB 1BH,2FH,0,0 ;----- SET UP FOR 320X200 16 COLOR MODE 9
DB 08H,23H,0,0 ;----- SET UP FOR 640X200 4 COLOR MODE A
DB 00H,00H,0,0 ;----- INVALID MODE B
DB 00H,00H,0,0 ;----- INVALID MODE C
DB 00H,00H,0,0 ;----- INVALID MODE D
DB 00H,00H,0,0 ;----- INVALID MODE E
DB 00H,00H,0,0 ;----- INVALID MODE F

```

Appendix A.

```

0281 4C 27 00 00      DB      4CH,27H,0,0      ;----- SET UP FOR 40X11 (BW)   MODE 10
0285 48 27 00 00      DB      48H,27H,0,0      ;----- SET UP FOR 40X11   COLOR   MODE 11
0289 4D 27 00 00      DB      4DH,27H,0,0      ;----- SET UP FOR 80X11 (BW)   MODE 12
028D 49 27 00 00      DB      49H,27H,0,0      ;----- SET UP FOR 80X11   COLOR   MODE 13
0291 0A 23 00 00      DB      0AH,23H,0,0      ;----- SET UP FOR 320X200 4 COLOR  MODE 14
0295 0E 23 00 00      DB      0EH,23H,0,0      ;----- SET UP FOR 320X200 4 (SHADE) MODE 15
0299 0E 21 00 08      DB      0EH,21H,0,8      ;----- SET UP FOR 640X200 2 (SHADE) MODE 16
029D 00 00 00 00      DB      00H,00H,0,0      ;----- INVALID                                     MODE 17
02A1 1A 2F 00 00      DB      1AH,2FH,0,0      ;----- SET UP FOR 160X200 16 COLOR  MODE 18
02A5 1B 2F 00 00      DB      1BH,2FH,0,0      ;----- SET UP FOR 320X200 16 COLOR  MODE 19
02A9 0B 23 00 00      DB      0BH,23H,0,0      ;----- SET UP FOR 640X200 4 COLOR   MODE 1A
02AD 8B 23 00 00      DB      8BH,23H,0,0      ;----- SET UP FOR 640X200 16 COLOR  MODE 1B

```

----- TABLES OF PALETTE COLORS FOR 2 AND 4 COLOR MODES

```

02B1          PLTC20          LABEL  BYTE
02B1 00 0F 00 00      DB      0,0FH,0,0      ;----- 2 COLOR, SET 0
= 0004          EQU          6-PLTC20      ; ENTRY LENGTH
02B5 0F 00 00 00      DB      0FH,0,0,0      ;----- 2 COLOR, SET 1
02B9          PLTC40          LABEL  BYTE
02B9 00 02 04 06      DB      0,2,4,6        ;----- 4 COLOR, SET 0
02BD          PLTC41          LABEL  BYTE
02BD 00 03 05 0F      DB      0,3,5,0FH      ;----- 4 COLOR, SET 1

02C1          PLTCS16 DB      10,11,8,9,14,15,12,13,2,3,0,1,6,7,4,5 ; SUPER 16 COLOR STD PLT
02C1 0A 0B 08 09 0E 0F
02C1 0C 0D 02 03 00 01
02C1 06 07 04 05

```

```

02D1          SET_MODE      PROC   NEAR
02D1 FF 36 0355 R      PUSH   WORD PTR IEP_CTRL      ; SAVE INTERRUPT ENABLE PROHIBIT FLAG
02D5 80 0E 0355 R FF  OR      BYTE PTR IEP_CTRL,TRUE ; PROHIBIT INTERRUPT ENABLE
02DA E8 1BE4 R          CALL   DISABLE_INT          ; DISABLE HARDWARE INTERRUPT

02DD 80 3E 0049 R 10  CMP     CRT_MODE,KJ_MODE     ; KANJI MODE ?
02E2 72 11          JB     SETM0                 ; NO

02E4 50          PUSH   AX                    ;
02E5 B8 0702      MOV     AX,KKN OFF          ;
02E8 CD 16          INT     KEYBOARD            ;
02EA B8 853F      MOV     AX,KKN TERM         ; TERMINATE KANA-KAN
02ED CD 16          INT     KEYBOARD            ;
02EF B8 0700      MOV     AX,KKNI_ON          ;
02F2 CD 16          INT     KEYBOARD            ;
02F4 58          PDP     AX                   ;
02F5          SETM0:
02F5 50          PUSH   AX                    ; SAVE INPUT MODE ON STACK
02F6 24 7F      AND     AL,7FH              ; REMOVE CLEAR REGEN SWITCH

02F8 3C 07          CMP     AL,7                ; CHECK FOR VALID MODES
02FA 74 10          JE     SETM1                 ; MODE 7 IS INVALID
02FC 3C 17          CMP     AL,17H              ; CHECK FOR VALID MODES
02FE 74 0C          JE     SETM1                 ; MODE 17H IS INVALID

0300 3C 0A          CMP     AL,0AH              ; UNDER 0AH ?
0302 76 0A          JBE    SETM2                 ; YES, OK
0304 3C 0F          CMP     AL,0FH              ; UNDER 0FH ?
0306 76 04          JBE    SETM1                 ; YES, INVALID

0308 3C 1B          CMP     AL,1BH              ;
030A 76 02          JBE    SETM2                 ; GREATER THAN 1BH IS INVALID

030C          SETM1:
030C B0 10          MOV     AL,DEFAULT_MODE     ; DEFAULT TO DEFAULT_MODE
030E          SETM2:
030E E8 0940 R      CALL   ERASE_SCURSOR        ; ERASE CURSOR

0311 8A 03D4      MOV     DX,A6845            ; ADDRESS OF COLOR CARD
0314 8A E0          MOV     AH,AL                ; SAVE MODE IN AH
0316 A2 0049 R      MOV     CRT_MODE,AL         ; SAVE IN GLOBAL VARIABLE
0319 A2 033B R      MOV     CRT_MODE2,AL        ; SAVE MODE OF VIDEO PROCESSOR 2
031C 89 16 D063 R  MOV     ADDR_6845,DX         ; SAVE ADDRESS OF BASE
0320 24 0F          AND     AL,KJ_OFF           ; MASK KANJI MODE FLAG OFF
0322 8B F8          MOV     DI,AX                ; SAVE MODE IN DI

0324 E8 1DFC R      CALL   ENABLE_VG12          ; ENABLE BOTH VG1 AND VG2
0327 E8 1A44 R      CALL   WAIT_VERTRET        ; WAIT UNTIL VERTICAL RETRACE

032A BA 03DA      MOV     DX,VGA_CTL          ; POINT TO CONTROL REGISTER
032D EC          IN      AL,DX                ; SYNC CONTROL REG TO ADDRESS
032E 32 C0          XOR     AL,AL                ; SET VGA REG 0
0330 EE          OUT     DX,AL                ; SELECT IT

0331 A0 0065 R      MOV     AL,CRT_MODE_SET     ; GET LAST MODE SET
0334 24 F7          AND     AL,NOT_VIDE0EMB     ; TURN OFF VIDEO
0336 EE          OUT     DX,AL                ; SET IN GATE ARRAY

0337 F6 06 03FC R FF TEST   SUPPRESS_PAL,TRUE    ; SET PALETTE IS SUPPRESSED ?
033C 75 37          JNZ    SETM5                 ; YES, SKIP SET PALETTE

033E B0 06          MOV     AL,PCSUPER          ;
0340 EE          OUT     DX,AL                ; CLEAR SUPERIMPOSE CONTROL REGISTER
0341 32 C0          XOR     AL,AL                ; FOR SET PALETTE
0343 EE          OUT     DX,AL                ;

0344 8B C7          MOV     AX,DI                ; GET MODE
0346 B4 10          MOV     AH,IXPALET          ; SET PALETTE REG 0

0348 B8 02B1 R      MOV     BX,OFFSET PLTC20    ; POINT TO TABLE ENTRY
034B 3C 06          CMP     AL,6                 ; 2 COLOR MODE?
034D 74 0F          JE     SETM3                 ; YES, JUMP

034F B8 02BD R      MOV     BX,OFFSET PLTC41    ; POINT TO TABLE ENTRY
0352 3C 05          CMP     AL,5                 ; CHECK FOR 4 COLOR MODE

```

```

0354 74 08      JE      SETM3      ; YES, JUMP
0356 3C 04      CMP      AL,4      ; CHECK FOR 4 COLOR MODE
0358 74 04      JE      SETM3      ; YES JUMP
035A 3C 0A      CMP      AL,0AH    ; CHECK FOR 4 COLOR MODE
035C 75 05      JNE     SETM4      ; NO, JUMP
035E
035E E8 0535 R   SETM3:   CALL     SET_PALETTE4 ; SET PALETTES FOR DEFAULT 4 COLOR
0361 EB 12      JMP     SHORT SETM5

0363
0363 3C 0B      SETM4:   CMP      AL,0BH    ; SUPER 16 COLOR ?
0365 74 05      JE      SETM41      ; YES

0367 E8 0545 R   CALL     SET_PALETTE16 ; SET PALETTES FOR DEFAULT 16 COLOR
036A EB 09      JMP     SHORT SETM5

036C
036C BB 02C1 R   SETM41:  MOV      BX,OFFSET PLTCS16;GET ADDRESS OF SUPER 16 COLOR PALETTE TABLE
036F B9 0010      MOV      CX,16      ; SET NUMBER OF PALETTE
0372 E8 0538 R   CALL     SPAL41      ; SET PALETTE

0375
0375 8B C7      SETM5:   MOV      AX,DI      ;----- SET UP M0 & M1 IN PAGREG for V-RAM1
0377 32 DB      XOR     BL,BL      ; GET CURREHT MODE
; SET UP FOR ALPHA MODE

0379 3C 04      CMP     AL,GRAPHICS ; IN ALPHA MODE
037B 72 08      JC      SETM6      ; YES, JUMP

037D B3 40      MOV     BL,40H     ; SET UP FOR 16K REGEN
037F 3C 09      CMP     AL,09H    ; MODE USE 16K
0381 72 02      JC      SETM6      ; YES, JUMP

0383 B3 C0      MOV     BL,0C0H   ; SET UP FOR 32K REGEN
0385
0385 BA 03DF     SETM6:   MOV     DX,PAGREG ; SET PORT ADDRESS OF PAGREG
0388 A0 008A R   MOV     AL,PAGDAT ; GET LAST DATA OUTPUT
038B 24 3F      AND    AL,3FH     ; CLEAR M0 & M1 BITS
038D 0A C3      OR     AL,BL      ; SET NEW BITS
038F EE        OUT    DX,AL      ; STUFF BACK IN PORT
0390 A2 008A R   MOV     PAGDAT,AL ; SAVE COPY IN RAM

;--- ENABLE VIDEO AND CORRECT PORT SETTING
; RESET VIDED GATE ARRAY

0393 E8 0551 R   CALL     VGA RESET
0396 EB 07      JMP     SHORT SETM8

0398
0398 8A C4      SETM7:   MOV     AL,AH     ; GET VGA REG NUMBER
039A EE        OUT    DX,AL      ; SELECT REG
039B 2E: 8A 07  MOV     AL,CS:[BX] ; GET TABLE VALUE
039E EE        OUT    DX,AL      ; PUT IN VGA REG
039F
039F 43      SETM8:   INC    BX         ; NEXT IN TABLE
03A0 FE C4      INC    AH         ; NEXT REG
03A2 E2 F4      LOOP   SETM7      ; DO ENTIRE ENTRY

03A4 2E: 8A 47 FD  MOV     AL,CS:[BX-(GAPARML-1)] ; GET VALUE OF PALETTE MASK REG.
03A8 A2 0352 R   MOV     PALETTE_MASK,AL ; SAVE IT FOR SUPERIMPOSE

03AB B0 05      MOV     AL,PCTRPALT ; POINT SUPERIMPOSE TRANSPARENT REGISTER
03AD EE        OUT    DX,AL      ;
03AE B0 00      MOV     AL,0       ; SET TRANSPARENT PALETTER NUMBER AS 0
03B0 EE        OUT    DX,AL      ;

03B1 B0 06      MOV     AL,PCSUPER ; POINT SUPERIMPOSE CONTROL REGISTER
03B3 EE        OUT    DX,AL      ;
03B4 B0 01      MOV     AL,FOREVRAM ; MAKE VRAM 2 DISPLAYD IN FOREGROUND
03B6 EE        OUT    DX,AL      ;
03B7 A2 0347 R   MOV     SUPIPCR,AL ; SAVE IT TO RAM

;---- SET UP CRT AND CPU PAGE REGS ACCORDING TO MODE & MEMORY SIZE
;
03BA BA 03DF     MOV     DX,PAGREG ; SET IO ADDRESS OF PAGREG
03BD A0 008A R   MOV     AL,PAGDAT ; GET LAST DATA OUTPUT
03C0 24 C0      AND    AL,0C0H   ; CLEAR REG BITS
03C2 B3 36      MOV     BL,36H   ; SET UP FOR GRAPHICS MODE WITH 32K REGEN
03C4 A8 80      TEST   AL,80H   ; IN THIS MODE?
03C6 75 0C      JNZ    SETM9     ; YES, JUMP

03C8 B3 3F      MOV     BL,3FH   ; SET UP FOR 16K REGEN AND 128K MEMORY
03CA 50        PUSH  AX         ; SAVE AX
03CB E4 62      IN     AL,PORT_C ; READ 8255 PORT C
03CD A8 08      TEST   AL,EXP64K ; 64K CARD INSTALLED ?
03CF 58        POP    AX         ; RESTORE AX
03D0 74 02      JZ     SETM9     ; YES

03D2 B3 1B      MOV     BL,1BH   ; SET UP FOR 16K REGEN AND 64K MEMORY
03D4
03D4 0A C3     SETM9:  OR     AL,BL     ; COMBINE MODE BITS AND REG VALUES
03D6 EE        OUT    DX,AL      ; SET PORT

03D7 A2 008A R   MOV     PAGDAT,AL ; SAVE COPY IN RAM

;---- SET UP CRT AND CPU PAGE REGS ACCORDING TO MODE & MEMORY SIZE
;
03DA BA 03D9     MOV     DX,PAGREG2 ; SET IO ADDRESS OF PAGREG
03DD 32 C0      XOR    AL,AL     ; SET CPU/CRT PAGE 0 IN V-RAM2
03DF EE        OUT    DX,AL      ; SET PORT

03E0 A2 033C R   MOV     PAGDAT2,AL ; SAVE COPY IN RAM
03E3 8B C6      MOV     AX,SI    ; PUT MODE SET & PALETTE IN RAM
03E5 88 26 0065 R MOV     CRT_MODE_SET,AH ; SAVE MODE CONTROL REG VALUE
03E9 88 26 033D R MOV     CRT_MODE_SET2,AH ; SAVE MODE CONTROL REG VALUE FOR SUPERIMPOSE
03ED A2 0066 R   MOV     CRT_PALETTE,AL ; SAVE BORDER COLOR

```

Appendix A.

```

03F0 1E                               ;---- SET UP 6845
03F1 8B C7                             ; SAVE DATA SEGMENT VALUE
03F3                                     ; GET CURRENT MODE IN AX
03F3 33 DB                               SETM10:
03F5 8E DB                               XOR    BX,BX
                                           ; SET UP FOR ABSO SEGMENT
                                           ; ESTABLISH VECTOR TABLE ADDRESSING
                                           ASSUME DS: ABSO

03F7 C5 1E 0074 R                       LDS    BX,PARAM_PTR
                                           ; GET POINTER TO VIDEO PARMS
                                           ASSUME DS: CODE

03FB 89 0010                             MOV    CX,VPARML
                                           ; LENGTH OF EACH ROW OF TABLE

03FE 80 FC 02                             CMP    AH,2
0401 72 28                             JNC   SETM11
                                           ; DETERMINE WHICH TO USE
                                           ; MODE IS 0 OR 1

0403 03 D9                             ADD    BX,CX
0405 80 FC 04                             CMP    AH,4
0408 72 21                             JNC   SETM11
                                           ; MODE IS 2 OR 3

040A 03 D9                             ADD    BX,CX
040C 80 FC 09                             CMP    AH,9
040F 72 1A                             JNC   SETM11
                                           ; MODE IS 4, 5, 6, 8

0411 03 D9                             ADD    BX,CX
0413 80 FC 0B                             CMP    AH,0BH
0416 76 13                             JBE   SETM11
                                           ; MODE IS 9, A OR B

0418 03 D9                             ADD    BX,CX
041A 80 FC 12                             CMP    AH,12H
041D 72 0C                             JNC   SETM11
                                           ; MODE IS 10, 11

041F 03 D9                             ADD    BX,CX
0421 80 FC 14                             CMP    AH,14H
0424 72 05                             JNC   SETM11
                                           ; MODE IS 12,13

0426 80 E4 0F                             AND    AH,KJ_OFF
0429 EB C8                             JMP   SHORT SETM10
                                           ; MASK KJ BIT OFF
                                           ; SERCH AGAIN FOR MODE 14H-1AH

042B                                     ;---- BX POINTS TO CORRECT ROW OF INITIALIZATION TABLE
042B 8B F7                               SETM11:
042D 8A 47 02                             MOV    SI,DI
0430 8B 57 0A                             MOV    AL,DS:[BX+2]
0433 86 F2                               XCHG  DX,WORD PTR DS:[BX+10]
                                           ; GET HORZ. SYNC POSITION
                                           ; GET CURSOR TYPE
                                           ; SWAP FOR HIGH-LOW REVERSE

0435 1E                                     PUSH  DS
0436 E8 0000 E                             CALL  DDS
                                           ASSUME DS:DATA

0439 A2 0089 R                             MOV    HORZ_POS,AL
043C 89 16 0060 R                         MOV    CURSOR_MODE,DX
                                           ; SAVE HORZ. SYNC POSITION VARIABLE
                                           ; SAVE CURSOR MODE

0440 A0 0086 R                             MOV    AL,VAR_DELAY
0443 24 0F                             AND    AL,0FH
0445 A2 0086 R                             MOV    VAR_DELAY,AL
                                           ASSUME DS:CODE

0448 1F                                     POP   DS

0449 32 E4                             XOR    AH,AH
044B BA 03D4                             MOV    DX,A6845
                                           ; AH WILL SERVE AS REGISTER NUMBER DURING LOOP
                                           ; POINT TO 6845
                                           ;---LOOP THROUGH TABLE, OUTPUTTING REG ADDRESS, THEN VALUE FROM TABLE

044E                                     SETM12:
044E 8A C4                             MOV    AL,AH
0450 EE                             OUT   DX,AL
                                           ; GET 6845 REGISTER NUMBER

0451 42                             INC    DX
0452 FE C4                             INC    AH
0454 8A 07                             MOV    AL,[BX]
0456 EE                             OUT   DX,AL
                                           ; POINT TO DATA PORT
                                           ; NEXT REGISTER VALUE
                                           ; GET TABLE VALUE
                                           ; OUT TO CHIP

0457 43                             INC    BX
0458 4A                             DEC    DX
0459 E2 F3                             LOOP  SETM12
                                           ; NEXT IN TABLE
                                           ; BACK TO POINTER REGISTER
                                           ; DO THE WHOLE TABLE

045B 8B C6                             MOV    AX,SI
045D 1F                             POP   DS
                                           ; GET MODE BACK
                                           ; RECOVER SEGMENT VALUE
                                           ASSUME DS:DATA

045E 33 FF                             XOR    DI,DI
0460 89 3E 004E R                         MOV    CRT_START,DI
                                           ;--- FILL REGEN AREA WITH BLANK
                                           ; SET UP POINTER FOR REGEN
                                           ; START ADDRESS SAVED IN GLOBAL

0464 C6 06 0062 R 00                       MOV    ACTIVE_PAGE,0
0469 C6 06 034C R 08                       MOV    CPU_PAGE,VRAM2_PAGE
046E C6 06 034D R 08                       MOV    CRT_PAGE,VRAM2_PAGE
0473 E8 1888 R                             CALL  ENABLE_KJROM
0476 E8 1884 R                             CALL  ENABLE_VRAM
                                           ; SET PAGE VALUE
                                           ; SET CURRENT ACTIVE PAGE AS 8
                                           ; SET CURRENT ACTIVE PAGE AS 8
                                           ; DISABLE BOTH V-RAM 1 & 2 ONCE
                                           ; ENABLE V-RAM 2

0479 8B D8                             MOV    BX,AX
047B 5A                             POP   DX
                                           ; SET MODE TO BX
                                           ; GET ORIGINAL INPUT BACK

047C 8A F2                             MOV    DH,DL
047E 80 E6 7F                             AND    DH,07FH
0481 80 FE 1B                             CMP    DH,18H
0484 75 29                             JNE   SETM121
                                           ; SUPER 16 COLOR ?
                                           ; NO

0486 50                                     PUSH  AX
0487 53                                     PUSH  BX
0488 52                                     PUSH  DX
0489 57                                     PUSH  DI
048A 56                                     PUSH  SI
                                           ; SAVE AX
                                           ; SAVE BX
                                           ; SAVE DX
                                           ; SAVE DI
                                           ; SAVE SI

048B E8 1DB0 R                             CALL  ENABLE_VG1
048E 8A C2                             MOV    AL,DL
0490 2C 11                             SUB    AL,11H
                                           ; SETUP VIDEO PROCESSOR-1

```



```

0492 E8 1C7D R      CALL SUP_SET_MODE ;
0495 C6 06 0049 R 1B MOV CRT_MODE,1BH ;
049A E8 1DFC R      CALL ENABLE_VG12 ;

049D BA 03DA        MOV DX,SZABASE ;
04A0 EC            IN AL,DX ;
04A1 B0 06        MOV AL,PCSUPER ;
04A3 EE            OUT DX,AL ; SETUP SUPERIMPOSE LOGIC
04A4 B0 06        MOV AL,0110B ;
04A6 EE            OUT DX,AL ;
04A7 A2 0347 R    MOV SUPIPCR,AL ;
04AA 5E            POP SI ;
04AB 5F            POP DI ;
04AC 5A            POP DX ; RESTORE REGISTERS
04AD 5B            POP BX ;
04AE 58            POP AX ;

SETM121:
04AF 80 E2 80     AND DL,80H ; NO CLEAR OF REGEN ?
04B2 75 4E       JNZ SETM17 ; SKIP CLEARING REGEN

04B4 B9 2000      MOV CX,8192 ; NUMBER OF WORDS TO CLEAR

04B7 3C 09       CMP AL,09H ; REQUIRE 32K BYTE REGEN ?
04B9 72 02       JC SETM13 ; NO, JUMP

04BB D1 E1       SHL CX,1 ; SET 16K WORDS TO CLEAR

04BD            SETM13:
04BD BA B800      MOV DX,REGEN_START ; SET UP SEGMENT FOR V-RAM2
04C0 8E C2       MOV ES,DX ; SET REGEN SEGMENT

04C2 B8 0F20     MOV AX,' '+15*256 ; FILL CHAR FOR ALPHA
04C5 80 FF 10    CMP BH,KJ_MODE ; KJ MODE ?
04C8 72 03       JB SETM14 ; NO

04CA B8 0720     MOV AX,' '+7*256 ; FILL CHAR FOR KANJI
04CD            SETM14:
04CD 80 FB 04    CMP BL,GRAPHICS ; TEST FOR GRAPHICS
04D0 72 1A       JC SETM16 ; NO_GRAPHICS_INIT

04D2 80 FF 10    CMP BH,KJ_MODE ; KJ GRAPHICS MODE ?
04D5 72 13       JB SETM15 ; NO
;--- CLEAR PESUDO CODE BUFFER
04D7 06         PUSH ES ; SAVE CURRENT ES
04D8 57         PUSH DI ; SAVE CURRENT DI
04D9 51         PUSH CX ; SAVE CURRENT CX

04DA B9 00A0     MOV CX,P_CODE_START ; SET PESUDO CODE BUFFER SEGMENT
04DD 8E C1       MOV ES,CX ; TO ES
; ASSUME ES:P_CODE_BUFFER

04DF B9 0400     MOV CX,P_CODE_SIZE/2 ; SET BUFFER SIZE IN WORD

04E2 B8 0720     MOV AX,' '+7*256 ; FILL CHAR FOR ALPHA IN ATTRIBUTE TYPE 2
04E5 F3/ AB      REP STOSW ; FILL THE PESUDO CODE BUFFER

04E7 59         POP CX ; RESTORE CX
04E8 5F         POP DI ; RESTORE DI
04E9 07         POP ES ; RESTORE ES
; ASSUME ES: VIDEO_RAM

04EA            SETM15:
04EA 33 C0       XOR AX,AX ; FILL FOR GRAPHICS MODE
04EC            SETM16:
04EC F3/ AB      REP STOSW ; FILL THE REGEN BUFFER WITH BLANKS

04EE 33 C0       XOR AX,AX ;
04F0 A2 0348 R   MOV AC_PRESENT,AL ; CLEAR ALTERNATE CURSOR FLAG
04F3 A2 0349 R   MOV GC_PRESENT,AL ; CLEAR GRAPHICS CURSOR FLAG
04F6 C7 06 034E R 1011 MOV GC_CURSOR_MODE,UNDER_CURSOR ; SETUP GRAPHICS CURSOR MODE
04FC C7 06 0350 R 2011 MOV AC_CURSOR_MODE,CURSOR_DISABLE*100H OR BLOCK_CURSOR ; SETUP ALT CSR

0502            SETM17:
0502 BA 03DA      MOV DX,VGA_CTL ; ----- ENABLE VIDEO
0505 32 C0       XOR AL,AL ; SET PORT ADDRESS OF VGA
0507 EE            OUT DX,AL ;
0508 A0 0065 R    MOV AL,CRT_MODE_SET ; SELECT VGA REG 0
050B EE            OUT DX,AL ; GET MODE SET VALUE
; SET MODE

050C E8 1DD6 R    CALL ENABLE_V02 ; ENABLE VIDEO GRNARATER 2

;----- DETERMINE NUMBER OF COLUMNS AND ROWS, BOTH FOR ENTIRE
;----- DISPLAY AND THE NUMBER TO BE USED FOR TTY INTERFACE
050F 8B DE      MOV BX,SI ; GET CURRENT CRT MODE
0511 E8 059C R  CALL SHMODE_SET ; SET SCREEN PARAMETERS
;----- SET CURSOR POSITIONS
0514 32 FF      XOR BH,BH ;
0516 D1 E3     SHL BX,1 ; WORD OFFSET INTO CLEAR LENGTH TABLE
0518 2E: 8B 8F 01FD R MOV CX,CS:(BX + OFFSET REGEN_L) ; LENGTH TO CLEAR
051D 89 0E 04C R MOV CRT_LEN,CX ; SAVE LENGTH OF CRT

0521 B9 0010     MOV CX,NO_ACT_PAGE ; CLEAR ALL CURSOR POSITIONS
0524 BF 035C R  MOV DI,OFFSET CURSOR_POSH

0527 1E         PUSH DS ; ESTABLISH SEGMENT
0528 07         POP ES ; ADDRESSING
; ASSUME ES:DATA

0529 33 C0       XOR AX,AX
052B F3/ AB      REP STOSW ; FILL WITH ZEROES

```



```

0573 EE          OUT      DX,AL          ; SEND TO GATE ARRAY
0574 80 02      MOV      AL,SYNCRST      ; SET SYNCHRONOUS RESET
0576 EE          OUT      DX,AL          ; DO IT

; WHILE THE GATE ARRAY IS IN RESET STATE, WE CANNOT ACCESS RAM
0577 8B C6      MOV      AX,SI          ; RESTORE NEW MODE SET
0579 80 E4 F7    AND      AH,HOT VIDEOENB ; TURN OFF VIDEO ENABLE

057C 32 C0      XOR      AL,AL          ; SET UP TO SELECT VGA REG 0
057E EE          OUT      DX,AL          ; SELECT IT
057F 86 E0      XCHG     AH,AL          ; AH IS VGA REG COUNTER
0581 EE          OUT      DX,AL          ; SET MODE

0582 80 04      MOV      AL,IXRESET      ; SET UP TO SELECT VGA REG 4
0584 EE          OUT      DX,AL          ; SELECT IT
0585 32 C0      XOR      AL,AL          ;
0587 EE          OUT      DX,AL          ; REMOVE RESET FROM VGA
; NOW OKAY TO ACCESS RAM AGAIN

0588 E8 058C R   CALL     MODE_ALIVE      ; KEEP MEMORY DATA VALID
058B C3          RET
058C          VGA_RESET   ENDP

```

```

;-----
;
; MODE_ALIVE
;
; THIS ROUTINE READS 256 LOCATIONS IN MEMORY AS EVERY OTHER
; LOCATION IN 512 LOCATIONS. THIS IS TO INSURE THE DATA
; INTEGRITY OF MEMORY DURING MODE CHANGES.
;
; INPUT          NONE
; OUTPUT         NONE
; VOLATILE       NONE
;-----

```

```

058C          MODE_ALIVE   PROC   NEAR
058C 50          PUSH     AX          ;SAVE USED REGS
058D 56          PUSH     SI
058E 51          PUSH     CX
058F 33 F6      XOR      SI,SI
0591 89 0100    MOV      CX,256
0594          MALIVE1:
0594 AC '        LODSB
0595 46          INC      SI
0596 E2 FC      LOOP    MALIVE1

0598 59          POP      CX
0599 5E          POP      SI
059A 58          POP      AX
059B C3          RET
059C          MODE_ALIVE   ENDP

```

```

;-----
;
; SMODE_SET      SOFTWARE MODE SET
;
; THIS ROUTINE INITIALIZES KANA-KAN AND
; SET ROW,COLUMN NUMBER OF SCREEN
;
; INPUT          BH = CRT MODE
;                BL = CRT MODE (MASKED)
;                DS = DATA SEGMENT
; OUTPUT         NONE
; VOLATILE       BH
;-----

```

```

059C          SMODE_SET   PROC   NEAR
059C 50          PUSH     AX          ;----- DETERMINE NUMBER OF ROWS
059D 53          PUSH     BX          ; SAVE AX
; SAVE MODE

059E 32 FF      XOR      BH,BH          ; CLEAR BH FOR CONVERT BYTE TO WORD
05A0 2E: 8A 87 0235 R MOV     AL,CS:[BX + OFFSET COL_L]
05A5 32 E4      XOR      AH,AH
05A7 A3 004A R  MOV     CRT_COLS,AX    ; NUMBER OF COLUMNS IN THIS SCREEN

05AA 58          POP      BX          ; RESTORE MODE
05AB 88 0701    MOV     AX,INDICATOR_ON ; ENABLE INDICATOR
05AE 80 FF 18  CMP     BH,18H         ; 20X11 GRAPHICS ?
05B1 74 10      JE      SSET1         ; YES

05B3 C6 06 0346 R 18 MOV     CRT_ROWS,ROW_ANK ; SET ROW NUMBER OF ANK MODE
05B8 88 0703    MOV     AX,KKNI_OFF    ; DISABLE KANA-KAN
05BB 80 FF 10  CMP     BH,KJ_MODE     ; KJ MODE ?
05BE 72 08      JB      SSET2         ; NO

05C0 88 0700    MOV     SSET1,AX       ; ENABLE KANA-KAN
05C3          SSET1:
05C3 C6 06 0346 R 09 MOV     CRT_ROWS,ROW_KJ ; SET ROW NUMBER OF KJ MODE
05C8          SSET2:
05C8 CD 16      INT     KEYBORD        ; KANA-KAN ON/OFF

05CA 58          POP      AX          ; RESTORE AX
05CB C3          RET

```

Appendix A.

05CC

```

SMODE_SET      ENDP
;-----
;
; SET_CTYPE          INT 10H, AH = 1
; THIS ROUTINE SETS THE CURSOR VALUE
;
; INPUT  AH = CURRENT CRT MODE ( MASKED )
;        CX = CURSOR VALUE CH-START LINE, CL-STOP LINE
;
; OUTPUT NONE
;
; VOLATILE          AL,CX,DX
;-----
;
; ASSUME  CS:CODE, DS:DATA

```

05CC

```

05CC 8B FC 04
05CF 72 27
05D1 8B 16 035C R
05D5 F6 06 0349 R FF
05DA 74 09
05DC 51
05DD 8B 0E 034E R
05E1 E8 0618 R
05E4 59
05E5 89 0E 034E R
05E8 E8 0618 R
05EC C6 06 0349 R FF
05F1 81 E1 3FFF
05F5 80 CD 20
05F8 B4 0A
05FA 89 0E 0060 R
05FE E8 0602 R

```

```

SET_CTYPE      PROC    NEAR
;
; CMP  AH,GRAPHICS    ; IN GRAPHICS MODE?
; JC   SCT2           ; NO, JUMP
;
; MOV  DX,CURSOR_POSH ; GET CURRENT CURSOR POSITION
;
; TEST GC_PRESENT,TRUE ; GRAPHICS CURSOR PRESENT ?
; JZ   SCT1           ; NO
;
; PUSH CX             ; SAVE NEW CURSOR MODE
; MOV  CX,GCURSOR_MODE ; GET OLD CURSOR MODE
; CALL WRITE_GCURSOR ; ERASE CURRENT GRAPHICS CURSOR
; POP  CX             ; RESTORE NEW CURSOR MODE
;
; SCT1: MOV GCURSOR_MODE,CX ; SAVE NEW GRAPHICS CURSOR MODE
;       CALL WRITE_GCURSOR ; WRITE NEW GRAPHICS CURSOR
;       MOV GC_PRESENT,TRUE ; SET GRAPHICS CURSOR FLAG ON
;
; AND  OR             ; MASK FOR GRAPHICS CURSOR
;     CX,GCURSOR_MASK ; MASK FOR GRAPHICS CURSOR
;     CH,CURSOR_DISABLE ; DISABLE CURSOR OF 6845
;
; SCT2: MOV AH,10      ; 6845 REGISTER FOR CURSOR SET
;       MOV CURSOR_MODE,CX ; SAVE IN DATA AREA
;       CALL OUT6845     ; OUTPUT CX REG
;
; RET

```

0601 C3  
0602

```

SET_CTYPE      ENDP
;-----
;
; OUT6845
; THIS ROUTINE OUTPUTS THE CX REGISTER TO THE 6845 REGS NAMED IN AH
;
; INPUT  AH = 6845 REGISTER ADDRESS
;        CH = DATA SHOULD BE WRITE TO (AH)
;        CL = DATA SHOULD BE WRITE TO (AH+1)
;
; OUTPUT NONE
;
; VOLATILE          AL, DX
;-----

```

0602

```

0602 8B 16 0063 R
0606 8A C4
0608 EE
0609 42
060A 8A C5
060C EE
060D 4A
060E 8A C4
0610 FE C0
0612 EE
0613 42
0614 8A C1
0616 EE
0617 C3
0618

```

```

OUT6845 PROC    NEAR
;
; MOV  DX,ADDR_6845  ; ADDRESS REGISTER
;
; MOV  AL,AH         ; GET VALUE
; OUT  DX,AL         ; REGISTER SET
; INC  DX            ; DATA REGISTER
; MOV  AL,CH         ; DATA
; OUT  DX,AL
; DEC  DX
;
; MOV  AL,AH         ; POINT TO OTHER DATA REGISTER
; INC  AL            ; SET FOR SECOND REGISTER
; OUT  DX,AL
; INC  DX
; MOV  AL,CL         ; SECOND DATA VALUE
; OUT  DX,AL
;
; RET                ; ALL DONE
;
; OUT6845 ENDP

```

```

;-----
;
; WRITE_GCURSOR
; THIS ROUTINE WRITES GRAPHICS CURSOR
;
; INPUT  AH = CRT MODE ( MASKED )
;        CX = GRAPHICS CURSOR MODE
;        DX = ROW,COLUMN POSITION TO WRITE
;        DS = DATA SEGMENT
;
; OUTPUT NONE
; VOLATILE NONE
;-----

```

```

0618                                     WRITE_GCURSOR      PROC      NEAR
0618 80 3E 0049 R 14                       CMP      CRT_MODE,KJGRAPH; KJ GRAPHICS MODE ?
061D 72 69                                   JB       WGC2                ; NO
                                           TEST     CH,CURSOR_DISABLE; CURSOR DISABLED ?
061F F6 C5 20                               JNZ     WGC2                ; YES
0622 75 64
0624 81 E1 3FFF                             AND     CX,GCURSOR_MASK ; MASK FOR GRAPHICS CURSOR MODE
0628 80 F9 11                             CMP     CL,CBOX_ROW - 1 ; END CURSOR EXCEED CHARACTER BOX ?
062B 76 02                             JBE     WGC1                ; NO
                                           MOV     CL,CBOX_ROW - 1 ; SET MAX ROW OF CHARACTER BOX
062D B1 11                                     WGC1:  CMP     CH,CL                ; CURSOR START > END ?
062F 3A E9                                   JA      WGC2                ; YES, CURSOR DOES NOT APPEAR
0631 77 55
                                           PUSH    BP                  ; SAVE BP
0633 55                                     SUB     SP,SAC_LOCAL        ; ALLOCATE LOCAL WORK AREA
0634 83 EC 1C                             MOV     BP,SP              ; ASSIGH BP AS FRAME POINTER
0637 8B EC
0639 50                                     PUSH    AX                  ;
063A 53                                     PUSH    BX                  ;
063B 51                                     PUSH    CX                  ;
063C 52                                     PUSH    DX                  ;
063D 56                                     PUSH    SI                  ;
063E 57                                     PUSH    DI                  ;
063F 1E                                     PUSH    DS                  ;
0640 06                                     PUSH    ES                  ;
                                           MOV     [BP+WC_MODE],AH ; SET CRT MODE
0641 88 66 09                             MOV     [BP+WPOS],DX      ; SAVE ROW/COLUMN POSIITON
0644 89 56 02
                                           MOV     AX,DX              ; SET IT TO AX FOR GET LOCATION
0647 8B C2                                 CALL    GRAPH_POSH        ; DETERMINE LOCATION IN REGEN BUFFER
0649 E8 15E8 R                             MOV     [BP+WGPOS],AX    ; SAVE WRITE POSITION
064C 89 46 00                             MOV     BX,CX              ; SET GRAPHICS CURSOR MODE TO BX
064F 8B D9
0651 06                                     PUSH    ES                  ; SAVE ES
0652 16                                     PUSH    SS                  ;
0653 1F                                     POP     DS                  ; POINT TO STACK SEGMENT
0654 16                                     PUSH    SS                  ;
0655 07                                     POP     ES                  ; POINT TO STACK SEGMENT
                                           ASSUME DS:STACK, ES:STACK
0656 8D 76 0A                             LEA     SI,[BP+WFONT]     ; SET ADDRESS OF FONT BUFFER AREA TO SI
0659 8B FE                                 MOV     DI,SI              ; SET IT TO DI
065B B9 0009                             MOV     CX,CBOX_ROW/2    ;
065E 33 C0                                 XOR     AX,AX              ; CLEAR 18 BYTE FOR CURSOR
0660 F3 AB                                 REP
0662 33 D2                                 XOR     DX,DX              ;
0664 8A D7                                 MOV     DL,BH              ;
0666 8B FE                                 MOV     DI,SI              ; SET CURSOR START LINE TO DI
0668 03 FA                                 ADD     DI,DX              ;
066A 32 FF                                 XOR     BH,BH              ;
066C 2B DA                                 SUB     BX,DX              ;
066E 43                                     INC     BX                  ; SET CURSOR LINE NUMBER TO CX.
066F 8B CB                                 MOV     CX,BX              ;
0671 48                                     DEC     AX                  ;
0672 F3 AA                                 REP     STOSB              ; SET CURSOR
0674 07                                     POP     ES                  ; RESTORE REGEN SEGMENT
                                           ASSUME ES:VIDEO_RAM
0675 B3 8F                                 MOV     BL,XOR_BIT OR 0FH;SET COLOR 16 AND X'OR WRITE FUNCTION
0677 32 D2                                 XOR     DL,DL              ; SET 0 TO DISPLACEMENT
0679 E8 1265 R                             CALL    G_WRT1            ; WRITE CURSOR
067C 07                                     POP     ES                  ;
067D 1F                                     POP     DS                  ;
067E 5F                                     POP     DI                  ;
067F 5E                                     POP     SI                  ; RESTORE REGISTERS
0680 5A                                     POP     DX                  ;
0681 59                                     POP     CX                  ;
0682 5B                                     POP     BX                  ;
0683 58                                     POP     AX                  ;
0684 83 C4 1C                             ADD     SP,SAC_LOCAL      ; DEALLOCATE WORK AREA
0687 5D                                     POP     BP                  ; RESTORE BP
0688                                     WGC2:  RET
0688 C3
0689                                     WRITE_GCURSOR      ENDP

```

```

-----
SET_CPOS          INT 10H, AH = 2
-----
THIS ROUTINE SETS THE CURRENT CURSOR POSITION TO THE
NEW X-Y VALUES PASSED
INPUT  AH = CRT MODE ( MASKED )
       DX = ROW,COLUMN OF NEW CURSOR
       BH = DISPLAY PAGE OF CURSOR
OUTPUT NONE
VOLATILE AX,CX,SI,DI

```

Appendix A.

```

;          CURSOR IS SET AT 6845 IF DISPLAY PAGE IS CURRENT DISPLAY
;
;-----
;          ASSUME CS:CODE, DS:DATA
;-----
0689          SET_CPOS          PROC          NEAR
0689 8A CF          MOV          CL,BH
068B 32 ED          XOR          CH,CH          ; ESTABLISH LOOP COUNT
068D D1 E1          SAL          CX,1          ; WORD OFFSET
068F 8B F1          MOV          SI,CX          ; USE INDEX REGISTER
0691 8B BC 035C R   MOV          DI,[SI+OFFSET CURSOR_POSN] ; GET OLD CURSOR POSITION
0695 89 94 035C R   MOV          [SI+OFFSET CURSOR_POSN],DX ; SAVE THE POINTER

0699 38 JE 0062 R   CMP          ACTIVE_PAGE,BH
069D 75 24          JNZ          SCP3          ; SET_CPOS_RETURN

069F 80 FC 04          CMP          AH,GRAPHICS          ; GRAPHIS MODE ?
06A2 72 1A          JB          SCP2          ; NO

06A4 8B 0E 034E R   MOV          CX,GCURSOR_MODE ; SET GRAPHICS CURSOR MODE TO CX
06A8 F6 06 0349 R FF TEST         GC_PRESENT,TRUE ; GRAPHICIS CURSOR PRESENT ?
06AD 74 07          JZ          SCP1          ; NO

06AF 52          PUSH         DX          ; SAVE NEW CURSOR POSITION

06B0 8B D7          MOV          DX,DI          ; SET OLD CURSOR POSITION TO DX
06B2 E8 0618 R      CALL         WRITE_GCURSOR ; ERASE CURRENT GRAPHICIS CURSOR

06B5 5A          POP          DX          ; RESTORE NEW CURSOR POSITION
06B6          SCP1:          CALL         WRITE_GCURSOR ; WRITES NEW GRAPHIS CURSOR
06B9 C6 06 0349 R FF MOV          GC_PRESENT,TRUE ; SET GRAPHICS CURSOR FLAG ON

06BE 8B C2          MOV          AX,DX          ; GET ROW/COLUMN TO AX
06C0 E8 06C4 R      CALL         SET_CURSOR    ; CURSOR_SET
06C3          SCP2:          RET
06C3 C3          SCP3:          RET

06C4          SET_CPOS          ENDP

;-----
;          SET_CURSOR
;
;          THIS ROUTINE SETS THE CURRENT CURSOR POSITION TO THE 6845
;
;          INPUT  AX = ROW,COLUMN VALUE TO SET
;
;          OUTPUT          NONE
;          VOLATILE      AX,CX
;-----
06C4          SET_CURSOR          PROC          NEAR
06C4 E8 06D5 R      CALL         POSITION          ; DETERMINE LOCATION IN REGEN BUFFER
06C7 8B C8          MOV          CX,AX
06C9 03 0E 004E R   ADD          CX,CRT_START ; ADD IN THE START ADDRESS FOR THIS PAGE

06CD D1 F9          SAR          CX,1          ; DIVIDE BY 2 FOR CHAR ONLY COUNT
06CF B4 0E          MOV          AH,14          ; REGISTER NUMBER FOR CURSOR
06D1 E8 06D2 R      CALL         OUT6845        ; OUTPUT THE VALUE TO THE 6845

06D4 C3          RET

06D5          SET_CURSOR          ENDP

;-----
;          POSITION
;
;          THIS SERVICE ROUTINE CALCULATES THE REGEN BUFFER ADDRESS
;          OF A CHARACTER IN THE ALPHA MODE
;
;          INPUT  AX = ROW, COLUMN POSITION
;
;          OUTPUT AX = OFFSET OF CHAR POSITION IN REGEN BUFFER
;
;          VOLATILE          NONE
;-----
06D5          POSITION          PROC          NEAR
06D5 53          PUSH         BX          ; SAVE REGISTER

06D6 8B D8          MOV          BX,AX
06D8 8A C4          MOV          AL,AH
06DA F6 26 004A R   MUL          BYTE PTR CRT_COLS ; ROWS TO AL
;          ; DETERMINE BYTES TO ROW

06DE 32 FF          XOR          BH,BH
06E0 03 C3          ADD          AX,BX
06E2 D1 E0          SAL          AX,1          ; ADD IN COLUMN VALUE
06E4 5B          POP          BX          ; * 2 FOR ATTRIBUTE BYTES
06E5 C3          RET

```

06E6

```

POSITION      ENDP
;-----
;
;   READ_CURSOR      INT 10H, AH = 3
;-----
;   THIS ROUTINE READS THE CURRENT CURSOR VALUE FROM THE
;   6845, FORMATS IT, AND SENDS IT BACK TO THE CALLER
;
;   INPUT  BH = PAGE OF CURSOR
;
;   OUTPUT DX = ROW, COLUMN OF THE CURRENT CURSOR POSITION
;          CX = CURRENT CURSOR MODE
;
;   VOLATILE      BX
;-----
ASSUME CS:CODE, DS:DATA

```

06E6

```

06E6 55
06E7 8A DF
06E9 32 FF
06EB D1 E3
06ED 8B 97 035C R
06F1 8B 0E 0060 R

06F5 80 3E 0049 R 14
06FA 72 04

06FC 8B 0E 034E R
0700
0700 8B EC
0702 89 56 0C
0705 89 4E 0A

0708 5D
0709 C3

070A

```

```

READ_CURSOR  PROC   NEAR
;
;   PUSH      BP          ; SAVE BP
;
;   MOV      BL,BH
;   XOR      BH,BH
;   SAL     BX,1         ; WORD OFFSET
;   MOV     DX,[BX+OFFSET CURSOR_POSN]
;   MOV     CX,CURSOR_MODE
;
;   CMP     CRT_MODE,KJGRAPH; KJ GRAPHICS MODE ?
;   JB     RCSR1         ; NO
;
;   MOV     CX,GCURSOR_MODE ; SET GRAPHICS CURSOR MODE
RCSR1:
;   MOV     BP,SP          ; SET FRAME POINTER
;   MOV     [BP+F_DX],DX   ; SET RETURN DX
;   MOV     [BP+F_CX],CX   ; SET RETURN CX
;
;   POP     BP          ; RESTORE BP
;   RET

```

```

READ_CURSOR  ENDP
;-----
;
;   LIGHT PEN      INT 10H, AH = 4
;-----
;   THIS ROUTINE TESTS THE LIGHT PEN SWITCH AND THE LIGHT
;   PEN TRIGGER. IF BOTH ARE SET, THE LOCATION OF THE LIGHT
;   PEN IS DETERMINED. OTHERWISE, A RETURN WITH NO INFORMATION
;   IS MADE.
;
;   INPUT  AH = CURRENT CRT MODE ( MASKED )
;
;   ON EXIT:
;   AH = 0 IF NO LIGHT PEN INFORMATION IS AVAILABLE
;        BX,CX,DX ARE DESTROYED
;   AH = 1 IF LIGHT PEN IS AVAILABLE
;        DH,DL = ROW,COLUMN OF CURRENT LIGHT PEN POSITION
;        CH = RASTER POSITION
;        BX = BEST GUESS AT PIXEL HORIZONTAL POSITION
;-----
ASSUME CS:CODE,DS:DATA

```

070A

```

070A 06 06 09 09 05 05
070A 05 00 05 08 08 08

```

```

;-----SUBTRACT_TABLE
SUBTBL LABEL BYTE
; MODE-0---1---2---3---4---5---6---7---8---9---A---B---C---D---E---F
; MODE-10--11--12--13--14--15--16--17--18--19--1A--1B
DB 06H, 06H, 09H, 09H, 05H, 05H, 0, 0, 05H, 08H, 08H, 08H; 0, 0, 0, 0

```

0716

```

0716 55
0717 E8 1DB0 R

071A 32 E4
071C BA 03DA

071F EC
0720 A8 04
0722 74 03

0724 E9 07E5 R

0727
0727 A8 02
0729 75 03

072B E9 07EF R

072E
072E B4 10

0730 8B 16 0063 R
0734 8A C4
0736 EE
0737 42
0738 EC
0739 8A E8
073B 4A
073C FE C4

```

```

READ_LPEN    PROC   NEAR
;
;   PUSH     BP          ; SAVE BP
;   CALL    ENABLE_V01   ; ENABLE VIDEO GENERATER 1
;
;   XOR     AH,AH
;   MOV     DX,VGA_CTL   ; GET ADDRESS OF VGA CONTROL REG
;
;   IN     AL,DX         ; GET STATUS REGISTER
;   TEST   AL,LPENSW    ; TEST LIGHT PEN SWITCH
;   JZ     RLPEN1
;
;   JMP     RLPEN14      ; NOT SET, RETURN
;
;   RLPEN1: TEST   AL,LPENTRG ;--- NOW TEST FOR LIGHT PEN TRIGGER
;           JNZ   RLPEN2 ; TEST LIGHT PEN TRIGGER
;           ; RETURN WITHOUT RESETTING TRIGGER
;
;   JMP     RLPEN15
;
;   RLPEN2: ;--- TRIGGER HAS BEEN SET, READ THE VALUE IN
;           MOV   AH,16 ; LIGHT PEN REGISTERS ON 6845
;           ;---INPUT REGS POINTED TO BY AH, AND CONVERT TO ROW COLUMN IN DX
;           DX,ADDR_6845 ; ADDRESS REGISTER FOR 6845
;           MOV   AL,AH ; REGISTER TO READ
;           OUT   DX,AL ; SET IT UP
;           INC   DX ; DATA REGISTER
;           IN   AL,DX ; GET THE VALUE
;           MOV   CH,AL ; SAVE IN CX
;           DEC   DX ; ADDRESS REGISTER
;           INC   AH

```

Appendix A.

```

073E 8A C4          MOV    AL,AH          ; SECOND DATA REGISTER
0740 EE            OUT    DX,AL          ;
0741 42            INC    DX              ; POINT TO DATA REGISTER
0742 EC            IN     AL,DX          ; GET SECOND DATA VALUE
0743 8A E5          MOV    AH,CH          ; AX HAS INPUT VALUE
                                ;--- AX HAS THE VALUE READ IN FROM THE 6845

0745 8A 1E 0049 R   MOV    BL,CRT_MODE    ; BL,CRT_MODE
0749 81 E3 000F     AND    BX,KJ_OFF      ; BX,KJ_OFF
074D 2E: 8A 9F 070A R MOV    BL,CS:SUBTBL[BX]; DETERMINE AMOUNT TO SUBTRACT
0752 2B C3          SUB    AX,BX          ; TAKE IT AWAY

0754 3D 0FA0        CMP    AX,4000        ; IN TOP OR BOTTOM BORDER?
0757 72 02          JB     RLPEN3         ; NO, OKAY
0759 33 C0          XOR    AX,AX         ; YES, SET TO ZERO
RLPEN3:
075B 8B 1E 004E R   MOV    BX,CRT_START  ; BX,CRT_START
075F D1 EB          SHR    BX,1          ;
0761 2B C3          SUB    AX,BX          ; CONVERT TO CORRECT PAGE ORIGIN
0763 79 02          JNS   RLPEN4         ; IF POSITIVE, DETERMINE MODE
0765 2B C0          SUB    AX,AX          ; <0 PLAYS AS 0
                                ;--- DETERMINE MODE OF OPERATION
RLPEN4:
0767              MOV    CL,3          ; DETERMINE_MODE
0767 B1 03          MOV    CL,3          ; SET *8 SHIFT COUNT

0769 8A 36 0049 R   MOV    DH,CRT_MODE    ; SET CRT MODE TO DH
076D 80 E6 0F     AND    DH,KJ_OFF      ; STRIP KJ BIT OFF
0770 80 FE 04     CMP    DH,GRAPHICS    ; GRAPHICS MODE ?
0773 72 4D          JB     RLPEN11       ; ALPHA_PEN
                                ;--- GRAPHICS MODE
0775 B2 28          MOV    DL,40          ; DIVISOR FOR GRAPHICS
0777 80 FE 09     CMP    DH,9           ; USING 32K REGEN?
077A 72 02          JB     RLPEN5         ; NO, JUMP
077C B2 50          MOV    DL,80          ; YES, SET RIGHT DIVSOR
077E              RLPEN5:
077E F6 F2          DIV    DL              ; DETERMINE ROW(AL) AND COLUMN(AH)
                                ; AL RANGE 0-99, AH RANGE 0-39
                                ;--- DETERMINE GRAPHIC ROW POSITION
0780 8A E8          MOV    CH,AL          ; SAVE ROW VALUE IN CH
0782 02 ED          ADD    CH,CH          ; *2 FOR EVEN/ODD FIELD
0784 80 FE 09     CMP    DH,9           ; USING 32K REGEN?
0787 72 06          JB     RLPEN6         ; NO, JUMP
RLPEN6:
0789 D0 EC          SHR    AH,1          ; ADJUST ROW & COLUMN
078B D0 E0          SHL    AL,1          ;
078D 02 ED          ADD    CH,CH          ; *4 FOR 4 SCAN LINES
078F              RLPEN6:
078F 8A DC          MOV    BL,AH          ; COLUMN VALUE TO BX
0791 2A FF          SUB    BH,BH          ; MULTIPLY BY 8 FOR MEDIUM RES
0793 80 FE 06     CMP    DH,6           ; DETERMINE MEDIUM OR HIGH RES
0796 72 13          JB     RLPEN9         ; MODE 4 OR 5
0798 77 06          JA     RLPEN8         ; MODE 8, 9, OR A,B
RLPEN7:
079A B1 04          MOV    CL,4          ; SHIFT VALUE FOR HIGH RES
079C D0 E4          SAL    AH,1          ; COLUMN VALUE TIMES 2 FOR HIGH RES
079E EB 0B          JMP    SHORT RLPEN9

07A0              RLPEN8:
07A0 80 FE 09     CMP    DH,9           ; CHECK MODE
07A3 77 F5          JA     RLPEN7         ; MODE A,B
07A5 74 04          JE     RLPEN9         ; MODE 9
                                ;
07A7 B1 02          MOV    CL,2          ; MODE 8 SHIFT VALUE
07A9 D0 EC          SHR    AH,1          ;
07AB D3 E3          SHL    BX,CL          ; NOT HIGH_RES
                                ; MULTIPLY *16 FOR HIGH RES
07AD 8A D4          MOV    DL,AH          ;--- DETERMINE ALPHA CHAR POSITION
                                ; COLUMN VALUE FOR RETURN
07AF 32 E4          XOR    AH,AH          ; CLEAT TO CONVERT TO WORD
                                ;
07B1 B6 04          MOV    DH,8/2        ; DIVISOR FOR ANK (8 ROW/ 2 SCAN)
07B3 80 3E 0049 R 10 CMP    CRT_MODE,KJ_MODE; KANJI MODE ?
07B8 72 02          JB     RLPEN10       ; NO
07BA B6 09          MOV    DH,CBOX_ROW/2 ; DIVISOR FOR KJ (18 ROW/ 2 SCAN)
RLPEN10:
07BC F6 F6          DIV    DH              ; DIVIDE FOR CHAR POSITION
07BE 8A F0          MOV    DH,AL          ; SET ROW POSITION TO DH
                                ;
07C0 EB 21          JMP    SHORT RLPEN13 ; LIGHT_PEN_RETURN_SET
RLPEN11:
07C2              RLPEN11:
07C2 F6 36 004A R   DIV    BYTE PTR CRT_COLS;--- ALPHA MODE ON LIGHT PEN
07C6 8A F0          MOV    DH,AL          ; DETERMINE ROW,COLUMN VALUE
07C8 8A D4          MOV    DL,AH          ; ROWS TO DH
                                ; COLS TO DL
07CA 8A E8          MOV    CH,AL          ; SET AL*1 TO CH
07CC D2 E0          SAL    AL,CL          ; MULTIPLY ROWS * 8
07CE 80 3E 0049 R 10 CMP    CRT_MODE,KJ_MODE; KANJI MODE ?
07D3 72 06          JB     RLPEN12       ; NO
                                ;--- MULTIPLY ROWS * 18
07D5 D0 E0          SAL    AL,1          ; MULTIPLY ROWS * 16
07D7 D0 E5          SAL    CH,1          ; CH HAS CL*2
07D9 02 C5          ADD    AL,CH          ; AL HAS CL*18
RLPEN12:
07DB 8A E8          MOV    CH,AL          ; GET RASTER VALUE TO RETURN REQ
07DD 8A DC          MOV    BL,AH          ; COLUMN VALUE
07DF 32 FF          XOR    BH,BH          ; TO BX
07E1 D3 E3          SAL    BX,CL          ;
RLPEN13:
07E3 B4 01          MOV    AH,1          ; LIGHT_PEN_RETURN_SET
07E5              RLPEN14:
07E5 52          PUSH DX              ; INDICATE EVERYTHING SET
07E6 8B 16 0063 R   MOV    DX,ADDR_6845 ; LIGHT_PEN_RETURN
07EA 83 C2 07       ADD    DX,7           ; SAVE RETURN VALUE (IN CASE)
                                ; GET BASE ADDRESS
                                ; POINT TO RESET PARM

```



```

07ED EE          OUT    DX,AL          ; ADDRESS, NOT DATA, IS IMPORTANT
07EE 5A          POP     DX              ; RECOVER VALUE
07EF             RLPEN15:          ; RETURN_MO_RESET
07EF 8B EC       MOV     BP,SP        ; SET FRAME POINTER
07F1 89 5E 08    MOV     [BP+F_BX],BX    ; SET RETURN BX
07F4 89 4E 0A    MOV     [BP+F_CX],CX    ; SET RETURN CX
07F7 89 56 0C    MOV     [BP+F_DX],DX    ; SET RETURN DX

07FA E8 1DD6 R   CALL    ENABLE_VG2       ; ENABLE VIDEO GENERATER 2

07FD 5D          POP     BP              ; RESTORE BP
07FE C3          RET

07FF             READ_LPEN      ENDP
;-----
;
; ACT_DISP_PAGE          INT 10H, AH = 5
;
; THIS ROUTINE SETS THE ACTIVE DISPLAY PAGE, ALLOWING
; THE FULL USE OF THE RAM SET ASIDE FOR THE VIDEO ATTACHMENT
;
; INPUT  AH =    CRT MODE (MASKED)
;        AL =    NEW ACTIVE DISPLAY PAGE
;
; OUTPUT NONE
;
; THE 6845 IS RESET TO DISPLAY THAT PAGE
;-----
ASSUME CS:CODE, DS:DATA

07FF             ACT_DISP_PAGE  PROC    NEAR
07FF 55          PUSH    BP              ; SAVE BP
0800 A8 80       TEST    AL,080H          ; CRT/CPU PAGE REG FUNCTION
0802 75 2A       JNZ     SET_CRTCPU      ; YES, GO HANDLE IT

0804 80 FC 04    CMP     AH,GRAPHICS     ; GRAPHICS MODE ?
0807 73 23       JAE     ACTDP1          ; YES, SKIP

0809 50          PUSH    AX              ; SAVE CRT MODE

080A A2 0062 R   MOV     ACTIVE_PAGE,AL  ; SAVE ACTIVE PAGE VALUE
080D 8B 0E 004C R MOV     CX,CRT_LEN      ; GET SAVED LENGTH OF REGEN BUFFER
0811 98          CBW     ; CONVERT AL TO WORD
0812 50          PUSH    AX              ; SAVE PAGE VALUE

0813 F7 E1       MUL     CX              ; DISPLAY PAGE TIMES REGEN LENGTH
0815 A3 004E R   MOV     CRT_START,AX    ; SAVE START ADDRESS FOR LATER USE
0818 8B C8       MOV     CX,AX           ; START ADDRESS TO CX
081A D1 F9       SAR     CX,1            ; DIVIDE BY 2 FOR 6845 HANDLING
081C 84 0C       MOV     AH,12           ; 6845 REGISTER FOR START ADDRESS
081E E8 0602 R   CALL    OUT6845

0821 5B          POP     BX              ; RECOVER PAGE VALUE
0822 D1 E3       SAL     BX,1           ; *2 FOR WORD OFFSET
0824 8B 87 035C R MOV     AX,[BX + OFFSET CURSOR_POSN] ; GET CURSOR FOR THIS PAGE
0828 E8 06C4 R   CALL    SET_CURSOR     ; SET THE CURSOR POSITION

082B 58          POP     AX              ; RESTORE CRT MODE
082C             ACTDP1:          ;
082C 5D          POP     BP              ; RESTORE BP
082D C3          RET

082E             ACT_DISP_PAGE  ENDP
;-----
;
; SET_CRTCPU
;
; THIS ROUTINE READS OR WRITES THE CRT/CPU PAGE REGISTERS
;
; INPUT  AH =    CRT MODE (MASKED)
;        AL = 83H = SET BOTH CRT AND CPU PAGE REGS
;             BH = VALUE TO SET IN CRT PAGE REG
;             BL = VALUE TO SET IN CPU PAGE REG
;             CL = CRT MODE FOR CPU PAGE
;        AL = 82H = SET CRT PAGE REG
;             BH = VALUE TO SET IN CRT PAGE REG
;        AL = 81H = SET CPU PAGE REG
;             BL = VALUE TO SET IN CPU PAGE REG
;             CL = CRT MODE FOR CPU PAGE
;        AL = 80H = READ CURRENT VALUE OF CRT/CPU PAGE REGS
;
; OUTPUT ALL FUNCTIONS RETURN
;        BH = CURRENT CONTENTS OF CRT PAGE REG
;        BL = CURRENT CONTENTS OF CPU PAGE REG
;-----

082E             SET_CRTCPU    PROC    NEAR
082E FF 36 0355 R PUSH    WORD PTR IEP_CTRL ; SAVE INTERRUPT ENABLE PROHIBIT FLAG
0832 80 0E 0355 R OR     BYTE PTR IEP_CTRL,TRUE ; PROHIBIT INTERRUPT ENABLE
0837 E8 1BE4 R   CALL    DISABLE_INT     ; DISABLE ALL HARDWARE INTERRUPT

083A 80 FC 04    CMP     AH,GRAPHICS     ; GRAPHICS MODE ?
083D 73 10       JAE     SETCC00        ; YES

083F 50          PUSH    AX              ; SAVE AX
0840 51          PUSH    CX              ; SAVE CX

```

Appendix A.

```

0841 88 0E 0060 R      MOV    CX,CURSOR_MODE ; GET CURSOR MODE OF TEXT
0845 80 CD 20          OR     CH,CURSOR_DISABLE; DISABLE CURSOR
0848 B4 9A            MOV    AH,10          ; SET 6845 CURSOR REGISTER
084A E8 0602 R        CALL   OUT6845        ; OUT DATA TO 6845
084D 59              POP    CX             ; RESTORE CX
084E 58              POP    AX             ; RESTORE AX
084F E8 0940 R        SETCC00: CALL   ERASE_SCURSOR  ; ERASE SOFTWARE CURSOR
0852 80 3E 0049 R 10  CMP    CRT_MODE,KJ_MODE; KANJI MODE ?
0857 72 07          JB     SCC0           ; NO
0859 50              PUSH   AX             ;
085A B8 853F        MOV    AX,KKH_TERM   ; TERMINATE KANA-KAN
085D CD 16          INT    KEYBOARD      ;
085F 58              POP    AX             ;
0860 E8 1DB0 R        SCC0:  CALL   ENABLE_VG1   ;----- WAIT VERTICAL RETRACE
0863 8A E0          MOV    AH,AL         ; ENABLE VIDEO GENERATER 1
0865 BA 03DA        MOV    DX,VGA_CTL    ; SAVE REQUEST IN AH
0868 EC          SCC1:  IN     AL,DX         ; SET ADDRESS OF GATE ARRAY
0869 24 08        AND    AL,VERTRET    ; GET STATUS
086B 74 FB        JZ     SCC1          ; VERTICAL RETRACE?
                        ; NO, WAIT FOR IT
086D 80 FB 08      CMP    BL,VRAM2_PAGE ;----- SELECT VIDED RAM 1 OR 2
0870 73 08          JAE   SCC2          ; VIDEO RAM 2 ?
                        ; YES
0872 BA 03DF        MOV    DX,PAGREG     ; SET IO ADDRESS OF PAGE REG
0875 A0 008A R      MOV    AL,PAGDAT     ; GET DATA LAST OUTPUT TO REG
0878 EB 06          JMP    SHORT SCC3
087A E8 03D9        SCC2:  MOV    DX,PAGREG2   ; SET IO ADDRESS OF PAGE REG
087D A0 033C R      MOV    AL,PAGDAT2   ; GET DATA LAST OUTPUT TO REG
0880 E8 80          SCC3:
0883 80 FC 80      CMP    AH,80H       ;----- CHECK FUNCTION
0885 74 3E          JZ     SCC32        ; READ FUNCTION REQUESTED?
0888 80 FC 84      CMP    AH,84H       ; YES, DON'T SET ANYTHING
088A F6 C4 01     JNC    SCC32        ; VALID REQUEST?
088D 74 39          TEST   AH,1         ; NO, PRETEND IT WAS A READ REQUEST
088F 53              JZ     SCC5          ; SET CPU REG?
0890 88 0E 0049 R  PUSH   BX           ; NO, GO SEE ABOUT CRT REG
0894 8A F9        MOV    CRT_MODE,CL  ; SAVE PAGE NO.
0896 8A D9        MOV    BH,CL        ; SET MODE
0898 80 E3 0F     MOV    BL,CL        ; SET UNMASKED MODE TO BH
089B E8 059C R    AND    BL,KJ_OFF    ; SET MASKED MODE TO BL
089E 58          CALL   SMODE_SET    ; SET SOFTWARE MODE
089F 58          POP    BX
089F 88 1E 034C R  MOV    CPU_PAGE,BL  ; SET NEW CPU PAGE
08A3 E8 1B88 R    CALL   ENABLE_KJROM ; DISABLE BOTH VRAM1 AND VRAM2 ONCE
08A6 E8 1B84 R    CALL   ENABLE_VRAM ; SELECT V-RAM ACCORDING TO CPU PAGE
08A9 D0 E3        SHL    BL,1         ;-- NEW CPU PAGE IS IN CURRENT V-RAM
08AB D0 E3        SHL    BL,1         ; SHIFT VALUE TO RIGHT BIT POSITION
08AD D0 E3        SHL    BL,1
08AF 24 C7        AND    AL,NOT CPUREG ; CLEAR OLD CPU VALUE
08B1 80 E3 38     AND    BL,CPUREG    ; BE SURE UNRELATED BITS ARE ZERO
08B4 0A C3        OR     AL,BI         ; OR IN NEW VALUE
08B6 EE          OUT    DX,AL        ; SET NEW VALUE
08B7 80 3E 034C R 08  CMP    CPU_PAGE,VRAM2_PAGE ; VIDEO RAM 2 ?
08BC 73 07          JAE   SCC4          ; YES
08BE A2 008A R      MOV    PAGDAT,AL    ; SAVE COPY IN RAM
08C1 EB 05          JMP    SHORT SCC5
08C3 E8 43          SCC32: JMP    SHORT SCC11  ; INTERMEDIATE POINT TO JUMP
08C5 A2 033C R      SCC4:  MOV    PAGDAT2,AL   ; SAVE COPY IN RAM
08C8 F6 C4 02     TEST   AH,2         ; SET CRT REG?
08CB 74 38          JZ     SCC11        ; NO, GO RETURN CURRENT SETTINGS
08CD 88 3E 034D R  MOV    CRT_PAGE,BH  ;----- SET CRT PAGE
08D1 80 FF 08      CMP    BH,VRAM2_PAGE ; SAVE CRT PAGE TO RAM
08D4 73 08          JAE   SCC7          ; VIDEO RAM 2 ?
08D6 BA 03DF        MOV    DX,PAGREG     ;
08D9 A0 008A R      MOV    AL,PAGDAT     ;
08DC EB 06          JMP    SHORT SCC8
08DE 8A 03D9        SCC7:  MOV    DX,PAGREG2   ;
08E1 A0 033C R      MOV    AL,PAGDAT2   ;
08E4 24 F8        SCC8:  AND    AL,NOT CRTREG ; CLEAR OLD CRT VALUE
08E6 80 E7 07     AND    BH,CRTREG    ; BE SURE UNRELATED BITS ARE ZERO
08E9 0A C7        OR     AL,BH         ; OR IN NEW VALUE
08EB EE          OUT    DX,AL        ; SET NEW VALUES
08EC 8A 26 0347 R  MOV    AH,SUPIPCR   ; GET LAST DATA OF SUPERIMPOSE CONTROL REG.

```

```

08F0 80 3E 034D R 08      CMP     CRT_PAGE,VRAM2_PAGE ; VIDEO RAM 2 ?
08F5 73 08                JAE     SCC9                  ; YES
08F7 A2 008A R           MOV     PAGDAT,AL            ;
08FA 80 E4 FE           AND     AH,NOT FOREVRAM ; SET V-RAM AS BACKGROUND
08FD EB 06                JMP     SHORT SCC10
08FF                    SCC9:  MOV     PAGDAT2,AL          ;
08FF A2 033C R           OR      AH,FOREVRAM         ; SET V-RAM AS FOREGROUND
0902 80 CC 01           SCC10: CALL    SET_SUPREG        ; SET AH TO SUPERIMPOSE REGISTER
0905                    SCC11: MOV     BH,CRT_PAGE         ; GET CURRENT CRT PAGE
0908 8A 3E 034D R       MOV     BL,CPU_PAGE         ; GET CURRENT CPU PAGE
090C 8A 1E 034C R       CALL    ENABLE_VG2          ; ENABLE VIDEO GENERATER 2
0910 E8 1DD6 R           MOV     AH,3                ; GET CURSOR TYPE
0913 B4 03                INT     VIDEO               ;
0915 CD 10                MOV     AH,1                ; SET CURSOR TYPE
0917 B4 01                INT     VIDEO               ;
0919 CD 10                SCC12: POP     WORD PTR IEP_CTRL;RESTORE INTERRUPT ENABLE PROHIBIT FLAG
091B                    CALL    ENABLE_INT          ; ENABLE INTERRUPT IF IT IS NOT PROHIBITED
0918 8F 06 0355 R       MOV     BP,SP               ; SET FRAME POINTER
091F E8 1BEC R       MOV     [BP+F_BX],BX        ; SET RETURN BX
0922 8B EC                POP     BP                  ; RESTORE BP
0924 89 5E 08                RET
0927 5D                    POP     BP
0928 C3                    RET
0929                    SET_CRTCPU  ENDP

```

```

;-----
;
; SET_SUPREG          SET SUPERIMPOSE REGISTER
;-----
;
; THIS ROUTINE SET SUPERIMPOSE REGISTER
;
; INPUT              AH = VALUE TO SET
;                   DS = DATA SEGMENT
;
; OUTPUT             NONE
; VOLATILE           AL,BX,DX
;-----

```

```

0929                    SET_SUPREG  PROC   NEAR
0929 8A DC                MOV     BL,AH              ; SET OUT DATA TO BL
092B F6 C3 02           TEST    BL,TRANSON         ; SET SUPERIMPOSE ON ?
092E 75 03                JNE     SUPREG1           ; YES
0930 80 E3 01           AND     BL,FOREVRAM        ; MASK MODE CONTROL BITS FOR SUPERIMPOSE OFF
0933                    SUPREG1: MOV     BH,PCSUPER         ; SET SUPERIMPOSE CONTROL REG
0935 8A 03DA           MOV     DX,SZABASE         ; GET ADDRESS OF SX-02A
0938 E8 1E22 R           CALL    OUT_GA             ; OUT BX TO GATE ARRAY
093B 88 26 0347 R       MOV     SUPIPCR,AH         ; SAVE NEW VALUE TO RAM
093F C3                    RET
0940                    SET_SUPREG  ENDP

```

```

;-----
;
; ERASE_SCURSOR      ERASE SOFTWARE CURSOR
;-----
;
; THIS ROUTINE ERASE SOFTWARE CURSOR IF IT PRESENTS
;
; INPUT              AH = CRT MODE (MASKED)
;                   DS = DATA SEGMENT
;
; OUTPUT             NONE
; VOLATILE           NONE
;-----

```

```

0940                    ERASE_SCURSOR  PROC   NEAR
0940 51                    PUSH    CX                  ; SAVE CX
0941 52                    PUSH    DX                  ; SAVE DX
0942 80 FC 04           CMP     AH,GRAPHICS        ; GRAPHICS MODE ?
0945 72 17                JB      ERACRS1            ; NO.
0947 F6 06 0349 R FF   TEST    GC_PRESENT,TRUE    ; GRAPHICS CURSOR PRESENT ?
094C 74 10                JZ      ERACRS1            ; NO
094E 8B 0E 034E R       MOV     CX,GCURSOR_MODE    ; SET CURSOR MODE
0952 8B 16 035C R       MOV     DX,CURSOR_POSM     ; SET CURSOR POSITION IN GRAPHICS
0956 E8 0618 R           CALL    WRITE_GCURSOR      ; WRITE GRAPHICS CURSOR
0959 C6 06 0349 R 00   MOV     GC_PRESENT,FALSE    ; FLAG OFF
095E                    ERACRS1: POP     DX                ; RESTORE DX
095E 5A                    POP     CX                  ; RESTORE CX
095F 59                    RET
0960 C3

```

Appendix A.

0961

ERASE\_SCURSOR ENDP

```

-----
;
; APPEAR_SCURSOR APPEAR SOFTWARE CURSOR
;
; THIS ROUTINE APPEAR SOFTWARE CURSOR
;
; INPUT AH = CRT MODE
; DS = DATA SEGMENT
;
; OUTPUT NONE
;
; VOLATILE NONE
;
-----

```

0961

APPEAR\_SCURSOR PROC NEAR

```

0961 51 PUSH CX ; SAVE REGISTERS
0962 52 PUSH DX ;
;
0963 80 FC 04 CMP AH,GRAPHICS ; GRAPHICS MODE ?
0966 72 10 JB APPCRS1 ; NO,
;
0968 88 0E 034E R MOV CX,CURSOR_MODE ; SET CURSOR MODE
096C 88 16 035C R MOV DX,CURSOR_POSM ; SET CURSOR POSITION IN GRAPHICS
0970 E8 0618 R CALL WRITE_GCURSOR ; WRITE GRAPHICS CURSOR
;
0973 C6 06 0349 R FF MOV GC_PRESENT,TRUE ; FLAG ON
0978 APPCRS1:
0978 5A POP DX ; RESTORE REGISTERS
0979 59 POP CX ;
;
097A C3 RET
;
097B

```

APPEAR\_SCURSOR ENDP

```

-----
;
; SCROLL_UP INT 10H, AH = 6
;
; THIS ROUTINE MOVES A BLOCK OF CHARACTERS UP
; ON THE SCREEN
;
; INPUT AH = CURRENT CRT MODE ( MASKED )
; AL = NUMBER OF ROWS TO SCROLL
; CX = ROW/COLUMN OF UPPER LEFT CORNER
; DX = ROW/COLUMN OF LOWER RIGHT CORNER
; BH = ATTRIBUTE TO BE USED ON BLANKED LINE
; DS = DATA SEGMENT
; ES = REGEN BUFFER SEGMENT
;
; OUTPUT NONE -- THE REGEN BUFFER IS MODIFIED
;
; WORK [BP+SUC_MODE] = CURRENT CRT MODE ( MASKED )
; [BP+SU_ULR] = ROW OF UPPER LEFT CORNER
; [BP+SU_TSR] = TOP OF SOURCE ROW
; [BP+SU_ES1] = SEGMENT OF V-RAM1
;
-----

```

ASSUME CS:CODE, DS:DATA, ES:VIDEO\_RAM

097B

SCROLL\_UP PROC NEAR

```

097B 55 PUSH BP ; SAVE BP
097C 83 EC 06 SUB SP,SUP_LOCAL ; ALLOCATE LOCAL WORK AREA
097F 8B EC MOV BP,SP ; SET BP AS FRAME POINTER
;
0981 50 PUSH AX ; SAVE CRT MODE
0982 1E PUSH DS ; SAVE DS
0983 E8 0940 R CALL ERASE_SCURSOR ; ERASE SOFTWARE CURSOR
;
0986 8A D8 MOV BL,AL ; SAVE LINE COUNT IN BL
0988 80 E7 77 AND BH,HAN_MASK ; STRIP KANJI BITS OFF FOR SPACE CODE
;
098B 88 66 00 MOV BYTE PTR [BP+SUC_MODE],AH ; SET MASKED CRT MODE
;
098E 80 FC 04 CMP AH,GRAPHICS ; TEST FOR GRAPHICS MODE
0991 73 05 JAE SCRUP1 ; YES, HANDLE SEPARATELY
;
0993 E8 09C9 R CALL TEXT_UP ; SCROLL TEXT UP
0996 EB 27 JMP SHORT SCRUP3 ; GO TO END
;
0998 SCRUP1:
0998 80 3E 0049 R 10 CMP CRT_MODE,KJ_MODE; KJ GRAPHICS MODE ?
099D 72 10 JB SCRUP2 ; NO
;
099F 88 6E 01 MOV BYTE PTR [BP+SU_ULR],CH ; SET UPPER LEFT ROW POSITION
09A2 88 6E 02 MOV BYTE PTR [BP+SU_TSR],CH ;
09A5 00 46 02 ADD BYTE PTR [BP+SU_TSR],AL ; SET TOP OF SOURCE ROW POSITION
;
09A8 50 PUSH AX ;
09A9 53 PUSH BX ;
09AA 51 PUSH CX ;
09AB 52 PUSH DX ; SAVE REGISTERS
09AC 1E PUSH DS ;
09AD 06 PUSH ES ;
;
09AE 88 00A0 MOV AX,P_CODE_START ; SET PESUDO CODE BUFFER SEGMENT
09B1 AE C0 MOV ES,AX ; TO ES
;
09B3 E8 09C9 R CALL TEXT_UP ; SCROLL TEXT UP
;

```

```

0986 07          POP     ES          ;
0987 1F          POP     DS          ;
0988 5A          POP     DX          ;
0989 59          POP     CX          ; RESTORE REGISTERS
098A 5B          POP     BX          ;
098B 58          POP     AX          ;
098C             SCRUP2: CALL    GRAPHICS_UP ; SCROLL IN GRAPHICS MODE
098C E8 0A2B R   SCRUP3:
098F             ASSUME DS: DATA
098F 1F          POP     DS          ; RESTORE DS
09C0 58          POP     AX          ; RESTORE CRT MODE
09C1 E8 0961 R   CALL    APPEAR_SCURSOR ; WRITES SOFTWARE CURSOR

09C4 83 C4 06   ADD     SP,SUP_LOCAL ; DEALLOCATE LOCAL WORK AREA
09C7 5D          POP     BP          ; RESTORE BP
09C8 C3          RET              ; RETURN TO CALLER

09C9             SCROLL_UP   ENDP

```

```

-----
;
; TEXT_UP
;
; THIS ROUTINE MOVES A BLOCK OF CHARACTERS UP
; ON THE TEXT SCREEN
;
; INPUT  BH = ATTRIBUTE TO BE USED ON BLANKED LINE
;        BL = NUMBER OF ROWS TO SCROLL
;        CX = ROW/COLUMN OF UPPER LEFT CORNER
;        DX = ROW/COLUMN OF LOWER RIGHT CORNER
;        DS = DATA SEGMENT
;        ES = REGEN BUFFER SEGMENT
;
; OUTPUT NONE
;
; VOLATILE      AX,BL,CX,DX,SI,DI,DS
;
-----

```

```

09C9             TEXT_UP PROC    NEAR
09C9 55          PUSH    BP          ; SAVE BP
09CA 53          PUSH    BX          ; SAVE FILL ATTRIBUTE IN BH

09CB 8B C1       MOV     AX,CX          ; UPPER LEFT POSITION
09CD E8 09F5 R   CALL    SCROLL_POSITION ; DO SETUP FOR SCROLL
09D0 74 1F       JZ     TUP4          ; BLANK_FIELD
;                ASSUME DS:VIDEO_RAM

09D2 03 F0       ADD     SI,AX          ; FROM ADDRESS
09D4 8A E6       MOV     AH,DH          ; # ROWS IN BLOCK
09D6 2A E3       SUB     AH,BL          ; # ROWS TO BE MOVED

TUP1:          CALL    MOVE_ROW       ; MOVE ONE ROW
09D8 E8 0A1B R   ADD     SI,F5          ;
09DB 03 F5       ADD     DI,BP          ; POINT TO NEXT LINE IN BLOCK
09DD 03 FD       ADD     AH            ; COUNT OF LINES TO MOVE
09DF FE CC       DEC     AH            ;
09E1 75 F5       JNZ    TUP1          ; ROW_LOOP

TUP2:          POP     AX          ; RECOVER ATTRIBUTE IN AH
09E3 58          MOV     AL,' '         ; FILL WITH BLANKS

TUP3:          CALL    CLEAR_ROW      ; CLEAR THE ROW
09E4 80 20       ADD     DI,BP          ; POINT TO NEXT LINE
09E6 E8 0A24 R   DEC     BL            ; COUNTER OF LINES TO SCROLL
09E9 03 FD       JNZ    TUP3          ; CLEAR_LOOP

09EF 5D          POP     BP          ; RESTORE BP
09F0 C3          RET              ; RETURN TO CALLER

TUP4:          MOV     BL,DH          ; GET ROW COUNT
09F1 8A DE       JMP     TUP2          ; GO CLEAR THAT AREA
09F3 EB EE

09F5             TEXT_UP ENDP

```

```

-----
;
; SCROLL_POSITION
;
; THIS ROUTINE SET UP SOME PARAMETERS FOR SCROLL FUNCTION
;
; INPUT  AX = ROW/COLUMN
;        BL = SCROLL ROW NUMBER
;
; OUTPUT AX = BL * CRT_COLS * 2
;        CH = 0
;        DX = DIFFERENCE OF ROW/COLUMN
;        BP = CRT_COLS * 2
;        DI,SI = REGEN ADDRESS CORRESPONDING TO ROW/COLUMN
;        FLAG = CONTENT OF BL
;        DS = ES
;
;
; ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM

```

Appendix A.

```

09F5          SCROLL_POSITION PROC    NEAR
09F5 E8 04D5 R      CALL    POSITION          ; CONVERT TO REGEN POINTER
09F8 03 06 004E R  ADD     AX,CRT_START     ; OFFSET OF ACTIVE PAGE
09FC 8B F8         MOV     DI,AX            ; TO ADDRESS FOR SCROLL
09FE 8B F0         MOV     SI,AX            ; FROM ADDRESS FOR SCROLL

0A00 2B D1         SUB     DX,CX            ; DX = #ROWS, #COLS IN BLOCK
0A02 FE C6         INC     DH              ;
0A04 FE C2         INC     DL              ; INCREMENT FOR 0 ORIGIN
0A06 32 ED         XOR     CH,CH           ; SET HIGH BYTE OF COUNT TO ZERO

0A08 8B 2E 004A R  MOV     BP,CRT_COLS     ; GET NUMBER OF COLUMNS IN DISPLAY
0A0C 03 ED         ADD     BP,BP           ; TIMES 2 FOR ATTRIBUTE BYTE
0A0E 8A C3         MOV     AL,BL           ; GET LINE COUNT
0A10 F6 26 004A R  MUL     BYTE PTR CRT_COLS ; DETERMINE OFFSET TO FROM ADDRESS
0A14 03 C0         ADD     AX,AX           ; *2 FOR ATTRIBUTE BYTE

0A16 06           PUSH   ES              ; ESTABLISH ADDRESSING TO REGEN BUFFER
0A17 1F           POP    DS              ; FOR BOTH POINTERS

0A18 0A DB         OR     BL,BL           ; 0 SCROLL MEANS BLANK FIELD
0A1A C3           RET                    ; RETURN WITH FLAGS SET

0A1B          SCROLL_POSITION ENDP

;-----
;
;          MOVE_ROW
;
;          THIS ROUTINE MOVES ONE ROW IN TEXT MODE
;
;          INPUT  DL = NUMBER OF CHARACTERS TO MOVE
;                DS:SI = SOURCE TOP ADDRESS
;                ES:DI = DESTINATION TOP ADDRESS
;
;          OUTPUT NOTHING
;
;          VOLATILE      CL
;-----
          ASSUME  CS:CODE, DS:VIDEO_RAM, ES:VIDEO_RAM

0A1B          MOVE_ROW PROC    NEAR
0A1B 8A CA         MOV     CL,DL           ; GET # OF COLS TO MOVE
0A1D 56           PUSH   SI              ;
0A1E 57           PUSH   DI              ; SAVE START ADDRESS
0A1F F3/ A5        REP     MOVSW          ; MOVE THAT LINE ON SCREEN
0A21 5F           POP    DI              ;
0A22 5E           POP    SI              ; RECOVER ADDRESSES
0A23 C3           RET

0A24          MOVE_ROW ENDP

;-----
;
;          CLEAR_ROW
;
;          THIS ROUTINE MOVES ONE ROW IN TEXT MODE
;
;          INPUT  AX = ATTRIBUTE/CHARACTER TO FILL
;                ES:DI = DESTINATION TOP ADDRESS
;
;          OUTPUT NOTHING
;
;          VOLATILE      CL
;-----
0A24          CLEAR_ROW      PROC NEAR
0A24 8A CA         MOV     CL,DL           ; GET # COLUMNS TO CLEAR
0A26 57           PUSH   DI              ;
0A27 F3/ AB        REP     STOSW          ; STORE THE FILL CHARACTER
0A29 5F           POP    DI              ;
0A2A C3           RET

0A2B          CLEAR_ROW      ENDP

;-----
;
;          SCROLL UP (GRAPHICS)
;
;          THIS ROUTINE SCROLLS UP THE INFORMATION ON THE CRT
;
;          INPUT  [BP+SUC_MODE] = CURRENT CRT MODE ( MASKED )
;                [BP+SU_ULR] = ROW OF UPPER LEFT CORNER
;                [BP+SU_TSR] = TOP OF SOURCE ROW
;                CH,CL = UPPER LEFT CORNER OF REGION TO SCROLL
;                DH,DL = LOWER RIGHT CORNER OF REGION TO SCROLL
;                BOTH OF THE ABOVE ARE IN CHARACTER POSITIONS
;
;                BH = FILL VALUE FOR BLANKED LINES
;                AL = # LINES TO SCROLL (AL=0 MEANS BLANK THE ENTIRE FIELD)
;                DS = DATA SEGMENT
;                ES = REGEN SEGMENT
;
;          OUTPUT NOTHING, THE SCREEN IS SCROLLED
;
;          VOLATILE      AX,BL,CX,DX,SI,DI,DS
;-----
          ASSUME  CS:CODE, DS:DATA, ES:VIDEO_RAM

```



Appendix A.

```

0AC7
0AC7 3C 14
0AC9 72 14

0ACB F6 46 01 01
0ACF 74 04
0AD1 81 C7 3FB0
0AD5
0AD5 F6 46 02 01
0AD9 74 04
0ADB 81 C6 3FB0
0ADF

0ADF 80 7E 00 0B
0AE3 75 11

0AE5 57
0AE6 56
0AE7 1E
0AE8 06

0AE9 8E 5E 04
0AEC 8E 46 04
0AEF E8 0D63 R
0AF2 07
0AF3 1F
0AF4 5E
0AF5 5F

0AF6

0AF6 E8 0D63 R
0AF9 81 C6 2000
0AFD 81 FE 8000
0B01 72 04

0B03 81 EE 7F60
0B07
0B07 81 C7 2000
0B0B 81 FF 8000
0B0F 72 04

0B11 81 EF 7F60
0B15
0B15 FE CC
0B17 75 C6

0B19
0B19 8A C7
0B1B

0B1B 80 7E 00 0B
0B1F 75 0A

0B21 57
0B22 06

0B23 8E 46 04
0B26 E8 0D7C R

0B29 07
0B2A 5F

0B2B

0B2B E8 0D7C R
0B2E 80 7E 00 09
0B32 72 33

0B34 FE CB
0B36 74 37

0B38 81 C7 2000
0B3C 81 FF 8000
0B40 72 04

0B42 81 EF 7F60
0B46

0B46 80 7E 00 0B
0B4A 75 0A
0B4C 57
0B4D 06

0B4E 8E 46 04
0B51 E8 0D7C R

0B54 07
0B55 5F

0B56

0B56 E8 0D7C R
0B59 81 C7 3FB0
0B5D 81 FF 9FB0
0B61 72 04

0B63 81 EF 7F60
0B67

```

```

GRUP7:
CMP AL,KJGRAPH ;--- 32K REGEN
JB ; KJ GRAPHICS MODE ?
; NO

TEST BYTE PTR [BP+SU_ULR],1 ; ODD ROW ?
JZ GRUP8 ; NO
ADD DI,4000H-80 ; ADJUST POINTER

GRUP8:
TEST BYTE PTR [BP+SU_TSR],1 ; ODD ROW ?
JZ GRUP9 ; NO
ADD SI,4000H-80 ; ADJUST POINTER

GRUP9:
CMP BYTE PTR [BP+SUC_MODE],0BH ; 640 X 200 X 16 COLOR ?
JNE GRUP91 ; NO
; --- SCROLL V-RAM 1
PUSH DI ;
PUSH SI ; SAVE REGISTERS
PUSH DS ;
PUSH ES ;

MOV DS,[BP+SU_ES1] ; GET SEGMENT OF V-RAM 1
MOV ES,[BP+SU_ES1] ;
CALL G_MOVE_ROW ; MOVE ROW
POP ES ;
POP DS ; RESTORE REGISTERS
POP SI ;
POP DI ;

GRUP91:
CALL G_MOVE_ROW ; MOVE ONE ROW

ADD SI,2000H ; NEXT ROW
CMP SI,8000H ; IN 32K REGEN ?
JB GRUP10 ; YES

GRUP10:
SUB SI,8000H-160 ; NO, WRAP

ADD DI,2000H ; NEXT ROW
CMP DI,8000H ; IN 32K REGEN ?
JB GRUP11 ; YES

GRUP11:
SUB DI,8000H-160 ; NO, WRAP

DEC AH ; NUMBER OF ROWS TO MOVE
JNZ GRUP9 ; CONTINUE TILL ALL MOVED

GRUP12:
; --- FILL IN THE VACATED LINE(S)
MOV AL,BH ; CLEAR_ENTRY
; ATTRIBUTE TO FILL WITH

GRUP13:
CMP BYTE PTR [BP+SUC_MODE],0BH ; 640 X 200 X 16 COLOR ?
JNE GRUP131 ; NO
; SAVE REGISTERS
PUSH DI ;
PUSH ES ;

MOV ES,[BP+SU_ES1] ; GET SEGMENT OF V-RAM 1
CALL G_CLEAR_ROW ; CLEAR ROW
POP ES ; RESTORE REGISTERS
POP DI ;

GRUP131:
CALL G_CLEAR_ROW ; CLEAR THAT ROW

CMP BYTE PTR [BP+SUC_MODE],9 ; MODE USES 32K REGEN?
JC GRUP15 ; NO, JUMP

DEC BL ; ADJUST COUNT
JZ GRUP16 ; IF ZERO, THEN DONE

ADD DI,2000H
CMP DI,8000H ; IN 32K REGEN RANGE ?
JB GRUP14 ; YES

GRUP14:
SUB DI,8000H-160 ; ADJUST POINTER

CMP BYTE PTR [BP+SUC_MODE],0BH ; 640 X 200 X 16 COLOR ?
JNE GRUP141 ; NO
; SAVE REGISTERS
PUSH DI ;
PUSH ES ;

MOV ES,[BP+SU_ES1] ; GET SEGMENT OF V-RAM 1
CALL G_CLEAR_ROW ; CLEAR ROW
POP ES ; RESTORE REGISTERS
POP DI ;

GRUP141:
CALL G_CLEAR_ROW ; CLEAR 2 MORE ROWS

ADD DI,2000H + (2000H-80)
CMP DI,8000H + (2000H-80) ; IN 32K REGEN RANGE ?
JB GRUP15 ; YES

GRUP15:
SUB DI,8000H-160 ; BACK UP POINTERS

```



0B67 81 EF 1FB0  
 0B68 FE CB  
 0B6D 75 AC  
 0B6F C3  
 0B70

SUB DI,2000H-80 ; POINT TO NEXT LINE  
 DEC BL ; NUMBER OF LINES TO FILL  
 JNZ GRUP13 ; CLEAR\_LOOP  
 GRUP16: RET ; EVERYTHING DONE

GRAPHICS\_UP ENDP

```

;-----
;
; SCROLL_DOWN INT 10H, AH = 7
;
; THIS ROUTINE MOVES THE CHARACTERS WITHIN A DEFINED
; BLOCK DOWN ON THE SCREEN, FILLING THE TOP LINES
; WITH A DEFINED CHARACTER
;
; INPUT AH = CURRENT CRT MODE ( MASKED )
; AL = NUMBER OF LINES TO SCROLL
; CX = UPPER LEFT CORNER OF REGION
; DX = LOWER RIGHT CORNER OF REGION
; BH = ATTRIBUTE TO BE USED ON BLANKED LINE
; DS = DATA SEGMENT
; ES = REGEN SEGMENT
;
; OUPUT NONE -- SCREEN IS SCROLLED
;
; WORK [BP+SDC_MODE] = CURRENT CRT MODE ( MASKED )
; [BP+SD_LRR] = LOWER RIGHT ROW POSITION
; [BP+SD_BSR] = BOTTOM OF SOURCE ROW POSITION
; [BP+SD_ES1] = SEGMENT OF V-RAM 1
;-----

```

ASSUME CS:CODE, DS:DATA, ES:VIDEO\_RAM

```

0B70 SCROLL_DOWN PROC NEAR
0B70 55 PUSH BP ; SAVE BP
0B71 83 EC 06 SUB SP,SDN_LOCAL ; ALLOCATE 2 BYTE FOR LOCAL WORK AREA
0B74 8B EC MOV BP,SP ; SET BP AS FRAME POINTER

0B76 50 PUSH AX ; SAVE CRT MODE
0B77 1E PUSH DS ; SAVE DS
0B78 E8 0940 R CALL ERASE_CURSOR ; ERASE SOFTWARE CURSOR

0B7B FD STD ;
0B7C 8A D8 MOV BL,AL ; DIRECTION FOR SCROLL DOWN
0B7E 80 E7 77 AND BH,HAN_MASK ; STRIP KANJI BITS OFF FOR SPACE CODE

0B81 88 66 00 MOV BYTE PTR [BP+SDC_MODE],AH ; SET MASKED CRT MODE
0B84 80 FC 04 CMP AH,GRAPHICS ; TEST FOR GRAPHICS
0B87 73 05 JAE SCRDN1 ; YES, HANDLE SEPARATELY

0B89 E8 0BDF R CALL TEXT_DOWN ; SCROLL TEXT DOWN
0B8C EB 27 JMP SHORT SCRDN3 ; END

0B8E SCRDN1:
0B8E 80 3E 0049 R 14 CMP CRT_MODE,KJGRAPH ; KJ GRAPHICS MODE ?
0B93 72 1D JB SCRDN2 ; NO

0B95 88 76 01 MOV BYTE PTR [BP+SD_LRR],DH ; SAVE LOWER RIGHT ROW POSITION
0B98 88 76 02 MOV BYTE PTR [BP+SD_BSR],DH ;
0B9B 28 46 02 SUB BYTE PTR [BP+SD_BSR],AL ; SAVE BOTTOM OF SOURCE ROW POSITION

0B9E 50 PUSH AX ;
0B9F 53 PUSH BX ;
0BA0 51 PUSH CX ;
0BA1 52 PUSH DX ; SAVE REGISTERS
0BA2 1E PUSH DS ;
0BA3 06 PUSH ES ;

0BA4 B8 00A0 MOV AX,P_CODE_START ; SET PEDUDO CODE BUFFER
0BA7 8E C0 MOV ES,AX ; TO ES
; ASSUME ES:P_CODE_BUFFER

0BA9 E8 0BDF R CALL TEXT_DOWN ; SCROLL TEXT DOWN

0BAC 07 POP ES ;
0BAD 1F POP DS ;
0BAE 5A POP DX ;
0BAF 59 POP CX ;
0BB0 5B POP BX ; RESRORE REGISTERS
0BB1 56 POP AX ;
0BB2 SCRDN2:
0BB2 E8 0BEB R CALL GRAPHICS_DOWN ; SCROLL DOWN IN GRAPHICS MODE
0BB5 SCRDN3:
; ASSUME DS: DATA
0BB5 1F POP DS ; RESTORE DS
0BB6 58 POP AX ; RESTORE CRT MODE
0BB7 E8 0961 R CALL APPEAR_CURSOR ; WRITES SOFTWARE CURSOR

0BBA 83 C4 06 ADD SP,SDN_LOCAL ; DEALLOCATE LOCAL WORK AREA
0BBD 5D POP BP ; RESTORE BP
0BBE C3 RET ; SCROLL_END

0BBF SCROLL_DOWN ENDP

```

```

;-----
;
; TEXT_DOWN
;
; THIS ROUTINE MOVES THE CHARACTERS WITHIN A DEFINED
; BLOCK DOWN ON THE TEXT SCREEN, FILLING THE TOP LINES
; WITH A DEFINED CHARACTER
;-----

```

Appendix A.

```

;
; INPUT BH = ATTRIBUTE TO BE USED ON BLANKED LINE
; BL = NUMBER OF LINES TO SCROLL
; CX = UPPER LEFT CORNER OF REGION
; DX = LOWER LEFT CORNER OF REGION
; DS = DATA SEGMENT
; ES = REGEN SEGMENT
;
;
; OUTPUT NONE
;
; VOLATILE AX,BL,CX,DX,SI,DI,DS
;-----
0BBF TEXT_DOWN PROC NEAR
0BBF 55 PUSH BP ; SAVE BP
0BC0 53 PUSH BX ; SAVE ATTRIBUTE IN BH
0BC1 8B C2 MOV AX,DX ; LOWER RIGHT CORNER
0BC3 E8 09F5 R CALL SCROLL_POSITION ; GET REGEN LOCATION
0BC6 74 1F JZ TDOWN4 ASSUME DS:VIDEO_RAM
0BC8 2B F0 SUB SI,AX ; SI IS FROM ADDRESS
0BCA 8A E6 MOV AH,DH ; GET TOTAL # ROWS
0BCC 2A E3 SUB AH,BL ; COUNT TO MOVE IN SCROLL
0BCE E8 0A1B R TDOWN1: CALL MOVE_ROW ; MOVE ONE ROW
0BD1 2B F5 SUB SI,BP
0BD3 2B FD SUB DI,BP
0BD5 FE CC DEC AH
0BD7 75 F5 JNZ TDOWN1
0BD9 58 TDOWN2: POP AX ; RECOVER ATTRIBUTE IN AH
0BDA 8D 20 MOV AL,0
0BDC E8 0A24 R TDOWN3: CALL CLEAR_ROW ; CLEAR ONE ROW
0BDF 2B FD SUB DI,BP ; GO TO NEXT ROW
0BE1 FE C8 DEC BL
0BE3 75 F7 JNZ TDOWN3
0BE5 5D POP BP ; RESTORE BP
0BE6 C3 RET ; SCROLL_END
0BE7 8A DE TDOWN4: MOV BL,DH
0BE9 EB EE JMP SHORT TDOWN2
0BE8 TEXT_DOWN ENDP
;-----
;
; SCROLL DOWN (GRAPHICS)
;
; THIS ROUTINE SCROLLS DOWN THE INFORMATION ON THE CRT
;
; INPUT [BP+SDC_MODE] = CURRENT CRT MODE ( MASKED )
; [BP+SD_LRR] = LOWER RIGHT ROW POSITION
; [BP+SD_BSR] = BOTTOM OF SOURCE ROW POSITION
; CH,CL = UPPER LEFT CORNER OF REGION TO SCROLL
; DH,DL = LOWER RIGHT CORNER OF REGION TO SCROLL
; BOTH OF THE ABOVE ARE IN CHARACTER POSITIONS
; BH = FILL VALUE FOR BLANKED LINES
; AL = # LINES TO SCROLL (AL=0 MEANS BLANK THE ENTIRE FIELD)
; DS = DATA SEGMENT
; ES = REGEN SEGMENT
;
; OUTPUT NOTHING, THE SCREEN IS SCROLLED
;
; VOLATILE AX,BL,CX,DX,SI,DI,DS
;-----
ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM
0BE8 GRAPHICS_DOWN PROC NEAR
0BE8 FD STD ; SET DIRECTION
0BEC 8A DB MOV BL,AL ; SAVE LINE COUNT IN BL
0BEE 8B C2 MOV AX,DX ; GET LOWER RIGHT POSITION INTO AX REG
;----- USE CHARACTER SUBROUTINE FOR POSITIONING
;----- ADDRESS RETURNED IS MULTIPLIED BY 2 FROM CORRECT VALUE
0BF0 E8 15E8 R CALL GRAPH_POSH
0BF3 8B F8 MOV DI,AX ; SAVE RESULT AS DESTINATION
; ADDRESS
;----- DETERMINE SIZE OF WINDOW
0BF5 2B D1 SUB DX,CX
0BF7 81 C2 0101 ADD DX,101H ; ADJUST VALUES
0BF8 8A E6 MOV AH,DH ; SAVE VALUE OF DH#1 TO AH
0BFD D0 E6 SAL DH,1 ; MULTIPLY # ROWS BY 4 SINCE 8 VERT DOTS/CHAR
0BFF D0 E6 SAL DH,1 ; AND EVEN/ODD ROWS
0C01 80 3E 0049 R 14 CMP CRT_MODE,KJGRAPH; KANJI GRAPHICS MODE ?
0C06 72 04 JB GRDN1 ; NO
0C08 D0 E6 SAL DH,1
0C0A 02 F4 ADD DH,AH ; MULTIPLY # ROWS BY 9 SINCE 18 VERT DOTS/CHAR
0C0C GRDN1:

```

```

0C0C E8 1D95 R      CALL GET_DIRSEG ; GET DIRECT ACCESS SEGMENT OF V-RAM1
0C0F 89 46 04      MOV [BP+SD_ES1],AX ; SAVE IS

0C12 8A 66 00      MOV AH,[BP+SDC_MODE] ;--- DETERMINE CRT MODE
0C15 80 FC 06      CMP AH,6 ; SET CRT MODE TO AH
0C18 74 21          JZ GRDN2 ; TEST FOR HIGH RES
                                ; FIND_SOURCE_DOWN
                                ;--- MEDIUM RES DOWN
0C1A D0 E2          SAL DL,1 ; 8 COLUMNS * 2, SINCE 2 BYTES/CHAR
                                ; (OFFSET OK)
0C1C D1 E7          SAL DI,1 ; OFFSET *2 SINCE 2 BYTES/CHAR
0C1E 47            INC DI ; POINT TO LAST BYTE

0C1F 80 FC 04      CMP AH,4 ; TEST FOR MEDIUM RES
0C22 74 17          JZ GRDN2 ; FIND_SOURCE_DOWN
0C24 80 FC 05      CMP AH,5 ; TEST FOR MEDIUM RES
0C27 74 12          JZ GRDN2 ; FIND_SOURCE_DOWN
0C29 80 FC 0A      CMP AH,0AH ; TEST FOR MEDIUM RES
0C2C 74 0D          JZ GRDN2 ; FIND_SOURCE_DOWN

0C2E 80 FC 0B      CMP ah,0bh ; test for 640 x 200 x 16 color
0C31 74 08          Jz grdn2 ; find_source_down

0C33 4F            DEC DI
0C34 D0 E2          SAL DL,1 ; 8 COLUMNS * 2 AGAIN, SINCE 4 BYTES/CHAR
                                ; (OFFSET OK)
0C36 D1 E7          SAL DI,1 ; OFFSET *2 AGAIN, SINCE 4 BYTES/CHAR
0C38 83 C7 03      ADD DI,3 ; POINT TO LAST BYTE
                                ;----- DETERMINE THE SOURCE ADDRESS IN THE BUFFER
0C38 2A ED          SUB CH,CH ; FIND_SOURCE_DOWN
                                ; ZERO TO HIGH OF COUNT REG

0C3D 80 3E 0049 R 14 CMP CRT_MODE,KJGRAPH; KJ GRAPHICS MODE ?
0C42 73 0D          JAE GRDN3 ; YES
                                ;--- ANK GRAPHICS
0C44 80 FC 09      CMP AH,9 ; USING 32K REGEN?
0C47 B8 00F0        MOV AX,80H(8/2-1) ; OFFSET TO LAST ROW OF PIXELS IF 16K REGEN
0C4A 72 08          JC GRDN4 ; NO, JUMP
0C4C B8 00A0        MOV AX,160H(8/4-1) ; OFFSET TO LAST ROW OF PIXELS IF 32K REGEN

0C4F EB 03          JMP SHORT GRDN4

0C51 88 0280        MOV AX,80H(18/2-1) ;--- KJ GRAPHICS
0C54 03 F8          ADD DI,AX ; OFFSET TO LAST ROW OF PIXELS
                                ; POINT TO LAST ROW OF PIXELS
0C56 0A DB          OR BL,BL ; 8 LINES TO SCROLL IS ZERO ?
0C58 75 19          JNZ GRDN7

0C5A 8A DE          MOV BL,DH ; SET ENTIRE FIELD
0C5C 80 3E 0049 R 19 CMP CRT_MODE,19H ; KJ GRAPHICS 32K REGEN ?
0C61 72 0D          JB GRDN6

0C63 F6 46 01 01   TEST BYTE PTR [BP+SD_LRR],1 ; ODD ROW ?
0C67 74 04          JZ GRDN5 ; YES
0C69 81 C7 3FB0    ADD DI,4000H-80 ; ADJUST POINTER
0C6D E9 0D19 R      JMP GRDN16
0C70 E9 0D41 R      JMP GRDN19

0C73 8A C3          MOV AL,BL ; SAVE VALUE OF BL*1 TO AL
0C75 D0 E3          SAL BL,1 ; MULTIPLY NUMBER OF LINES BY 4
0C77 D0 E3          SAL BL,1

0C79 80 3E 0049 R 14 CMP CRT_MODE,KJGRAPH; KANJI GRAPHICS MODE ?
0C7E 72 04          JB GRDN8 ; NO

0C80 D0 E3          SAL BL,1 ; MULTIPLY NUMBER OF LINES BY 9
0C82 02 DB          ADD BL,AL ;
0C84 8A C3          MOV AL,BL ; GET NUMBER OF LINES IN AL
0C86 B4 50          MOV AH,80 ; 80 BYTES/ROW
0C88 F6 E4          MUL AH ; DETERMINE OFFSET TO SOURCE
0C8A 8B F7          MOV SI,DI ; SET UP SOURCE
0C8C 2B F0          SUB SI,DI ; SUBTRACT THE OFFSET
0C8E 8A E6          MOV AH,DH ; NUMBER OF ROWS IN FIELD
0C90 2A E3          SUB AH,BL ; DETERMINE NUMBER TO MOVE

0C92 80 3E 0049 R 14 CMP CRT_MODE,KJGRAPH; KJ GRAPHICS MODE ?
0C97 06            PUSH ES ; BOTH SEGMENTS TO REGEN
0C98 1F            POP DS
0C99 72 06          JB GRDN9 ASSUME DS:VIDEO_RAM
                                ; NO
                                ;--- LOOP THROUGH, MOVING ONE ROW
                                ; AT A TIME, BOTH EVEN AND ODD FIELDS
0C9B 80 7E 00 09   CMP BYTE PTR [BP+SDC_MODE],9 ; MODE USES 32K REGEN ?
0C9F 73 2C          JAE GRDN11 ; YES

0CA1 EB 0D63 R      CALL G_MOVE_ROW ; ROW_LOOP_DOWN
                                ; MOVE ONE ROW

0CA4 80 7E 00 09   CMP BYTE PTR [BP+SDC_MODE],9 ; MODE USES 32K REGEN?
0CA8 72 15          JC GRDN10 ; NO, JUMP

0CAA 81 C6 2000    ADD SI,2000H ; ADJUST POINTERS
0CAE 81 C7 2000    ADD DI,2000H
0CB2 E8 0D63 R      CALL G_MOVE_ROW ; MOVE 2 MORE ROWS

0CB5 81 EE 4050    SUB SI,4000H+80 ; BACK UP POINTERS
0CB9 81 EF 4050    SUB DI,4000H+80 ;
0CBD FE CC          DEC AH ; ADJUST COUNT

```

Appendix A.

```

OCBF      81 EE 2050
OCBF      81 EF 2050
OC33

OC7       FE CC
OC9       75 D6
OC8       EB 74
OC7D

OC7D      F6 46 01 01
OC7D      74 04
OC7D      81 C7 3FB0
OC7D      F6 46 02 01
OC7D      74 04
OC7D      81 C6 3FB0
OC7D      0CE1

OC7D      80 7E 00 0B
OC7D      75 11

OC7D      57
OC7D      56
OC7D      1E
OC7D      06

OC7D      8E 5E 04
OC7D      8E 46 04
OC7D      E8 0D63 R

OC7D      07
OC7D      1F
OC7D      5E
OC7D      5F
OC7D      06
OC7D      E8 0D63 R

OC7D      81 EE 6000
OC7D      83 FE 00
OC7D      7D 04
OC7D      81 C6 7F60
OC7D      0D08
OC7D      81 EF 6000
OC7D      83 FF 00
OC7D      7D 04
OC7D      81 C7 7F60
OC7D      0D15
OC7D      FE CC
OC7D      75 C8
OC7D      0D19
OC7D      8A C7
OC7D      0D1B

OC7D      80 7E 00 0B
OC7D      75 0A

OC7D      57
OC7D      06

OC7D      8E 46 04
OC7D      E8 0D7C R

OC7D      07
OC7D      5F
OC7D      06
OC7D      E8 0D7C R

OC7D      81 EF 6000
OC7D      83 FF 00
OC7D      7D 04
OC7D      81 C7 7F60
OC7D      0D3B
OC7D      FE CB
OC7D      75 DC

OC7D      EB 20

OC7D      8A C7
OC7D      E8 0D7C R

OC7D      80 7E 00 09
OC7D      72 0D

OC7D      81 C7 2000
OC7D      E8 0D7C R

OC7D      81 EF 4050
OC7D      FE CB
OC7D      81 EF 2050
OC7D      FE CB
OC7D      75 E2
OC7D      FC
OC7D      C3

OC7D      0D63

GRDN10:   SUB      SI,2000H+80      ; MOVE TO NEXT ROW
          SUB      DI,2000H+80
          DEC      AH                ; NUMBER OF ROWS TO MOVE
          JNZ     GRDN9             ; CONTINUE TILL ALL MOVED
          JMP     SHORT GRDN19

GRDN11:   TEST     BYTE PTR [BP+SD_LRR],1 ; --- 32K REGEN KJ MODE
          JZ      GRDN12           ; ODD ROW ?
          ADD     DI,4000H-80      ; YES
          ; ADJUST POINTER

GRDN12:   TEST     BYTE PTR [BP+SD_BSR],1 ; ODD ROW ?
          JZ      GRDN13           ; YES
          ADD     SI,4000H-80      ; ADJUST POINTER

GRDN13:   CMP      BYTE PTR [BP+SDC_MODE],0BH ; 640 X 200 X 16 COLOR ?
          JNE     GRDN131          ; NO
          ; --- SCROLL V-RAM 1
          PUSH    DI
          PUSH    SI
          PUSH    DS
          PUSH    ES
          ;
          MOV     DS,[BP+SD_ES1]   ; GET SEGMENT OF V-RAM 1
          MOV     ES,[BP+SD_ES1]
          CALL    G_MOVE_ROW       ; MOVE ROW

          POP     ES
          POP     DS
          POP     SI
          POP     DI
          ;
          ; RESTORE REGISTERS

GRDN131:  CALL    G_MOVE_ROW       ; MODE ONE ROW

          SUB     SI,6000H         ; NEXT ROW
          CMP     SI,0000H         ; IN 32K REGEN ?
          JGE    GRDN14           ; YES
          ADD     SI,8000H-160     ; NO, WRAP

GRDN14:   SUB     DI,6000H         ; NEXT ROW
          CMP     DI,0000H         ; IN 32K REGEN ?
          JGE    GRDN15           ; YES
          ADD     DI,8000H-160     ; NO, WRAP

GRDN15:   DEC     AH              ; ADJUST COUNT
          JNZ     GRDN13          ; CONTINUE TILL ALL MOVED

GRDN16:   MOV     AL,BH          ; ATTRIBUTE TO FILL WITH

GRDN17:   CMP     BYTE PTR [BP+SDC_MODE],0BH ; 640 X 200 X 16 COLOR ?
          JNE     GRDN171         ; NO
          ;
          PUSH    DI
          PUSH    ES
          ;
          MOV     ES,[BP+SD_ES1]   ; GET SEGMENT OF V-RAM 1
          CALL    G_CLEAR_ROW      ; CLEAR ROW

          POP     ES
          POP     DI
          ;
          ; RESTORE REGISTERS

GRDN171:  CALL    G_CLEAR_ROW      ; CLEAR A ROW

          SUB     DI,6000H         ; NEXT ROW
          CMP     DI,0000H         ; IN 32K REGEN ?
          JGE    GRDN18           ; YES
          ADD     DI,8000H-160     ; NO, WRAP

GRDN18:   DEC     BL              ; NUMBER OF LINES TO FILL
          JNZ     GRDN17          ; CLEAR ROW LOOP DOWN BY 2 ROW

          JMP     SHORT GRDN22     ; DONE

GRDN19:   ;----- FILL IN THE VACATED LINE(S)
          ; CLEAR_ENTRY_DOWN
          ; ATTRIBUTE TO FILL WITH
          ; CLEAR_LOOP_DOWN
          ; CLEAR A ROW

GRDN20:   MOV     AL,BH
          CALL    G_CLEAR_ROW

          CMP     BYTE PTR [BP+SDC_MODE],9 ; MODE USES 32K REGEN?
          JNC    GRDN21           ; NO, JUMP

          ADD     DI,2000H         ; NEXT ROW
          CALL    G_CLEAR_ROW      ; CLEAR 2 MORE ROWS

          SUB     DI,4000H+80      ; BACK UP POINTERS
          DEC     BL
          ; ADJUST COUNT

GRDN21:   SUB     DI,2000H+80      ; POINT TO NEXT LINE
          DEC     BL
          JNZ     GRDN20          ; NUMBER OF LINES TO FILL
          ; CLEAR_LOOP_DOWN

GRDN22:   CLD
          RET
          ; RESET THE DIRECTION FLAG
          ; EVERYTHING DONE

GRAPHICS_DOWN  ENDP

```

-----  
;
;
;
MOVE ONE ROW ( GRAPHICS )

```

;
; THIS ROUTINE MOVES ONE GRAPHICS ROW
;
; INPUT          DL = MOVE COUNT IN BYTE
;                DS:SI = SOURCE ROW ADDRESS
;                ES:DI = DESTINATION ROW ADDRESS
;
; OUTPUT        SI = SI + 2000H
;                DI = DI + 2000H
;
; VOLATILE      CL
;
;-----

```

```

0D63
0D63 8A CA      MOV CL,DL      ; NUMBER OF BYTES IN THE ROW
0D65 56        PUSH SI
0D66 57        PUSH DI        ; SAVE POINTERS
0D67 F3/ A4    REP MOVSB     ; MOVE THE EVEN FIELD
0D69 5F        POP DI
0D6A 5E        POP SI
0D6B 81 C6 2000 ADD SI,2000H
0D6F 81 C7 2000 ADD DI,2000H   ; POINT TO THE ODD FIELD

0D73 56        PUSH SI
0D74 57        PUSH DI        ; SAVE THE POINTERS
0D75 8A CA    MOV CL,DL     ; COUNT BACK
0D77 F3/ A4    REP MOVSB     ; MOVE THE ODD FIELD
0D79 5F        POP DI
0D7A 5E        POP SI        ; POINTERS BACK
0D7B C3        RET          ; RETURN TO CALLER

```

```

0D7C          G_MOVE_ROW      ENDP

```

```

;-----
; CLEAR ONE ROW ( GRAPHICS )
; THIS ROUTINE MOVES ONE GRAPHICS ROW
;
; INPUT          DL = CLEAR COUNT IN BYTE
;                ES:DI = DESTINATION ROW ADDRESS
;
; OUTPUT        DI = DI + 2000H
;
; VOLATILE      CL
;
;-----

```

```

0D7C
0D7C 8A CA      MOV CL,DL     ; NUMBER OF BYTES IN FIELD
0D7E 57        PUSH DI        ; SAVE POINTER
0D7F F3/ AA    REP STOSB    ; STORE THE NEW VALUE
0D81 5F        POP DI        ; POINTER BACK

0D82 81 C7 2000 ADD DI,2000H ; POINT TO ODD FIELD

0D86 57        PUSH DI
0D87 8A CA    MOV CL,DL
0D89 F3/ AA    REP STOSB    ; FILL THE ODD FILELD
0D8B 5F        POP DI

0D8C C3        RET          ; RETURN TO CALLER

```

```

0D8D          G_CLEAR_ROW     ENDP

```

```

;-----
; READ_AC_CURRENT          INT 10H, AH = 8
; THIS ROUTINE READS THE ATTRIBUTE AND CHARACTER AT THE
; CURRENT CURSOR POSITION AND RETURNS THEM TO THE CALLER
;
; INPUT  AH = CURRENT CRT MODE (MASKED)
;        BH = DISPLAY PAGE ( ALPHA MODES ONLY )
;        DS = DATA SEGMENT
;        ES = REGEN SEGMENT
;
; OUTPUT AL = CHAR READ
;        AH = ATTRIBUTE READ
;
;-----

```

```

0D8D          ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM

READ_AC_CURRENT PROC NEAR
0D8D 80 3E 0049 R 04  CMP CRT_MODE,GRAPHICS;IS THIS ANK TEXT MODE ?
0D92 72 43          JC      RDACC4          ; YES

0D94 80 3E 0049 R 14  CMP CRT_MODE,KJGRAPH;KANJI GRAPHICS MODE ?
0D99 73 0C          JAE     RDACC1          ; YES

0D9B 80 3E 0049 R 10  CMP CRT_MODE,KJ_MODE;KANJI TEXT MODE ?
0DA0 73 0C          JAE     RDACC2          ; YES

0DA2 E8 0DFC R        CALL GRAPHICS_READ ; READ ANK GRAPHICS
0DA5 EB 38          JMP     SHORT RDACC5 ; END

RDACC1:
0DA7 89 00A0         MOV CX,P_CODE_START ; SET PEDUDO CODE BUFFER SEGMENT
0DAA 8E C1          MOV ES,CX           ; TO ES

```

Appendix A.

```

0DAC 32 FF
0DAE
0DAE E8 0DE0 R
0DB1 8B F3
0DB3 06
0DB4 1F

0DB5 AD

0DB6 F6 C4 80
0DB9 74 24

0DBB F6 C4 08
0DBE 75 08

0DC0 8A E8
0DC2 8A 0C
0DC4 E8 1B42 R

0DC7 8A C5
0DC9 EB 14

0DCB
0DCB 8A C8
0DCD 8A 6C FC
0DD0 E8 1B42 R

0DD3 8A C1
0DD5 EB 08

0DD7
0DD7 E8 0DE0 R
0DDA 8B F3
0DDC 06
0DDD 1F
0DDE AD
0DDF
0DDF C3

0DE0

```

```

0DE0
0DE0 8A CF
0DE2 32 ED
0DE4 8B F1
0DE6 D1 E4
0DE8 8B 84 035C R

0DEC
0DEC 33 DB
0DEE E3 06
0DF0
0DF0 03 1E 004C R
0DF4 E2 FA
0DF6
0DF6 E8 06D5 R
0DF9 03 DB

0DFB C3
0DFC

```

```

RDACC2: XOR BH,BH ; CLEAR DISPLAY PAGE
; READ_AC_CONTINUE
CALL FIND_POSITION
MOV SI,BX ; ESTABLISH ADDRESSING IN SI
PUSH ES ;
POP DS ; GET SEGMENT FOR QUICK ACCESS
ASSUME DS:VIDEO_RAM

LODSW ; GET THE CHAR/ATTR

TEST AH,ZENBIT ; 2 BYTE CODE ?
JZ RDACC5 ; NO

TEST AH,ZEN2BIT ; 2ND BYTE CODE ?
JNZ RDACC3 ; YES
; --- 1ST BYTE
MOV CH,AL ; SET 1ST CHARACTER'S CODE TO CH
MOV CL,DS:[SI] ; SET 2ND CHARACTER'S CODE TO CL
CALL CHECK_ROSS_CODE ; CHECK AND CONVERT ROSSIAN CHARACTER CODE

MOV AL,CH ; SET 1ST BYTE OF CONVERTED CODE
JMP SHORT RDACC5 ; END

RDACC3: ; --- 2ND BYTE
MOV CL,AL ; SET 2ND CHARACTER'S CODE TO CL
MOV CH,DS:[SI-4] ; SET 1ST CHARACTER'S CODE TO CH
CALL CHECK_ROSS_CODE ; CHECK AND CONVERT ROSSIAN CHARACTER CODE

MOV AL,CL ; SET 2ND BYTE OF CONVERTED CODE
JMP SHORT RDACC5

ASSUME DS:DATA
RDACC4: ; READ_AC_CONTINUE
CALL FIND_POSITION
MOV SI,BX ; ESTABLISH ADDRESSING IN SI
PUSH ES ;
POP DS ; GET SEGMENT FOR QUICK ACCESS
LODSW ; GET THE CHAR/ATTR

RDACC5:
RET

READ_AC_CURRENT ENDP

```

```

;-----
;
; FIND_POSITION
;
; THIS ROUTINE DETERMINES THE REGEN ADDRESS FROM
; CURRENT CURSOR POSITION
;
; INPUT BH = DISPLAY PAGE
;
; OUTPUT BX = REGEN ADDRESS CORRESPONDING TO
; CURRENT CURSOR POSITION
;
; VOLATILE AX,CX
;-----
ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM
FIND_POSITION PROC NEAR
MOV CL,BH ; DISPLAY PAGE TO CX
XOR CH,CH
MOV SI,CX ; MOVE TO SI FOR INDEX
SAL SI,1 ; * 2 FOR WORD OFFSET
MOV AX,[SI+ OFFSET CURSOR_POSH] ; GET ROW/COLUMN OF THAT PAGE
FIND_POSH LABEL NEAR ; CALLED FROM WRITE_ALT_CURSOR
XOR BX,BX ; SET START ADDRESS TO ZERO
JCXZ FPOS2 ; NO_PAGE
FPOS1: ADD BX,CRT_LEN ; PAGE_LOOP
LOOP FPOS1 ; LENGTH OF BUFFER
FPOS2: CALL POSITION ; NO_PAGE
ADD BX,AX ; DETERMINE LOCATION IN REGEN
; ADD TO START OF REGEN
RET
FIND_POSITION ENDP

```

```

;-----
;
; GRAPHICS READ
;
; THIS ROUTINE READS THE ASCII CHARACTER AT THE CURRENT CURSOR
; POSITION ON THE SCREEN BY MATCHING THE DOTS ON THE SCREEN TO
; THE CHARACTER GENERATOR CODE POINTS
;
; INPUT AH = CRT MODE ( MASKED )
;
; INPUT NONE ( 0 IS ASSUMED AS THE BACKGROUND COLOR )
;
; OUTPUT AL = CHARACTER READ AT THAT POSITION ( 0 RETURNED IF NONE FOUND )
;
; NOTE 0 IS ASSUMED AS THE BACKGROUND COLOR
;
; VOLATILE BX,CX,DX,SI,DI,DS,ES
;-----
ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM

```

```

0DFC
0DFC 55
0DFD 83 EC 10
0E00 88 EC

0E02 50

0E03 E8 15E5 R
0E06 88 F0

0E08 58
0E09 06
0E0A 1F

0E0B 86 04

0E0D 80 FC 06
0E10 74 11

0E12 80 FC 04
0E15 74 54

0E17 80 FC 05
0E1A 74 4F

0E1C 80 FC 0A
0E1F 74 4A

0E21 EB 17

0E23
0E23 8A 04
0E25 88 46 00
0E28 45

0E29 8A 84 2000
0E2D 88 46 00
0E30 45

0E31 83 C6 50
0E34 FE CE
0E36 75 EB

0E38 EB 5E

0E3A D1 E6
0E3A D1 E6
0E3C D1 E6
0E3E
0E3E E8 0F03 R
0E41 81 C6 2000
0E45 E8 0F03 R

0E48 80 FC 09
0E4B 75 14

0E4D 81 C6 2000
0E51 E8 0F03 R

0E54 81 C6 2000
0E58 L8 0F03 R

0E5B 81 EE 3FB0
0E5F FE CE
0E61
0E61 81 EE 1FB0
0E65 FE CE
0E67 75 D5

0E69 EB 2D

0E6B D1 E6
0E6B D1 E6
0E6D
0E6D E8 0ECF R
0E70 81 C6 2000
0E74 E8 0ECF R

0E77 80 FC 0A
0E7A 75 14

0E7C 81 C6 2000
0E80 E8 0ECF R

0E83 81 C6 2000
0E87 E8 0ECF R

0E8A 81 EE 3FB0
0E8E FE CE
0E90
0E90 81 EE 1FB0
0E94 FE CE

0E96 75 D5

0E98
0E98 8B DD
0E9A 83 ED 08

GRAPHICS_READ PROC NEAR
PUSH BP ; SAVE BP
SUB SP,RAC_LOCAL ; ALLOCATE SPACE TO SAVE THE READ CODE POINT
MOV BP,SP ; POINTER TO SAVE AREA

PUSH AX ; SAVE CRT MODE

CALL GRAPH_POSITION ; CONVERTED TO OFFSET IN REGEN
MOV SI,AX ; SAVE IN SI
;----- DETERMINE GRAPHICS MODES
POP AX ; RESTORE CRT MODE
PUSH ES ; POINT TO REGEN SEGMENT
POP DS ASSUME DS:VIDEO_RAM
MOV DH,4 ; NUMBER OF PASSES

CMP AH,6
JZ GREAD1 ; HIGH RESOLUTION

CMP AH,4
JZ GREAD5 ; MEDIUM RESOLUTION

CMP AH,5
JZ GREAD5 ; MEDIUM RESOLUTION

CMP AH,0AH
JZ GREAD5 ; MEDIUM RESOLUTION

JMP SHORT GREAD2 ; LOW RESOLUTION

;----- HIGH RESOLUTION READ
;--GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
GREAD1: MOV AL,[SI] ; GET FIRST BYTE
MOV [BP],AL ; SAVE IN STORAGE AREA
INC BP ; NEXT LOCATION

MOV AL,[SI+2000H] ; GET LOWER REGION BYTE
MOV [BP],AL ; ADJUST AND STORE
INC BP

ADD SI,80 ; POINTER INTO REGEN
DEC DH ; LOOP CONTROL
JNZ GREAD1 ; DO IT SOME MORE

JMP SHORT GREAD8 ; GO MATCH THE SAVED CODE POINTS

;----- LOW RESOLUTION READ
GREAD2: SAL SI,1 ; OFFSET#4 SINCE 4 BYTES/CHAR
SAL SI,1

GREAD3: CALL LOW_READ_BYTE ; GET 4 BYTES FROM REGEN INTO SINGLE SAVE
ADD SI,2000H ; GOTO LOWER REGION
CALL LOW_READ_BYTE ; GET 4 BYTES FROM REGEN INTO SINGLE SAVE

CMP AH,9 ; DO WE HAVE A 32K REGEN AREA?
JNE GREAD4 ; NO, JUMP

ADD SI,2000H ; GOTO LOWER REGION
CALL LOW_READ_BYTE ; GET 4 BYTES FROM REGEN INTO SINGLE SAVE

ADD SI,2000H ; GOTO LOWER REGION
CALL LOW_READ_BYTE ; GET 4 BYTES FROM REGEN INTO SINGLE SAVE

SUB SI,4000H-80 ; ADJUST POINTER
DEC DH

GREAD4: SUB SI,2000H-80 ; ADJUST POINTER BACK TO UPPER
DEC DH
JNZ GREAD3 ; DO IT SOME MORE

JMP SHORT GREAD8 ; GO MATCH THE SAVED CODE POINTS

;----- MEDIUM RESOLUTION READ
GREAD5: SAL SI,1 ; MED_RES_READ
; OFFSET#2 SINCE 2 BYTES/CHAR

CALL MID_READ_BYTE ; GET PAIR BYTES FROM REGEN INTO SINGLE SAVE
ADD SI,2000H ; GO TO LOWER REGION
CALL MID_READ_BYTE ; GET THIS PAIR INTO SAVE

CMP AH,0AH ; DO WE HAVE A 32K REGEN AREA?
JNE GREAD7 ; NO, JUMP

ADD SI,2000H ; GOTO LOWER REGION
CALL MID_READ_BYTE ; GET PAIR BYTES FROM REGEN INTO SINGLE SAVE

ADD SI,2000H ; GOTO LOWER REGION
CALL MID_READ_BYTE ; GET PAIR BYTES FROM REGEN INTO SINGLE SAVE

SUB SI,4000H-80 ; ADJUST POINTER
DEC DH

GREAD7: SUB SI,2000H-80 ; ADJUST POINTER BACK INTO UPPER
DEC DH

JNZ GREAD6 ; KEEP GOING UNTIL ALL 8 DONE
;--- SAVE AREA HAS CHARACTER IN IT, MATCH IT
; FIND_CHAR
; SET POINTER TO FONT PATTERN
; ADJUST POINTER TO BEGINNING OF SAVE AREA
GREAD8: MOV BX,BP
SUB BP,8

```





```

0EF7 D1 E9      SHR    CX,1
0EF9 D1 E9      SHR    CX,1      ; MOVE THE MASK TO THE RIGHT BY 2 BITS
0EFB 73 F3      JNC    MRBYTE3   ; DO IT AGAIN IF MASK DIDN'T FALL OUT

0EFD 88 56 00   MOV    [BP],DL   ; STORE RESULT IN SAVE AREA
0F00 45          INC    BP        ; ADJUST POINTER

0F01 58          POP    AX        ; RESTORE AX
0F02 C3          RET             ; ALL DONE

0F03          MID_READ_BYTE ENDP

```

```

;-----
;
; LOW_READ_BYTE
;
; THIS ROUTINE WILL TAKE 4 BYTES FROM THE REGEN BUFFER,
; COMPARE FOR BACKGROUND COLOR, AND PLACE
; THE CORRESPONDING ON/OFF BIT PATTERN INTO THE CURRENT
; POSITION IN THE SAVE AREA
;
; INPUT          SI,DS = POINTER TO REGEN AREA OF INTEREST
;                BP = POINTER TO SAVE AREA
;
; OUTPUT         BP = BP + 1
;
; VOLATILE      CX,DL
;-----

```

```

0F03          LOW_READ_BYTE PROC NEAR
0F03 50          PUSH   AX          ; SAVE CURRENT AX
0F04 8A 24      MOV    AH,[SI]     ; GET FIRST 2 BYTES
0F06 8A 44 01   MOV    AL,[SI+1]
0F09 32 D2      XOR    DL,DL
0F0B E8 0F1D R  CALL   BUILD_HIIBBLE ; BUILD HIGH HIIBBLE
0F0E 8A 64 02   MOV    AH,[SI+2]   ; GET SECOND 2 BYTES
0F11 8A 44 03   MOV    AL,[SI+3]
0F14 E8 0F1D R  CALL   BUILD_LOIIBBLE ; BUILD LOW HIIBBLE
0F17 88 56 00   MOV    [BP],DL     ; STORE RESULT IN SAVE AREA
0F1A 45          INC    BP        ; ADJUST POINTER
0F1B 58          POP    AX        ; RESTORE AX
0F1C C3          RET
0F1D          LOW_READ_BYTE ENDP

```

```

;-----
;
; BUILD_HIIBBLE
;
; THIS ROUTINE WILL TAKE 1 WORD FROM THE REGEN BUFFER,
; AND MAKE 4 BIT PATTERN FROM THAT.
;
; INPUT          AX = ANY WORD OF REGEN BUFFER
;
; OUTPUT         DL = DOT PATTERN
;                (LOW HIIBBLE, HIGH IS LOW HIIBBLE OF INPUT DL)
;
; VOLATILE      CX
;-----

```

```

0F1D          BUILD_HIIBBLE PROC NEAR
0F1D B9 F000    MOV    CX,0F000H   ; 4 BIT MASK TO TEST THE ENTRIES
0F20          BLDN1: TEST   AX,CX        ; IS THIS SECTION BACKGROUND?
0F20 85 C1      JZ     BLDN2       ; IF ZERO, IT IS BACKGROUND
0F22 74 01      ;
0F24 F9          STC          ; WASH'T, SO SET CARRY
0F25          BLDN2: RCL    DL,1      ; MOVE THAT BIT INTO RESULT
0F25 D0 D2      SHR    CX,1        ; MOVE MASK RIGH 4 BITS
0F27 D1 E9      SHR    CX,1
0F29 D1 E9      SHR    CX,1
0F28 D1 E9      SHR    CX,1
0F2D D1 E9      SHR    CX,1
0F2F 73 EF      JNC    BLDN1       ; DO IT AGAIN IF MASK DID'T FALL OUT
0F31 C3          RET
0F32          BUILD_HIIBBLE ENDP

```

```

;-----
;
; WRITE_AC_CURRENT INT 10H, AH = 9
;-----
;
; THIS ROUTINE WRITES THE ATTRIBUTE AND CHARACTER AT
; THE CURRENT CURSOR POSITION
;
; INPUT
;
; AH = CURRENT CRT MODE (MASKED)
; BH = DISPLAY PAGE
; CX = COUNT OF CHARACTERS TO WRITE
; AL = CHAR TO WRITE
; BL = ATTRIBUTE OF CHAR TO WRITE
; DS = DATA SEGMENT
; ES = REGEN SEGMENT
;-----

```

Appendix A.

```

;
; OUTPUT
; NONE
;
; CALL (GRAPHICS_WRITE)
; FIND_POSITION
; WRITE_AC
;
; VOLATILE AX,BX,CX,DX,SI,DI
;
-----
ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM
WRITE_AC_CURRENT PROC NEAR
0F32 80 3E 0049 R 10    CMP CRT_MODE,KJ_MODE; KJ DISPLAY MODE ?
0F37 73 17             JAE WRTACC3 ; YES
0F39 80 FC 04         CMP AH,GRAPHICS ; IS THIS GRAPHICS?
0F3C 72 03             JC WRTACC1 ; NO
0F3E E9 1625 R        JMP GRAPHICS_WRITE ; GO TO WRITE ANK IN GRAPHICS
0F41                 WRTACC1:
0F41 8A E3             MOV AH,BL ; WRITE_AC_CONTINUE
0F43 50                 PUSH AX ; GET ATTRIBUTE TO AH
0F44 51                 PUSH CX ; SAVE ON STACK
0F45 E8 0DE0 R        CALL FIND_POSITION ; SAVE WRITE COUNT
0F48 8B FB             MOV DI,BX ; ADDRESS TO DI REGISTER
0F4A 59                 POP CX ; WRITE COUNT
0F4B 58                 POP AX ; CHARACTER IN AX REG
0F4C                 WRTACC2:
0F4C AB              STOSW ; WRITE_LOOP
0F4D E2 FD             LOOP WRTACC2 ; PUT THE CHAR/ATTR
0F4F C3                 RET ; AS MANY TIMES AS REQUESTED
0F50                 WRTACC3:
0F50 B2 00             MOV DL,FALSE ; RESET WRITE CHAR ONLY FLAG
0F52 E8 0F7B R        CALL WRITE_AC ; WRITE ATTRIBUTE AND CHARACTER
0F55 C3                 RET
0F56                 WRITE_AC_CURRENT ENDP

```

```

;
; WRITE_C_CURRENT INT 10H, AH = 10 (0AH)
;
; THIS ROUTINE WRITES THE CHARACTER AT
; THE CURRENT CURSOR POSITION, ATTRIBUTE UNCHANGED
;
; INPUT
; AH = CURRENT CRT MODE (MASKED)
; BH = DISPLAY PAGE
; CX = COUNT OF CHARACTERS TO WRITE
; AL = CHAR TO WRITE
; BL = COLOR OF CHAR (GRAPHICS)
; DS = DATA SEGMENT
; ES = REGEN SEGMENT
;
; OUTPUT
; NONE
;
; CALL (GRAPHICS_WRITE)
; FIND_POSITION
; WRITE_AC
;
; VOLATILE AX,BX,CX,DX,SI,DI
;
-----
ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM
WRITE_C_CURRENT PROC NEAR
0F56 80 3E 0049 R 10    CMP CRT_MODE,KJ_MODE; KJ DISPLAY MODE ?
0F5B 73 18             JAE WRTCC3 ; YES
0F5D 80 FC 04         CMP AH,GRAPHICS ; IS THIS GRAPHICS?
0F60 72 03             JC WRTCC1 ; NO
0F62 E9 1625 R        JMP GRAPHICS_WRITE ; GO TO WRITE ANK IN GRAPHICS
0F65                 WRTCC1:
0F65 50                 PUSH AX ; SAVE ON STACK
0F66 51                 PUSH CX ; SAVE WRITE COUNT
0F67 E8 0DE0 R        CALL FIND_POSITION ; SAVE WRITE COUNT
0F6A 8B FB             MOV DI,BX ; ADDRESS TO DI
0F6C 59                 POP CX ; WRITE COUNT
0F6D 58                 POP BX ; BL HAS CHAR TO WRITE
0F6E                 WRTCC2:
0F6E 8A C3             MOV AL,BL ; WRITE_LOOP
0F70 AA              STOSB ; RECOVER CHAR
0F71 47              INC DI ; PUT THE CHAR/ATTR
0F72 E2 FA             LOOP WRTCC2 ; BUMP POINTER PAST ATTRIBUTE
0F74 C3                 RET ; AS MANY TIMES AS REQUESTED
0F75                 WRTCC3:
0F75 B2 FF             MOV DL,TRUE ; SET WRITE CHAR ONLY FLAG
0F77 E8 0F7B R        CALL WRITE_AC ; WRITE ATTRIBUTE AND CHARACTER

```

0F7A C3  
0F7B

RET  
WRITE\_C\_CURRENT ENDP

```

-----
WRITE_AC
;
; THIS ROUTINE WRITES THE ATTRIBUTE(IF DL=0) AND CHARACTER AT
; THE CURRENT CURSOR POSITION
;
; INPUT
; AH = CURRENT CRT MODE (MASKED)
; BH = DISPLAY PAGE
; CX = COUNT OF CHARACTERS TO WRITE
; AL = CHAR TO WRITE
; BL = ATTRIBUTE OF CHAR TO WRITE
; DL = FLAG OF WRITE CHARACTER ONLY
; DS = DATA SEGMENT
; ES = REGEN SEGMENT
;
; WORK
; [BP+WGPOSH] = REGEN ADDRESS TO WRITE IN GRAPHICS
; [BP+WPOSH] = ROW/COLUMN POSITION TO WRITE
; [BP+W2CODE] = ATTRIBUTE/CODE OF 2ND BYTE OF 2 BYTE CODE
; [BP+WR SEG] = REGEN SEGMENT
; [BP+WGMODE] = FLAG OF GRAPHICS MODE
; [BP+WC MODE]= CURRENT CRT MODE (MASKED)
; [BP+WFONT - WFONT+35] = FONT PATTERN
;
; OUTPUT      NONE
;
; CALL        FINT_POSITION
;             GRAPH_POSITION
;             WRITE_ONE_CHAR
;             WRITE_TWO_CHAR
;
; VOLATILE    AX,BX,CX,DI,SI
-----

```

0F7B

```

0F7B 55
0F7C 83 EC 2E
0F7F 8B EC

0F81 50
0F82 53
0F83 51
0F84 E8 0DE0 R
0F87 8B FB
0F89 59
0F8A 5B
0F8B 58

0F8C C6 46 08 00
0F90 88 46 09
0F93 80 FC 04
0F96 72 18

0F98 50
0F99 EB 15E5 R
0F9C 89 46 00

0F9F 8C 46 06
0FA2 C6 46 08 FF

0FA6 B8 00A0
0FA9 8E C0

0FAB 81 E7 07FF
0FAF 58
0FB0
0FB0 53

0FB1 8A DF
0FB3 32 FF
0FB5 D1 E3

0FB7 8B D7 035C R
0FB8 89 76 02

0FBE 5B

0FBF F7 06 0340 R FFFF
0FC5 75 21

0FC7 3C 80
0FC9 76 2F
0FCB 3C FD
0FCD 73 2B
0FCF 3C A0
0FD1 72 04
0FD3 3C DF
0FD5 76 23

0FD7
0FD7 8A E3

0FD9 24 7F
0FDB F7 C6 00FF

```

```

WRITE_AC      PROC      NEAR
PUSH          BP          ; SAVE BP
SUB           SP,W_LOCAL ; ALLOCATE LOCAL WORK AREA
MOV           BP,SP      ; ASSIGN BP AS FRAME POINTER

PUSH          AX          ; SAVE MODE
PUSH          BX          ; SAVE DISPLAY PAGE AND ATTRIBUTE
PUSH          CX          ; SAVE COUNTER
CALL         FIND_POSITION ; GET REGEN ADDRESS TO WRITE
MOV           DI,BX       ; IN DI
POP           CX          ; RESTORE COUNTER
POP           BX          ; RESTORE DISPLAY PAGE AND ATTRIBUTE
POP           AX          ; RESTORE MODE

MOV           BYTE PTR [BP+WGMODE],FALSE ; SET NO GRAPHICS MODE
MOV           BYTE PTR [BP+WC_MODE],AH   ; SET CRT MODE
CMP           AH,GRAPHICS ; IS THIS GRAPHICS?
JRTAC1       ; NO
;--- GRAPHICS MODE
; SAVE MODE AND CHARACTER CODE
CALL         GRAPH_POSITION ; GET GRAPHICS REGEN ADDRESS TO WRITE
MOV           [BP+WGPOSH],AX ; IN [BP]

MOV           [BP+WR_SEG],ES ; SAVE REGEN SEGMENT
MOV           BYTE PTR [BP+WGMODE],TRUE ; SET GRAPHICS MODE

MOV           AX,P_CODE_START ; SET PESUDO CODE BUFFER SEGMENT
MOV           ES,AX          ; TO ES

AND           DI,PCB_MASK   ; MASK POINTER TO INSURE RANGE
POP           AX           ; RESTORE AX

WRTAC1:      PUSH        BX          ; SAVE DISPLAY PAGE AND ATTRIBUTE
MOV           BL,BH         ; SET PAGE NUMBER TO BL
XOR           BH,BH         ; CLEAR FOR WORD
SAL           BX,1         ; CONVERT TO WORD OFFSET

MOV           SI,[BX+OFFSET CURSOR_POSH] ; GET CURSOR POSITION
MOV           [BP+WPOSH],SI ; SET IT TO LOCAL WORK AREA

POP           BX           ; RESTORE DISPLAY PAGE AND ATTRIBUTE

TEST          W_1ST_CHAR,TRUE ; 1ST BYTE OF 2 BYTE CODE HAS BEEN SET ?
JNZ          WRTAC5       ; YES

CMP           AL,080H      ; 1ST BYTE OF 2 BYTE CODE ?
JBE          WRTAC6       ; NO, GO TO WRITE ONE CHARACTER
CMP           AL,0FDH      ;
JAE          WRTAC6       ;
CMP           AL,0A0H      ; NO, GO TO WRITE ONE CHARACTER
JBE          WRTAC2       ;
CMP           AL,0DFH      ; YES
JBE          WRTAC6       ; NO, GO TO WRITE ONE CHARACTER

WRTAC2:      MOV           AH,BL      ;--CHARACTER IS 1ST BYTE OF 2 BYTE CODE
; SET ATTRIBUTE TO AH
; SET ZERO COLUMN FLAG FOR AVOID
; DESTRUCT OF PREVIOUS ROW
; SET ZERO COLUMN BIT OFF
; CURRENT COLUMN IS ZERO ?
AND           AL,NOT ZEROCOL
TEST          SI,00FFH

```



```

1051 F6 46 08 FF      TEST   BYTE PTR [BP+WGMODE],TRUE ; GRAPHICS MODE ?
1055 74 05              JZ     WOC2                ; NO

1057 32 D2             XOR    DL,DL                ; SET 0 TO DISPLACEMENT
1059 E8 1213 R          CALL   G_WRITE1             ; WRITE ONE CHAR IN GRAPHICS
105C EB 5E              JMP    SHORT WOC8           ;--- END
WOC2:

105E F6 C4 08           TEST   AH,ZEN2BIT           ; SECOND BYTE OF ZENKAKU ?
105F 75 2D             JNZ    WOC6                ; YES

;----- OVERRIDE 1ST BYTE OF ZENKAKU
1063 0A D2             OR     DL,DL                ; WRITE CHARACTER ONLY ?
1065 75 02             JNZ    WOC4                ; YES

1067 8A E3             MOV    AH,BL                ; SET ATTRIBUTE
1069 80 E4 77          AND    AH,HAN_MASK          ; MASK OFF ZEN,1ST/2ND BIT
106C A3                 STOSH                ; WRITE CHAR/ATTRIBUTE

106D 26: C6 05 20      MOV    BYTE PTR ES:[DI],' ' ; ERASE 2ND BYTE OF ZENKAKU
1071 26: 80 65 01 77  AND    BYTE PTR ES:[DI+1],HAN_MASK; MASK OFF ZEN,1ST/2ND BIT

1076 F6 46 08 FF      TEST   BYTE PTR [BP+WGMODE],TRUE ; GRAPHICS MODE ?
107A 74 12              JZ     WOC5                ; NO

107C 8A E3             MOV    AH,BL                ; SAVE ATTRIBUTE
107E 50                 PUSH   AX                ; AND CHARACTER

107F 80 20             MOV    AL,' '               ; SET SPACE
1081 82 01             MOV    DL,1                 ; SET +1 TO DISPLACEMENT
1083 E8 1213 R          CALL   G_WRITE1             ; ERASE 2ND BYTE OF ZENKAKU

1086 58                 POP    AX                  ; RESTORE CHARACTER
1087 8A DC             MOV    BL,AH                ; AND ATTRIBUTE
1089 32 D2             XOR    DL,DL                ; SET 0 TO DISPLACEMENT
108B E8 1213 R          CALL   G_WRITE1             ; WRITE ONE CHAR IN GRAPHICS
108E EB 2C              JMP    SHORT WOC8           ;--- END
WOC5:

1090 0A D2             OR     DL,DL                ; WRITE CHARACTER ONLY ?
1092 75 02             JNZ    WOC7                ; YES

1094 8A E3             MOV    AH,BL                ; SET ATTRIBUTE
1096 80 E4 77          AND    AH,HAN_MASK          ; MASK OFF ZEN,1ST/2ND BIT
1099 26: C6 45 FE 20      MOV    BYTE PTR ES:[DI-2],' ' ; ERASE 1ST BYTE OF ZENKAKU
109E 26: 80 65 FF 77  AND    BYTE PTR ES:[DI-1],HAN_MASK; MASK OFF ZEN,1ST/2ND BIT

10A3 AB              STOSH                ; WRITE CHAR/ATTRIBUTE

10A4 F6 46 08 FF      TEST   BYTE PTR [BP+WGMODE],TRUE; GRAPHICS MODE ?
10A8 74 12              JZ     WOC8                ; NO

10AA 8A E3             MOV    AH,BL                ; SAVE ATTRIBUTE
10AC 50                 PUSH   AX                ; AND CHARACTER

10AD 80 20             MOV    AL,' '               ; SET SPACE
10AF 82 FF             MOV    DL,-1                ; SET -1 TO DISPLACEMENT
10B1 E8 1213 R          CALL   G_WRITE1             ; ERASE 2ND BYTE OF ZENKAKU

10B4 58                 POP    AX                  ; RESTORE CHARACTER
10B5 8A DC             MOV    BL,AH                ; AND ATTRIBUTE
10B7 32 D2             XOR    DL,DL                ; SET 0 TO DISPLACEMENT
10B9 E8 1213 R          CALL   G_WRITE1             ; WRITE ONE CHAR IN GRAPHICS
10BC EB 02              JMP    SHORT WOC8           ;--- END
WOC8:

10BC 8B 46 02          MOV    AX,[BP+WPOSN]        ; GET COLUMN COUNT
10BF FE C0             INC    AL                   ; INCREMENT COLUMN

10C1 3A 06 004A R      CMP    AL,BYTE PTR CRT_COLS ; EXCEED END OF LINE ?
10C5 72 12             JB     WOC9                ; NO

10C7 32 C0             XOR    AL,AL                ; CLEAR SINCE MODULO OF CRT_COLS
10C9 FE C4             INC    AH                   ; INCREMENT ROW

10CB F6 46 08 FF      TEST   BYTE PTR [BP+WGMODE],TRUE ; GRAPHICS MODE ?
10CF 74 08              JZ     WOC9                ; NO

10D1 50                 PUSH   AX                  ; SAVE AX
10D2 E8 15E8 R          CALL   GRAPH_POSN           ; SET NEW GRAPHICS WRITE POSITION
10D5 89 46 00          MOV    [BP+WGPSN],AX        ; TO [BP]
10D8 58                 POP    AX                  ; RESTORE AX
10D9 89 46 02          MOV    [BP+WPOSN],AX        ; SET NEW ROW/COLUMN POSITION
WOC9:

10DC 5A                 POP    DX                   ; RESTORE FLAG OF WRITE CHARACTER ONLY
10DD 59                 POP    CX                   ; RESTORE NUMBER OF CHARACTER
10DE 5B                 POP    BX                   ; RESTORE ATTRIBUTE
10DF 58                 POP    AX                   ; RESTORE CHARACTER CODE
10E0 C3                 RET

10E1 WRITE_ONE_CHAR ENDP

```

```

;-----
;
; WRITE_TWO_CHAR
;
; THIS ROUTINE WRITES TWO BYTE CHARACTER
;

```

Appendix A.

```

; INPUT [BP+WPOSH] = ROW/COLUMN POSITION TO WRITE
; [BP+W2CODE] = 2ND BYTE OF 2 BYTE CODE TO WRITE
; [BP+W2ATTR] = ATTRIBUTE FOR 2ND BYTE
; [BP+WGMODE] = FLAG OF GRAPHICS MODE
; DL = FLAG OF WRITE CHARACTER ONLY
; ES:DI = REGEN ADDRESS TO WRITE
;
; OUTPUT ES:DI REGEN ADDRESS TO WRITE NEXT CHARACTER
;
; CALL WRITE_TWO
; G_WRITE1
; G_WRITE2
; GRAPH_POSH
;
; VOLATILE AX,BX,SI
;
-----
10E1 WRITE_TWO_CHAR PROC NEAR
10E1 51 PUSH CX ; SAVE NUMBER OF CHARACTER TO WRITE
10E2 52 PUSH DX ; SAVE FLAG OF WRITE CHARACTER ONLY
10E3 8B 46 02 MOV AX,[BP+WPOSH] ; GET ROW/COLUMN POSITION
10E4 FE C0 INC AL ; ADJUST FOR COMPARE
10E8 3A 06 004A R CMP AL,BYTE PTR CRT_COLS ; AT LINE END BOUNDARY ?
10EC 72 2C JB WTC1 ; NO
10EE FE C4 INC AH ; INCREMENT ROW
10F0 32 C0 XOR AL,AL ; SET 0 TO COLUMN
10F2 89 46 02 MOV [BP+WPOSH],AX ; SET IT TO WORK
10F5 26: C6 05 20 MOV BYTE PTR ES:[DI],' ' ; WRITE SPACE
10F9 47 INC DI
10FA 26: 80 25 77 AND BYTE PTR ES:[DI],HAN_MASK ; MASK KJ-BIT OFF
10FE 47 INC DI ; ADVANCE POINTER
10FF F6 46 08 FF TEST BYTE PTR [BP+WGMODE],TRUE ; GRAPHICS MODE ?
1103 74 15 JZ WTC1 ; NO
1105 50 PUSH AX ; SAVE ROW/COLUMN
1106 52 PUSH DX ; SAVE FLAG OF WRITE CHAR ONLY
1107 80 20 MOV AL,' ' ; WRITE SPACE
1109 8A 1E 0341 R BL,BYTE PTR W_1ST_CHAR+1 ; GET ATTRIBUTE OF 1ST BYTE
110D 32 D2 XOR DL,DL ; WRITE AT CURRENT CURSOR POSITON +0
110F EA 1213 R CALL G_WRITE1 ; WRITE ONE BYTE CHAR IN GRAPHICS
1112 5A POP DX ; RESTORE FLAG
1113 58 POP AX ; RESTORE ROW/COLUMN
1114 E8 15E8 R CALL GRAPH_POSH ; SET NEW GRAPHICS WRITE POSITION
1117 89 46 00 MOV [BP+WGMODE],AX ; TO AX
111A 26: 8A 7D 01 WTC1: MOV BH,ES:[DI+1] ; GET ATTR. AT CURRENT CURSOR POSH
111E 26: 8A 5D 03 MOV BL,ES:[DI+3] ; GET ATTR. AT CURRENT CURSOR POSH+1
1122 F6 C7 80 TEST BH,ZENBIT ; HANKAKU ?
1125 74 05 JZ WTC2 ; YES
1127 F6 C7 08 TEST BH,ZEN2BIT ; 2ND BYTE OF 2 BYTE CODE ?
112A 75 3A JNZ WTC2 ; YES
112C WTC2: ;--- HANKAKU OR 1ST BYTE IS PRESENT AT CURRENT
112C F6 C3 80 TEST BL,ZENBIT ; HANKAKU ?
112F 74 05 JZ WTC3 ; YES
1131 F6 C3 08 TEST BL,ZEN2BIT ; 2ND BYTE OF 2 BYTE CODE ?
1134 74 0E JZ WTC3 ; NO
1136 WTC3: ;----- OVERRIDE TWO HANKAKU OR 1ST,2ND BYTE
1136 E8 11E4 R CALL WRITE_TWO ; WRITE TWO BYTE CHARACTER IN REGEN
1139 F6 46 08 FF TEST BYTE PTR [BP+WGMODE],TRUE ; GRAPHICS MODE ?
113D 74 03 JZ WTC4 ; NO
113F E8 1335 R CALL G_WRITE2 ; WRITE 2 BYTE CHARACTER IN GRAPHICS
1142 EB 7D JMP SHORT WTC10 ;--- END
1144 WTC4: ;----- OVERRIDE HANKAKU,1ST BYTE OF 2 BYTE CODE
1144 E8 11E4 R CALL WRITE_TWO ; WRITE TWO BYTE CHARACTER IN REGEN
1147 26: C6 05 20 MOV BYTE PTR ES:[DI ],' ' ; ERASE 2ND BYTE OF 2 BYTE CODE
114B 26: 80 65 01 77 AND BYTE PTR ES:[DI+1],HAN_MASK ; MASK OFF KJ-BIT
1150 F6 46 08 FF TEST BYTE PTR [BP+WGMODE],TRUE ; GRAPHICS MODE ?
1154 74 0E JZ WTC5 ; NO
1156 80 20 MOV AL,' ' ; ERASE 2ND BYTE OF 2 BYTE CODE
1158 8A 1E 0341 R BL,BYTE PTR W_1ST_CHAR+1 ; GET ATTRIBUTE OF 1ST BYTE
115C B2 02 MOV DL,2 ; WRITE AT CURRENT CURSOR POSITON +2
115E E8 1213 R CALL G_WRITE1 ; WRITE ONE BYTE CHAR IN GRAPHICS
1161 E8 1335 R WTC6: CALL G_WRITE2 ; WRITE 2 BYTE CHARACTER IN GRAPHICS
1164 EB 5B JMP SHORT WTC10 ;--- END
1166 WTC7: ;-----
1166 F6 C3 80 TEST BL,ZENBIT ; ATTRIBUTE AT CURRENT +1 IS HANKAKU ?
1169 75 23 JNZ WTC9 ; NO
116B 26: C6 45 FE 20 ;----- OVERRIDE 2ND BYTE, HANKAKU
1170 26: 80 65 FF 77 MOV BYTE PTR ES:[DI-2],' ' ; ERASE 1ST BYTE OF 2 BYTE CODE
1175 E8 11E4 R AND BYTE PTR ES:[DI-1],HAN_MASK ; MASK OFF KJ-BIT
CALL WRITE_TWO ; WRITE TWO BYTE CHARACTER IN REGEN

```

```

1178 F6 46 08 FF      TEST   BYTE PTR [BP+WGMODE],TRUE; GRAPHICS MODE ?
117C 74 0E             JZ     WTC8           ; NO
117E B0 20             MOV    AL,' '        ; ERASE 1ST BYTE OF 2 BYTE CODE
1180 8A 1E 0341 R     MOV    BL, BYTE PTR W_1ST_CHAR+1; GET ATTRIBUTE OF 1ST BYTE
1184 B2 FF             MOV    DL,-1         ; WRITE AT CURRENT CURSOR POSITOM -1
1186 E8 1213 R        CALL   G_WRITE1      ; WRITE ONE BYTE CHAR IN GRAPHICS
1189 E8 1335 R        CALL   G_WRITE2      ; WRITE 2 BYTE CHARACTER IN GRAPHICS
118C                                     WTC8:
118C EB 33             JMP    SHORT WTC10   ;--- END

118E                                     WTC9:
118E ;----- OVERRIDE 2ND,1ST BYTE OF 2 BYTE CODE
118E MOV    BYTE PTR ES:[DI-2],' ' ; ERASE 1ST BYTE OF 2 BYTE CODE
1193 AND    BYTE PTR ES:[DI-1],HAN_MASK; MASK OFF KJ-BIT
1198 E8 11E4 R        CALL   WRITE_TWO     ; WRITE TWO BYTE CHARACTER IN REGEN
119B 26: C6 05 20     MOV    BYTE PTR ES:[DI ],' ' ; ERASE 1ST BYTE OF 2 BYTE CODE
119F 26: 80 65 01 77   AND    BYTE PTR ES:[DI+1],HAN_MASK; MASK OFF KJ-BIT
11A4 F6 46 08 FF      TEST   BYTE PTR [BP+WGMODE],TRUE ; GRAPHICS MODE ?
11A8 74 17             JZ     WTC10         ; NO
11AA B0 20             MOV    AL,' '        ; ERASE 1ST BYTE OF 2 BYTE CODE
11AC 8A 1E 0341 R     MOV    BL, BYTE PTR W_1ST_CHAR+1; GET ATTRIBUTE OF 1ST BYTE
11B0 50             PUSH   AX             ; SAVE AX
11B1 53             PUSH   BX             ; SAVE BX
11B2 B2 FF             MOV    DL,-1         ; WRITE AT CURRENT CURSOR POSITOM -1
11B4 E8 1213 R        CALL   G_WRITE1      ; WRITE ONE BYTE CHAR IN GRAPHICS
11B7 5B             POP    BX             ; RESTORE BX
11B8 58             POP    AX             ; RESTORE AX
11B9 B2 02             MOV    DL,2          ; WRITE CURRENT CURSOR POSITION +2
11BB E8 1213 R        CALL   G_WRITE1      ; WRITE ONE BYTE CHAR IN GRAPHICS
11BE E8 1335 R        CALL   G_WRITE2      ; WRITE 2 BYTE CHARACTER IN GRAPHICS
11C1                                     WTC10:
11C1 MOV    AX,[BP+WPOSN] ; GET COLUMN COUNT
11C1 8B 46 02         ADD    AL,2           ; ADVANCE 2 BY 2 BYTE CHAR WRITING
11C4 04 02
11C6 3A 06 004A R     CMP    AL, BYTE PTR CRT_COLS ; EXCEED END OF LINE ?
11CA 72 12             JB     WTC11         ; NO
11CC 32 C0             XOR    AL,AL         ; CLEAR SINCE MODULO OF CRT_COLS
11CE FE C4             INC    AH             ; INCREMENT ROW
11D0 F6 46 08 FF      TEST   BYTE PTR [BP+WGMODE],TRUE; GRAPHICS MODE ?
11D4 74 08             JZ     WTC11         ; NO
11D6 50             PUSH   AX             ; SAVE ROW/COLUMN
11D7 E8 15E8 R        CALL   GRAPH_POSN    ; SET NEW GRAPHICS WRITE POSITION
11DA 89 46 00         MOV    [BP+WGPOSH],AX ; TO [BP+WGPOSH]
11DD 58             POP    AX             ; RESTORE ROW/COLUMN
11DE 89 46 02         MOV    [BP+WPOSH],AX ; SET NEW ROW/COLUMN POSITION
11E1 5A             POP    DX             ; RESTORE FLAG OF WRITE CHARACTER ONLY
11E2 59             POP    CX             ; RESTORE NUMBER OF CHARECTER
11E3 C3
11E4

```

WRITE\_TWO\_CHAR ENDP

```

;-----
;
; WRITE_TWO
;
; THIS ROUTINE WRITES TWO BYTE CHARACTER IN REGEN
;
; INPUT [BP+W2CODE] = 2ND BYTE OF 2 BYTE CODE TO WRITE
;        [BP+W2ATTR] = ATTRIBUTE FOR 2ND BYTE
;        BH = ATTRIBUTE AT CURRENT CURSOR POSITION
;        BL = ATTRIBUTE AT CURRENT CURSOR POSITION + 1
;        DL = FLAG OF WRITE CHARACTER ONLY
;        ES:DI = REGEN ADDRESS TO WRITE
;
; OUTPUT ES:DI = REGEN ADDRESS TO WRITE NEXT CHARACTER
;
; CALL CHECK_ROSS_CHAR
;
; VOLATILE AX
;-----

```

```

11E4 WRITE_TWO PROC NEAR
11E4 51             PUSH   CX             ; SAVE CX
11E5 8A 2E 0340 R     MOV    CH, BYTE PTR W_1ST_CHAR ; GET 1ST CHARACTER'S CODE
11E9 8A 4E 04         MOV    CL, BYTE PTR [BP+W2CODE] ; GET 2ND CHARACTER'S CODE
11EC E8 1B65 R        CALL   CHECK_ROSS_CHAR ; CHECK AND CONVERT ROSSIAN CHARACTER
11EF 8A C5             MOV    AL,CH         ; GET 1ST CHARACTER'S CODE
11F1 8A 26 0341 R     MOV    AH, BYTE PTR W_1ST_CHAR+1; GET 1ST CHARACTER'S ATTRIBUTE
11F5 0A D2             OR     DL,DL         ; WRITE CHARACTER ONLY ?
11F7 74 02             JZ     WRTTW01       ; NO
11F9 8A E7             MOV    AH,BH         ; GET ATTRIBUTE AT CURRENT
11FB WRTTW01:
11FB AND    AH,HAN_MASK   ; STRIP OFF ZEN,1ST/2ND BIT
11FE 80 CC 80         OR     AH,ZENBIT     ; SET ZENKAKU BIT
1201 AB             STOSW                ; WRITE TO REGEN

```

Appendix A.

```

1202 8A C1          MOV     AL,CL          ; GET 2ND CHARACTER'S CODE
1204 8A 66 05      MOV     AH,[BP+WZATTR] ; GET 2ND CHARACTER'S ATTR/CODE

1207 0A D2          OR      DL,DL          ; WRITE CHARACTER ONLY ?
1209 74 02          JZ      WRTTWO2        ; NO

120B 8A E3          MOV     AH,BL          ; GET ATTRIBUTE AT CURRENT + 1
120D                OR      AH,ZEN2_MASK    ; SET ZENKAKU,2ND_BYTE BIT
120E 80 CC 88      STOSW                    ; WRITE TO REGEN
1210 AB

1211 59             POP     CX             ; RESTORE CX
1212 C3             RET

1213                WRITE_TWO      ENDP
-----
;
;
; G_WRITE1
;
; THIS ROUTINE WRITES ONE CHARACTER IN GRAPHICS
;
; INPUT  AL = CHARACTER CODE TO WRITE
;        BL = ATTRIBUTE
;        DL = DISPLACEMENT FOR WRITE POSITION
;        [BP+WGPOSN] = WRITE POSITION
;        [BP+WPOSN]= ROW/COLUMN POSITION TO WRITE
;        [BP+WR_SEG]= REGEN SEGMENT FOR GRAPHICS
;        [BP+WC_MODE]= CRT MODE (MASKED)
;        WORK [BP+WFONT - WFONT+17] = FONT PATTERN
;
; OUTPUT [BP+WGPOSN] = NEXT WRITE POSITION (IF DL = 0)
;
; CALL   ENABLE_VRAM
;        FONT
;        G_WRT1
;
; VOLATILE AX,BX,CX,DX,SI
;
-----
1213                G_WRITE1 PROC      NEAR
1213 1E             PUSH    DS          ; SAVE CURRENT DS
1214 57             PUSH    DI          ; SAVE CURRENT DI
1215 06             PUSH    ES          ; SAVE CURRENT ES

1216 F6 C3 80      TEST    BL,XOR_BIT    ; XOR WRITE BIT ON ?
1219 75 05          JNZ    G_WRT11        ; YES

121B C6 06 0349 R 00 MOV    GC_PRESENT,FALSE ; GRAPHICS CURSOR FLAG OFF
1220 53             G_WRT11:  PUSH    BX          ; SAVE COLOR ATTRIBUTE

1221 16             PUSH    SS          ; SET DESTINATION SEGMENT FOR FONT
1222 07             POP     ES          ; TO ES
;                ASSUME ES:STACK

1223 8D 5E 0A      LEA    BX,[BP+WFONT] ; SET DESTINATION OFFSET FOR FONT TO BX

1226 32 E4          XOR    AH,AH          ; CLEAR FOR 1 BYTE CODE
1228 8B C8          MOV    CX,AX          ; SET CHARACTER CODE TO CX

122A 80 F9 A0      CMP    CL,HALFTONE    ; HALF TONE CHARACTER ?
122D B8 2A55      MOV    AX,HT_FONT     ; SET FONT PATTERN OF HALF TONE
1230 74 07          JE     G_WRT12        ; YES

1232 80 F9 20      CMP    CL,' '         ; SPACE ?
1235 75 18          JNE   G_WRT13        ; NO

;                ;--- SPACE & HALF TONE CODE IS HANDLED SPECIALLY FOR
;                ;--- IMPROVE THROUGHPUT OF "BASIC" AND KANA-KAM
;                ; CLEAR FOR FONT PATTERN OF SPACE
1237 33 C0          G_WRT12: XOR    AX,AX

1239                MOV    DI,BX          ; SET DESTINATION ADDRESS
123B B9 0008      MOV    CX,(CBOX_ROW-2)/2 ; SET CLEAR COUNT BY WORD

123E F3 AB          REP    STOSW          ; SET FONT PATTERN
1240 33 C0          XOR    AX,AX          ;
1242 AB          STOSW                    ; CLEAR BOTTOM 2 ROW

1243 F6 06 0353 R FF TEST    KJROM_STAT,TRUE ;--- INSURE V-RAM IS ON FOR KANA-KAM INTERFACE
1248 74 0C          JZ     G_WRT14        ; KJ-ROM IS ON ?
;                ; NO

124A E8 1BB4 R      CALL   ENABLE_VRAM    ; MUST ENABLE VRAM FOR WRITE GRAPHICS PATTERN

124D EB 07          JMP    SHORT G_WRT14

124F                G_WRT13:
124F 80 80          MOV    AL,80H         ; SET FUNCTION OF REQUEST FONT WITH FULL BOX
1251 B6 10          MOV    DH,KJ_MODE     ; INDICATES KJ MODE
1253 E8 1A63 R      CALL   FONT           ; GET FONT PATTERN
1256                G_WRT14:  PUSH    ES          ; SET SEGMENT FOR FONT PATTERN
1257 1F             POP     DS          ; TO DS
;                ASSUME DS:STACK

1258 8B F3          MOV    SI,BX          ; SET TOP ADDRESS OF FONT PATTERN
125A 5B             POP     BX          ; RESTORE COLOR ATTRIBUTE

```



```

125B 8E 46 06      MOV     ES,[BP+WR_SEG] ; GET ACTUAL REGEN SEGMENT FOR GRAPHICS
125E E8 1265 R    CALL   G_WRT1         ASSUME ES:VIDEO_RAM
                                ; WRITE FONT PATTERN
1261 07           POP     ES             ; RESTORE ES
1262 5F           POP     DI            ; RESTORE DI
1263 1F           POP     DS            ; RESTORE DS
1264 C3           RET
1265

```

G\_WRITE1 ENDP

```

;-----
;
; G_WRT1
;
; THIS ROUTINE WRITES GRAPHICS PATTERN
;
; INPUT  BL           = ATTRIBUTE
;        DL           = DISPLACEMENT
;        [BP+WGPOSH] = WRITE POSITION
;        [BP+WPOSH]  = ROW/COLUMN POSITION TO WRITE
;        [BP+WC_MODE]= CRT MODE (MASKED)
;        DS:SI       = FONT PATTERN ADDRESS
;        ES          = REGEN SEGMENT
;
; OUTPUT [BP+WGPOSH] = NEXT WRITE POSITION (IF DL=0)
;
; CALL   G_W_1
;        G_W_10
;        G_W_2
;        G_W_4
;        G_W_40
;        GET_DIRSEG
;
; VOLATILE  AX,BX,DX,SI,DI,ES
;-----

```

ASSUME CS:CODE, DS:INDETERMINATE, ES:VIDEO\_RAM

```

1265      G_WRT1 PROC NEAR
1265 8A C2      MOV     AL,DL          ; SET DISPLACEMENT
1267 98        CBW        ; TO AX
1268 50        PUSH     AX          ; SAVE IT
1269 03 46 00  ADD     AX,[BP+WGPOSH] ; GET POSITION TO WRITE
126C 8B F8      MOV     DI,AX         ; IN DI
126E 8A 76 09  MOV     DH,[BP+WC_MODE] ; GET CRT MODE
1271 80 FE 09  CMP     DH,9          ; 320X200 16 COLOR ?
1274 74 25      JE      GWRT11        ; YES
1276 80 FE 0B  CMP     DH,0BH        ; 640X200 16 COLOR ?
1279 74 3B      JE      GWRT121       ; YES
127B 80 FE 0A  CMP     DH,0AH        ; 640X200 4 COLOR ?
127E 74 68      JE      GWRT13        ; YES
1280 80 FE 04  CMP     DH,4          ; 320X200 4 COLOR ?
1283 74 7C      JE      GWRT15        ; YES
1285 80 FE 05  CMP     DH,5          ; 320X200 4 SHADE ?
1288 74 77      JE      GWRT15        ; YES
128A 80 FE 06  CMP     DH,6          ; 640X200 2 SHADE ?
128D 74 7B      JE      GWRT16        ; YES
128F D1 E7      SAL     DI,1          ;--- 160X200 16 COLOR: MODE 8
1291 D1 E7      SAL     DI,1          ; OFFSET*4 SINCE 4 BYTES/CHAR
1293 86 09      MOV     DH,CBOX_ROW/2 ; SET LOOP COUNT ( 18 ROW / 2 SCAN )
1295 E8 14F3 R   CALL   G_W_4          ; WRITE ONE CHAR
1298 E9 132C R   JMP     GWRT110       ;--- END
129B          GWRT11:
129B D1 E7      SAL     DI,1          ;--- 320X200 16 COLOR
129D D1 E7      SAL     DI,1          ; OFFSET*4 SINCE 4 BYTES/CHAR
129F 86 09      MOV     DH,CBOX_ROW/(4/2); SET LOOP COUNT ( 18 ROW / 4 SCAN / 2 DEC. )
12A1 8B 46 02  MOV     AX,[BP+WPOSH] ; GET ROW COLUMN POSITION
12A4 D0 EC      SHR     AH,1          ; ODD ROW ?
12A6 72 05      JC      GWRT12        ; YES
12A8 E8 14F3 R   CALL   G_W_4          ;--- WRITE EVEN ROW CHARACTER
12AB EB 7F      JMP     SHORT GWRT110 ; WRITE ONE CHAR
12AD          GWRT12:
12AD 81 C7 3FB0  ADD     DI,4000H-80   ;--- WRITE ODD ROW CHARACTER
12B1 E8 1527 R   CALL   G_W_40        ; ADJUST WRITE ADDRESS FOR ODD ROW
                                ; WRITE ONE CHAR
12B4 EB 76      JMP     SHORT GWRT110
12B6          GWRT121:
12B6          ;--- 640X200 16 COLOR
12B6 53        ;--- WRITE V-RAM 2
12B7 57        PUSH     BX          ; SAVE ATTRIBUTE
12B8 56        PUSH     DI          ; SAVE REGEN POINTER
                                ; SAVE CHARACTER PATTERN ADDRESS
12B9 8A FB      MOV     BH,BL         ; SAVE ATTRIBUTE TO BH
12BB 80 E7 80  AND     BH,XOR_BIT    ; GET XOR BIT
12BE D0 EB      SHR     BL,1         ; SHIFT ATTRIBUTE FOR WRITE TO V-RAM2
12C0 D0 EB      SHR     BL,1

```

Appendix A.

```

12C2 0A DF          OR    BL,BH          ; RESTORE XOR BIT
12C4 D1 E7          SAL    DI,1          ; OFFSET*2 SINCE 2 BYTES/CHAR

12C6 B6 09          MOV    DH,CBOX_ROW/(4/2); SET LOOP COUNT ( 18 ROW / 4 SCAN / 2 DEC.)

12C8 8B 46 02      MOV    AX,[BP+WPOSN]  ; GET ROW COLUMN POSITION
12CB D0 EC          SHR    AH,1          ; ODD ROW ?
12CD 72 05          JC     GWRT122       ; YES

12CF E8 1498 R      CALL   G_W_1         ; --- WRITE EVEN ROW CHARACTER
                          ; WRITE ONE CAHAR

12D2 EB 07          JMP    SHORT GWRT123

12D4              GWRT122:          ;--- WRITE ODD ROW CHARACTER
12D4 81 C7 3FB0      ADD    DI,4000H-80    ; ADJUST WRITE ADDRESS FOR ODD ROW
12D8 E8 14C2 R      CALL   G_W_10        ; WRITE ONE CHAR
12DB              GWRT123:          ;
12DB 5E             POP    SI            ; RESTORE CHARACTER PATTERN ADDRESS
12DC 5F             POP    DI            ; RESTORE REGEN POINTER
12DD 5B             POP    BX            ; RESTORE ATTRIBUTE

12DE 1E             PUSH   DS            ; SAVE DS
12DF E8 0000 E      CALL   DDS           ; POINT TO DATA AREA

12E2 E8 1D95 R      CALL   GET_DIRSEG    ; GET DIRECT ACCESS SEGMENT OF V-RAM 1
12E5 8E C0          MOV    ES,AX         ; SET IT TO ES
12E7 1F             POP    DS           ; RESTORE DS
                          ;--- WRITE V-RAM 1

12E8              GWRT13:          ;--- 640X200 4 COLOR
12E8 D1 E7          SAL    DI,1          ; OFFSET*2 SINCE 2 BYTES/CHAR

12EA B6 09          MOV    DH,CBOX_ROW/(4/2); SET LOOP COUNT ( 18 ROW / 4 SCAN / 2 DEC.)

12EC 8B 46 02      MOV    AX,[BP+WPOSN]  ; GET ROW COLUMN POSITION
12EF D0 EC          SHR    AH,1          ; ODD ROW ?
12F1 72 05          JC     GWRT14        ; YES

12F3 E8 1498 R      CALL   G_W_1         ; --- WRITE EVEN ROW CHARACTER
                          ; WRITE ONE CAHAR

12F6 EB 34          JMP    SHORT GWRT110 ; --- END

12F8              GWRT14:          ;--- WRITE ODD ROW CHARACTER
12F8 81 C7 3FB0      ADD    DI,4000H-80    ; ADJUST WRITE ADDRESS FOR ODD ROW
12FC E8 14C2 R      CALL   G_W_10        ; WRITE ONE CHAR

12FF EB 2B          JMP    SHORT GWRT110

1301              GWRT15:          ;--- 320X200 4 COLOR/SHADE
1301 D1 E7          SAL    DI,1          ; OFFSET*2 SINCE 2 BYTES/CHAR

1303 B6 09          MOV    DH,CBOX_ROW/2  ; SET LOOP COUNT ( 18 ROW / 2 SCAN )
1305 E8 14C8 R      CALL   G_W_2         ; WRITE ONE CHAR

1308 EB 22          JMP    SHORT GWRT110 ; --- END

130A              GWRT16:          ;--- 640X200 2 SHADE
130A B6 09          MOV    DH,CBOX_ROW/2  ; SET LOOP COUNT ( 16 ROW / 2 SCAN )
130C              GWRT17:          ;
130C AC             LODSB                ; GET WORD FROM CODE POINTS
130D F6 C3 80      TEST   BL,XOR_BIT    ; SHOULD WE USE THE FUNCTION
1310 75 04          JNZ   GWRT18        ; TO PUT CHAR IN?

1312 AA             STOSB                ; STORE IN REGEN BUFFER
1313 AC             LODSB                ; GET NEXT ROW
1314 EB 0A          JMP    SHORT GWRT19

1316              GWRT18:          ;
1316 26 32 05      XOR    AL,ES:[DI]    ; EXCLUSIVE OR WITH CURRENT DATA
1319 AA             STOSB                ; STORE THE CODE POINT
131A AC             LODSB                ; AGAIN FOR ODD FIELD
131B 26 32 85 1FFF XOR    AL,ES:[DI+2000H-1]
1320              GWRT19:          ;
1320 26 88 85 1FFF MOV    ES:[DI+2000H-1],AL ; STORE IN SECOND HALF
1325 83 C7 4F      ADD    DI,79         ; MOVE TO NEXT ROW IN REGEN
1328 FE CE          DEC    DH            ; DONE WITH LOOP ?
132A 75 E0          JNZ   GWRT17        ; NO

132C              GWRT110:         ;--- END
132C 5B             POP    AX            ; RESTORE DISPLACEMENT
132D 9A C0          OR     AL,AL         ; ZERO ?
132F 75 03          JNE   GWRT111       ; NO

1331 FF 46 00      INC   WORD PTR [BP+WPOSN] ; ADVANCE WRITE POSITION
1334 C3             RET

1335              G_WRT1 ENDP

```

```

-----
;
;
;   G_WRITE2
;
;   THIS ROUTINE WRITES TWO BYTE CHARACTER IN GRAPHICS
;
;   INPUT  [BP+WGPOSN] = WRITE POSITION
;          [BP+WPOSN]  = ROW/COLUMN POSITION TO WRITE
;          [BP+W2CODE] = ATTRIBUTE/CODE OF 2ND BYTE
;          [BP+W2R_SEG] = REGEN SEGMENT FOR GRAPHICS
;          [BP+W2_MODE] = CURRENT CRT MODE (MASKED)
;
;

```

```

;
; OUTPUT [BP+WGPOSH] = NEXT WRITE POSITION
; WORK [BP+WFONT - WFONT+35] = FONT PATTERN
;
;
; CALL CHECK_ROSS_CHAR
; G_W_1
; G_W_10
; G_W_2
;
; G_W_4
; G_W_40
; GET_DIRSEG
;
; VOLATILE AX,BX,CX,DX,SI
;
-----
ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM
1335 G_WRITE2 PROC NEAR
1335 1E PUSH DS ; SAVE CURRENT DS
1336 57 PUSH DI ; SAVE CURRENT DI
1337 06 PUSH ES ; SAVE CURRENT ES
1338 F6 C3 80 TEST BL,XOR_BIT ; XOR WRITE BIT ON ?
1338 75 05 JNZ GVRT21 ; YES
133D C6 06 0349 R 00 MOV GC_PRESENT,FALSE ; GRAPHICS CURSOR FLAG OFF
1342 GVRT21: MOV BX,W_1ST_CHAR ; SET ATTRIBUTE OF 1ST BYTE TO BH
1344 8A E3 MOV AH,BL ; SET 1ST BYTE OF 2 BYTE CODE TO AH
1348 8A 46 04 MOV AL,[BP+W2CODE] ; SET 2ND BYTE OF 2 BYTE CODE TO AL
1348 8A 5E 05 MOV BL,[BP+W2ATTR] ; SET ATTRIBUTE OF 2ND BYTE TO BL
134E 53 PUSH BX ; SAVE COLOR ATTRIBUTE
134F 16 PUSH SS ; SET DESTINATION SEGMENT FOR FONT
1350 07 POP ES ; TO ES
ASSUME ES:STACK
1351 8D 5E 0A LEA BX,[BP+WFONT] ; SET DESTINATION OFFSET FOR FONT TO BX
1354 8B C8 MOV CX,AX ; SET CHARACTER CODE TO CX
1356 E8 1B65 R CALL CHECK_ROSS_CHAR ; CHECK AND CONVERT ROSSIAN CHARACTER
1359 B0 80 MOV AL,80H ; SET FUNCTION OF REQUEST FONT WITH FULL BOX
135B B6 10 MOV DH,KJ_MODE ; INDICATES KJ MODE
135D E8 1A63 R CALL FONT ; GET FONT PATTERN
1360 06 PUSH ES ; SET SEGMENT FOR FONT PATTERN
1361 1F POP DS ; TO DS
ASSUME DS:STACK
1362 8B F3 MOV SI,BX ; SET TOP ADDRESS OF FONT PATTERN
1364 5B POP BX ; RESTORE COLOR ATTRIBUTE
1365 8E 46 06 MOV ES,[BP+W2_SEG] ; GET ACTUAL REGEN SEGMENT FOR GRAPHICS
ASSUME ES:VIDEO_RAM
1368 8B 7E 00 MOV DI,[BP+WGPOSH] ; SET WRITE POSITION TO DI
136B 8A 76 09 MOV DH,[BP+W2_MODE] ; GET CRT MODE
136E 80 FE 06 CMP DH,6 ; 640X200 2 SHADE ?
1371 75 2F JNE GWRT26 ; NO, MODE IS 8
1373 B9 0002 MOV CX,2 ;--- 640X200 2 SHADE
1376 GVRT22: ; REPEAT 2 TIMES FOR LEFT/RIGHT PART
1376 57 PUSH DI ; SAVE REGEN ADDRESS TO WRITE
1377 B6 09 MOV DH,CBOX_ROW/2 ; NUMBER OF TIMES THROUGH LOOP
1379 GVRT23: LODSB ; GET WORD FROM CODE POINTS
137A F6 C7 80 TEST BH,XOR_BIT ; SHOULD WE USE THE FUNCTION
137D 75 04 JNZ GWRT24 ; TO PUT CHAR IN?
137F AA STOSB ; STORE IN REGEN BUFFER
1380 AC LODSB ; GET NEXT ROW
1381 EB 0A JMP SHORT GWRT25
1383 GVRT24: XOR AL,ES:[DI] ; EXCLUSIVE OR WITH CURRENT DATA
1383 26: 32 05 STOSB ; STORE THE CODE POINT
1386 AA LODSB ; AGAIN FOR ODD FIELD
1387 AC XOR AL,ES:[DI+2000H-1]
1388 26: 32 85 1FFF GVRT25: MOV ES:[DI+2000H-1],AL ; STORE IN SECOND HALF
138D 26: 88 85 1FFF
1392 83 C7 4F ADD DI,79 ; MOVE TO NEXT ROW IN REGEN
1395 FE CE DEC DH ; DONE WITH LOOP ?
1397 75 E0 JNZ GWRT23 ; NO
1399 5F POP DI ; RESTORE REGEN ADDRESS
139A 47 INC DI ; NEXT POSITION
139B 8A FB MOV BH,BL ; SET 2ND ATTRIBUTE TO BH
139D E2 D7 LOOP GWRT22
139F E9 148E R JMP GWRT212 ;--- END
13A2 GVRT26: CMP DH,8 ; 160X200 16 COLOR ?
13A2 80 FE 08 JNE GWRT27 ; NO
13A5 75 18
13A7 D1 E7 SAL DI,1 ;--- 160X200 16 COLOR: MODE 8
;

```

Appendix A.

```

13A9 D1 E7          SAL      DI,1          ; OFFSET*4 SINCE 4 BYTES/CHAR
13AB 53            PUSH     BX           ; SAVE ATTRIBUTE
13AC 8A DF         MOV      BL,BH        ; SET 1ST ATTRIBUTE
13AE B6 09         MOV      DH,CBOX_ROW/2 ; SET LOOP COUNT ( 18 ROW / 2 SCAN )
13B0 E8 14F3 R    CALL     G_W_4        ; WRITE LEFT PART
13B3 83 C7 04     ADD      DI,4         ; NEXT WRITE POSITION
13B6 5B            POP      BX           ; RESTORE ATTRIBUTE
13B7 B6 09         MOV      DH,CBOX_ROW/2 ; SET LOOP COUNT ( 18 ROW / 2 SCAN )
13B9 E8 14F3 R    CALL     G_W_4        ; WRITE RIGHT PART
13BC E9 148E R    JMP      GWRT212      ;--- END
13BF             GWRT27:
13BF 80 FE 09     CMP      DH,9         ; 320X200 16 COLOR ?
13C2 75 32         JNE     GWRT281      ; NO
13C4 D1 E7          SAL      DI,1          ;--- 320X200 16 COLOR
13C6 D1 E7          SAL      DI,1          ;
13C8 53            PUSH     BX           ; OFFSET*4 SINCE 4 BYTES/CHAR
13C9 8A DF         MOV      BL,BH        ; SAVE ATTRIBUTE
13CB B6 09         MOV      DH,CBOX_ROW/(4/2); SET LOOP COUNT ( 18 ROW / 4 SCAN / 2 DEC. )
13CD 8B 46 02     MOV      AX,[BP+WPOSN] ; GET ROW COLUMN POSITION
13D0 D0 EC         SHR     AH,1         ; ODD ROW ?
13D2 72 0F         JC      GWRT28      ; YES
13D4 E8 14F3 R    CALL     G_W_4        ;--- WRITE EVEN ROW CHARACTER
13D7 83 C7 04     ADD      DI,4         ; NEXT WRITE POSITION
13DA 5B            POP      BX           ; RESTORE ATTRIBUTE
13DB B6 09         MOV      DH,CBOX_ROW/(4/2); SET LOOP COUNT ( 18 ROW / 4 SCAN / 2 DEC. )
13DD E8 14F3 R    CALL     G_W_4        ; WRITE RIGHT PART
13E0 E9 148E R    JMP      GWRT212      ;--- END
13E3             GWRT28:
13E3 81 C7 3FB0     ADD     DI,4000H-80   ;--- WRITE ODD ROW CHARACTER
13E7 E8 1527 R    CALL     G_W_40       ; ADJUST FOR ODD ROW
13EA 83 C7 04     ADD     DI,4          ; WRITE LEFT PART
13ED 5B            POP      BX           ; NEXT WRITE POSITION
13EE B6 09         MOV      DH,CBOX_ROW/(4/2); SET LOOP COUNT ( 18 ROW / 4 SCAN / 2 DEC. )
13F0 E8 1527 R    CALL     G_W_40       ; WRITE RIGHT PART
13F3 E9 148E R    JMP      GWRT212      ;--- END
13F6             GWRT281:
13F6 80 FE 0B     CMP     DH,0BH       ; 640X200 16 COLOR ?
13F9 75 4D         JNE     GWRT29       ; NO
13FB 53            PUSH     BX           ;--- 640X200 16 COLOR
13FC 57            PUSH     DI           ;--- WRITE V-RAM 2
13FD 56            PUSH     SI           ; SAVE ATTRIBUTE
13FE 8B C3         MOV     AX,BX        ; SAVE REGEN POINTER
1400 25 8080      MOV     AX,8080H     ; SAVE CHARACTER PATTERN ADDRESS
1403 81 E3 7C7C   AND     AX,XOR_BIT*100H OR XOR_BIT ; SAVE ATTRIBUTE TO BH
1407 D1 EB         AND     DX,7C7CH    ; GET XOR BIT
1409 D1 EB         SHR     DX,1        ; MASK XOR BIT
140B 0B D8         SHR     BX,1        ; SHIFT ATTRIBUTE FOR WRITE TO V-RAM2
140D D1 E7         OR      BX,AX       ;
140F 53            POP      DI,1       ; RESTORE XOR BIT
1410 8A DF         PUSH     BL,BH      ; OFFSET*2 SINCE 2 BYTES/CHAR
1412 B6 09         MOV     BL,BH       ; SAVE ATTRIBUTE
1414 8B 46 02     MOV     DH,CBOX_ROW/(4/2); SET 1ST ATTRIBUTE
1417 D0 EC         SHR     AH,1        ; SET LOOP COUNT ( 18 ROW / 4 SCAN / 2 DEC. )
1419 72 0E         JC      GWRT282    ; YES
141B E8 1498 R    CALL     G_W_1        ;--- WRITE EVEN ROW CHARACTER
141E 83 C7 02     ADD     DI,2         ; WRITE LEFT PART
1421 5B            POP      BX           ; NEXT WRITE POSITION
1422 B6 09         MOV     DH,CBOX_ROW/(4/2); SET LOOP COUNT ( 18 ROW / 4 SCAN / 2 DEC. )
1424 E8 1498 R    CALL     G_W_1        ; WRITE RIGHT PART
1427 EB 10         JMP     SHORT GWRT283 ;--- END
1429             GWRT282:
1429 81 C7 3FB0     ADD     DI,4000H-80   ;--- WRITE ODD ROW CHARACTER
142D E8 14C2 R    CALL     G_W_10       ; ADJUST FOR ODD ROW
1430 83 C7 02     ADD     DI,2         ; WRITE LEFT PART
1433 5B            POP      BX           ; NEXT WRITE POSITION
1434 B6 09         MOV     DH,CBOX_ROW/(4/2); SET LOOP COUNT ( 18 ROW / 4 SCAN / 2 DEC. )
1436 E8 14C2 R    CALL     G_W_10       ; WRITE RIGHT PART
1439 5E            POP     SI           ; RESTORE CHARACTER PATTERN ADDRESS
143A 5F            POP     DI           ; RESTORE REGEN POINTER
143B 5B            POP     BX           ; RESTORE ATTRIBUTE

```

```

143C 1E          PUSH DS          ; SAVE DS
143D E8 0000 E  CALL DD5          ; POINT TO DATA AREA

1440 E8 1D95 R   CALL GET_DIRSEG   ; GET DIRECT ACCESS SEGMENT OF V-RAM 1
1443 8E C0       MOV ES,AX         ; SET IT TO ES
1445 1F          POP DS           ; RESTORE DS
1446 EB 05       JMP SHORT GWRT291 ;--- WRITE V-RAM 1

1448           GWRT29: CMP DH,0AH        ; 640X200 4 COLOR ?
1448 80 FE 0A   JNE GWRT211      ; NO, MODE = 4,5

144D           GWRT29: SAL DI,1          ;--- 640X200 4 COLOR
144D D1 E7     ; OFFSET#2 SINCE 2 BYTES/CHAR

144F 53         PUSH BX          ; SAVE ATTRIBUTE
1450 8A DF     MOV BL,BH        ; SET 1ST ATTRIBUTE
1452 B6 09     MOV DH,CBOX_ROW/(4/2); SET LOOP COUNT ( 18 ROW / 4 SCAN / 2 DEC.)

1454 8B 46 02   MOV AX,[BP+WPOSH]; GET ROW COLUMN POSITION
1457 D0 EC     SHR AH,1         ; ODD ROW ?
1459 72 0E     JC GWRT210       ; YES
;--- WRITE EVEN ROW CHARACTER
145B E8 1498 R   CALL G_W_1       ; WRITE LEFT PART

145E 83 C7 02   ADD DI,2         ; NEXT WRITE POSITION

1461 5B         POP BX           ; RESTORE ATTRIBUTE
1462 B6 09     MOV DH,CBOX_ROW/(4/2); SET LOOP COUNT ( 18 ROW / 4 SCAN / 2 DEC.)
1464 E8 1498 R   CALL G_W_1       ; WRITE RIGHT PART

1467 EB 25       JMP SHORT GWRT212 ;--- END

1469           GWRT210: ADD DI,4000H-80   ;--- WRITE ODD ROW CHARACTER
1469 81 C7 3FB0  CALL G_W_10      ; ADJUST FOR ODD ROW
146D E8 14C2 R   ; WRITE LEFT PART

1470 83 C7 02   ADD DI,2         ; NEXT WRITE POSITION
1473 5B         POP BX           ; RESTORE ATTRIBUTE
1474 B6 09     MOV DH,CBOX_ROW/(4/2); SET LOOP COUNT ( 18 ROW / 4 SCAN / 2 DEC.)
1476 E8 14C2 R   CALL G_W_10      ; WRITE RIGHT PART

1479 EB 13       JMP SHORT GWRT212 ;--- END

147B           GWRT211: SAL DI,1          ;--- 320X200 4 COLOR/SHADE
147B D1 E7     ; OFFSET#2 SINCE 2 BYTES/CHAR

147D 53         PUSH BX          ; SAVE ATTRIBUTE
147E 8A DF     MOV BL,BH        ; SET 1ST ATTRIBUTE
1480 B6 09     MOV DH,CBOX_ROW/2 ; SET LOOP COUNT ( 18 ROW / 2 SCAN )
1482 E8 14C8 R   CALL G_W_2       ; WRITE LEFT PART

1485 83 C7 02   ADD DI,2         ; NEXT WRITE POSITION

1488 5B         POP BX           ; RESTORE ATTRIBUTE
1489 B6 09     MOV DH,CBOX_ROW/2 ; SET LOOP COUNT ( 18 ROW / 2 SCAN )
148B E8 14C8 R   CALL G_W_2       ; WRITE RIGHT PART

148E           GWRT212: MOV AX,2          ;--- END
148E B8 0002   ADD [BP+WGPOSH],AX ; ADVANCE WRITE POSITION

1494 07         POP ES           ; RESTORE ES
1495 5F         POP DI           ; RESTORE DI
1496 1F         POP DS           ; RESTORE DS

1497 C3        RET

1498           G_WRITE2 ENDP

```

```

;-----
;
; G_W_1
;
; THIS ROUTINE WRITES ONE CHARACTER PATTERN INTO REGEN
; ( 640X200 4 COLOR )
;
; INPUT  BL =  ATTRIBUTE
;        DH =  ROW NUMBER OF ONE SCAN BANK
;        DS:SI = CHARACTER PATTERN ADDRESS
;        ES:DI = REGEN ADDRESS TO WRITE
;
; OUTPUT  NONE
;
; CALL   EX_2BIT
;        G_W_R1
;
; VOLATILE  AX,BX,DH,SI
;-----
; ASSUME CS:CODE, DS:STACK, ES:VIDEO_RAM

```

```

1498           G_W_1 PROC NEAR
1498 E8 152D R   CALL EX_2BIT     ; EXPAND LOW 2 BITS IN BX
;--- WRITE CHARACTER
149B 57         PUSH DI          ; SAVE REGEN POINTER
149C           GW11: CALL G_W_R1        ; DO FIRST DOT ROW
149C E8 1554 R   ADD DI,2000H     ; ADJUST REGEN POINTER
149F 81 C7 2000 CALL G_W_R1        ; DO NEXT DOT ROW
14A3 E8 1554 R   ADD DI,2000H     ; ADJUST REGEN POINTER
14A6 81 C7 2000

```

Appendix A.

```

14AA FE CE
14AC 74 12
14AE
14AE E8 1554 R
14B1 81 C7 2000
14B5 E8 1554 R
14B8 81 EF 5F60

14BC FE CE
14BE 75 DC
14C0
14C0 5F
14C1 C3
14C2

```

```

DEC DH ; ALL DONE ?
JZ GW13 ; NO

GW12: CALL G_W_R1 ; DO NEXT DOT ROW
      ADD DI,2000H ; ADJUST REGEN POINTER
      CALL G_W_R1 ; DO NEXT DOT ROW
      SUB DI,6000H-160 ; ADJUST REGEN POINTER TO NEXT ROW

DEC DH
JNZ GW11 ; KEEP GOING

GW13: POP DI ; RECOVER REGEN POINTER
      RET

G_W_1 ENDP

```

```

-----
;
; G_W_10
;
; THIS ROUTINE WRITES ONE CHARACTER PATTERN INTO REGEN ( FOR ODD ROW )
; ( 640X200 4 COLOR )
;
; INPUT BL = ATTRIBUTE
;        DH = ROW NUMBER OF ONE SCAN BANK
;        DS:SI = CHARACTER PATTERN ADDRESS
;        ES:DI = REGEN ADDRESS TO WRITE
;
; OUTPUT NONE
;
; CALL EX_2BIT
;      (G_W_1)
;
; VOLATILE AX,BX,DH,SI
;
-----

```

ASSUME CS:CODE, DS:STACK, ES:VIDEO\_RAM

```

14C2
14C2 E8 152D R
14C5 57
14C6 E8 E6
14C8

```

```

G_W_10 PROC NEAR
      CALL EX_2BIT ; EXPAND LOW 2 COLOR BITS IN BL
              ; --- WRITE CHARACTER
      PUSH DI ; SAVE REGEN POINTER
      JMP GW12
G_W_10 ENDP

```

```

-----
;
; G_W_2
;
; THIS ROUTINE WRITES ONE CHARACTER PATTERN INTO REGEN
; ( 320X200 4 COLOR, 320X200 4 SHADE )
;
; INPUT BL = ATTRIBUTE
;        DH = ROW NUMBER OF ONE SCAN BANK
;        DS:SI = CHARACTER PATTERN ADDRESS
;        ES:DI = REGEN ADDRESS TO WRITE
;
; OUTPUT NONE
;
; CALL G_W_R2
;
; VOLATILE AX,BX,DH,SI
;
-----

```

```

14C8
14C8 8A D3
14CA 80 E3 03
14CD 8A C3
14CF 51
14D0 B9 0003
14D3
14D3 D0 E0
14D5 D0 E0
14D7 0A D8
14D9 E2 F8
14DB 8A FB
14DD 59
14DE 57
14DF
14DF E8 1572 R
14E2 81 C7 2000
14E6 E8 1572 R
14E9 81 EF 1FB0
14ED FE CE
14EF 75 EE
14F1 5F
14F2 C3
14F3

```

```

G_W_2 PROC NEAR
      MOV DL,BL ; COPY ATTRIBUTE TO DL
              ; ---EXPANDS THE LOW 2 BITS IN BL TO FILL THE BX
      AND BL,3 ; ISOLATE THE COLOR BITS
      MOV AL,BL ; COPY TO AL
      PUSH CX ; SAVE REGISTER
      MOV CX,3 ; NUMBER OF TIMES TO DO THIS

GW21: SAL AL,1 ; LEFT SHIFT BY 2
      SAL AL,1 ; ANOTHER COLOR VERSION INTO BL
      OR BL,AL ; FILL ALL OF BL
      LOOP GW21

      MOV BH,BL ; FILL UPPER PORTION
      POP CX ; REGISTER BACK

      PUSH DI ; --- WRITE CHARACTER
              ; SAVE REGEN POINTER

GW22: CALL G_W_R2 ; DO FIRST 2 BYTES
      ADD DI,2000H ; NEXT SPOT IN REGEN
      CALL G_W_R2 ; DO NEXT 2 BYTES
      SUB DI,2000H-80

      DEC DH
      JNZ GW22 ; KEEP GOING

      POP DI ; RECOVER REGEN POINTER
      RET

G_W_2 ENDP

```

```

;-----
;
; G_M_4
;
; THIS ROUTINE WRITES ONE CHARACTER PATTERN INTO REGEN
; ( 160X200 16 COLOR, 320X200 16 COLOR )
;
; INPUT  BL =  ATTRIBUTE (LOW 4 BITS)
;        DH =  ROW NUMBER OF ONE SCAN BANK
;        [BP+WC_MODE]= CURRENT CRT MODE (MASKED)
;        DS:SI = CHARACTER PATTERN ADDRESS
;        ES:DI = REGEN ADDRESS TO WRITE
;
; OUTPUT  NONE
;
; CALL    EX_4BIT
;        G_M_R4
;
; VOLATILE  AX,BX,DH,SI
;-----

```

```

14F3
14F3 EB 1542 R
14F6 57
14F7
14F7 E8 158C R
14FA 81 C7 2000
14FE E8 158C R

1501 80 7E 09 09
1505 75 16

1507 FE CE
1509 74 1A

150B 81 C7 2000
150F
150F E8 158C R
1512 81 C7 2000
1516 E8 158C R
1519 81 EF 3FB0
151D
151D 81 EF 1FB0

1521 FE CE
1523 75 D2
1525
1525 5F
1526 C3

1527

```

```

G_M_4 PROC NEAR
CALL EX_4BIT ; EXPANDS THE LOW 4 BITS
;--- WRITE CHARACTER
PUSH DI ; SAVE REGEN POINTER
GW41:
CALL G_M_R4 ; EXPAND DOT ROW IN REGEN
ADD DI,2000H ; POINT TO NEXT REGEN ROW
CALL G_M_R4 ; EXPAND DOT ROW IN REGEN

CMP BYTE PTR [BP+WC_MODE],09H ; USING 32K REGEN AREA?
JNE GW43 ; JUMP IF 16K REGEN

DEC DH ; DECREMENT COUNTER
JZ GW44 ; ALL DONE

ADD DI,2000H ; POINT TO NEXT REGEN ROW
GW42:
CALL G_M_R4 ; EXPAND DOT ROW IN REGEN
ADD DI,2000H ; POINT TO NEXT REGEN ROW
CALL G_M_R4 ; EXPAND DOT ROW IN REGEN
SUB DI,4000H-80 ; ADJUST REGEN POINTER
GW43:
SUB DI,2000H-80 ; ADJUST REGEN POINTER TO NEXT ROW

DEC DH
JNZ GW41 ; KEEP GOING

GW44:
POP DI ; RECOVER REGEN POINTER
RET

G_M_4 ENDP

```

```

;-----
;
; G_M_40
;
; THIS ROUTINE WRITES ONE CHARACTER PATTERN INTO REGEN (ODD ROW)
; ( 160X200 16 COLOR, 320X200 16 COLOR )
;
; INPUT  BL =  ATTRIBUTE (LOW 4 BITS)
;        DH =  ROW NUMBER OF ONE SCAN BANK
;        [BP+WC_MODE]= CURRENT CRT MODE (HIBBLE)
;        DS:SI = CHARACTER PATTERN ADDRESS
;        ES:DI = REGEN ADDRESS TO WRITE
;
; OUTPUT  NONE
;
; CALL    EX_4BIT
;        G_M_R4
;
; VOLATILE  AX,BX,DH,SI
;-----

```

```

1527
1527 EB 1542 R
152A 57
152B EB E2
152D

```

```

G_M_40 PROC NEAR
CALL EX_4BIT ; EXPANDS THE LOW 4 BITS
;--- WRITE CHARACTER
PUSH DI ; SAVE REGEN POINTER
JMP GW42 ;
G_M_40 ENDP

```

```

;-----
;
; EX_2BIT
;
; THIS ROUTINE EXPANDS LOW 2 COLOR BITS IN BL
; ( 640X200 4 COLOR )
;
; INPUT  BL =  ATTRIBUTE
;
; OUTPUT DL =  ATTRIBUTE
;        BX =  EXPANDED COLOR
;
; CALL    NONE
;
; VOLATILE  AX,BX
;-----

```

Appendix A.

```

;-----
; ASSUME CS:CODE, DS:STACK, ES:VIDEO_RAM
EX_2BIT PROC NEAR
152D      MOV     DL,BL           ; COPY ATTRIBUTE TO DL
152D 8A D3  XOR     AX,AX           ;--- EXPAND LOW 2 COLOR BITS IN BL (C1C0)
152F 33 C0  TEST    BL,1             ; INTO BX (C0C0C0C0C0C0C0C0C1C1C1C1C1C1)
1531 F6 C3 01 JZ     EX2B1            ; C0 COLOR BIT ON?
1534 74 D2  MOV     AH,0FFH        ; NO, JUMP
1536 B4 FF  ; YES, SET ALL C0 BITS ON
EX2B1:   TEST    BL,2           ; C1 COLOR BIT ON?
1538 F6 C3 02 JZ     EX2B2            ; NO, JUMP
1538 74 D2  MOV     AL,0FFH      ; YES, SET ALL C1 BITS ON
153D 80 FF  ;
EX2B2:   MOV     BX,AX       ; COLOR MASK IN BX
153F      RET
153F 8B D8  ;
1541 C3    ;
EX_2BIT ENDP

```

```

;-----
;
; EX_4BIT
;
; THIS ROUTINE EXPANDS LOW 4 COLOR BITS IN BL
; ( 160X200 16 COLOR, 320X200 16 COLOR )
;
; INPUT  BL = ATTRIBUTE (LOW 4 BITS)
;
; OUTPUT DL = ATTRIBUTE
;        BX = EXPANDED ATTRIBUTE
;
; CALL   NONE
;
; VOLATILE AX
;-----

```

```

EX_4BIT PROC NEAR
1542      MOV     DL,BL           ; COPY ATTRIBUTE TO DL
1542 8A D3  PUSH    CX             ;---EXPANDS THE LOW 4 BITS IN DL TO FILL BX
1544 51    ;
1545 80 E3 0F AND     BL,0FH          ; ISOLATE THE COLOR BITS
1548 8A FB  MOV     BH,BL          ; COPY TO BH
154A B1 04  MOV     CL,4           ; MOVE TO HIGH NIBBLE
154C D2 E7  SHL     BH,CL         ;
154E 0A FB  OR      BH,BL          ; MAKE BYTE FROM HIGH AND LOW NIBBLES
1550 8A DF  MOV     BL,BH          ;
1552 59    POP     CX
1553 C3    RET
EX_4BIT ENDP

```

```

;-----
;
; G_W_R1
;
; THIS ROUTINE WRITES ONE ROW OF CHARACTER PATTERN INTO REGEN
; EXPAND 1 DOT ROW OF A CHAR INTO 2 BYTES
; ( 640X200 4 COLOR )
;
; INPUT  BX = EXPANDED ATTRIBUTE
;        DL = ATTRIBUTE
;        DS:SI = CHARACTER PATTERN ADDRESS
;        ES:DI = REGEN ADDRESS TO WRITE
;
; OUTPUT SI = SI+1
;
; CALL   NONE
;
; VOLATILE AX
;-----

```

```

;-----
; ASSUME CS:CODE, DS:STACK, ES:VIDEO_RAM
G_W_R1 PROC NEAR
1554      LODSB
1554 AC    ; GET CODE POINT
1555 8A E0  MOV     AH,AL       ; COPY INTO AH
1557 23 C3  AND     AX,BX         ; SET COLOR
1559 F6 C2 80 TEST    DL,XOR_BIT     ; XOR FUNCTION?
155C 74 07  JZ     GWR11          ; NO, JUMP
155E 26: 32 25 XOR     AH,ES:[DI]    ; EXCLUSIVE OR WITH CURRENT DATA
1561 26: 32 45 01 XOR     AL,ES:[DI+1]
1565      GWR11:
1565 26: 88 25  MOV     ES:[DI],AH   ; STORE IN REGEN BUFFER
1568 26: 88 45 01 MOV     ES:[DI+1],AL
156C C3    RET
156D      G_W_R1 ENDP

```

```

;-----
;
; EX_W_R2
;

```



```

; THIS ROUTINE EXPANDS ONE ROW OF CHARACTER PATTERN
; AND WRITES IT INTO REGEN
;
; INPUT  AL = CHARACTER PATTERN
;        BX = EXPANDED ATTRIBUTE
;        DL = ATTRIBUTE
;        DS:SI = CHARACTER PATTERN ADDRESS
;        ES:DI = REGEN ADDRESS TO WRITE
;
; OUTPUT SI = SI+1
;
; CALL   EX_NIBBLE
;        (G_W_R2)
;
; VOLATILE  AX
;-----
156D
156D E8 15C2 R
1570 EB 04
1572
EX_W_R2 PROC NEAR
CALL EX_NIBBLE ; QUAD UP THE LOW NIBBLE
JMP SHORT GWR21
EX_W_R2 ENDP

```

```

;-----
; G_W_R2
; THIS ROUTINE WRITES ONE ROW OF CHARACTER PATTERN INTO REGEN
; EXPAND 1 DOT ROW OF A CHAR INTO 2 BYTES
; ( 320X200 4 COLOR, 320X200 4 SHADE )
;
; INPUT  BX = EXPANDED ATTRIBUTE
;        DL = ATTRIBUTE
;        DS:SI = CHARACTER PATTERN ADDRESS
;        ES:DI = REGEN ADDRESS TO WRITE
;
; OUTPUT SI = SI+1
;
; CALL   EX_BYTE
;
; VOLATILE  AX
;-----

```

```

1572
1572 AC
1573 E8 15A0 R
1576
1576 23 C3
1578 F6 C2 80
157B 74 07
157D 26: 32 25
1580 26: 32 45 01
1584
1584 26: 88 25
1587 26: 88 45 01
158B C3
158C
G_W_R2 PROC NEAR
LODSB ; GET CODE POINT
CALL EX_BYTE ; DOUBLE UP ALL THE BITS
GWR21: AND AX,BX ; CONVERT THEM TO FOREGROUND COLOR
; ( 0 BACK )
TEST DL,XOR_BIT ; IS THIS XOR FUNCTION?
JZ GWR22 ; NO, STORE IT IN AS IT IS
GWR22: XOR AH,ES:[DI] ; DO FUNCTION WITH HALF
XOR AL,ES:[DI+1] ; AND WITH OTHER HALF
MOV ES:[DI],AH ; STORE FIRST BYTE
MOV ES:[DI+1],AL ; STORE SECOND BYTE
RET
G_W_R2 ENDP

```

```

;-----
; G_W_R4
; THIS ROUTINE WRITES ONE ROW OF CHARACTER PATTERN INTO REGEN
; EXPAND 1 DOT ROW OF A CHAR INTO 4 BYTES
; ( 160X200 16 COLOR, 320X200 16 COLOR )
;
; INPUT  BX = EXPANDED ATTRIBUTE
;        DL = ATTRIBUTE
;        DS:SI = CHARACTER PATTERN ADDRESS
;        ES:DI = REGEN ADDRESS TO WRITE
;
; OUTPUT SI = SI+1
;
; CALL   EX_W_R2
;
; VOLATILE  AX
;-----

```

```

158C
158C AC
158D 50
158E 51
158F B1 04
1591 D2 E8
1593 59
1594 E8 156D R
1597 58
1598 47
1599 47
159A E8 156D R
G_W_R4 PROC NEAR
LODSB ; GET CODE POINT
PUSH AX ; SAVE
;
; MOV HIGH NIBBLE TO LOW
PUSH CX
MOV CL,4
SHR AL,CL
POP CX
;
; EXPAND TO 2 BYTES & PUT IN REGEN
; RECOVER CODE POINT
CALL EX_W_R2
POP AX
;
; ADJUST REGEN POINTER
INC DI
INC DI
CALL EX_W_R2 ; EXPAND LOW NIBBLE & PUT IN REGEN

```

Appendix A.

```

159D 4F          DEC     DI          ; RESTORE REGEN POINTER
159E 4F          DEC     DI
159F C3          RET
15A0            G_W_R4 ENDP

```

```

-----
;
; EX_BYTE          EXPAND BYTE
;
; THIS ROUTINE TAKES THE BYTE IN AL AND DOUBLES ALL
; OF THE BITS, TURNING THE 8 BITS INTO 16 BITS.
; THE RESULT IS LEFT IN AX
;
; INPUT  AL = BIT TO DOUBLE
;
; OUTPUT AX = DOUBLED BITS
;
; CALL   NONE
;
; VOLATILE  NONE
;
-----

```

```

15A0            EX_BYTE PROC    NEAR
15A0 52          PUSH    DX          ; SAVE REGISTERS
15A1 51          PUSH    CX
15A2 53          PUSH    BX
15A3 2B D2       SUB     DX,DX        ; RESULT REGISTER
15A5 89 0001     MOV     CX,1        ; MASK REGISTER
15A8            EXBYTE1:
15A8 8B D8       MOV     BX,AX        ; BASE INTO TEMP
15AA 23 D9       AND     BX,CX        ; USE MASK TO EXTRACT A BIT
15AC 0B D3       OR      DX,BX        ; PUT INTO RESULT REGISTER
15AE D1 E0       SHL    AX,1
15B0 D1 E1       SHL    CX,1        ; SHIFT BASE AND MASK BY 1
15B2 8B D8       MOV     BX,AX        ; BASE TO TEMP
15B4 23 D9       AND     BX,CX        ; EXTRACT THE SAME BIT
15B6 0B D3       OR      DX,BX        ; PUT INTO RESULT
15B8 D1 E1       SHL    CX,1        ; SHIFT ONLY MASK NOW, MOVING TO NEXT BASE
15BA 73 EC       JNC    EXBYTE1     ; USE MASK BIT COMING OUT TO TERMINATE
15BC 8B C2       MOV     AX,DX        ; RESULT TO PARM REGISTER
15BE 5B          POP     BX
15BF 59          POP     CX
15C0 5A          POP     DX
15C1 C3          RET          ; ALL DONE
15C2            EX_BYTE ENDP

```

```

-----
;
; EX_NIBBLE        EXPAND NIBBLE
;
; THIS ROUTINE TAKES THE LOW NIBBLE IN AL AND QUADS ALL
; OF THE BITS, TURNING THE 4 BITS INTO 16 BITS.
; THE RESULT IS LEFT IN AX
;
; INPUT  AL = NIBBLE DATA
;
; OUTPUT AX = QUADED BITS
;
; CALL   NONE
;
; VOLATILE  NONE
;
-----

```

```

15C2            EX_NIBBLE PROC  NEAR
15C2 52          PUSH    DX          ; SAVE REGISTERS
15C3 33 D2       XOR     DX,DX        ; RESULT REGISTER
15C5 A8 08       TEST   AL,8
15C7 74 03       JZ     EXNBL1
15C9 80 CE F0     OR     DH,0F0H
15CC            EXNBL1:
15CC A8 04       TEST   AL,4
15CE 74 03       JZ     EXNBL2
15D0 80 CE 0F     OR     DH,0FH
15D3            EXNBL2:
15D3 A8 02       TEST   AL,2
15D5 74 03       JZ     EXNBL3
15D7 80 CA F0     OR     DL,0F0H
15DA            EXNBL3:
15DA A8 01       TEST   AL,1
15DC 74 03       JZ     EXNBL4
15DE 80 CA 0F     OR     DL,0FH
15E1            EXNBL4:
15E1 8B C2       MOV     AX,DX        ; RESULT TO PARM REGISTER
15E3 5A          POP     DX          ; RECOVER REGISTERS
15E4 C3          RET          ; ALL DONE
15E5            EX_NIBBLE ENDP

```

```

;-----;
;
; GRAPH_POSITION
;
; THIS ROUTINE TAKES THE CURSOR POSITION CONTAINED IN
; THE MEMORY LOCATION, AND CONVERTS IT INTO AN OFFSET
; INTO THE REGEN BUFFER, ASSUMING ONE BYTE/CHAR.
; FOR MEDIUM RESOLUTION GRAPHICS, THE NUMBER MUST
; BE DOUBLED.
;
; INPUT NO REGISTERS.MEMORY LOCATION CURSOR_POSH IS USED
; OUTPUT AX CONTAINS OFFSET INTO REGEN BUFFER
;
; VOLATILE NONE
;-----;

```

ASSUME CS:CODE, DS:DATA, ES:VIDEO\_RAM

```

15E5
15E5 A1 035C R
15E8
15E8 53
15E9 51
15EA 8B DB
15EC 8A C4
15EE F6 26 004A R
15F2 8A 3E 0049 R
15F6 80 E7 0F
15F9 80 3E 0049 R 14
15FE 73 0B
1600 80 FF 09
1603 73 02
1605 D1 E0
1607 D1 E0
1609 EB 13
160B
160B 8B C8
160D 80 FF 09
1610 73 04
1612 D1 E0
1614 D1 E1
1616
1616 D1 E0
1618 D1 E0
161A D1 E9
161C 03 C1
161E
161E 2A FF
1620 03 C3
1622 59
1623 5B
1624 C3
1625

```

```

GRAPH_POSITION PROC NEAR
MOV AX,CURSOR_POSH ; GET CURRENT CURSOR
GRAPH_POSH LABEL NEAR
PUSH BX ; SAVE REGISTER
PUSH CX ; SAVE CX
MOV BX,AX ; SAVE A COPY OF CURRENT CURSOR
MOV AL,AH ; GET ROWS TO AL
MUL BYTE PTR CRT_COLS ; MULTIPLY BY BYTES/COLUMN
MOV BH,CRT_MODE ; GET CRT MODE
AND BH,KJ_OFF ; MASK VIDEO PROCESSOR NO. OFF
CMP CRT_MODE,KJGRAPH; KANJI GRAPHICS MODE ?
JAE GRPOS2 ; YES
;--- AN/ANK GRAPHICS MODE
CMP BH,9 ; MODE USING 32K REGEN?
JNC GRPOS1 ; YES, JUMP
SHL AX,1 ; MULTIPLY * 4 SINCE 4 ROWS/BYTE
GRPOS1: SHL AX,1
JMP SHORT GRPOS4
GRPOS2: ;--- KANJI GRAPHICS MODE
MOV CX,AX ; SAVE AX VALUE FOR AFTER CALCULATION
CMP BH,9 ; MODE USING 32K REGEN?
JNC GRPOS3 ; YES, JUMP
SAL AX,1 ; MULTIPLY * 9 SINCE 18/2=9 ROWS/BYTE
SAL CX,1 ;
GRPOS3: SAL AX,1 ;
SAL AX,1 ; MULTIPLY * 4.5 SINCE 18/4=4.5 ROWS/BYTE
SHR CX,1 ;
ADD AX,CX ;
GRPOS4: SUB BH,BH ; ISOLATE COLUMN VALUE
ADD AX,BX ; DETERMINE OFFSET
POP CX ; RESTORE CX
POP BX ; RECOVER POINTER
RET ; ALL DONE
GRAPH_POSITION ENDP

```

### GRAPHICS WRITE

THIS ROUTINE WRITES THE ASCII CHARACTER TO THE CURRENT POSITION ON THE SCREEN.

```

INPUT AH = CURRENT CRT MODE (MASKED)
AL = CHARACTER TO WRITE
BL = COLOR ATTRIBUTE TO BE USED FOR FOREGROUND COLOR
IF BIT 7 IS SET, THE CHAR IS XOR'D INTO THE REGEN BU
(0 IS USED FOR THE BACKGROUND COLOR)
CX = NUMBER OF CHARS TO WRITE
DS = DATA SEGMENT
ES = REGEN SEGMENT

```

OUTPUT NOTHING

```

CALL FONT
GRAPH_POSITION
G_W_1
G_W_2
G_W_4

```

```

WORK [BP+WC_MODE]= CURRENT CRT MODE (MASKED)
[BP+WFONT - WFONT+7] = FONT PATTERN

```

VOLATILE AX,BX,CX,DX,SI,DI,DS

FOR BOTH ROUTINES, THE IMAGES USED TO FORM CHARS ARE CONTAINED IN ROM.

ASSUME CS:CODE, DS:DATA, ES:VIDEO\_RAM

GRAPHICS\_WRITE PROC NEAR

1625

Appendix A.

```

1625 55          PUSH  BP          ; SAVE CURRENT BP
1626 83 EC 12    SUB   SP,GW_LOCAL    ; ALLOCATE LOCAL WORK AREA
1629 88 EC       MOV   BP,SP      ; ASSIGN BP AS FRAME POINTER

162B 88 66 09    MOV   [BP+WC_MODE],AH ; SAVE CURRENT CRT MODE

162E 06         PUSH  ES          ; SAVE REGEN SEGMENT
162F 50         PUSH  AX          ; SAVE CODE POINT AND CRT MODE VALUE

1630 53         PUSH  BX          ; SAVE COLOR ATTRIBUTE
1631 51         PUSH  CX          ; SAVE NUMBER OF CHAR

1632 16         PUSH  SS          ; SET DESTINATION SEGMENT FOR FONT
1633 07         POP   ES          ; TO ES
                        ASSUME ES:STACK

1634 8D 5E 0A    LEA  BX,[BP+WFONT]   ; SET DESTINATION OFFSET FOR FONT TO BX

1637 32 E4      XOR   AH,AH         ; CLEAR FOR 1 BYTE CODE
1639 8B C8      MOV   CX,AX        ; SET CHARACTER CODE TO CX

163B 80 80      MOV   AL,80H       ; SET FUNCTION OF REQUEST FONT WITH FULL BOX
163D 86 00      MOV   DH,0         ; INDICATES ANK MODE
163F E8 1A63 R  CALL  FONT         ; GET FONT PATTERN

1642 8B F3      MOV   SI,BX        ; SET TOP ADDRESS OF FONT PATTERN

1644 59         POP   CX          ; RESTORE NUMBER OF CHAR
1645 5B         POP   BX          ; RESTORE COLOR ATTRIBUTE

1646 E8 15E5 R   CALL  GRAPH_POSITION ;---DETERMINE POSITION IN REGEN BUFFER TO PUT CODE POINTS
1649 8B F8      DI,AX          ; FIND LOCATION IN REGEN BUFFER
                        ; REGEN POINTER IN DI
164B 58         POP   AX          ;--- DETERMINE REGION TO GET CODE POINTS FROM
                        ; RECOVER CODE POINT AND CRT MODE

164C 06         PUSH  ES          ; SET FONT PATTERN SEGMENT
164D 1F         POP   DS          ; TO DS
                        ASSUME DS:STACK

164E 07         POP   ES          ; RESTORE REGEN SEGMENT
                        ASSUME ES:VIDEO_RAM

164F 80 FC 04    CMP   AH,4         ; 320X200 4 COLOR ?
1652 74 5B      JE    GRWRT9      ; YES

1654 80 FC 05    CMP   AH,5         ; 320X200 4 SHADE ?
1657 74 56      JE    GRWRT9      ; YES

1659 80 FC 0A    CMP   AH,0AH      ; 640X200 4 COLOR ?
165C 74 42      JE    GRWRT7      ; YES

165E 80 FC 06    CMP   AH,6         ; 640X200 2 SHADE ?
1661 75 2B      JHE   GRWRT5      ; NO, MODE IS 8 OR 9
1663 57         GRWRT1: PUSH  DI          ; SAVE POINTERS
1664 56         PUSH  SI          ;
                        ;--- 640X200 2 SHADE
1665 86 04      MOV   DH,8/2      ; NUMBER OF TIMES THROUGH LOOP
1667 3C         GRWRT2: LODSB          ; GET WORD FROM CODE POINTS
1668 F6 C3 80   TEST  BL,XOR_BIT  ; SHOULD WE USE THE FUNCTION
166B 75 04      JNZ  GRWRT3      ; TO PUT CHAR IN?

166D AA        STOSB          ; STORE IN REGEN BUFFER
166E AC        LODSB          ; GET NEXT ROW
166F EB 0A      JMP  SHORT GRWRT4

1671 26: 32 05    GRWRT3: XOR   AL,ES:[DI]   ; EXCLUSIVE OR WITH CURRENT DATA
1674 AA        STOSB          ; STORE THE CODE POINT
1675 AC        LODSB          ; AGAIN FOR ODD FIELD
1676 26: 32 85 1FFF XOR   AL,ES:[DI+2000H-1]
167B 26: 88 85 1FFF GRWRT4: MOV   ES:[DI+2000H-1],AL ; STORE IN SECOND HALF
1680 83 C7 4F   ADD   DI,79        ; MOVE TO NEXT ROW IN REGEN
1683 FE CE     DEC   DH          ; DONE WITH LOOP ?
1685 75 E0     JNZ  GRWRT2      ; NO

1687 5E        POP   SI          ;
1688 5F        POP   DI          ;

1689 47        INC   DI          ;
168A E2 D7     LOOP GRWRT1      ;

168C EB 2E     JMP  SHORT GRWRT11 ;--- END

168E D1 E7      GRWRT5: SAL  DI,1        ;--- 160/320X200 16 COLOR
168E D1 E7      SAL  DI,1        ;
1692 56        GRWRT6: SAL  DI,1        ; OFFSET*4 SINCE 4 BYTES/CHAR
1692 56        ;
1693 B6 04     PUSH  SI          ;
1695 EB 14F3 R MOV   DH,8/(4/2)  ; SET LOOP COUNT ( 8 ROW / 4 SCAN / 2 DEC.)
1698 83 C7 04 CALL  G_M_4        ; WRITE ONE CHAR
169B 5E        ADD   DI,4        ;
169B 5E        POP   SI          ;

```

```

169C E2 F4          LOOP   GRWRT6          ;
169E EB 1C          JMP    SHORT GRWRT11 ;--- END
16A0 D1 E7          GRWRT7: SAL  DI,1          ;--- 640X200 4 COLOR
16A2 56             ; OFFSET#2 SINCE 2 BYTES/CHAR
16A2 56             GRWRT8: PUSH  SI          ;
16A3 B6 04          MOV    DH,8/(4/2)      ; SET LOOP COUNT ( 8 ROW / 4 SCAN / 2 DEC.)
16A5 E8 1498 R      CALL   G_W_1          ; WRITE ONE CHAR
16A8 47             INC    DI          ;
16A9 47             INC    DI          ;
16AA 5E             POP    SI          ;
16AB E2 F5          LOOP   GRWRT8          ;
16AD EB 0D          JMP    SHORT GRWRT11 ;--- END
16AF D1 E7          GRWRT9: SAL  DI,1          ;--- 320X200 4 COLOR/SHADE
16AF D1 E7          ; OFFSET#2 SINCE 2 BYTES/CHAR
16B1 56             GRWRT10: PUSH  SI          ;
16B1 56             ;
16B2 B6 04          MOV    DH,8/2         ; SET LOOP COUNT ( 8 ROW / 2 SCAN )
16B4 E8 14C8 R      CALL   G_W_2          ; WRITE ONE CHAR
16B7 47             INC    DI          ;
16B8 47             INC    DI          ;
16B9 5E             POP    SI          ;
16BA E2 F5          LOOP   GRWRT10         ;
16BC 83 C4 12       GRWRT11: ADD    SP,GW_LOCAL    ;--- END
16BC 5D             POP    BP            ; DEALLOCATE LOCAL WORK
16BF 5D             ; RESTORE BP
16C0 C3             RET
16C1

```

```

GRAPHICS_WRITE ENDP
;-----
; SET COLOR          INT 10H, AH = 11 (0BH)
; THIS ROUTINE WILL ESTABLISH THE BACKGROUND COLOR, THE
; OVERSCAN COLOR, AND THE FOREGROUND COLOR SET FOR GRAPHICS
;
; INPUT
; AH = CURRENT CRT MODE (MASKED)
; (BH) HAS COLOR ID
; IF BH=0, THE BACKGROUND COLOR VALUE IS SET
; FROM THE LOW BITS OF BL (0-31)
; IN GRAPHIC MODES, BOTH THE BACKGROUND AND
; BORDER ARE SET. IN ALPHA MODES, ONLY THE
; BORDER IS SET.
; IF BH=1, THE PALETTE SELECTION IS MADE
; BASED ON THE LOW BIT OF BL:
; 2 COLOR MODE:
; 0 = WHITE FOR COLOR 1
; 1 = BLACK FOR COLOR 1
; 4 COLOR MODES:
; 0 = GREEN, RED, YELLOW FOR
;   COLORS 1,2,3
; 1 = BLUE, CYAN, MAGENTA FOR
;   COLORS 1,2,3
; 16 COLOR MODES:
; ALWAYS SETS UP PALETTE AS:
; BLUE          FOR COLOR 1
; GREEN         FOR COLOR 2
; CYAN          FOR COLOR 3
; RED           FOR COLOR 4
; MAGENTA       FOR COLOR 5
; YELLOW        FOR COLOR 6
; LIGHT GRAY    FOR COLOR 7
; DARK GRAY     FOR COLOR 8
; LIGHT BLUE    FOR COLOR 9
; LIGHT GREEN   FOR COLOR 10
; LIGHT CYAN    FOR COLOR 11
; LIGHT RED     FOR COLOR 12
; LIGHT MAGENTA FOR COLOR 13
; LIGHT YELLOW  FOR COLOR 14
; WHITE        FOR COLOR 15
; (BL) HAS THE COLOR VALUE TO BE USED
;
; OUTPUT
; THE COLOR SELECTION IS UPDATED
;
; CALL          ENABLE_VG12
;              ENABLE_VG2
;              SUPREG
;
; VOLATILE     AX,BX,CX,DX
;-----
ASSUME CS:CODE, DS:DATA

```

```

16C1
16C1 EB 1DFC R      SET_COLOR PROC NEAR
16C4 BA 03DA       CALL  ENABLE_VG12 ; ENABLE VIDEO GENERATER 1 AND 2
; MOV    DX,VGA_CTL ; I/O PORT FOR PALETTE

```

Appendix A.

```

16C7
16C7 EC
16C8 A8 08
16CA 74 FB
16CC 80 06
16CE EE
16CF 32 C0
16D1 EE

16D2 0A FF
16D4 75 1D

16D6 80 FC 04
16D9 72 0D

16DB 80 10
16DD 80 FC 0B
16E0 75 02

16E2 34 0A
16E4
16E4 EE
16E5 8A C3
16E7 EE
16E8
16E8 80 02
16EA EE
16EB 8A C3
16ED EE

16EE A2 0066 R
16F1 EB 4F

16F3
16F3 B9 02B1 R

16F6 80 FC 06
16F9 74 12

16FB 80 FC 04
16FE 74 0A
1700 80 FC 05
1703 74 05

1705 80 FC 0A
1708 75 20
170A
170A B9 02B9 R
170D
170D 00 C8
170F 73 03

1711 83 C1 04
1714
1714 8B D9
1716 43

1717 B9 0083
171A 84 11
171C

171C 8A C4
171E EE
171F 2E: 8A 07
1722 EE

1723 FE C4
1725 43
1726 E2 F4

1728 EB 18

172A
172A 8A FC
172C B4 11
172E B9 800F
1731
1731 8A C4
1733 80 FF 0B
1736 75 02
1738 34 0A
173A
173A EE
173B 8A C4
173D EE

173E FE C4
1740 E2 EF

1742
1742 8A 26 8347 R
1746 E8 0929 R

1749 E8 1DD6 R
174C C3

174D

SETC1:
IN AL,DX ; SYNC UP VGA FOR REG ADDRESS
JZ AL,VERTRET ; IS VERTICAL RETRACE ON?
SETC1 SETC1 ; NO, WAIT UNTIL IT IS
;
MOV AL,PCSUPER ;
OUT DX,AL ; CLEAR SUPERIMPOSE CONTROL REGISTER
XOR AL,AL ; FOR SET PALETTE
OUT DX,AL ;
;
OR BH,BH ; IS THIS COLOR 0?
JNZ SETC3 ; OUTPUT COLOR 1
;--- HANDLE COLOR 0 BY SETTING THE
; BACKGROUND COLOR AND BORDER COLOR
CMP AH,GRAPHICS ; IN ALPHA MODE?
JZ SETC2 ; YES, JUST SET BORDER REG
;
MOV AL,IXPALET ; SET PALETTE REG 0
CMP AH,0BH ; 640 X 200 X 16 COLOR ?
JNE SETC11 ; NO
;
XOR AL,1010B ;
;
SETC11:
OUT DX,AL ; SELECT VGA REG
MOV AL,BL ; GET COLOR
OUT DX,AL ; SET IT
;
SETC2:
MOV AL,IXBORD ; SET BORDER REG
OUT DX,AL ; SELECT VGA BORDER REG
MOV AL,BL ; GET COLOR
OUT DX,AL ; SET IT
;
MOV CRT_PALLETTE,AL ; SAVE THE COLOR VALUE
JMP SHORT SETC10
;
SETC3:
MOV ;----- HANDLE COLOR 1 BY CHANGING PALETTE REGISTERS
CX,OFFSET PLTC20; POINT TO 2 COLOR TABLE ENTRY
;
CMP AH,6 ; 2 COLOR MODE?
JE SETC5 ; YES, JUMP
;
CMP AH,4 ; 4 COLOR MODE?
JE SETC4 ; YES, JUMP
CMP AH,5 ; 4 COLOR MODE?
JE SETC4 ; YES, JUMP
;
CMP AH,0AH ; 4 COLOR MODE?
JNE SETC8 ; NO, GO TO 16 COLOR SET UP
;
MOV CX,OFFSET PLTC40; POINT TO 4 COLOR TABLE ENTRY
;
ROR BL,1 ; SELECT ALTERNATE SET?
JNC SETC6 ; NO, JUMP
;
ADD CX,PLTC20L ; POINT TO NEXT ENTRY
;
MOV BX,CX ; TABLE ADDRESS IN BX
INC BX ; SKIP OVER BACKGROUND COLOR
;
MOV CX,PLTC20L-1 ; SET NUMBER OF REGS TO FILL
MOV AH,11H ; AH IS REGISTER COUNTER
;
SETC4:
MOV AL,AH ; GET REG NUMBER
OUT DX,AL ; SELECT IT
MOV AL,CS:[BX] ; GET DATA
OUT DX,AL ; SET IT
;
INC AH ; NEXT REG
INC BX ; NEXT TABLE VALUE
LOOP SETC7
;
JMP SHORT SETC10
;
SETC5:
ROR BL,1 ; SELECT ALTERNATE SET?
JNC SETC6 ; NO, JUMP
;
ADD CX,PLTC20L ; POINT TO NEXT ENTRY
;
MOV BX,CX ; TABLE ADDRESS IN BX
INC BX ; SKIP OVER BACKGROUND COLOR
;
MOV CX,PLTC20L-1 ; SET NUMBER OF REGS TO FILL
MOV AH,11H ; AH IS REGISTER COUNTER
;
SETC6:
MOV AL,AH ; GET REG NUMBER
OUT DX,AL ; SELECT IT
MOV AL,CS:[BX] ; GET DATA
OUT DX,AL ; SET IT
;
INC AH ; NEXT REG
INC BX ; NEXT TABLE VALUE
LOOP SETC7
;
JMP SHORT SETC10
;
SETC7:
MOV AL,AH ; GET REG NUMBER
OUT DX,AL ; SELECT IT
MOV AL,CS:[BX] ; GET DATA
OUT DX,AL ; SET IT
;
INC AH ; NEXT REG
INC BX ; NEXT TABLE VALUE
LOOP SETC7
;
JMP SHORT SETC10
;
SETC8:
MOV BH,AH ; SET MODE TO BH
MOV AH,11H ; AH IS REGISTER COUNTER
MOV CX,15 ; NUMBER OF PALETES
;
SETC9:
MOV AL,AH ; GET REG NUMBER
CMP BH,0BH ; 640 X 200 X 16 COLOR ?
JNE SETC91 ; NO
XOR AL,1010B ;
;
SETC91:
OUT DX,AL ; SELECT IT
MOV AL,AH ; GET REG NUMBER
OUT DX,AL ; SET PALETTE VALUE
;
INC AH ; NEXT REG
LOOP SETC9
;
SETC10:
MOV AH,SUPIPCR ;
CALL SET_SUPREG ; RESTORE SUPERIMPOSE CONTROL REGISTER
;
CALL ENABLE_VG2 ; ENABLE VIDEO GENERATER 2
RET
;
SET_COLOR
ENDP
;-----
;
; WRITE DOT
; INT 10H, AH = 12 (0CH)
;
; THESE ROUTINES WILL WRITE THE DOT AT THE INDICATED LOCATION

```

```

;
; INPUT
;
; AH = CURRENT CRT MODE (MASKED)
; DX = ROW (0-199) (THE ACTUAL VALUE DEPENDS ON THE MODE)
; CX = COLUMN (0-639) ( THE VALUES ARE NOT RANGE CHECKED )
; AL = DOT VALUE TO WRITE (1,2 OR 4 BITS DEPENDING ON MODE,
; REQ'D FOR WRITE DOT ONLY, RIGHT JUSTIFIED)
; BIT 7 OF AL = 1 INDICATES XOR THE VALUE INTO THE LOCATION
;
; DS = DATA SEGMENT
; ES = REGEN SEGMENT
;
; OUTPUT      NONE
;
; CALL        DOT_POSITION
;             GET_DIRSEG
;
; VOLATILE    BX,CX,DX,SI
;
;-----

```

ASSUME CS:CODE, DS:DATA, ES:VIDEO\_RAM

```

;
; REGISTER USAGE
;
; AH      DOT VALUE (TEMPORALY SAVED)
; AL      ACCUMULATER
;
; BH      CRT MODE
; BL      DOT VALUE (NOT CHANGED)
;
; CH      MOVED DATA FROM APA
; CL      SHIFT COUNT
;
; DH      MASK
; DL      MASK(COMPLIMENT)
;
;

```

```

174D      WRITE_DOT      PROC      NEAR
174D      8B D8          MOV      BX,AX      ; SAVE CRT MODE AND DOT VALUE TO BX
174F      80 FF 0B      CMP      BH,0BH      ; 640 X 200 X 16 COLOR ?
1752      75 04          JNE      ; NO
;
1754      D0 E8          SHR      AL,1        ; SHIFT PAL ADDR 2-3 TO LSB FOR WRITE IN V-RAM2
1756      D0 E8          SHR      AL,1
;
WDOT1:    CALL      DOT_POSITION      ; DETERMINE BYTE POSITION OF THE BIT
;
1758      8A F4          MOV      DH,AH      ; SET MASK TO DH
175D      F6 D4          NOT      AH
175F      8A D4          MOV      DL,AH      ; SET COMPLIMENT OF MASK TO DL
;
1761      D2 E8          SHR      AL,CL      ; SHIFT TO ADJUST IN THE DOT POSITION
;
1763      22 C6          AND      AL,DH      ; STRIP OFF THE OTHER BITS
1765      26 8A 2C      MOV      CH,ES:[SI] ; GET THE BYTE
;
1768      F6 C3 80      TEST     BL,XOR_BIT  ; XOR THE DOT ?
176B      75 6A          JNZ      WDOT7      ; YES
;
WDOT2:    AND      CH,DL      ; REMOVE THE INDICATED BITS
; OR      AL,CH          ; MERGE NEW BIT TO APA
; ; (ALL POINT ADDRESSABLE BUFFER)
; MOV      ES:[SI],AL    ; SET IT TO APA
;
1774      80 FF 0A      CMP      BH,0AH      ; 640 X 200 X 4/16 COLOR ?
1777      72 5D          JB       WDOT6      ; NO, END
;
1779      8A C3          MOV      AL,BL      ; GET THE DOT VALUE
177B      74 04          JE       WDOT2L     ; 4 COLOR
;
WDOT2L:   SHR      AL,1        ; SHIFT PAL ADDR 2-3 TO LSB FOR WRITE IN V-RAM2
177D      D0 E8          SHR      AL,1
;
WDOT21:   SHR      AL,1        ; SHIFT 1 BIT TO FIT IN ODD BYTE
1781      D0 E8          ROR      AL,1      ; LEFT JUSTIFY THE VALUE
1783      D0 C8          SHR      AL,CL      ; SHIFT TO ADJUST IN THE DOT POSITION
1785      D2 E8
;
1787      22 C6          AND      AL,DH      ; STRIP OFF THE OTHER BITS
1789      26 8A 6C 01   MOV      CH,ES:[SI+1] ; GET THE BYTE
;
178D      F6 C3 80      TEST     BL,XOR_BIT  ; XOR THE DOT ?
1790      75 49          JNZ      WDOT8      ; YES
;
WDOT3:    AND      CH,DL      ; REMOVE THE INDICATED BITS
; OR      AL,CH          ; MERGE NEW BITS TO APA
; MOV      ES:[SI+1],AL  ; SET IT TO APA
;
179A      80 FF 0B      CMP      BH,0BH      ; 640 X 200 X 16 COLOR ?
179D      75 37          JNE      ; NO
;
179F      E8 1D95 R     CALL     GET_DIRSEG  ; GET SEGMENT OF V-RAM 1
17A2      8E C0          MOV      ES,AX      ; SET IT TO ES
;
17A4      8A C3          MOV      AL,BL      ; GET DOT VALUE
17A6      D0 C8          ROR      AL,1      ; LEFT JUSTIFY THE VALUE
17A8      D2 E8          SHR      AL,CL      ; SHIFT TO ADJUST IN THE DOT POSITION
;
17AA      22 C6          AND      AL,DH      ; STRIP OFF THE OTHER BITS
17AC      26 8A 2C      MOV      CH,ES:[SI] ; GET THE BYTE
;
17AF      F6 C3 80      TEST     BL,XOR_BIT  ; XOR THE DOT ?
17B2      75 28          JNZ      WDOT9      ; YES
;
17B4      22 EA          AND      CH,DL      ; REMOVE OTHER BITS

```

Appendix A.

```

1786 0A C5
1788 26: 88 04
1788 26: 88 04
178B 8A C3
178D D0 E8
178F D0 C8
17C1 D2 E8
17C3 22 C6
17C5 26: 8A 6C 01
17C9 F6 C3 80
17CC 75 15
17CE 22 EA
17D0 0A C5
17D2
17D4 26: 88 44 01
17D6
17D6 C3
17D7
17D7 32 C5
17D9 EB 96
17DB
17DB 32 C5
17DD EB B7
17DF
17DF 32 C5
17E1 EB D5
17E3
17E3 32 C5
17E5 EB EB
17E7

WDOT4: OR AL,CH ; MERGE NEW DOT TO APA
MOV ES:[SI],AL ; SET IT TO APA
MOV AL,BL
SHR AL,1 ; SHIFT NEXT BIT TO FIT IN ODD BYTE
ROR AL,1
SHR AL,CL ; SHIFT TO SDJUST IN THE DOT POSITION
AND AL,DH ; MASK OTHER BITS OFF
MOV CH,ES:[SI+1] ; GET THE BYTE
TEST BL,XOR_BIT ; XOR THE DOT ?
JNZ WDOT10 ; NO
AND CH,DL ; REMOVE THE OTHER BITS
OR AL,CH ; MERGE MEW BITS TO APA
WDOT5: MOV ES:[SI+1],AL ; SET IT TO APA
WDOT6: RET
WDOT7: XOR AL,CH
JMP SHORT WDOT2
WDOT8: XOR AL,CH
JMP SHORT WDOT3
WDOT9: XOR AL,CH
JMP SHORT WDOT4
WDOT10: XOR AL,CH
JMP SHORT WDOT5
WRITE_DOT ENDP

```

```

-----
;
; DOT_POSITION
;
; THIS SUBROUTINE DETERMINES THE REGEN BYTE LOCATION OF THE
; INDICATED ROW COLUMN VALUE IN GRAPHICS MODE.
;
; INPUT
; AH = CURRENT CRT MODE (MASKED)
; AL = DOT VALUDE
; DX = ROW VALUE (0-199)
; CX = COLUMN VALUE (0-639)
;
; OUTPUT
; SI = OFFSET INTO REGEN BUFFER FOR BYTE OF INTEREST
; AH = MASK TO STRIP OFF THE BITS OF INTEREST
; AL = DOT VALUE (LEFT JUSTIFYED)
; CL = BITS TO SHIFT TO RIGHT JUSTIFY THE MASK IN AH
; DH = 0 BITS IN RESULT
;
; CALL NONE
;
; VOLATILE .CH,DL
;
-----

```

```

17E7
17E7 53
17E8 50
17E9 80 28
17EB 52
17EC 80 E2 FE
17EF 8A FC
17F1 80 FC 09
17F4 72 03
17F6 80 E2 FC
17F9
17F9 F6 E2
17FB 5A
17FC F6 C2 01
17FF 74 03
1801 05 2000
1804
1804 80 FF 09
1807 72 08
1809 F6 C2 02
180C 74 03
180E 05 4000
1811
1811 88 F0
1813 58
1814 88 D1

DOT_POSITION PROC NEAR
PUSH BX ; SAVE BX DURING OPERATION
PUSH AX ; WILL SAVE AL DURING OPERATION
;---DETERMINE 1ST BYTE IN INDICATED ROW BY MULTIPLYING ROW VALUE
; BY 40C LOW BIT OF ROW DETERMINES EVEN/ODD, 80 BYTES/ROW
MOV AL,40
PUSH DX ; SAVE ROW VALUE
AND DL,0FEH ; STRIP OFF ODD/EVEN BIT
MOV BH,AH ; SAVE CRT MODE TO BH
CMP AH,09H ; MODE USING 32K REGEN?
JC ; NO, JUMP
AND DL,0FCH ; STRIP OFF LOW 2 BITS
MUL DL ; AX HAS ADDRESS OF 1ST BYTE OF INDICATED ROW
POP DX ; RECOVER IT
TEST DL,1 ; TEST FOR EVEN/ODD
JZ DPOS2 ; JUMP IF EVEN ROW
ADD AX,2000H ; OFFSET TO LOCATION OF ODD ROWS
; EVEN_ROW
CMP BH,09H ; MODE USING 32K REGEN?
JC ; NO, JUMP
TEST DL,2 ; TEST FOR ROW 2 OR ROW 3
JZ DPOS3 ; JUMP IF ROW 0 OR 1
ADD AX,4000H ; OFFSET TO LOCATION OF ROW 2 OR 3
MOV SI,AX ; MOVE POINTER TO SI
POP AX ; RECOVER AL VALUE AND CRT MODE
MOV DX,CX ; COLUMN VALUE TO DX

```





Appendix A.

```

1872 8A D8          MOV     BL,AL          ; SAVE EVEN VALUE TO BL
1874 26: 8A 44 01  MOV     AL,ES:[SI+1]   ; GET THE ODD BYTE
1878 22 C4          AND     AL,AH          ; STRIP THE OTHER BITS OFF
187A 8A CD          MOV     CL,CH          ; RESTORE SHIFT COUNT
187C D2 E0          SHL     AL,CL          ; LEFT JUSTIFY THE VALUE
187E D0 C0          ROL     AL,1           ; RIGHT JUSTIFY THE VALUE
1880 D0 C0          ROL     AL,1           ; MOVE TO PALETTE ADDRESS 1 POSITION
1882 0A C3          OR      AL,BL          ; COMBINE EVEN/ODD BIT

1884 80 FF 0B       CMP     BH,0BH         ; 640 X 200 X 16 COLOR ?
1887 75 26          JNE                    ; NO, END

1889 D0 E0          SHL     AL,1           ; SHIFT LOW 2 BIT TO PALETTE ADDR 2-3 POSITION
188B D0 E0          SHL     AL,1           ;
188D 8A D8          MOV     BL,AL          ; SAVE EVEN VALUE TO BL

188F 30             PUSH    AX             ; SAVE AX
1890 E8 1D95 R      CALL   GET_DIRSEG     ; GET DIRECT SEGMENT OF V-RAM 1
1893 8E C0          MOV     ES,AX          ; SET IT TO ES
1895 58             POP     AX             ; RESTORE AX

1896 26: 8A 04       MOV     AL,ES:[SI]    ; GET THE BYTE
1899 22 C4          AND     AL,AH          ; STRIP THE OTHER BITS OFF
189B D2 E0          SHL     AL,CL          ; LEFT JUSTIFY THE VALUE
189D D0 C0          ROL     AL,1           ; RIGHT JUSTIFY THE VALUE
189F 0A D8          OR      BL,AL          ; COMEBINE EVEN BIT OF V-RAM 1 TO PAL ADDR 0

18A1 26: 8A 44 01  MOV     AL,ES:[SI+1]   ; GET THE BYTE
18A5 22 C4          AND     AL,AH          ; STRIP THE OTHER BITS OFF
18A7 D2 E0          SHL     AL,CL          ; LEFT JUSTIFY THE VALUE
18A9 D0 C0          ROL     AL,1           ; RIGHT JUSTIFY THE VALUE
18AB D0 C0          ROL     AL,1           ; RIGHT JUSTIFY THE VALUE
18AD 0A C3          OR      AL,BL          ; MOVE TO PALETTE ADDRESS 0 POSITION
18AF 18AF          RET                    ; COMBINE ALL 4 BITS

RDOT1:
18B0          RET
READ_DOT      ENDP

```

```

-----
WRITE_TTY          INT 10H, AH = 14 (0EH)
THIS INTERFACE PROVIDES A TELETYPE LIKE INTERFACE TO THE VIDEO CARD. THE INPUT CHARACTER IS WRITTEN TO THE CURRENT CURSOR POSITION, AND THE CURSOR IS MOVED TO THE NEXT POSITION. IF THE CURSOR LEAVES THE LAST COLUMN OF THE FIELD, THE COLUMN IS SET TO ZERO, AND THE ROW VALUE IS INCREMENTED. IF THE ROW VALUE LEAVES THE FIELD, THE CURSOR IS PLACED ON THE LAST ROW, FIRST COLUMN, AND THE ENTIRE SCREEN IS SCROLLED UP ONE LINE. WHEN THE SCREEN IS SCROLLED UP, THE ATTRIBUTE FOR FILLING THE NEWLY BLANKED LINE IS READ FROM THE CURSOR POSITION ON THE PREVIOUS LINE BEFORE THE SCROLL, IN CHARACTER MODE. IN GRAPHICS MODE, THE 0 COLOR IS USED.

INPUT
AH = CURRENT CRT MODE (MASKED)
AL = CHARACTER TO BE WRITTEN
NOTE THAT BACK SPACE, CAR RET, BELL AND LINE FEED ARE HANDLED AS COMMANDS RATHER THAN AS DISPLAYABLE GRAPHICS
BL = FOREGROUND COLOR FOR CHAR WRITE IF CURRENTLY IN A GRAPHICS MODE

OUTPUT
CALL          NONE
              BEEP
              VIDEO

VOLATILE      BH, CX, DX
-----

```

```

ASSUME CS:CODE, DS:DATA
18B0          WRITE_TTY  PROC  NEAR
18B0 50          PUSH    AX             ; SAVE REGISTERS
18B1 50          PUSH    AX             ; SAVE CHAR TO WRITE

18B2 8A 3E 0062 R  MOV     BH,ACTIVE_PAGE ; GET CURRENT PAGE SETTING
18B4 53          PUSH    BX             ; SAVE IT
18B7 8A DF          MOV     BL,BH          ; IN BL
18B9 32 FF          XOR     BH,BH
18BB D1 E3          SAL     BX,1           ; CONVERT TO WORD OFFSET
18BD 8B 97 035C R  MOV     DX,[BX*OFFSET CURSOR_POSM] ; GET CURSOR POSITION
18C1 5B          POP     BX             ; RECOVER CURRENT PAGE

18C2 58          POP     AX             ; RECOVER CHAR
18C3 8B 0E 0342 R  MOV     CX,TTY_1ST_CHAR ; --- DX NOW HAS THE CURRENT CURSOR POSITION
18C7 C7 06 0342 R 0000 MOV     TTY_1ST_CHAR,0 ; SAVE 1ST BYTE OF 2 BYTE CODE
18CD 3C 08          CMP     AL,BS          ; IS IT A BACKSPACE?
18CF 75 03          JNE                    ; NO

18D1 E9 19C2 R      JMP     WTY1           ; BACK_SPACE

18D4          WTY1:
18D4 3C 0D          CMP     AL,CR          ; IS IT A CARRIAGE RETURN?
18D6 75 05          JNE                    ; NO

18D8 32 D2          XOR     DL,DL          ;--- CARRIAGE RETURN
18DA E9 19BE R      JMP     WTY18         ; MOVE TO FIRST COLUMN
                          ; SET_CURSOR

```

18DD		WTY2:			
18DD	3C 0A		CMP	AL,LF	; IS IT A LINE FEED
18DF	75 03		JNE	WTY3	; NO
18E1	E9 1982 R		JMP	WTY11	; LINE_FEED
18E4		WTY3:			
18E4	3C 07		CMP	AL,BELL	; IS IT A BELL
18E6	75 08		JNE	WTY4	; NO
18E8	83 02		MOV	BL,2	;--- BELL
18EA	E8 0000 E		CALL	BEEP	; SET UP COUNT FOR BEEP
18ED	E9 198A R		JMP	WTY16	; SOUND THE POD BELL
18F0					; TTY_RETURN
18F0		WTY4:			
18F0	80 3E 0049 R 10		CMP	CRT_MODE,KJ_MODE;	KANJI MODE ?
18F5	72 7A		JB	WTY10	; NO, SKIP 2 BYTE CODE HANDLING
18F7	89 0E 0342 R		MOV	TTY_1ST_CHAR,CX	; RESTORE 1ST BYTE OF 2 BYTE CODE
18FB	0B C9		OR	CX,CX	; 1ST BYTE OF 2 BYTE CODE HAS BEEN SET ?
18FD	75 18		JNZ	WTY6	; YES
18FF	3C 80		CMP	AL,0A0H	; 1ST BYTE OF 2 BYTE CODE ?
1901	76 6E		JBE	WTY10	; NO, GO TO WRITE ONE CHARACTER
1903	3C FD		CMP	AL,0FDH	; NO, GO TO WRITE ONE CHARACTER
1905	73 6A		JAE	WTY10	; NO, GO TO WRITE ONE CHARACTER
1907	3C A0		CMP	AL,0A0H	; NO, GO TO WRITE ONE CHARACTER
1909	72 04		JB	WTY5	; YES
1908	3C DF		CMP	AL,0DFH	; NO, GO TO WRITE ONE CHARACTER
190D	76 62		JBE	WTY10	; NO, GO TO WRITE ONE CHARACTER
190F		WTY5:			
190F	8A E3		MOV	AH,BL	;--- CHARACTER IS 1ST BYTE OF 2 BYTE CODE
1911	A3 0342 R		MOV	TTY_1ST_CHAR,AX	; SET COLOR
1914	E9 198A R		JMP	WTY16	; SAVE 1ST BYTE OF 2 BYTE CODE AND SET FLAG
1917					; RETURN; WRITE PROCESS IS PENDING
1917	3C 40	WTY6:			
1917	72 08		CMP	AL,040H	;--- CHARACTER MUST BE 2ND BYTE OF 2 BYTE CODE
1918	3C FC		JB	WTY7	; 2ND BYTE OF 2 BYTE CODE ?
191D	77 04		CMP	AL,0FCH	; NO
191F	3C 7F		JA	WTY7	; NO
1921	75 08		CMP	AL,07FH	; NO
1921			JNE	WTY8	; YES
1923		WTY7:			
1923	C7 06 0342 R 0000		MOV	TTY_1ST_CHAR,0	;--- IGNORE 1ST BYTE
1929	EB 46		JMP	SHORT WTY10	; RESET FLAG OF 1ST BYTE
1928					; GO TO WRITE ONE BYTE
1928	8B 0E 004A R	WTY8:			
192F	49		MOV	CX,CRT_COLS	;--- WRITE 2 BYTE CODE
1930	3A D1		DEC	CX	; CHECK COLUMN BOUNDARY
1932	72 21		CMP	DL,CL	; ADJUST FOR COMPARE
1932			JB	WTY9	; IS COLUMN AT END OF LINE ?
1932					; NO
1934	50		PUSH	AX	; SAVE 2ND BYTE OF 2 BYTE CODE
1935	53		PUSH	BX	; SAVE FOREGROUND COLOR
1936	FF 36 0342 R		PUSH	TTY_1ST_CHAR	; SAVE 1ST BYTE OF 2 BYTE CODE
193A	C7 06 0342 R 0000		MOV	TTY_1ST_CHAR,0	; CLEAR FOR RECURSIVE CALL
1940	B8 0E20		MOV	AX,0E00H + ' '	; (AH)=0EH; WRITE SPACE
1943	CD 10		INT	VIDEO	; VIDEO I/O
1945	8A DF		MOV	BL,BH	; PAGE NUMBER
1947	32 FF		XOR	BH,BH	;
1949	D1 E3		SAL	BX,1	; CONVERT TO WORD OFFSET
194B	8B 97 035C R		MOV	DX,[BX+OFFSET CURSOR_POSN]	; GET CURSOR POSITION
194F	8F 06 0342 R		POP	TTY_1ST_CHAR	; RESTORE 2ND BYTE OF 2 BYTE CODE
1953	5B		POP	BX	; RESTORE 2ND BYTE OF 2 BYTE CODE
1954	58		POP	AX	; RESTORE FOREGROUND COLOR
1955		WTY9:			
1955	50		PUSH	AX	; RESTORE 2ND BYTE
1956	53		PUSH	BX	;--- WRITE 1ST BYTE OF 2 BYTE CODE
1957	A1 0342 R		MOV	AX,TTY_1ST_CHAR	; SAVE 2ND BYTE OF 2 BYTE CODE
195A	8A DC		MOV	BL,AH	; SAVE COLOR
195C	B4 0A		MOV	AH,0AH	; GET 1ST BYTE OF 2 BYTE CODE
195E	B9 0001		MOV	BL,AH	; SET COLOR OF 1ST BYTE
1961	CD 10		MOV	CX,1	; (AH)=0AH; WRITE CHARACTER AT CURRENT CURSOR
1963	5B		INT	VIDEO	; WRITE ONE CHARACTER
1963			POP	BX	; VIDEO I/O
1963					; RESTORE COLOR OF 2ND BYTE
1964	C7 06 0342 R 0000		MOV	TTY_1ST_CHAR,0	; RESET FLAG OF 1ST BYTE
196A	FE C2		INC	DL	; INCREMENT ROW
196C	B4 02		MOV	AH,2	; FUNCTION OF SET CURSOR POSITION
196E	CD 10		INT	VIDEO	; DO IT
1970	58		POP	AX	; RESTORE 2ND BYTE
1971		WTY10:			
1971	B4 0A		MOV	AH,10	;--- WRITE THE CHAR TO THE SCREEN
1973	B9 0001		MOV	CX,1	; WRITE CHAR ONLY
1976	CD 10		INT	VIDEO	; ONLY ONE CHAR
1976					; WRITE THE CHAR
1976					;--- POSITION THE CURSOR FOR NEXT CHAR
1978	FE C2		INC	DL	; POSITION THE CURSOR FOR NEXT CHAR
197A	3A 16 004A R		CMP	DL,BYTE PTR CRT_COLS	; TEST FOR COLUMN OVERFLOW
197E	72 3E		JB	WTY18	; SET_CURSOR
1980	32 D2		XOR	DL,DL	; COLUMN FOR CURSOR
1982		WTY11:			
1982	3A 36 0346 R		CMP	DH,CRT_ROWS	;--- LINE FEED
1986	72 34		JB	WTY17	; BOTTOM OF SCREEN ?
1986					; NO, SET_CURSOR INC
1986					;--- SCROLL REQUIRED

Appendix A.

```

1988 B4 02      MOV     AH,2
198A CD 10      INT     VIDEO          ; SET THE CURSOR
198C A0 0049 R  MOV     AL,CRT_MODE   ; --- DETERMINE VALUE TO FILL WITH DURING SCROLL
198F 24 0F      AND     AL,KJ_OFF     ; GET THE CURRENT MODE
1991 3C 04      CMP     AL,GRAPHICS   ; IN ALPHA MODE ?
1993 72 04      JC      WTY12         ; YES, READ ATTRIBUTE
1995 32 FF      XOR     BH,BH         ; FILL WITH BACKGROUND
1997 EB 10      JMP     SHORT WTY14   ; SCROLL-UP

1999          WTY12:
1999 B4 08      MOV     AH,8          ; --- READ ATTRIBUTE
199B CD 10      INT     VIDEO          ; READ CHAR/ATTR AT CURRENT CURSOR
199D 80 3E 0049 R 10  CMP     CRT_MODE,KJ_MODE; KJ MODE ?
19A2 72 03      JB      WTY13         ; NO
19A4 80 E4 77     AND     AH,HAM_MASK   ; MASK KJBIT OFF
19A7          WTY13:
19A7 8A FC      MOV     BH,AH         ; STORE IN BH
19A9          WTY14:
19A9 8B 0601     MOV     AX,601H       ; SCROLL ONE LINE
19AC 2B C9      SUB     CX,CX         ; UPPER LEFT CORNER
19AE 8A 36 0346 R  MOV     DH,CRT_ROWS   ; LOWER RIGHT ROW
19B2 8A 16 004A R  MOV     DL,BYTE PTR CRT_COLS ; LOWER RIGHT COLUMN
19B6 FE CA      DEC     DL
19B8          WTY15:
19B8 CD 10      INT     VIDEO          ; SCROLL UP THE SCREEN
19BA 58          WTY16:
19BA 58      POP     AX            ; RESTORE THE CHARACTER
19BB C3          RET                 ; RETURN TO CALLER

;----- SET NEW CURSOR POSITION
198C          WTY17:
198C FE C6     INC     DH            ; NEXT ROW
198E          WTY18:
198E B4 02     MOV     AH,2         ; ESTABLISH THE NEW CURSOR
19C0 EB F6     JMP     WTY15

;----- BACK SPACE
19C2          WTY19:
19C2 0A D2     OR      DL,DL        ; ALREADY AT END OF LINE
19C4 74 F8     JE     WTY18         ; SET_CURSOR
19C6 FE CA     DEC     DL           ; NO -- JUST MOVE IT BACK
19C8 EB F4     JMP     WTY18         ; SET_CURSOR

WRITE_TTY     ENDP
;-----
;
; VIDEO STATE          INT 10H, (AH) = 15 (0FH)
;-----
; RETURNS THE CURRENT VIDEO STATE IN AX
;
; INPUT  NONE
; OUTPUT NONE
;
; AH = NUMBER OF COLUMNS ON THE SCREEN
; AL = CURRENT VIDEO MODE
; BH = CURRENT ACTIVE DISPLAY PAGE
;
; CALL VOLATILE      NONE
;                   NONE
;-----
ASSUME CS:CODE, DS:DATA
VIDEO_STATE   PROC    NEAR
PUSH    BP          ; SAVE BP
MOV     AH,BYTE PTR CRT_COLS ; GET NUMBER OF COLUMNS
MOV     AL,CRT_MODE ; CURRENT MODE
MOV     BH,ACTIVE_PAGE ; GET CURRENT ACTIVE PAGE
MOV     BP,SP
MOV     [BP+FBX],BX ; SET FRAME POINTER
; SET RETURN BX
POP     BP
RET          ; RESTORE BP
; RETURN TO CALLER
VIDEO_STATE   ENDP
;-----
;
; SET_PALETTE          INT 10H, AH = 16 (10H)
;-----
; THIS ROUTINE WRITES THE PALETTE REGISTERS
;
; INPUT  AH = CRT MODE (MASKED)
;        AL = 0 SET PALETTE REGISTERS
;           (BH) = VALUE TO SET
;           (BL) = PALETTE REGISTER TO SET (00H-0FH)
;        AL = 1 SET BORDER COLOR REGISTER
;           (BH) = VALUE TO SET
;        AL = 2 SET ALL PALETTE REGS AND BORDER REG
;           ES:DX POINTS TO A 17 BYTE LIST
;           BYTE 0 - 15 ARE VALUES FOR PALETTE
;           BYTE 16 IS THE VALUE FOR THE BORDER REG
;
; OUTPUT NONE

```

```

; CALL      ENABLE_VG12
;           ENABLE_VG2
;           WAIT_VERTRET
;           SET_SUPREG
;
; VOLATILE  AX,BX,DX,SI,ES
;
; NOTE: PALETTE REGISTERS ARE WRITE ONLY.
-----
ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM

```

```

19DD          SET_PALETTE  PROC    NEAR
19DD 50        PUSH    AX          ; SAVE AX
19DE E8 1DFC R CALL    ENABLE_VG12   ; ENABLE BOTH VG1 AND VG2
19E1 8B F4    MOV     SI,SP       ; GET SEG FROM STACK
19E3 36: 8E 44 10 MOV    ES,SS:[SI+*F_ES] ; GET SEG FROM STACK
;                               ASSUME ES:INDETERMINATE
19E7 8B F2    MOV     SI,DX       ; OFFSET IN SI
19E9 E8 1A44 R CALL    WAIT_VERTRET   ; WAIT UNTIL VERTICAL RETRACE
19EC 80 06    MOV     AL,PCSUPER      ;
19EE EE       OUT     DX,AL       ; CLEAR SUPERIMPOSE CONTROL REGISTER
19EF 32 C0    XOR     AL,AL         ; FOR SET PALETTE
19F1 EE       OUT     DX,AL       ;
19F2 58       POP     AX          ; RESTORE AX
19F3 0A C0    OR     AL,AL         ; SET PALETTE REG?
19F5 74 0A    JZ     SPL1         ; YES, GO DO IT
19F7 3C 02    CMP    AL,2         ; SET ALL REGS?
19F9 74 1C    JE     SPL3         ; YES, GO DO IT
19FB 77 3C    JA     SPL5         ; SET BORDER COLOR REG?
;                               NO, DON'T DO ANYTHING
19FD 80 02    MOV    AL,IXBORD    ; --- SET BORDER COLOR REGISTER
19FF EB 0D    JMP    SHORT SPL2   ; SET BORDER COLOR REG NUMBER
1A01          SPL1:
1A01 8A C3    MOV    AL,BL        ; --- SET PALETTE REGISTER
1A03 24 0F    AND    AL,0FH       ; GET DESIRED REG NUMBER IN AL
1A05 0C 10    OR     AL,IXPALET   ; STRIP UNUSED BITS
;                               MAKE INTO REAL REG NUMBER
1A07 80 FC 0B MOV    AH,0BH       ; 640 X 200 X 16 COLOR ?
1A0A 75 02    JNE    SPL2         ; NO
1A0C 34 0A    XOR    AL,1010B     ; MAP TO REAL REG NUMBER
1A0E EE       OUT    DX,AL        ; SELECT REG
1A0F 8A C7    MOV    AL,BH        ; GET DATA IN AL
1A11 EE       OUT    DX,AL        ; SET NEW DATA
1A12 32 C0    XOR    AL,AL        ; SET REG 0 SO DISPLAY WORKS AGAIN
1A14 FE       OUT    DX,AL
1A15 EB 22    JMP    SHORT SPL5
1A17          SPL3:
1A17 B7 10    MOV    BH,IXPALET   ; --- SET ALL PALETTE REGS AND BORDER REG
1A19          SPL4:
1A19 8A C7    MOV    AL,BH        ; BH IS REG COUNTER
1A1B 80 FC 0B CMP    AH,0BH       ; REG ADDRESS IN AL
1A1E 75 02    JNE    SPL41        ; 640 X 200 X 16 COLOR ?
;                               NO
1A20 34 0A    XOR    AL,1010B     ; MAP TO REAL REG NUMBER
1A22 EE       OUT    DX,AL        ; SELECT IT
1A23 26: 8A 04 MOV    AL,BYTE PTR ES:[SI] ; GET DATA
1A26 EE       OUT    DX,AL        ; PUT IN VGA REG
1A27 46       INC    SI          ; NEXT DATA BYTE
1A28 FE C7    INC    BH          ; NEXT REG
1A2A 80 FF 20 CMP    BH,20H       ; LAST PALETTE REG?
1A2D 72 EA    JB     SPL4         ; NO, DO NEXT ONE
1A2F 80 02    MOV    AL,IXBORD    ; SET BORDER REG
1A31 EE       OUT    DX,AL        ; SELECT IT
1A32 26: 8A 04 MOV    AL,BYTE PTR ES:[SI] ; GET DATA
1A35 EE       OUT    DX,AL        ; PUT IN VGA REG
1A36 A2 0066 R MOV    CRT_PALETTE,AL ; SAVE IN RAM
1A39          SPL5:
1A39 8A 26 0347 R MOV    AH,SUPIPCR   ; RESTORE SUPERIMPOSE CONTROL REGISTER
1A3D E8 0929 R CALL    SET_SUPREG  ;
1A40 E8 1DD6 R CALL    ENABLE_VG2   ; ENABLE VIDEO GENERATER 2
1A43 C3       RET                ; ALL DONE
1A44          SET_PALETTE  ENDP

```

```

-----
;
; WAIT_VERTRET      WAIT VERTICAL RETRACE
;
; THIS ROUTINE WAITS UNTIL A VERTICAL RETRACE BEGIN
;
; INPUT            NONE

```

Appendix A.

```

;
; OUTPUT          NONE
; VOLATILE       AL,DX
;
;-----
WAIT_VERTRET    PROC    NEAR
1A44             MOV     DX,VGA_CTL    ; SET VGA CONTROL PORT
WAITV1:         IN      AL,DX          ; GET VGA STATUS
1A44 BA 03DA     AND     AL,VERTRET    ; IN VERTICAL RETRACE?
1A47 EC         JNZ     WAITV1      ; YES, WAIT FOR IT TO GO AWAY
1A48 24 08
1A4A 75 FB
WAITV2:         IN      AL,DX          ; GET VGA STATUS
1A4C EC         AND     AL,VERTRET    ; IN VERTICAL RETRACE?
1A4D 24 08     JZ      WAITV2      ; NO, WAIT FOR IT
1A4F 74 FB
RET
WAIT_VERTRET    ENDP
1A51 C3
1A52
;-----
; NO_OPERATION          INT 10H, AH = 17,18
;-----
ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM
NO_OPERATION    PROC    NEAR
1A52             RET
NO_OPERATION    ENDP
1A53
;-----
; FONT_PATTERN          INT 10H, AH = 19
;-----
; THIS ROUTINE ACCEPTS OR RETURNS FONT PATTERN FROM/IN USER AREA
;
; INPUT
; AL = 0  REQUEST BASE-FONT
; AL = 80H REQUEST BASE-FONT WITH FULL CHARACTER BOX
; AL = 40H WRITE FONT PATTERN FROM USER AREA TO GAIJI RAM
;
; CX = INTERNAL CODE FOR REQUESTED FONT
;       FOR HANKAKU-FONT (CH)=0
; BX = OFFSET OF DATA AREA FOR FONT
;       NORMAL-BOX      FULL-BOX
;       -16X16 ; 32 BYTE  36 BYTE
;       -16X8  ; 16 BYTE  18 BYTE
;
; DS = DATA SEGMENT
;
; OUTPUT          FONT PATTERN
;
; CALL            CHECK_ROSS_CHAR
;                   FONT
;
; VOLATILE       AX,CX,SI,DI,ES
;-----
ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM
FONT_PATTERN    PROC    NEAR
1A53             PUSH   BP           ; SAVE BP
1A54 8B EC       MOV     BP,SP         ; SET SP TO BP
1A56 8E 46 10    MOV     ES,[BP+F_ES]    ; GET SEGMENT VALUE FROM STACK
ASSUME ES:INDETERMINATE
1A59 E8 1B65 R   CALL    CHECK_ROSS_CHAR ; CHECK AND CONVERT ROSSIAN CHARACTER
1A5C B6 10       MOV     DH,KJ_MODE      ; INDICATES KJ MODE
1A5E E8 1A63 R   CALL    FONT           ; PERFORM FONT FUNCTION
1A61 5D         POP     BP           ; RESTORE BP
1A62 C3         RET              ; RETURN TO CALLER
1A63
FONT_PATTERN    ENDP
;-----
; FONT
;-----
; THIS ROUTINE ACCEPTS OR RETURNS FONT PATTERN FROM/IN USER AREA
;
; INPUT
; AL = 0  REQUEST BASE-FONT
; AL = 80H REQUEST BASE-FONT WITH FULL CHARACTER BOX
; AL = 40H WRITE FONT PATTERN FROM USER AREA TO APA
;
; CX = INTERNAL CODE FOR REQUESTED FONT
;       FOR HANKAKU-FONT (CH)=0
; DH = CRT MODE
; ES:BX = DATA AREA FOR FONT
;       NORMAL-BOX      FULL-BOX
;       -16X16 ; 32 BYTE  36 BYTE
;       -16X8  ; 16 BYTE  18 BYTE
;
; DS = DATA SEGMENT

```

```

;
; OUTPUT (ES:BX) = FONT PATTERN
;
CALL      ENABLE_VG1
;
;         ENABLE_VG2
;         ENABLE_KJROM
;         ENABLE_VRAM
;         CVTCR
;
VOLATILE  AX,CX,SI,DI,ES
;
;
;-----

```

ASSUME CS:CODE ,DS:DATA

1A63	FONT	PROC	NEAR	
1A63 52		PUSH	DX	; SAVE CURRENT DX
1A64 1E		PUSH	DS	; SAVE CURRENT DS
1A65 50		PUSH	AX	; SAVE REGISTERS
1A66 E8 1DB0 R		CALL	ENABLE_VG1	; ENABLE VIDEO GENERATER 1
1A69 52		PUSH	DX	; SAVE DX
1A6A BA 03DA		MOV	DX,VGA_CTL	; GET GATE ARRAY ADDRESS
1A6D	FONT1:			
1A6D EC		IN	AL,DX	; SEE STATUS REGISTER
1A6E 24 08		AND	AL,VERTRET	; IN VERTICAL RETRACE ?
1A70 74 FB		JZ	FONT1	; NO, WAIT UNTIL IT COMES
1A72 5A		POP	DX	; RESTORE DX
1A73 E8 1DD6 R		CALL	ENABLE_VG2	; ENABLE VIDEO GENERATER 2
1A76 58		POP	AX	
1A77 E8 1888 R		CALL	ENABLE_KJROM	; ENABLE KJ-ROM
1A7A 8A E0		MOV	AH,AL	; SAVE FUNCTION TO AH
1A7C 24 7F		AND	AL,7FH	; MASK FULL BOX FLAG OFF
1A7E 80 FC 40		CMP	AH,40H	; WRITE FONT PATTERN ?
1A81 74 50		JE	FONT9	; YES
1A83 3C 00		CMP	AL,0	;----- REQUEST FONT PATTERN
1A85 75 74		JNE	FONT11	; REQUEST FONT ?
1A87 50		PUSH	AX	; SAVE REQUESTED FUNCTION
1A88 51		PUSH	CX	; SAVE INTERNAL CODE
1A89 8B C1		MOV	AX,CX	
1A8B E8 1B01 R		CALL	CVTCR	; GET KJ ROM SEGMENT ADDRESS
1A8E 8E D8		MOV	DS,AX	ASSUME DS:INDETERMINATE
1A90 33 F6		XOR	SI,SI	; SET CHARACTER PATTERN ADDRESS TO DS
1A92 8B FB		MOV	DI,BX	; CLEAR FOR ROW 0
1A94 59		POP	CX	; SET PATTERN RETURN ADDRESS
1A95 0A ED		OR	CH,CH	; RESTORE INTERNAL CODE
1A97 75 12		JNZ	FONT3	; 2 BYTE CODE ?
1A99 80 FE 10		CMP	DH,KJ_MODE	; KJ GRAPHICS MODE ?
1A9C 73 23		JAE	FONT6	; YES
1A9E 89 0008		MOV	CX,8	; REPEAT COUNT FOR ANK
1AA1 BE 0001		MOV	SI,1	; OFFSET FOR ANK
1AA4 58		POP	AX	
1AA5 80 E4 7F		AND	AH,7FH	; MASK FULL BOX FLAG OFF
1AA8 50		PUSH	AX	
1AA9 EB 19		JMP	SHORT FONT7	; CONTINUE
1AAB	FONT3:			
1AAB 89 0010		MOV	CX,16	;--- GET LEFT PART FONT OF 2 BYTE CODE
1AAE	FONT4:			; SET ROW NUMBER OF CHARACTER BOX
1AAE AD		LDSW		; GET ONE ROW
1AAF 24 7F		AND	AL,KJMASKL	; MASK OFF CONTROL BIT
1AB1 AA		STOSB		; PUT LEFT PART OF ONE ROW
1AB2 E2 FA		LOOP	FONT4	; REPEAT UNTIL EXHAUST LEFT PART
1AB4 58		POP	AX	; RESTORE FUNCTION
1AB5 50		PUSH	AX	; SAVE IT
1AB6 F6 C4 80		TEST	AH,80H	; FULL CHARACTER BOX ?
1AB9 74 03		JZ	FONT5	; NO
1ABB 33 C0		XOR	AX,AX	
1ABD AB	FONT5:	STOSW		; SET 0 FOR FULL BOX
1ABE BE 0001		MOV	SI,1	; SET OFFSET FOR RIGHT PART
1AC1	FONT6:			
1AC1 89 0010		MOV	CX,16	--- GET FONT OF 1 BYTE CODE OR RIGHT PART
1AC4	FONT7:			; SET ROW NUMBER OF CHARACTER BOX
1AC4 AD		LDSW		; GET ONE ROW
1AC5 AA		STOSB		; PUT RIGHT PART OF ONE ROW
1AC6 E2 FC		LOOP	FONT7	; REPEAT UNTIL EXHAUST LEFT PART
1AC8 58		POP	AX	; RESTORE FUNCTION
1AC9 F6 C4 80		TEST	AH,80H	; FULL CHARACTER BOX ?
1ACC 74 03		JZ	FONT8	; NO
1ACE 33 C0		XOR	AX,AX	
1AD0 AB		STOSW		; SET 0 FOR FULL BOX

Appendix A.

```

1AD1
1AD1 EB 28

1AD3
1AD3 81 F9 F040
1AD7 72 22
1AD9 81 F9 F07E
1ADD 77 1C

1ADF D1 E1
1AE1 81 E1 03FE
1AE5 81 C9 8800

1AE9 06
1AEA 1F

1AEB 8B F3

1AED 8E C1

1AEF 33 FF

1AF1 B9 0010
1AF4
1AF4 AC
1AF5 8A 64 0F

1AF8 AB
1AF9 E2 F9
1AFB
1AFB E8 1BB4 R

1AFE 1F
1AFF 5A
1800 C3

1801

FONT8: JMP SHORT FONT11 ; END
;----- WRITE FONT PATTERN
ASSUME DS:DATA

FONT9: CMP CX,EXT_CHAR_L ; IN EXTERNAL CHARACTER RANGE ?
JB FONT11 ; NO
CMP CX,EXT_CHAR_H ;
JA FONT11 ; NO

SAL CX,1 ; ADJUST FOR CHARACTER PATTERN ADDRESS
AND CX,03FEH ;
OR CX,8800H ; FORCE '10001' TO MSB

PUSH ES ; SET SOURCE SEGMENT
POP DS ;
MOV SI,BX ASSUME DS:INDETERMINATE ; SET SOURCE OFFSET
MOV ES,CX ASSUME ES:INDETERMINATE ; SET DESTINATION SEGMENT
XOR DI,DI ASSUME ES:INDETERMINATE ; SET DESTINATION OFFSET
MOV CX,16 ; SET NUMBER OF ROW

FONT10: LODSB ; GET LEFT PART OF ONE ROW
MOV AH,DS:[SI+15] ; GET RIGHT PART OF ONE ROW

STOSW ; STORE ONE ROW
LOOP FONT10 ; REPEAT ALL ROW

FONT11: CALL ENABLE_VRAM ; ENABLE VIDEO RAM

POP DS ASSUME DS:DATA ; RESTORE DS
POP DX ; RESTORE DX
RET

FONT ENDP

;-----
;
; CVTCR
;-----
; THIS ROUTINE CONVERT CODE TO KJ-ROM ADDRESS
;
; INPUT AX = INTERNAL CODE FOR REQUESTED FONT
;
; OUTPUT AX = SEGMENT ADDRESS OF KJ-ROM
;
; CALL NONE
; VOLATILE NONE
;-----
ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM

1801 CVTCR PROC NEAR

1801 OR AH,AH ; 2 BYTE CODE ?
1803 JNZ CVTCR1 ; YES

;--- 1 BYTE CODE
1805 SAL AX,1 ; ADJUST FOR CHARACTER PATTERN ADDRESS
1807 OR AX,8000H ; FORCE '1000000' TO MSB

180A JMP SHORT CVTCR5 ; END

180C
180C 3D 8100
180F 72 2D
1811 3D 8120
1814 76 0A

1816 3D 8140
1819 72 23
181B 3D 9873
181E 77 0A
1820
1820 D1 E0
1822 25 3FFE
1825 9D 8000
1828 EB 17

182A
182A 3D F040
182D 72 0F
182F 3D F07E
1832 77 0A

1834 D1 E0
1836 25 03FE
1839 9D 8800
183C EB 03

183E
183E 8B B0E8
1841
1841 C3

1842

CVTCR1:
CMP AX,RROSS_L_LOW ;--- 2 BYTE CODE
JB CVTCR4 ; IN REGEN CODE RANGE OF ROSSIAN CHARACTER ?
CMP AX,RROSS_L_HIGH ; NO
JBE CVTCR2 ; YES

CVTCR2: CMP AX,JIS1_L ; IN JIS 1 RANGE ?
JB CVTCR4 ; NO
CMP AX,JIS1_H ;
JA CVTCR3 ; NO

SAL AX,1 ; ADJUST FOR CHARACTER PATTERN ADDRESS
AND AX,3FFEH ;
OR AX,8000H ; FORCE '10' TO MSB
JMP SHORT CVTCR5 ; END

CVTCR3:
CMP AX,EXT_CHAR_L ;--- EXTERNAL CHARACTER
JB CVTCR4 ; IN EXTERNAL CHARACTER RANGE ?
CMP AX,EXT_CHAR_H ; NO
JA CVTCR4 ; NO

SAL AX,1 ; ADJUST FOR CHARACTER PATTERN ADDRESS
AND AX,03FEH ;
OR AX,8800H ; FORCE '10001' TO MSB
JMP SHORT CVTCR5 ; END

CVTCR4: MOV AX,OFFSET WHITE_BOX ; SET WHITE BOX
CVTCR5: RET
CVTCR ENDP

```



```

;-----
;
; CHECK_ROSS_CODE
;
; THIS ROUTINE TRANSLATE REGEN CODE TO ROSSIAN CHARACTER CODE
;
; INPUT CX = 2 BYTE REGEN CHARACTER CODE
;
; OUTPUT CX = 2 BYTE CHARACTER CODE
;
; CALL NONE
; VOLATILE NONE
;-----

```

```

1B42 CHECK_ROSS_CODE PROC NEAR
1B42 81 F9 8100 CMP CX,RROSS_L_LOW ; CHECK LOWER CASE OF ROSSIAN CHARACTER
1B46 72 1C JB RCODE2 ; NO
1B48 81 F9 8120 CMP CX,RROSS_L_HIGH ; NO
1B4C 77 06 JA RCODE1 ; NO
1B4E 81 C1 0340 ADD CX,ROSS_L_LOW - RROSS_L_LOW ; TRANSLATE
1B52 EB 10 JMP SHORT RCODE2
1B54 RCODE1: ; CHECK UPPER CASE OF ROSSIAN CHARACTER
1B54 81 F9 8200 CMP CX,RROSS_U_LOW ; NO
1B58 72 0A JB RCODE2 ; NO
1B5A 81 F9 8221 CMP CX,RROSS_U_HIGH ; NO
1B5E 77 04 JA RCODE2 ; NO
1B60 81 C1 0270 ADD CX,ROSS_U_LOW - RROSS_U_LOW ; TRANSLATE
1B64 C3 RCODE2: RET
1B65 CHECK_ROSS_CODE ENDP

```

```

;-----
;
; CHECK_ROSS_CHAR
;
; THIS ROUTINE TRANSLATE ROSSIAN CHARACTER CODE TO REGEN CODE
;
; INPUT CX = 2 BYTE CHARACTER CODE
;
; OUTPUT CX = 2 BYTE REGEN CHARACTER CODE
;
; CALL NONE
; VOLATILE NONE
;-----

```

```

1B65 CHECK_ROSS_CHAR PROC NEAR
1B65 81 F9 8440 CMP CX,ROSS_L_LOW ; CHECK LOWER CASE OF ROSSIAN CHARACTER
1B69 72 1C JB RCHAR2 ; NO
1B6B 81 F9 8460 CMP CX,ROSS_L_HIGH ; NO
1B6F 77 06 JA RCHAR1 ; NO
1B71 81 E9 0340 SUB CX,ROSS_L_LOW - RROSS_L_LOW ; TRANSLATE
1B75 EB 10 JMP SHORT RCHAR2
1B77 RCHAR1: ; CHECK UPPER CASE OF ROSSIAN CHARACTER
1B77 81 F9 8470 CMP CX,ROSS_U_LOW ; NO
1B7B 72 0A JB RCHAR2 ; NO
1B7D 81 F9 8491 CMP CX,ROSS_U_HIGH ; NO
1B81 77 04 JA RCHAR2 ; NO
1B83 81 E9 0270 SUB CX,ROSS_U_LOW - RROSS_U_LOW ; TRANSLATE
1B87 C3 RCHAR2: RET
1B88 CHECK_ROSS_CHAR ENDP

```

```

;-----
;
; ENABLE_KJROM
;
; THIS ROUTINE ENABLES KJ-ROM
;
; INPUT NONE
; OUTPUT NONE
; CALL DDS
; DISABLE_INT
; ENABLE_INT
; VOLATILE NONE
;-----

```

```

1B88 ENABLE_KJROM PROC NEAR
1B88 50 PUSH AX ;
1B89 53 PUSH BX ; SAVE REGISRES
1B8A 52 PUSH DX ;
1B8B 1E PUSH DS ;
1B8C EB 0000 E CALL DDS ; POINT DATA AREA
; ASSUME DS:DATA
1B8F EB 1B4 R CALL DISABLE_INT ; DISABLE ALL INTERRUPT
; DURING VRAM AND KJ-ROM SWITHING
1B92 BA 01FF MOV DX,SX08BASE ; ADDRESS OF ADDRESS CONTROLLER

```

Appendix A.

```

1895 8B 0937      MOV    BX,S8VRAM1*100H + VRAM1_OFF
1898  E8 1E22 R   CALL   OUT_GA          ; DISABLE VIDEO RAM 1
1898 8B 0A37      MOV    BX,S8VRAM2*100H + VRAM2_OFF
189E  E8 1E22 R   CALL   OUT_GA          ; DISABLE VIDEO RAM 2
18A1 8B 0780      MOV    BX,S8KJROM*100H + KJROM_ON
18A4  E8 1E22 R   CALL   OUT_GA          ; ENABLE KANJI ROM
18A7 C6 06 0353 R FF MOV    KJROM_STAT,TRUE ; KJ-ROM FLAG ON
18AC  E8 18EC R   CALL   ENABLE_INT     ; ENABLE INTERRUPT
18AF 1F           POP    DS              ;
18B0 5A           POP    DX              ;
18B1 5B           POP    BX              ; RESTORE REGISTERS
18B2 58           POP    AX              ;
18B3 C3           RET
18B4

```

ENABLE\_KJROM ENDP

```

-----
;
;
;   ENABLE_VRAM
;
;   THIS ROUTINE ENABLES VIDEO RAM
;
;   INPUT          NONE
;   OUTPUT         NONE
;   CALL           DDS
;                 DISABLE_INT
;                 ENABLE_INT
;
;   VOLATILE      NONE
;
-----

```

```

18B4      ENABLE_VRAM   PROC   NEAR
18B4 50      PUSH    AX          ;
18B5 53      PUSH    BX          ; SAVE REGISTERS
18B6 52      PUSH    DX          ;
18B7 1E      PUSH    DS          ;
18B8 E8 0000 E CALL    DDS            ; POINT DATA AREA
                        ASSUME DS:DATA
18BB E8 18E4 R CALL    DISABLE_INT   ; DISABLE ALL INTERRUPT
                        ; DURING VRAM AND KJ-ROM SWITCHING
18BE BA 01FF      MOV    DX,SX08BASE   ; ADDRESS OF ADDRESS CONTROLLER
18C1 8B 0730      MOV    BX,S8KJROM*100H + KJROM_OFF
18C4 E8 1E22 R   CALL   OUT_GA          ; DISABLE KANJI ROM
18C7 8B 0987      MOV    BX,S8VRAM1*100H + VRAM1_ON
18CA 80 3E 034C R 08 CMP    CPU_PAGE,VRAM2_PAGE ; VIDEO RAM 1 ?
18CF 72 03      JB     EVRAM1         ; YES
18D1 8B 0AB7      MOV    BX,S8VRAM2*100H + VRAM2_ON
18D4  E8 1E22 R   CALL   OUT_GA          ; ENABLE VIDEO RAM
18D7 C6 06 0353 R 00 MOV    KJROM_STAT,FALSE; KJ-ROM FLAG OFF
18DC  E8 18EC R   CALL   ENABLE_INT     ; ENABLE INTERRUPT
18DF 1F           POP    DS              ;
18E0 5A           POP    DX              ;
18E1 5B           POP    BX              ; RESTORE REGISTERS
18E2 58           POP    AX              ;
18E3 C3           RET
18E4      ENABLE_VRAM   ENDP

```

```

-----
;
;
;   DISABLE_INT
;
;   THIS ROUTINE DISABLES ALL INTERRUPT
;
;   INPUT          NONE
;   OUTPUT         NONE
;   CALL           NONE
;                 NONE
;   VOLATILE      NONE
;
-----

```

```

18E4      DISABLE_INT  PROC   NEAR
18E4 50      PUSH    AX
18E5 FA      CLI
18E6 80 10      MOV    AL,DISABLE_NMI ; DISABLE INTERRUPTS
18E8 E6 A0      OUT   NMI_PORT,AL    ; DISABLE NMI AND HOLD REQUEST
18EA 58      POP    AX
18EB C3      RET
18EC      DISABLE_INT  ENDP

```

```

1BEC
1BEC 50
1BED 1E
1BEE E8 0000 E

1BF1 F6 06 0355 R FF
1BF6 75 05

1BF8 B0 80
1BFA E6 A0
1BFC FB
1BFD
1BFD 1F
1BFE 58
1BFF C3

1C00

```

```

-----
;
;
;     ENABLE_INT
;
;     THIS ROUTINE ENABLES ALL INTERRUPT
;
;     INPUT      NONE
;     OUTPUT     NONE
;     CALL       DDS
;     VOLATILE   NONE
;
-----
ENABLE_INT  PROC  NEAR
                PUSH  AX
                PUSH  DS
                CALL  DDS                ; POINT DATA AREA
                ASSUME DS:DATA
                TEST  BYTE PTR IEP_CTRL,TRUE ; INTERRUPT ENABLED PROHIBIT ?
                JNZ  ENBLI1             ; YES, DO NOT ENABLE
                MOV   AL,ENABLE_NMI     ; ENABLE NMI
                OUT  NMI_PORT,AL       ;
                STI   ; ENABLE INTERRUPTS
ENBLI1:
                POP  DS
                POP  AX
                RET
ENABLE_INT  ENDP

```

```

1C00
1C00 EB 1DB0 R
1C03 0A C0
1C05 75 0F
1C07 50
1C08 E8 0940 R
1C0B 8A C7
1C0D E8 1C7D R
1C10 58
1C11 E8 0961 R
1C14 EB 63
1C16
1C16 3C 02
1C18 73 18
1C1A 8A 26 0347 R
1C1E 8A C7
1C20 D0 E0

```

```

-----
;
;
;     SUPERIMPOSE          INT 10H, AH = 20 (14H)
;-----
;     THIS ROUTINE CONTROLS SUPERIMPOSE FUNCTION
;
;     INPUT  AL = 0  SET MODE OF VIDEO GENERATER 1
;             BH = 4  320X200 4 COLOR (ANK)
;             BH = 5  RESERVED
;             BH = 6  RESERVED
;             BH = 7  NOT VALID
;             BH = 8  160X200 16 COLOR (ANK)
;             BH = 9  320X200 16 COLOR (ANK)
;             BH = A  640X200 4 COLOR (ANK)
;             BH =14  320X200 4 COLOR (KJ)
;             BH =15  RESERVED
;             BH =16  RESERVED
;             BH =17  NOT VALID
;             BH =18  160X200 16 COLOR (KJ)
;             BH =19  320X200 16 COLOR (KJ)
;             BH =1A  640X200 4 COLOR (KJ)
;
;             AL = 1  SET SUPERIMPOSE
;                     BH = 0  OFF
;                     BH = 1  ON
;
;             AL = 2  SET FORGROUND PAGE
;                     BH = 0  VRAM-1
;
;             AL = 3  SET TRANSPARENT PALETTE REGISTER
;                     BH = PALETTE RIGISTER NUMBER
;
;             AL = 4  SET SUPERIMPOSE MODE
;                     BH = 0: PRIORITY
;                     BH = 1: XOR
;                     BH = 2: AND
;                     BH = 3: OR
;
;     OUTPUT      NONE
;
;     CALL        ENABLE_VG1
;                ERASE_SCURSOR
;                SUP_SET_MODE
;                APPEAR_SCURSOR
;                OUT_GA
;                ENABLE_VG2
;
;     VOLATILE    AX,BX,CX,DX,SI,DI,ES
;
-----
ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM
SUPERIMPOSE  PROC  NEAR
                CALL  ENABLE_VG1        ; SELECT VIDEO GENERATER 1
                OR   AL,AL              ; SUPERIMPOSE MODE SET ?
                JNZ  SIP1              ; NO
                PUSH  AX                ; SAVE CURRENT CRT MODE
                CALL  ERASE_SCURSOR    ; ERASE SOFTWARE CURSOR
                MOV   AL,BH            ; SET MODE TO AL
                CALL  SUP_SET_MODE     ; SET MODE OF VIDEO GENERATER 1
                POP  AX                ; RESTORE CRT MODE
                CALL  APPEAR_SCURSOR   ; APPEAR SOFTWARE CURSOR
                JMP  SHORT $IP7        ; END
SIP1:
                CMP  AL,2              ; SUPERIMPOSE ON/OFF FUCTION ?
                JAE  SIP2              ; NO
                ;--- AL=1 SUPERIMPOSE OFF/ON
                MOV  AH,SUPIPCR        ; GET LAST VALUE OF SUPERIM. CONT. REG.
                MOV  AL,BH            ; GET VALUE
                SHL  AL,1              ; SHIFT TO TRANSPARENT BIT POSITION

```



```

1CA6 72 0A                JB      SSETM1          ; NO
1CA8 80 FC 04            CMP     AH,4            ; CURRENT MODE IS HIGH BAND ?
1CAB 72 0F              JB      SSETM3          ; YES
1CAD 80 FC 09            CMP     AH,9            ; CURRENT MODE IS HIGH BAND ?
1CB0 73 0A              JAE     SSETM3          ; YES
1CB2                    SSETM1:                ; - CURRENT MODE IS LOW BAND
1CB2 3C 09              CMP     AL,9            ; CHECK LOW BAND
1CB4 73 02              JAE     SSETM2          ; MODE CONFRICT
1CB6 EB 10              JMP     SHORT SSETM4    ; OK

1CB8                    SSETM2:                ; RESTORE AX
1CB8 58                POP     AX              ; BREAK
1CB9 E9 1D8D R          JMP     SSETM11

1CBC                    SSETM3:                ; - CURRENT MODE IS HIGH BAND
1CBC 3C 09              CMP     AL,9            ; CHECK HIGH BAND
1CBE 72 F8              JB      SSETM2          ; MODE CONFRICT

1CC0 50                PUSH   AX              ; SAVE AX
1CC1 E4 62              IN     AL,PORT_C       ; GET DATA FROM PORT-C
1CC3 A8 08              TEST   AL,EXP64K       ; 64K EXPANTION INSTALLED ?
1CC5 58                POP     AX              ; RESTORE AX
1CC6 75 F0              JNZ    SSETM2          ; NO

1CC8                    SSETM4:                ; SAVE CURRENT CRT MODE AND SETTING MODE(MASKED)
1CC8 50                PUSH   AX              ; SAVE MODE IN AH
1CC9 8A E7              MOV     AH,BH          ; SAVE MODE IN DI
1CCB 8B F8              MOV     DI,AX          ; SAVE MODE IN DI

1CCD EB 1A44 R          CALL   WAIT_VERTRET    ; WAIT UNTIL VERTICAL RETRACE
1CD0 32 C0              XOR     AL,AL          ; ---- TURN OFF VIDEO
1CD2 EE                OUT    DX,AL          ; SET VGA REG 0
                          ; SELECT IT

1CD3 A0 033D R          MOV     AL,CRT_MODE_SET2 ; GET LAST MODE SET
1CD6 24 F7              AND    AL,NOT_VIDEOENB ; TURN OFF VIDEO
1CD8 EE                OUT    DX,AL          ; SET IN GATE ARRAY

1CD9 59                POP     CX              ; ---- SET DEFAULT PALETTES
                          ; GET CURRENT CRT MODE AND SETTING MODE

1CDA F6 06 03FC R FF   TEST   SUPPRESS_PAL.TRUE ; SET PALETTE IS SUPRESSED ?
1CDF 75 2E              JNZ    SSETM7          ; YES, SKIP

1CE1 80 06              MOV     AL,PCSUPER     ;
1CE3 EE                OUT    DX,AL          ;
1CE4 32 C0              XOR     AL,AL          ; CLEAR SUPERIMPOSE CONTROL REGISTER
1CE6 EE                OUT    DX,AL          ; FOR SET PALETTE

1CE7 B4 10              MOV     AH,PCPALET     ; SET PALETTE REG 0
1CE9 BB 02BD R          MOV     BX,OFFSET_PLTC41 ; POINT TO TABLE ENTRY
1CEC 80 F9 04          CMP     CL,4           ; CHECK FOR 4 COLOR MODE
1CEF 74 11              JE     SSETM5          ; YES, JUMP
1CF1 80 F9 05          CMP     CL,5           ; CHECK FOR 4 COLOR MODE
1CF4 74 0C              JE     SSETM5          ; YES JUMP

1CF6 80 F9 08          CMP     CL,8           ; CHECK FOR 16 COLOR MODE
1CF9 74 11              JE     SSETM6          ; YES, JUMP
1CFB 80 F9 09          CMP     CL,9           ; CHECK FOR 16 COLOR MODE
1CFE 74 0C              JE     SSETM6          ; YES, JUMP

1D00 EB 0D              JMP     SHORT SSETM7    ; SKIP SET PALETTES

1D02                    SSETM5:                ; 2 COLOR MODE ?
1D02 80 FD 06          CMP     CH,06H         ; NO, SKIP SET PALETTES
1D05 75 08              JNE    SSETM7

1D07 E8 0535 R          CALL   SET_PALETTE4    ; SET PALETTE 4 COLOR
1D0A EB 03              JMP     SHORT SSETM7

1D0C                    SSETM6:                ; SET PALETTE 16 COLOR
1D0C E8 0545 R          CALL   SET_PALETTE16

1D0F                    SSETM7:                ; ---- SET UP M0 & M1 IN PAGREG
1D0F 8B C7              MOV     AX,DI          ; GET CURRENT MODE

1D11 B3 40              MOV     BL,40H         ; SET UP FOR 16K REGEN
1D13 3C 09              CMP     AL,09H         ; MODE USE 16K
1D15 72 02              JC     SSETM8          ; YES, JUMP

1D17 B3 C0              MOV     BL,0C0H        ; SET UP FOR 32K REGEN

1D19                    SSETM8:                ; SET PORT ADDRESS OF PAGREG
1D19 BA 03DF          MOV     DX,PAGREG     ; GET LAST DATA OUTPUT
1D1C A0 008A R          MOV     AL,PAGDAT     ; CLEAR M0 & M1 BITS
1D1F 24 3F              AND    AL,3FH         ; SET NEW BITS
1D21 0A C3              OR     AL,BL          ; STUFF BACK IN PORT
1D23 EE                OUT    DX,AL          ; SAVE COPY IN RAM
1D24 A2 008A R          MOV     PAODAT,AL

1D27 E8 0551 R          CALL   VGA_RESET      ; --- ENABLE VIDEO AND CORRECT PORT SETTING
                          ; RESET VGA

1D2A B0 01              MOV     AL,PCPALETHM  ; SELECT PALETTE MASK REGISTER
1D2C EE                OUT    DX,AL          ; SET IT
1D2D 2E: 8A 47 01      MOV     AL,CS:[BX+PCPALETHM] ; MASK EXTRA DATA OFF
1D31 24 0F              AND    AL,0FH         ; SET IT
1D33 EE                OUT    DX,AL

1D34 B0 03              MOV     AL,PCHODE2     ; SELECT MODE REGISTER 2
1D36 EE                OUT    DX,AL          ; SET IT
1D37 2E: 8A 47 03      MOV     AL,CS:[BX+PCHODE2] ; MASK EXTRA DATA OFF
1D3B 24 0F              AND    AL,0FH         ; SET IT
1D3D EE                OUT    DX,AL

```

Appendix A.

```

1D3E 8A 26 0347 R      MOV    AH,SUPIPCR      ; RESTOER SUPERIMPOSE CONTROL REGISTER
1D42 E8 0929 R        CALL   SET_SUPREG      ;
1D45 E8 1DD6 R        CALL   ENABLE_VG2      ; ENABLE VIDEO GATE ARRAY 2
1D48 EC               IN     AL,DX           ; INSURE ADDRESS STATE
1D49 80 01            MOV    AL,IXPALETM     ; SELECT PALETTE MASK REG
1D4B EE              OUT    DX,AL           ; SET IT
1D4C A0 0352 R        MOV    AL,PALETTE_MASK ; GET LAST VALUE OF PALETTE MASK REGISTER
1D4F 0C 10            OR     AL,VRAMIENB     ; SET V-RAM1 ENABLE BIT
1D51 EE              OUT    DX,AL           ; SET IT
1D52 8B C6            MOV    AX,SI           ; PUT MODE SET & PALETTE IN RAM
1D54 88 26 033D R      MOV    CRT_MODE_SET2,AH ;
;----- SETUP PORT B
1D58 E4 61            IN     AL,PORT_B       ; GET CURRENT VALUE OF 8255 PORT B
1D5A 2A FB            AND    AL,NOT PORT_B_ALPHA ; SET UP GRAPHICS MODE
1D5C E6 61            OUT    PORT_B,AL       ; STUFF BACK IN 8255
1D5E 8B F7            MOV    SI,DI           ; SET MODE TO SI
1D60 8B C6            MOV    AX,SI           ; GET MODE BACK
1D62 33 FF            XOR    DI,DI           ; --- FILL REGEN AREA WITH BLANK
; SET UP POINTER FOR REGEN
1D64 5A              POP    DX              ; GET ORIGINAL INPUT BACK
1D65 80 E2 80        AND    DL,80H          ; NO CLEAR OF REGEN ?
1D68 75 15            JNZ    SSETM10         ; SKIP CLEARING REGEN
1D6A B9 2000         MOV    CX,8192         ; NUMBER OF WORDS TO CLEAR
1D6D 3C 09            CMP    AL,09H          ; REQUIRE 32K BYTE REGEN ?
1D6F 88 B800         MOV    AX,REGEN_START ; SET SEGMENT OF 16K REGEN BUFFER
1D72 72 05            JC     SSETM9          ; NO, JUMP
1D74 D1 E1            SHL    CX,1            ; SET 16K WORDS TO CLEAR
1D76 E8 1D95 R      CALL   GET_DIRSEG     ; GET SEGMENT OF 32K REGEN
SSETM9: 1D79               MOV    ES,AX           ; SET REGEN SEGMENT
1D79 8E C0            XOR    AX,AX           ; FILL FOR GRAPHICS MODE
1D7B 33 C0            REP    STOSW           ; FILL THE REGEN BUFFER WITH BLANKS
SSETM10: 1D7F              CALL   ENABLE_VG1     ; ----- ENABLE VIDEO
; ENABLE VIDEO GATE ARRAY 1
1D82 8A 03DA         MOV    DX,VGA_CTL      ; SET PORT ADDRESS OF VGA
1D85 EC              IN     AL,DX           ; INSURE ADDRESS MODE
1D86 32 C0            XOR    AL,AL           ;
1D88 EE              OUT    DX,AL           ; SELECT VGA REG 0
1D89 A0 033D R        MOV    AL,CRT_MODE_SET2 ; GET MODE SET VALUE
1D8C EE              OUT    DX,AL           ; SET MODE
SSETM11: 1D8D              POP    WORD PTR IEP_CTRL ; RESTORE INTERRUPT ENABLE PROHIBIT FLAG
1D8D 8F 06 0355 R      CALL   ENABLE_INT      ; ENABLE INTERRUPT IF IT IS NOT PROHIBITED
1D91 E8 1BEC R        RET
1D94 C3              RET
1D95                SUP_SET_MODE          ENDP

```

```

;-----
;
; GET_DIRSEG
;
; THIS ROUTINE DETERMINE DIRECT ADDRESS
; SEGMENT OF V-RAM1 (MAIN RAM)
;
; INPUT          DS = DATA SEGMENT
; OUTPUT         AX = SEGMENT VALUE
; CALL           NONE
; VOLATILE       NONE
;-----

```

```

1D95                GET_DIRSEG          PROC      NEAR
1D95 8A 26 008A R      MOV    AH,PAGDAT      ; GET COPY OF PAGE REGS
1D99 25 3800         AND    AX,CPUREG * 100H ; ISOLATE CPU REG
1D9C D0 EC              SHR    AH,1           ; SHIFT TO MAKE INTO SEGMENT VALUE
1D9E 53              PUSH   BX              ; SAVE BX
1D9F 8B 1E 0015 R      MOV    BX,TRUE_MEM    ; GET MEMORY SIZE
1DA3 51              PUSH   CX              ;
1DA4 B1 06            MOV    CL,6           ;
1DA6 D3 E3            SAL    BX,CL          ;
1DA8 59              POP    CX             ; ADJUST FOR SEGMENT VALUE
1DA9 80 EF 20         SUB    BH,00100000H   ;
1DAC 9A E7            OR     AH,BH          ;
1DAE 5B              POP    BX             ; SHIFT VIDEO RAM ADDRESS TO TOP 128K
1DAF C3              RET                   ; RESTORE BX
1DB0                GET_DIRSEG          ENDP

```

```

;-----
;
; ENABLE_VG1
;
; THIS ROUTINE ENABLES VIDEO GENERATER 1
;-----

```

```

; INPUT      NONE
; OUTPUT     NONE
; CALL       DISABLE_INT
;           ENABLE_INT
;           OUT_GA
; VOLATILE   NONE
;
;-----

```

```

1DB0
1DB0 50
1DB1 53
1DB2 52
1DB3 1E
1DB4 E8 0000 E
1DB7 E8 1BE4 R
1DBA BA 01FF
1DBD BB 8D7B
1DC0 E8 1E22 R
1DC3 BB 8CFB
1DC6 E8 1E22 R
1DC9 C6 06 0354 R 01
1DCE E8 1BEC R
1DD1 1F
1DD2 5A
1DD3 5B
1DD4 58
1DD5 C3
1DD6

ENABLE_VG1 PROC NEAR
    PUSH AX
    PUSH BX
    PUSH DX
    PUSH DS
    CALL DDS
    ASSUME DS:DATA
    CALL DISABLE_INT
    MOV DX,SX08BASE
    MOV BX,S8SX02B*100H + SX02B_OFF
    CALL OUT_GA
    MOV BX,S8SX02A*100H + SX02A_ON
    CALL OUT_GA
    MOV VG_STAT,VG1_ON
    CALL ENABLE_INT
    POP DS
    POP DX
    POP BX
    POP AX
    RET
ENABLE_VG1 ENDP

```

```

;-----
;
; ENABLE_VG2
;
; THIS ROUTINE ENABLES VIDEO GENERATER 2
;
; INPUT      NONE
; OUTPUT     NONE
; CALL       DISABLE_INT
;           ENABLE_INT
;           OUT_GA
; VOLATILE   NONE
;
;-----

```

```

1DD6
1DD6 50
1DD7 53
1DD8 52
1DD9 1E
1DDA E8 0000 E
1DDD E8 1BE4 R
1DE0 BA 01FF
1DE3 BB 8C7B
1DE6 E8 1E22 R
1DE9 BB 8DFB
1DEC E8 1E22 R
1DEF C6 06 0354 R 00
1DF4 E8 1BEC R
1DF7 1F
1DF8 5A
1DF9 5B
1DFA 58
1DFB C3
1DFC

ENABLE_VG2 PROC NEAR
    PUSH AX
    PUSH BX
    PUSH DX
    PUSH DS
    CALL DDS
    ASSUME DS:DATA
    CALL DISABLE_INT
    MOV DX,SX08BASE
    MOV BX,S8SX02A*100H + SX02A_OFF
    CALL OUT_GA
    MOV BX,S8SX02B*100H + SX02B_ON
    CALL OUT_GA
    MOV VG_STAT,VG2_ON
    CALL ENABLE_INT
    POP DS
    POP DX
    POP BX
    POP AX
    RET
ENABLE_VG2 ENDP

```

```

;-----
;
; ENABLE_VG12
;
; THIS ROUTINE ENABLES VIDEO GENERATER 1 AND 2
;
; INPUT      NONE
; OUTPUT     NONE
; CALL       DISABLE_INT
;           ENABLE_INT
;           OUT_GA
; VOLATILE   NONE
;
;-----

```

Appendix A.

```

1DFC
1DFC 50
1DFD 53
1DFE 52
1DFF 1E

1E00 E8 0000 E
1E03 E8 1BE4 R
1E06 8A 01FF
1E09 8B 8CFB
1E0C E8 1E22 R
1E0F 8B 8DFB
1E12 E8 1E22 R
1E15 C6 06 0354 R 02
1E1A E8 1BEC R
1E1D 1F
1E1E 5A
1E1F 5B
1E20 58
1E21 C3
1E22

ENABLE_VG12 PROC NEAR
    PUSH AX
    PUSH BX
    PUSH DX
    PUSH DS
    CALL DDS ; POINT BIOS DATA AREA
    CALL DISABLE_INT ASSUME DS:DATA ; DISABLE ALL INTERRUPT
    MOV DX,SX08BASE ; ADDRESS OF ADDRESS CONTROLLER
    MOV BX,S85X02A*100H + SX02A_ON
    CALL OUT_GA ; ENABLE VIDEO GENERATER 1
    MOV BX,S85X02B*100H + SX02B_ON
    CALL OUT_GA ; ENABLE VIDEO GENERATER 2
    MOV VG_STAT,VG12_ON ; SET FLAG
    CALL ENABLE_INT ; ENABLE INTERRUPT
    POP DS
    POP DX
    POP BX
    POP AX
    RET
ENABLE_VG12 ENDP

;-----
;
; OUT_GA
;
; THIS ROUTINE OUTPUT ADDRESS/DATA TO GATE ARRAY
;
; INPUT BH = INTERNAL ADDRESS
; BL = DATA TO WRITE
; DX = ADDRESS OF GATE ARRAY
;
; OUTPUT NONE
;
; CALL NONE
;
; VOLATILE AL
;-----

1E22 OUT_GA PROC NEAR
1E22 EC IN AL,DX ; SYNC TO ADDRESS STATE
1E23 8A C7 MOV AL,BH
1E25 EE OUT DX,AL ; WRITE INTERNAL ADDRESS
1E26 8A C3 MOV AL,BL
1E28 EE OUT DX,AL ; WRITE DATA
1E29 C3 RET
1E2A OUT_GA ENDP

;-----
;
; SET_ALT_CTYPE INT 10H, AH = 81H
;-----
;
; THIS ROUTINE SETS THE ALTERNATE CURSOR VALUE
;
; INPUT AH = CURRENT CRT MODE ( MASKED )
; CX = CURSOR VALUE CH-START LINE, CL-STOP LINE
;
; OUTPUT NONE
;
; CALL WRITE_ALT_CURSOR
; WRITE_GCURSOR
;
; VOLATILE BX,DX,DI
;-----
ASSUME CS:CODE, DS:DATA
SET_ALT_CTYPE PROC NEAR
    MOV DX,ALT_CURSOR_POSM; GET CURRENT CURSOR POSITION
    MOV BL,AC_PRESENT ; GET ALTERNATE CURSOR PRESENT FLAG
    OR BL,BL ; ALTERNATE CURSOR PRESENT ?
    JZ SACT3 ; NO
    PUSH CX
    MOV CX,ACURSOR_MODE ; SAVE NEW CURSOR MODE
    MOV CX,ACURSOR_MODE ; GET OLD CURSOR MODE
    CMP AH,GRAPHICS ; GRAPHICS MODE ?
    JAE SACT1 ; YES
    CALL WRITE_ALT_CURSOR; ERASE CURRENT ALTERNATE CURSOR
    JMP SHORT SACT2
SACT1: CALL WRITE_GCURSOR ; ERASE CURRENT ALTERNATE GRAPHICS CURSOR
SACT2: POP CX
XOR BL,BL ; RESTORE NEW CURSOR MODE
SACT3: ; CLEAR ALTERNATE CURSOR FLAG
MOV ACURSOR_MODE,CX ; SAVE NEW GRAPHICS CURSOR MODE

```



```

1E4F 80 FC 04      CMP     AH,GRAPHICS    ; GRAPHICS MODE ?
1E52 73 05          JAE     SACT4          ; YES
1E54 E8 1E9A R     CALL    WRITE_ALT_CURSOR; WRITE NEW ALTERNATE CURSOR
1E57 EB 04          JMP     SHORT SACT5    ;
1E59                                     SACT4:
1E59 E8 0618 R     CALL    WRITE_GCURSOR ; WRITE NEW GRAPHICS CURSOR
1E5C 4B             DEC     BX             ; SET ALTERNATE CURSOR FLAG
1E5D                                     SACT5:
1E5D 88 1E 0348 R   MOV     AC_PRESENT,BL ; SET ALTERNATE CURSOR FLAG
1E61 C3           RET
1E62

```

```

SET_ALT_CTYPE ENDP

```

```

-----
;
; SET_ALT_CPOS          INT 10H, AH = 82H
;
; THIS ROUTINE SETS THE ALTERNATE CURSOR POSITION TO THE
; NEW X-Y VALUES PASSED
;
; INPUT  AH = CRT MODE (MASKED)
;        DX = ROW,COLUMN OF NEW CURSOR
;
; OUTPUT NONE
;
; CALL   WRITE_ALT_CURSOR
;        WRITE_GCURSOR
;
; VOLATILE    BX,CX,DI
;
-----

```

```

ASSUME CS:CODE, DS:DATA

```

```

1E62                                     SET_ALT_CPOS  PROC  NEAR
1E62 8B 0E 0350 R   MOV     CX,ACURSOR_MODE ; GET ALTERNATE GRAPHICS CURSOR MODE
1E66 8A 1E 0348 R   MOV     BL,AC_PRESENT   ; GET ALTERNATE CURSOR PRESENT FLAG
1E6A 0A DB          OR      BL,BL           ; ALTERNATE CURSOR PRESENT ?
1E6C 74 15          JZ     SACPOS3         ; NO, SKIP ERASE OLD CURSOR
1E6E 52             PUSH    DX              ; SAVE NEW CURSOR POSITION
1E6F 8B 16 034A R   MOV     DX,ALT_CURSOR_POSM ; GET OLD ALTERNATE CURSOR POSITION
1E73 80 FC 04      CMP     AH,GRAPHICS     ; GRAPHICS MODE ?
1E76 73 05          JAE     SACPOS1        ; YES
1E78 E8 1E9A R     CALL    WRITE_ALT_CURSOR; WRITE ALTERNATE CURSOR
1E7B EB 03          JMP     SHORT SACPOS2  ;
1E7D                                     SACPOS1:
1E7D E8 0618 R     CALL    WRITE_GCURSOR  ; WRITE ALTERNATE CURSOR IN GRAPHICS
1E80                                     SACPOS2:
1E80 5A             POP     DX              ; RESTORE NEW CURSOR POSITION
1E81 32 DB          XOR     BL,BL          ; CLEAR ALTERNATE CURSOR FLAG
1E83                                     SACPOS3:
1E83 89 16 034A R   MOV     ALT_CURSOR_POSM,DX ; SET NEW ALTERNATE CURSOR POSITION
1E87 80 FC 04      CMP     AH,GRAPHICS     ; GRAPHICS MODE ?
1E8A 73 05          JAE     SACPOS4        ; YES
1E8C E8 1E9A R     CALL    WRITE_ALT_CURSOR; WRITE ALTERNATE CURSOR
1E8F EB 04          JMP     SHORT SACPOS5  ;
1E91                                     SACPOS4:
1E91 E8 0618 R     CALL    WRITE_GCURSOR  ; WRITE ALTERNATE CURSOR IN GRAPHICS
1E94 4B             DEC     BX             ; SET ALTERNATE CURSOR FLAG FOR GRAPHICS
1E95                                     SACPOS5:
1E95 88 1E 0348 R   MOV     AC_PRESENT,BL  ; ALTERNATE CURSOR PRESENT
1E99 C3           RET
1E9A                                     SET_ALT_CPOS  ENDP

```

```

-----
;
; WRITE_ALT_CURSOR
;
; THIS ROUTINE WRITES THE ALTERNATE CURSOR
;
; INPUT  BL = ATTRIBUTE TO BE USED AT ALTERNATE CURSOR POSITION
;        KANJI BIT IS MASKED
;        (IF BL=0, USE ATTRIBUTE AT REGEN BUFFER)
;        CX = CURSOR MODE
;        DX = ROW,COLUMN POSITION TO WRITE
;
; OUTPUT BL = NEW ATTRIBUTE AT ALTERNATE CURSOR POSITION
;        KANJI BIT IS MASKED
;        (IF NO CURSOR HAS WRITTEN, BL=0)
;
; CALL   FIND_POSH
;
; VOLATILE    BH,DI
;
-----

```

```

1E9A                                     WRITE_ALT_CURSOR  PROC  NEAR
1E9A F6 C5 20      TEST    CH,CURSOR_DISABLE ; CURSOR DISABLED ?
1E9D 75 36          JNZ    MALT2          ; YES
1E9F 83 F9 11      CMP     CX,BLOCK_CURSOR ; BLOCK CURSOR ?
1EA2 75 31          JNE    MALT2          ; NO

```

Appendix A.

```

1EA4 50          PUSH AX          ;
1EA5 51          PUSH CX          ; SAVE REGISTERS
1EA6 8B C2       MOV AX,DX          ; SET ROW/COLUMN POSITION
1EA8 53          PUSH BX          ; SAVE ATTRIBUTE
1EA9 32 ED       XOR CH,CH        ; SET ACTIVE PAGE TO CX
1EAB 8A 0E 0042 R MOV CL,ACTIVE_PAGE ;
1EAF E8 0DEC R   CALL FIND_POSH    ; DETERMINE LOCATION IN REGEN BUFFER
1EB2 8B FB       MOV DI,BX        ; SET OFFSET TO DI
1EB4 5B          POP BX           ; RESTORE ATTRIBUTE
1EB5 26: 8B 05   MOV AX,ES:[DI]   ; GET CODE/ATTR FROM REGEN
1EB8 0A DB       OR BL,BL         ; ATTRIBUTE SPECIFIED ?
1EBA 74 05       JZ MALT C1       ; NO
1EBC 80 E4 88    AND AH,ZEN2_MASK ; GET KJ BIT
1EBF 0A E3       OR AH,BL         ; SET ATTRIBUTE
1EC1
MALT C1:
1EC1 8A FC       MOV BH,AH        ; --- REVERSE ATTRIBUTE OF FORE/BACKGROUND
1EC3 B1 04       MOV CL,4         ; SET ATTRIBUTE TO BH FOR REVERSE
1EC5 D2 CF       ROR BH,CL       ; SET SHIFT COUNTER
; SWAP NIBBLE
1EC7 80 E7 77    AND BH,HAN_MASK  ; STRIP OFF KJ-BIT
1ECA 80 E4 88    AND AH,ZEN2_MASK ; STRIP FORE/BACKGROUND COLOR BIT OFF
1ECD 0A E7       OR AH,BH        ; SET REVERSED COLOR BIT TO AH
1ECF AB         STOSH           ; WRITE CODE/ATTR TO REGEN
1ED0 8A DF       MOV BL,BH       ; SET NEW ATTRIBUTE
1ED2 59         POP CX          ;
1ED3 58         POP AX          ; RESTORE REGISTERS
1ED4 C3         RET
1ED5
MALT C2:
1ED5 32 DB       XOR BL,BL        ; CLEAR FLAG
1ED7 C3         RET
1ED8
WRITE_ALT_CURSOR ENDP
;-----
;
; READ_ALT_CURSOR INT 10H, AH = 85H
;
; THIS ROUTINE READS THE ALTERNATE CURSOR POSITION
;
; INPUT NONE
;
; OUTPUT DX = ROW, COLUMN OF THE ALTERNATE CURSOR POSITION
; CX = CURRENT ALTERNATE CURSOR MODE
;
; CALL NONE
;
; VOLATILE BX
;-----
ASSUME CS:CODE, DS:DATA
1ED8
READ_ALT_CURSOR PROC NEAR
1ED8 55          PUSH BP          ; SAVE BP
1ED9 8B 16 034A R MOV DX,ALT_CURSOR_POSH
1EDD 8B 0E 0350 R MOV CX,ACURSOR_MODE ; GET ALTERNATE CURSOR MODE
1EE1 8B EC       MOV BP,SP       ; SET FRAME POINTER
1EE3 89 56 0C   MOV [BP+F_DX],DX ; SET RETURN DX
1EE6 89 4E 0A   MOV [BP+F_CX],CX ; SET RETURN CX
1EE9 5D         POP BP          ; RESTORE BP
1EEA C3         RET
1EEB
READ_ALT_CURSOR ENDP
;-----
;
; READ_AC_CURRENT FOR KANA-KANJI CONVERSION
;-----
; INT 10H, AH = 88H
;
; THIS ROUTINE READS THE ATTRIBUTE AND CHARACTER AT THE
; CURRENT CURSOR POSITION AND RETURNS THEM TO THE CALLER
;
; INPUT AH = CURRENT CRT MODE (MASKED)
; DS = DATA SEGMENT
; ES = REGEN SEGMENT
;
; OUTPUT AL = CHAR READ
; AH = ATTRIBUTE READ
;
; CALL READ_AC_CURRENT
;-----
ASSUME CS:CODE, DS:DATA, ES:VIDEO_RAM
;K_READ_AC_CURRENT PROC NEAR
;
; MOV BH,ACTIVE_PAGE ; SET DISPLAY PAGE
;
; JMP READ_AC_CURRENT ; HANDLE BY NATIVE ROUTINE
;K_READ_AC_CURRENT ENDP
;-----
;
; WRITE_AC_CURRENT FOR KANA-KANJI CONVERSION
;-----
; INT 10H, AH = 89H
;

```

```

;      THIS ROUTINE WRITES THE ATTRIBUTE AND CHARACTER AT
;      THE CURRENT CURSOR POSITION
;
;      INPUT
;          AH = CURRENT CRT MODE (MASKED)
;          AL = CHAR TO WRITE
;          BL = ATTRIBUTE OF CHAR TO WRITE
;          DX = ROW/COLUMN FOR WRITE CHARACTER
;          DS = DATA SEGMENT
;          ES = REGEN SEGMENT
;
;      OUTPUT
;          NONE
;
;      CALL  WRITE_AC_CURRENT
;
;-----
;      ASSUME  CS:CODE, DS:DATA, ES:VIDEO_RAM
1EEB      K_WRITE_AC_CURRENT      PROC      NEAR
1EEB      CMP      AH,GRAPHICS      ; TEXT MODE ?
1EEE      JB      KWAC1              ; YES
1EF0      TEST     BL,XOR_BIT        ; XOR WRITE BIT ON ?
1EF3      JNZ     KWAC1              ; YES
1EF5      MOV     AC_PRESENT,FALSE; CLEAR ALTERNATE CURSOR PRESENT FLAG
1EFA      KWAC1:
1EFA      JMP     WRITE_AC_CURRENT; HANDLE BY NATIVE ROUTINE
1EFD      K_WRITE_AC_CURRENT      ENDP
;-----
;      WRITE_C_CURRENT FOR KANA-KANJI CONVERSION
;      -----
;      INT 10H, AH = 8AH
;
;      THIS ROUTINE WRITES THE CHARACTER AT
;      THE CURRENT CURSOR POSITION
;
;      INPUT
;          AH = CURRENT CRT MODE (MASKED)
;          AL = CHAR TO WRITE
;          BL = COLOR OF CHAR (GRAPHICS)
;          DX = ROW/COLUMN FOR WRITE CHARACTER
;          DS = DATA SEGMENT
;          ES = REGEN SEGMENT
;
;      OUTPUT
;          NONE
;
;      CALL  WRITE_C_CURRENT
;
;-----
;      ASSUME  CS:CODE, DS:DATA, ES:VIDEO_RAM
1EFD      K_WRITE_C_CURRENT      PROC      NEAR
1EFD      CMP      AH,GRAPHICS      ; TEXT MODE ?
1F00      JB      KWCI              ; YES
1F02      TEST     BL,XOR_BIT        ; XOR WRITE BIT ON ?
1F05      JNZ     KWCI              ; YES
1F07      MOV     AC_PRESENT,FALSE; CLEAR ALTERNATE CURSOR PRESENT FLAG
1F0C      KWCI:
1F0C      JMP     WRITE_C_CURRENT; HANDLE BY NATIVE ROUTINE
1F0F      K_WRITE_C_CURRENT      ENDP
;-----
;      WRITE_TTY FOR KANA-KANJI CONVERSION
;      -----
;      INT 10H, AH = (8EH)
;
;      THIS INTERFACE PROVIDES A TELETYPE LIKE INTERFACE TO THE
;      VIDEO CARD.  THE INPUT CHARACTER IS WRITTEN TO THE CURRENT ALTERNATE
;      CURSOR POSITION, AND THE CURSOR IS MOVED TO THE NEXT POSITION.
;
;      INPUT
;          AH = CURRENT CRT MODE (MASKED)
;          AL = CHARACTER TO BE WRITTEN
;          NOTE THAT BACK SPACE, CAR RET, BELL AND LINE FEED ARE
;          HANDLED AS COMMANDS RATHER THAN AS DISPLAYABLE GRAPHICS
;          BH = DISPLAY PAGE
;          BL = FOREGROUND COLOR FOR CHAR WRITE IF CURRENTLY IN A
;          GRAPHICS MODE
;
;      OUTPUT
;          NONE
;
;      CALL  VIDEO
;
;      VOLATILE  BH, CX, DX
;
;-----
;      ASSUME  CS:CODE, DS:DATA
1F0F      K_WRITE_TTY          PROC      NEAR
1F0F      PUSHP  AX                  ; SAVE REGISTER
1F10      MOV   DX,ALT_CURSOR_POSH  ; GET CURSOR POSITION
;      ;--- DX NOW HAS THE CURRENT CURSOR POSITION

```

Appendix A.

```

1F14 F7 06 0344 R FFFF TEST K_TTY_1ST_CHAR,TRUE; 1ST BYTE OF 2 BYTE CODE HAS BEEN SET ?
1F1A 75 17 JNZ KWTY2 ; YES

1F1C 3C 80 CMP AL,080H ; 1ST BYTE OF 2 BYTE CODE ?
1F1E 76 54 JBE KWTY6 ; NO, GO TO WRITE ONE CHARACTER
1F20 3C FD CMP AL,0FDH ;
1F22 73 50 JAE KWTY6 ; NO, GO TO WRITE ONE CHARACTER
1F24 3C A0 CMP AL,0A0H ;
1F26 72 04 JB KWTY1 ; YES
1F28 3C DF CMP AL,0DFH ;
1F2A 76 48 JBE KWTY6 ; NO, GO TO WRITE ONE CHARACTER

1F2C KWTY1: ;--- CHARACTER IS 1ST BYTE OF 2 BYTE CODE
1F2C 8A E3 MOV AH,BL ; SET COLOR
1F2E A3 0344 R MOV K_TTY_1ST_CHAR,AX; SAVE 1ST BYTE OF 2 BYTE CODE AND SET FLAG
1F31 EB 54 JMP SHORT KWTY7 ; RETURN; WRITE PROCESS IS PENDING

1F33 KWTY2: ;--- CHARACTER MUST BE 2ND BYTE OF 2 BYTE CODE
1F33 3C 40 CMP AL,040H ; 2ND BYTE OF 2 BYTE CODE ?
1F35 72 08 JB KWTY3 ; NO
1F37 3C FC CMP AL,0FCH ;
1F39 77 04 JA KWTY3 ; NO
1F3B 3C 7F CMP AL,07FH ;
1F3D 75 08 JNE KWTY4 ; YES

1F3F KWTY3: ;--- IGNORE 1ST BYTE
1F3F C7 06 0344 R 0000 MOV K_TTY_1ST_CHAR,0; RESET FLAG OF 1ST BYTE
1F45 EB 2D JMP SHORT KWTY6 ; GO TO WRITE ONE BYTE

1F47 KWTY4: ;--- WRITE 2 BYTE CODE
1F47 8B 0E 004A R MOV CX,CRT_COLS ; CHECK COLUMN BOUNDARY
1F4B 49 DEC CX ; ADJUST FOR COMPARE
1F4C 3A D1 CMP DL,CL ; IS COLUMN AT END OF LINE ?
1F4E 72 08 JB KWTY5 ; NO

;--- IN CASE OF WRITE 2 BYTE CHAR ON THE END
1F50 C7 06 0344 R 0000 MOV K_TTY_1ST_CHAR,0; CLEAR FIRST BYTE
1F56 EB 2F JMP SHORT KWTY7 ; TERMINATE

1F58 KWTY5: ;--- WRITE 1ST BYTE OF 2 BYTE CODE
1F58 50 PUSH AX ; SAVE 2ND BYTE OF 2 BYTE CODE
1F59 53 PUSH BX ; SAVE COLOR
1F5A A1 0344 R MOV AX,K_TTY_1ST_CHAR ; GET 1ST BYTE OF 2 BYTE CODE
1F5D 8A DC BL,AH ; SET COLOR
1F5F B4 8A AH,8AH ; (AH)=8AH: WRITE CHARACTER AT CURRENT CURSOR
1F61 B9 0001 MOV CX,1 ; WRITE ONE CHARACTER
1F64 CD 10 INT VIDEO ; VIDEO I/O
1F66 5B POP BX ; RESTORE COLOR

1F67 C7 06 0344 R 0000 MOV K_TTY_1ST_CHAR,0; RESET FLAG OF 1ST BYTE
1F6D FE C2 INC DL ; INCREMENT ROW

1F6F B4 82 MOV AH,82H ; FUNCTION OF SET CURSOR POSITION
1F71 CD 10 INT VIDEO ; DO IT
1F73 58 POP AX ; RESTORE 2ND BYTE
1F74 KWTY6: ;--- WRITE THE CHAR TO THE SCREEN
1F74 B4 8A MOV AH,8AH ; WRITE CHAR ONLY
1F76 B9 0001 MOV CX,1 ; ONLY ONE CHAR
1F79 CD 10 INT VIDEO ; WRITE THE CHAR
;--- POSITION THE CURSOR FOR NEXT CHAR

1F7B FE C2 INC DL
1F7D 3A 16 004A R DL,BYTE PTR CRT_COLS ; TEST FOR COLUMN OVERFLOW
1F81 73 04 CMP DL,KWTY7 ; SKIP CURSOR MOVE

1F83 B4 82 MOV AH,82H
1F85 CD 10 INT VIDEO ; ADVANCE CURSOR POSITION
1F87 58 POP AX ; RESTORE THE CHARACTER
1F88 C3 RET ; RETURN TO CALLER

1F89 K_WRITE_TTY ENDP
2000 ORG 2000H ; ADJUST SIZE TO 8K
2000 CODE ENDS
END

```

```

XXXXXXXXXXXX
XXXXXXXXXXXX
M          M
M  MODULE 4  M
M          M
XXXXXXXXXXXX
XXXXXXXXXXXX

```

```

----- INT 11 -----
; EQUIPMENT DETERMINATION
; THIS ROUTINE ATTEMPTS TO DETERMINE WHAT OPTIONAL
; DEVICES ARE ATTACHED TO THE SYSTEM.
; INPUT
; NO REGISTERS
; THE EQUIP_FLAG VARIABLE IS SET DURING THE POWER ON
; DIAGNOSTICS USING THE FOLLOWING HARDWARE ASSUMPTIONS:
; PORT 62 (0->3) = LOW ORDER BYTE OF EQUIPMENT
; PORT 3FA = INTERRUPT ID REGISTER OF 8250
;           BITS 7-3 ARE ALWAYS 0
; PORT 378 = OUTPUT PORT OF PRINTER -- 8255 PORT THAT
;           CAN BE READ AS WELL AS WRITTEN
; OUTPUT
; (AX) IS SET, BIT SIGNIFICANT, TO INDICATE ATTACHED I/O
; BIT 15,14 = NUMBER OF PRINTERS ATTACHED
; BIT 13 = RESERVED
; BIT 12 = GAME I/O ATTACHED
; BIT 11,10,9 = NUMBER OF RS232 CARDS ATTACHED
; BIT 8 0 = DMA CHIP PRESENT ON SYSTEM, 1 = NO DMA ON SYSTEM
; BIT 7,6 = NUMBER OF DISKETTE DRIVES
;           00=1, 01=2, 10=3, 11=4 ONLY IF BIT 0 = 1
;
; BIT 5,4 = APPLICATION MODE FLAG ( 1: EXTENSION , 0: NATIVE )
;
;           01 - NATIVE MODE
;           10 - EXTENSION MODE
;           11 - (RESERVED)
;
; BIT 3,2 = PLANAR RAM SIZE
; BIT 1 RESERVED
; BIT 0 = 1 (IPL DISKETTE INSTALLED)
; NO OTHER REGISTERS AFFECTED

```

```

0000
0000 FB
0001 1E
0002 B8 ---- R
0005 8E D8
0007 A1 0010 R
000A 1F
000B CF
000C

```

```

-----
; ASSUME CS:CODE,DS:DATA
EQUIPMENT PROC FAR
; STI ; INTERRUPTS BACK ON
; PUSH DS ; SAVE SEGMENT REGISTER
; MOV AX,DATA ; ESTABLISH ADDRESSING
; MOV DS,AX
; MOV AX,EQUIP_FLAG ; MOVE EQUIPMENT FLAG
; POP DS ; RECOVER SEGMENT
; IRET ; RETURN TO CALLER
EQUIPMENT ENDP

```

```

000C
000C FB
000D 1E
000E B8 ---- R
0011 8E D8
0013 A1 0013 R
0016 1F
0017 CF
0018

```

```

-----
; ASSUME CS:CODE,DS:DATA
MEMORY_SIZE_DETERMINE PROC FAR
; STI ; INTERRUPTS BACK ON
; PUSH DS ; SAVE SEGMENT
; MOV AX,DATA ; ESTABLISH ADDRESSING
; MOV DS,AX
; MOV AX,MEMORY_SIZE ; GET VALUE
; POP DS ; RECOVER SEGMENT
; IRET ; RETURN TO CALLER
MEMORY_SIZE_DETERMINE ENDP

```

```

----- INT 13 -----
; DISKETTE I/O
; THIS INTERFACE PROVIDES ACCESS TO THE 5 1/4" DISKETTE DRIVES
; INPUT
; (AH)=0 RESET DISKETTE SYSTEM
;           HARD RESET TO MEC, PREPARE COMMAND, RECAL REQD ON
;           ALL DRIVES
; (AH)=1 READ THE STATUS OF THE SYSTEM INTO (AL)
;           DISKETTE_STATUS FROM LAST OP'N IS USED
;           REGISTERS FOR READ/WRITE/VERIFY/FORMAT
; (DL) - DRIVE NUMBER (0-3 ALLOWED, VALUE CHECKED)
;           (BIT 6 :1 80 TRACK DISKETTE ACCESS)
;           ( :0 40 TRACK ACCESS)
; (DH) - HEAD NUMBER (0-1 ALLOWED, NOT VALUE CHECKED)
; (CH) - TRACK NUMBER (0-79, NOT VALUE CHECKED)
; (CL) - SECTOR NUMBER (1-9, NOT VALUE CHECKED, NOT USED FOR
;           FORMAT)
; (AL) - NUMBER OF SECTORS ( MAX = 8, NOT VALUE CHECKED, NOT
;           USED FOR FORMAT, HOWEVER, CANNOT BE ZERO!!)
; (ES:BX) - ADDRESS OF BUFFER ( NOT REQUIRED FOR VERIFY)
;
; (AH)=2 READ THE DESIRED SECTORS INTO MEMORY
; (AH)=3 WRITE THE DESIRED SECTORS FROM MEMORY
; (AH)=4 VERIFY THE DESIRED SECTORS
; (AH)=5 FORMAT THE DESIRED TRACK
;           FOR THE FORMAT OPERATION, THE BUFFER POINTER
;           (ES,BX) MUST POINT TO THE COLLECTION OF DESIRED
;           ADDRESS FIELDS FOR THE TRACK. EACH FIELD IS
;           COMPOSED OF 4 BYTES, (C,H,R,N), WHERE
;           C = TRACK NUMBER, H=HEAD NUMBER, R = SECTOR NUMBER,
;           N= NUMBER OF BYTES PER SECTOR (00-128, 01=256,
;           02=512, 03=1024,). THERE MUST BE ONE ENTRY FOR
;           EVERY SECTOR ON THE TRACK. THIS INFORMATION IS USED
;           TO FIND THE REQUESTED SECTOR DURING READ/WRITE
;           ACCESS.
; DATA VARIABLE -- DISK POINTER
; DOUBLE WORD POINTER TO THE CURRENT SET OF DISKETTE PARAMETERS
; OUTPUT
; AH = STATUS OF OPERATION
; STATUS BITS ARE DEFINED IN THE EQUATES FOR
; DISKETTE_STATUS VARIABLE IN THE DATA SEGMENT OF
; THIS MODULE

```

Appendix A.

```

; CY = 0 SUCCESSFUL OPERATION (AH=0 ON RETURN)
; CY = 1 FAILED OPERATION (AH HAS ERROR REASON)
; FOR READ/WRITE/VERIFY
; DS,BX,DX,CH,CL PRESERVED
; AL = NUMBER OF SECTORS ACTUALLY READ
; **** AL MAY NOT BE CORRECT IF TIME OUT ERROR OCCURS
; NOTE: IF AN ERROR IS REPORTED BY THE DISKETTE CODE, THE
; APPROPRIATE ACTION IS TO RESET THE DISKETTE, THEN
; RETRY THE OPERATION. ON READ ACCESSES, NO MOTOR
; START DELAY IS TAKEN, SO THAT THREE RETRIES ARE
;
; REQUIRED ON READS TO ENSURE THAT THE PROBLEM IS NOT
; DUE TO MOTOR START-UP.
;
;
;-----

```

```

0018          ASSUME CS:CODE,DS:DATA,ES:DATA
0018 FB       DISKETTE_IO PROC FAR
0019 06       STI             ; INTERRUPTS BACK ON
001A 50       PUSH          ES             ; SAVE ES
; ALLOCATE ONE WORD OF STORAGE FOR
; TIMER1 INITIAL VALUE
001B 50       PUSH          AX
; ALLOCATE ONE WORD ON STACK FOR
; USE IN PROCS ENABLE AND DISABLE.
; WILL HOLD 8259 MASK.
; SAVE COMMAND AND N_SECTORS
001C 50       PUSH          AX
001D 53       PUSH          BX
001E 51       PUSH          CX
001F 1E       PUSH          DS
0020 56       PUSH          SI
; SAVE SEGMENT REGISTER VALUE
; SAVE ALL REGISTERS DURING
; OPERATION
0021 57       PUSH          DI
0022 55       PUSH          BP
0023 52       PUSH          DX
0024 8B EC    MOV          BP,SP
; SET UP POINTER TO HEAD PARM
0026 E8 0000 E CALL        DDS
; SET DS=DATA
0029 E8 004F R CALL        J1
; CALL THE REST TO ENSURE DS
; RESTORED
; GET THE MOTOR WAIT PARAMETER
002C 83 04    MOV          BL,4
002E E8 0331 R CALL        GET_PARM
0031 88 26 0040 R MOV        MOTOR_COUNT,AH
; SET THE TIMER COUNT FOR THE MOTOR
0035 8A 26 0041 R MOV        AH,DISKETTE_STATUS
; GET STATUS OF OPERATION
0039 88 66 0F MOV        [BP+15],AH
; RETURN STATUS IN AL
; RESTORE ALL REGISTERS
003C 5A       POP           DX
003D 5D       POP           BP
003E 5F       POP           DI
003F 5E       POP           SI
0040 1F       POP           DS
0041 59       POP           CX
0042 5B       POP           BX
0043 58       POP           AX
; RECOVER OFFSET
0044 83 C4 04 ADD        SP,4
; DISCARD DUMMY SPACE FOR 8259 MASK
0047 07       POP           ES
; RECOVER SEGMENT
0048 80 FC 01 CMP        AH,1
; SET THE CARRY FLAG TO INDICATE
; SUCCESS OR FAILURE
004C CA 0002 RET
; THROW AWAY SAVED FLAGS
004F          DISKETTE_IO ENDP
004F J1        PROC
004F 8A F0    MOV        DH,AL
; SAVE # SECTORS IN DH
0051 80 26 003F R 7F AND        MOTOR_STATUS,07FH
; INDICATE A READ OPERATION
0056 80 E2 3F AND        DL,OFF_DBL_TRK
; RESET FOR 80 TRACK PARM FLAG JX
0059 0A E4    OR         AH,AH
; AH=0
005B 74 27    JZ        DISK_RESET
; AH=1
005D FE CC    DEC        AH
; DISK_STATUS
0061 C6 06 0041 R 00 MOV        DISKETTE_STATUS,0
; RESET THE STATUS INDICATOR
0066 80 FA 03 CMP        DL,DRV_RANGE
; TEST FOR DRIVE IN 0-3 RANGE JX
0069 77 13    JA        J3
; ERROR IF ABOVE
006B FE CC    DEC        AH
; AH=2
006D 74 6D    JZ        DISK_READ
006F FE CC    DEC        AH
; AH=3
0071 75 03    JNZ       J2
; TEST_DISK_VERF
0073 E9 00FF R JMP
; TEST_DISK_VERF
0076          J2:
0076 FE CC    DEC        AH
; AH=4
0078 74 62    JZ        DISK_VERF
; AH=5
007A FE CC    DEC        AH
007C 74 62    JZ        DISK_FORMAT
007E          J3:
007E C6 06 0041 R 01 MOV        DISKETTE_STATUS,BAD_CMD
; BAD_COMMAND
; ERROR CODE, NO SECTORS
; TRANSFERRED
; UNDEFINED OPERATION
0083 C3       RET
0084          J1
;----- RESET THE DISKETTE SYSTEM
DISK_RESET PROC NEAR
0084 BA 00F2 MOV        DX,NEC_CTL
; ADAPTER CONTROL PORT
0087 FA       CLI
; NO INTERRUPTS
0088 A0 003F R MOV        AL,MOTOR_STATUS
; FIND OUT IF MOTOR IS RUNNING
008B 24 0F MOV        AL,DRV_SUPPORT
; DRIVE BITS
008D EE       AND        DX,AL
; RESET THE ADAPTER
008E C6 06 003E R 00 OUT
; SET RECAL REQUIRED ON ALL DRIVES
0093 C6 06 0041 R 00 MOV        SEEK_STATUS,0
0098 0C 80 MOV        DISKETTE_STATUS,0
; SET OK STATUS FOR DISKETTE
009A EE       OR         AL,FDC_RESET
; TURN OFF RESET
009B FB       OUT
; TURN OFF THE RESET
009C 8E 00BC R STI
; REENABLE THE INTERRUPTS
009F 56       PUSH         SI,OFFSET J4_2
; DUMMY RETURN FOR
; PUSH RETURN IF ERROR
00A0 B9 0010 MOV
; IN NEC_OUTPUT
; NUMBER OF SENSE INTERRUPTS TO
00A3 B4 08    MOV        CX,10H
; ISSUE
; COMMAND FOR SENSE INTERRUPT
; STATUS

```

```

00A5 E8 0307 R      CALL   NEC_OUTPUT      ; OUTPUT THE SENSE INTERRUPT
00A8 E8 03A9 R      CALL   RESULTS          ; STATUS
00AB A0 0042 R      MOV    AL,NEC_STATUS    ; GET STATUS FOLLOWING COMPLETION
                                ; OF RESET
00AE 3C C0          CMP    AL,0C0H         ; IGNORE ERROR RETURN AND DO OWN
00B0 74 12          JZ     J7              ; TEST
00B2 E2 EF          LOOP   J4_0            ; TEST FOR DRIVE READY TRANSITION
00B4 80 0E 0041 R 20 J4_1: OR    DISKETTE_STATUS,BAD_NEC ; EVERYTHING OK
00B9 5E            POP    SI              ; RETRY THE COMMAND
00BA EB 18          JMP    SHORT J8        ; SET ERROR CODE

00BC BE 00BC R      J4_2: MOV   SI,OFFSET J4_2 ; NEC_OUTPUT FAILED, RETRY THE
00BF 56            PUSH   SI              ; SENSE INTERRUPT
                                ; OFFSET OF BAD RETURN IN
00C0 E2 E1          LOOP   J4_0            ; NEC_OUTPUT
00C2 EB F0          JMP    SHORT J4_1     ; RETRY
                                ;----- SEND SPECIFY COMMAND TO NEC
00C4 5E            J7:   POP    SI        ; GET RID OF DUMMY ARGUMENT
00C5 B4 03          MOV    AH,03H        ; SPECIFY COMMAND
00C7 E8 0307 R      CALL   NEC_OUTPUT    ; OUTPUT THE COMMAND
00CA B3 01          MOV    BL,1          ; STEP RATE TIME AND HEAD UNLOAD
00CC E8 0331 R      CALL   GET_PARM      ; OUTPUT TO THE NEC CONTROLLER
00CF B3 03          MOV    BL,3          ; PARM1 HEAD LOAD AND NO DMA
00D1 E8 0331 R      CALL   GET_PARM      ; TO THE NEC CONTROLLER
00D4 C3            J8:   RET             ; RESET_RET
00D5              DISK_RESET      ENDP      ; RETURN TO CALLER
                                ;----- DISKETTE STATUS ROUTINE
00D5 A0 0041 R      DISK_STATUS      PROC   NEAR
00D8 88 46 0E      MOV    AL,DISKETTE_STATUS
                                ; PUT STATUS ON STACK, IT WILL
                                ; POP IN AL
00DB C3            RET
00DC              DISK_STATUS      ENDP
                                ;----- DISKETTE VERIFY
00DC              DISK_VERIFY      PROC   NEAR
                                ;----- DISKETTE LABEL
00DC              DISK_READ       PROC   NEAR
00DC B4 46          J9:   MOV    AH,046H   ; DISK_READ_CONT
                                ; SET UP READ COMMAND FOR NEC
                                ; CONTROLLER
00DE EB 26          JMP    SHORT RW_OPN  ; GO DO THE OPERATION
00E0              DISK_READ       ENDP
                                ;----- DISKETTE FORMAT
00E0              DISK_FORMAT      PROC   NEAR
00E0 80 0E 003F R 80 OR    MOTOR_STATUS,80H ; INDICATE A WRITE OPERATION
00E5 B4 4D          MOV    AH,04DH      ; ESTABLISH THE FORMAT COMMAND
00E7 EB 1D          JMP    SHORT RW_OPN ; DO THE OPERATION
00E9              J10:
00E9 B3 07          MOV    BL,7          ; CONTINUATION OF RW_OPN FOR FMT
00EB E8 0331 R      CALL   GET_PARM      ; GET THE
00EE B3 09          CALL   GET_PARM      ; BYTES/SECTOR VALUE TO NEC
00F0 E8 0331 R      CALL   GET_PARM      ; GET THE
00F3 B3 0F          CALL   GET_PARM      ; SECTORS/TRACK VALUE TO NEC
00F5 E8 0331 R      CALL   GET_PARM      ; GET THE
00F8 B8 0011      CALL   GET_PARM      ; GAP LENGTH VALUE TO NEC
00FB 53            MOV    BX,17         ; GET THE FILLER BYTE
00FC E9 018F R      PUSH   BX            ; SAVE PARAMETER INDEX ON STACK
00FF              JMP    J16            ; TO THE CONTROLLER
                                ;----- DISKETTE WRITE ROUTINE
00FF              DISK_WRITE      PROC   NEAR
00FF 80 0E 003F R 80 OR    MOTOR_STATUS,80H ; INDICATE A WRITE OPERATION
0104 B4 45          MOV    AH,045H      ; NEC COMMAND TO WRITE TO DISKETTE
0106              DISK_WRITE      ENDP
                                ;----- ALLOW WRITE ROUTINE TO FALL INTO RW_OPN
                                ;-----
                                ; RW_OPN
                                ;----- THIS ROUTINE PERFORMS THE READ/WRITE/VERIFY OPERATION
0106              RW_OPN       PROC   NEAR
0106 50            PUSH   AX            ; SAVE THE COMMAND
0107 51            TURN ON THE MOTOR AND SELECT THE DRIVE
0108 FA          PUSH   CX            ; SAVE THE T/S PARMS
                                ; NO INTERRUPTS WHILE DETERMINING
                                ; MOTOR STATUS
0109 C6 06 0040 R FF MOV    MOTOR_COUNT,0FFH ; SET LARGE COUNT DURING OPERATION
010E EB 044E R      CALL   GET_DRIVE     ; GET THE DRIVE PARAMETER FROM THE
                                ; STACK
0111 84 06 003F R TEST   MOTOR_STATUS,AL ; TEST MOTOR FOR OPERATING
0115 75 1F          JNZ   J14            ; IF RUNNING, SKIP THE WAIT
0117 80 26 003F R F0 AND    MOTOR_STATUS,0F0H ; TURN OFF RUNNING DRIVE
011C 08 06 003F R OR     MOTOR_STATUS,AL ; TURN ON THE CURRENT MOTOR
0120 FB          STI             ; INTERRUPTS BACK ON
0121 0C 80          OR     AL,FDC_RESET ; NO RESET. TURN ON MOTOR
0123 E6 F2          OUT    NEC_CTL,AL
                                ;----- WAIT FOR MOTOR BOTH READ AND WRITE
0125 B3 14          MOV    BL,20         ; GET MOTOR START TIME
0127 E8 0331 R      CALL   GET_PARM
012A 0A E4          OR     AH,AH         ; TEST FOR NO WAIT
012C              J12:
012C 74 08          JZ     J14            ; TEST_WAIT_TIME
012E 2B C9          SUB    CX,CX         ; EXIT WITH TIME EXPIRED
0130 E2 FE          LOOP   J13           ; SET UP 1/8 SECOND LOOP TIME
0132 FE CC          DEC    AH            ; WAIT FOR THE REQUIRED TIME
0134 EB F6          JMP    J12           ; DECREMENT TIME VALUE
0136 FB          J14:
                                ; ARE WE DONE YET
                                ; MOTOR_RUNNING
                                ; INTERRUPTS BACK ON FOR BYPASS
                                ; WAIT

```

Appendix A.

```

0137 59
0138 E8 045D R
0138 58
013C 8A FC
013E 86 00
0140 73 03
0142 E9 0299 R
0145 DE 0299 R
0148 56
0149 E8 0307 R
014C 8A 66 01
014F D0 E4
0151 D0 E4
0153 80 E4 04
0156 0A E2
0158 E8 0307 R
015B 80 FF 4D
015E 75 02
0160 EB 87
0162 8A E5
0164 E8 0307 R
0167 8A 66 01
016A E8 0307 R
016D 8A E1
016F E8 0307 R
0172 83 07
0174 E8 0331 R
0177 83 08
0179 E8 0331 R
017C 02 4E 0E
017F FE C9
0181 8A E1
0183 E8 0307 R
0186 83 08
0188 E8 0331 R
018B 8B 000D
018E 53
018F FC
0190 80 70
0192 E6 43
0194 50
0195 58
0196 80 FF
0198 E6 41
019A 50
019B 58
019C E6 41
019E 8A 46 0F
01A1 A8 01
01A3 74 05
01A5 B9 0210 R
01A8 EB 0C
01AA 3C 02
01AC 75 05
01AE B9 01FC R
01B1 EB 03
01B3 B9 01E2 R
01B6
01B6 B0 10
01B8 E6 A0
01BA E8 043A R
01BD E8 044E R
01C0 BA 00F2
01C3 0C E0
01C5 EE

;----- POP CX
; DO THE SEEK OPERATION
CALL SEEK ; MOVE TO CORRECT TRACK
POP AX ; RECOVER COMMAND
MOV BH,AH ; SAVE COMMAND IN BH
MOV DH,0 ; SET NO SECTORS READ IN CASE OF
; ERROR
JNC J14_1 ; IF NO ERROR CONTINUE, JUMP AROUND
; JMP
JMP J17 ; CARRY SET JUMP TO MOTOR WAIT
J14_1: MOV SI,OFFSET J17 ; DUMMY RETURN ON STACK FOR
; NEC_OUTPUT
PUSH SI ; SO THAT IT WILL RETURN TO MOTOR
; OFF LOCATION
;----- SEND OUT THE PARAMETERS TO THE CONTROLLER
CALL NEC_OUTPUT ; OUTPUT THE OPERATION COMMAND
MOV AH,[BP+1] ; GET THE CURRENT HEAD NUMBER
SAL AH,1 ; MOVE IT TO BIT 2
SAL AH,1
AND AH,4 ; ISOLATE THAT BIT
OR AH,DL ; OR IN THE DRIVE NUMBER
CALL NEC_OUTPUT
;----- TEST FOR FORMAT COMMAND
CMP BH,04DH ; IS THIS A FORMAT OPERATION?
JNE J15 ; NO. CONTINUE WITH R/W/V
JMP J10 ; IF SO, HANDLE SPECIAL
; CYLINDER NUMBER
J15: MOV AH,CH ; HEAD NUMBER FROM STACK
CALL NEC_OUTPUT
MOV AH,[BP+1] ; SECTOR NUMBER
CALL NEC_OUTPUT
MOV AH,CL ; BYTES/SECTOR PARM FROM BLOCK
CALL GET_PARM ; TO THE NEC
MOV BL,8 ; EOT PARM FROM BLOCK
CALL GET_PARM ; RETURNED IN AH
ADD CL,[BP+14] ; ADD CURRENT SECTOR TO NUMBER IN
; TRANSFER
DEC CL ; CURRENT_SECTOR + N_SECTORS - 1
MOV AH,CL ; EOT PARAMETER IS THE CALCULATED
; ONE
CALL NEC_OUTPUT
MOV BL,I1 ; GAP LENGTH PARM FROM BLOCK
CALL GET_PARM ; TO THE NEC
MOV BX,I3 ; DTL PARM FROM BLOCK
PUSH BX ; SAVE INDEX TO DISK PARAMETER ON
; STACK
J16: CLD ; FORWARD DIRECTION
;----- START TIMER1 WITH INITIAL VALUE OF FFFF
MOV AL,01110000B ; SELECT TIMER1,LSB-MSB, MODE 0,
; BINARY COUNTER
; INITIALIZE THE COUNTER
OUT TIM_CTL,AL
PUSH AX
POP AX ; ALLOW ENOUGH TIME FOR THE 8253 TO
; INITIALIZE ITSELF
MOV AL,0FFH ; INITIAL COUNT VALUE FOR THE 8253
OUT TIMER+1,AL ; OUTPUT LEAST SIGNIFICANT BYTE
PUSH AX
POP AX ; WAIT
OUT TIMER+1,AL ; OUTPUT MOST SIGNIFACNT BYTE
;----- INITIALIZE CX FOR JUMP AFTER LAST PARAMETER IS PASSED TO NEC
MOV AL,[BP+15] ; RETRIEVE COMMAND PARAMETER
TEST AL,01H ; IS THIS AN ODD NUMBERED FUNCTION?
JZ J16_1 ; JUMP IF NOT ODD NUMBERED
MOV CX,OFFSET WRITE_LOOP
JMP SHORT J16_3
J16_1: CMP AL,2 ; IS THIS A READ?
JNZ J16_2 ; JUMP IF VERIFY
MOV CX,OFFSET READ_LOOP
JMP SHORT J16_3
J16_2: MOV CX,OFFSET VERIFY_LOOP
;----- FINISH INITIALIZATION
J16_3:
;-----
;*****
; ALL INTERRUPTS ARE ABOUT TO BE DISABLED. THERE IS A POTENTIAL
; THAT THIS TIME PERIOD WILL BE LONG ENOUGH TO MISS TIME OF
; DAY INTERRUPTS. FOR THIS REASON, TIMER1 WILL BE USED TO
; KEEP TRACK OF THE NUMBER OF TIME OF DAY INTERRUPTS WHICH
; WILL BE MISSED. THIS INFORMATION IS USED AFTER THE DISKETTE
; OPERATION TO UPDATE THE TIME OF DAY.
;-----
MOV AL,10H ; DISABLE NMI
OUT NMI_PORT,AL ; NO KEYBOARD INTERRUPT
CALL CLOCK_WAIT ; WAIT IF TIMER0 IS ABOUT TO
; INTERRUPT
;----- ENABLE WATCHDOG TIMER
;*****
; GIVEN THE CURRENT SYSTEM CONFIGURATION A METHOD IS NEEDED
; TO PULL THE NEC OUT OF "FATAL ERROR" SITUATIONS. A TIMER
; ON THE ADAPTER CARD IS PROVIDED WHICH WILL PERFORM THIS
; FUNCTION. THE WATCHDOG TIMER ON THE ADAPTER CARD IS ENABLED
; AND STROBED BEFORE THE 8259 INTERRUPT 6 LINE IS ENABLED.
; THIS IS BECAUSE OF A GLITCH ON THE LINE LARGE ENOUGH TO
; TRIGGER AN INTERRUPT.
;-----
CALL GET_DRIVE ; GET BIT MASK FOR DRIVE
MOV DX,NEC_CTL ; CONTROL PORT TO NEC
OR AL,FDC_RESET+WD_ENABLE+WD_STROBE
OUT DX,AL ; OUTPUT CONTROL INFO FOR
; WATCHDOG(WD) ENABLE

```





Appendix A.

```

0242 01 06 006C R      ADD    TIMER_LOW,AX      ; ADD NUMBER OF TIMER INTERRUPTS TO
0246 73 04              JNC     J16_4            ; TIME
                                ; JUMP IF TIMER_LOW DID NOT SPILL
                                ; OVER TO TIMER_HI
0248 FF 06 006E R      INC     TIMER_HIGH       ;
024C 83 3E 006E R 18   J16_4: CMP     TIMER_HIGH,018H ; TEST FOR COUNT TOTALING 24 HOURS
0251 75 19              JNZ     J16_5            ; JUMP IF NOT 24 HOURS
0253 81 3E 006C R 00B0 CMP     TIMER_LOW,0B0H  ; LOW VALUE = 24 HOUR VALUE?
0259 7C 11              JL      J16_5            ; NOT 24 HOUR VALUE?

;----- TIMER HAS GONE 24 HOURS
025B C7 06 006E R 0000 MOV     TIMER_HIGH,0    ; ZERO OUT TIMER_HIGH VALUE
0261 81 2E 006C R 00B0 SUB     TIMER_LOW,0B0H  ; VALUE REFLECTS CORRECT TICKS PAST
                                ; 00B0H
0267 C6 06 0070 R 01   J16_5: MOV     TIMER_OFL,1   ; INDICATES 24 HOUR THRESHOLD
026C EB 0414 R          CALL    ENABLE          ; ENABLE ALL INTERRUPTS
026F 59                  POP     CX              ; CX:=AX, COUNT FOR NUMBER OF USER
                                ; TIME INTERRUPTS
                                ; IF ZERO DO NOT ISSUE ANY
                                ; INTERRUPTS
0270 E3 26              JCXZ   J16_7            ; SAVE ALL REGISTERS SAVED PRIOR TO
                                ; INT 1C CALL FROM TIMERINT
0272 1E                  PUSH   DS              ; THIS PROVIDES A COMPATIBLE
                                ; INTERFACE TO 1C
0273 50                  PUSH   AX              ;
0274 52                  PUSH   DX              ;
0275 CD 1C              J16_6: INT     1CH       ; TRANSFER CONTROL TO USER
                                ; INTERRUPT
0277 E2 FC              LOOP   J16_6           ; DO ALL USER TIMER INTERRUPTS
0279 5A                  POP     DX              ;
027A 58                  POP     AX              ;
027B 1F                  POP     DS              ; RESTORE REGISTERS
                                ; INTERRUPTS 1C HAVE BEEN ISSUED.
;----- CLOCK IS UPDATED AND USER INTERRUPTS 1C HAVE BEEN ISSUED.
027C 0A C0              OR     AL,AL           ; AL WAS SET DURING CALL TO ENABLE
027E 74 18              JZ     J16_7            ; NO KEY WAS PRESSED WHILE SYSTEM
                                ; WAS MASKED
                                ; DURATION OF TONE
                                ; FREQUENCY OF TONE
                                ; NOTIFY USER OF MISSED KEYBORAD
                                ; INPUT
;----- CLEAR SHIFT STATES DONT LEAVE POSSIBILITY OF DANGLING STATES
; OF MISSED BREAKS
0289 80 26 0017 R F0   AND     KB_FLAG,0F0H   ; CLEAR ALT,CLRL,LEFT AND RIGHT
                                ; SHIFTS
028E 80 26 0018 R 0F   AND     KB_FLAG_1,0FH  ; CLEAR POTENTIAL BREAK OF INS,CAPS
                                ; NUM AND SCROLL SHIFT
0293 80 26 0088 R 1F   AND     KB_FLAG_2,1FH  ; CLEAR FUNCTION STATES
0298 9D                  POPF                    ; GET THE FLAGS
0299 J17:
0299 72 40              JC     J20              ;
029B E8 03A9 R          CALL   RESULTS          ; GET THE NEC STATUS
029E 72 3B              JC     J20              ; LOOK FOR ERROR
;----- CHECK THE RESULTS RETURNED BY THE CONTROLLER
02A0 FC                  CLD                    ; SET THE CORRECT DIRECTION
02A1 BE 0042 R          MOV     SI,OFFSET NEC_STATUS ; POINT TO STATUS FIELD
02A4 AC                  LODS   NEC_STATUS      ; GET ST0
02A5 24 C0              AND     AL,0C0H        ; TEST FOR NORMAL TERMINATION
02A7 74 58              JZ     J22              ; OPN_OK
02A9 3C 40              CMP     AL,040H        ; TEST FOR ABNORMAL TERMINATION
02AB 75 25              JNZ    J18              ; NOT ABNORMAL, BAD NEC

;-----
;*****NOTE****
; THE CURRENT SYSTEM CONFIGURATION HAS NO DMA. IN ORDER TO
; STOP THE NEC AN EOT MUST BE PASSED TO FORCE THE NEC TO HALT
; THEREFORE, THE STATUS RETURNED BY THE NEC WILL ALWAYS SHOW
; AN EOT ERROR. IF THIS IS THE ONLY ERROR RETURNED AND THE
; NUMBER OF SECTORS TRANSFERRED EQUALS THE NUMBER SECTORS
; REQUESTED IN THIS INTERRUPT CALL THEN THE OPERATION HAS
; COMPLETED SUCCESSFULLY. IF AN EOT ERROR IS RETURNED AND THE
; REQUESTED NUMBER OF SECTORS IS NOT THE NUMBER OF SECTORS
; TRANSFERRED THEN THE ERROR IS LEGITIMATE. WHEN THE EOT
; ERROR IS INVALID THE STATUS BYTES RETURNED ARE UPDATED TO
; REFLECT THE STATUS OF THE OPERATION IF DMA HAD BEEN PRESENT
;-----
02AD AC                  LODS   NEC_STATUS      ; GET ST1
02AE 3C 80              CMP     AL,80H         ; IS THIS THE ONLY ERROR?
02B0 74 2A              JE     J21_1           ; NORMAL TERMINATION, NO ERROR
02B2 D0 E0              SAL    AL,1            ; NOT EOT ERROR, BYPASS ERROR BITS
02B4 D0 E0              SAL    AL,1            ;
02B6 D0 E0              SAL    AL,1            ; TEST FOR CRC ERROR
02B8 B4 10              MOV     AH,BAD_CRC    ;
02BA 72 18              JC     J19             ; RW_FAIL
02BC D0 E0              SAL    AL,1            ; TEST FOR DMA OVERRUN
02BE B4 08              MOV     AH,BAD_DMA    ;
02C0 72 12              JC     J19             ; RW_FAIL
02C2 D0 E0              SAL    AL,1            ;
02C4 D0 E0              SAL    AL,1            ; TEST FOR RECORD NOT FOUND
02C6 B4 04              MOV     AH,RECORD_NOT_FND ;
02C8 72 0A              JC     J19             ; RW_FAIL
02CA D0 E0              SAL    AL,1            ;
02CC D0 E0              SAL    AL,1            ; TEST MISSING ADDRESS MARK
02CE B4 02              MOV     AH,BAD_ADDR_MARK ;
02D0 72 02              JC     J19             ; RW_FAIL
;-----
02D2 B4 20              J18: NEC MUST HAVE FAILED ; RW_FAIL
02D4
02D4 08 26 0041 R      J19: MOV     AH,BAD_NEC  ; RW-NEC-FAIL
02D8 E8 03EA R          OR     DISKETTE_STATUS,AH ; RW-FAIL
02DB CALL    NUM_TRANS      ; HOW MANY WERE REALLY TRANSFERRED
02DB C3              RET                    ; RW_ERR
;----- OPERATION WAS SUCCESSFUL ; RETURN TO CALLER

```

```

02DC
02DC 8A 5E 0E
02DF EB 03EA R
02E2 3A D8
02E4 74 0C

02E6 80 0E 0041 R 04
02EB C6 06 0043 R 80
02F0 F9
02F1 C3
02F2 33 C0
02F4 33 F6
02F6 88 84 0042 R
02FA 46
02FB 88 84 0042 R
02FF EB 03
0301 EB 03EA R
0304 32 E4
0306 C3
0307

```

```

J21_1: MOV BL,[BP+14] ; GET NUMBER OF SECTORS PASSED
; FROM STACK
CALL NUM_TRANS ; HOW MANY GOT MOVED, AL CONTAINS
; NUM OF SECTORS
CMP BL,AL ; NUMBER REQUESTED=NUMBER ACTUALLY
; TRANSFERRED?
JE J21_2 ; TRANSFER SUCCESSFUL
;----- OPERATION ATTEMPTED TO ACCESS DATA PAST REAL EOT. THIS IS
; A REAL ERROR
OR DISKETTE_STATUS,RECORD_NOT_FND
MOV NEC_STATUS+1,80H ; ST1 GETS CORRECT VALUE
STC
RET
J21_2: XOR AX,AX ; CLEAR AX FOR NEC_STATUS UPDATE
XOR SI,SI ; INDEX TO NEC_STATUS ARRAY
MOV NEC_STATUS[SI],AL ; ZERO OUT BYTE, STO
INC SI ; POINT INDEX AT SECOND BYTE
MOV NEC_STATUS[SI],AL ; ZERO OUT BUYE, ST1
JMP SHORT J21_3 ; OPN_OK
J22: CALL NUM_TRANS
J21_3: XOR AH,AH ; NO ERRORS
RET
RW_OPH ENDP

```

```

;-----
; NEC_OUTPUT
; THIS ROUTINE SENDS A BYTE TO THE NEC CONTROLLER
; AFTER TESTING FOR CORRECT DIRECTION AND CONTROLLER READY
; THIS ROUTINE WILL TIME OUT IF THE BYTE IS NOT ACCEPTED
; WITHIN A REASONABLE AMOUNT OF TIME, SETTING THE DISKETTE
; STATUS ON COMPLETION
; INPUT
; (AH) BYTE TO BE OUTPUT
; OUTPUT
; CY = 0 SUCCESS
; CY = 1 FAILURE -- DISKETTE STATUS UPDATED
; IF A FAILURE HAS OCCURRED, THE RETURN IS MADE ONE
; LEVEL HIGHER THAN THE CALLER OF NEC_OUTPUT
; THIS REMOVES THE REQUIREMENT OF TESTING AFTER EVERY
; CALL OF NEC_OUTPUT
; (AL) DESTROYED
;-----

```

```

0307
0307 52
0308 51
0309 BA 00F4
030C 33 C9
030E EC
030F A8 40
0311 74 0C
0313 E2 F9
0315
0315 80 0E 0041 R 80
031A 59
031B 5A
031C 58
031D F9
031E C3
031F 33 C9
0321 EC
0322 A8 80
0324 75 04
0326 E2 F9
0328 EB EB
032A
032A 8A C4
032C 42

032D EE
032E 59
032F 5A
0330 C3
0331

```

```

NEC_OUTPUT PROC NEAR
; SAVE REGISTERS
PUSH DX
PUSH CX
MOV DX,NEC_STAT ; STATUS PORT
XOR CX,CX ; COUNT FOR TIME OUT
J23: IN AL,DX ; GET STATUS
TEST AL,D10 ; TEST DIRECTION BIT
JZ J25 ; DIRECTION OK
LOOP J23
J24: OR DISKETTE_STATUS,TIME_OUT ; TIME_ERROR
POP CX
POP DX ; SET ERROR CODE AND RESTORE REGS
POP AX ; DISCARD THE RETURN ADDRESS
STC ; INDICATE ERROR TO CALLER
RET
J25: XOR CX,CX ; RESET THE COUNT
J26: IN AL,DX ; GET THE STATUS
TEST AL,RQM ; IS IT READY?
JNZ J27 ; YES, GO OUTPUT
LOOP J26 ; COUNT DOWN AND TRY AGAIN
JMP J24 ; ERROR CONDITION
J27: ; OUTPUT
MOV AL,AH ; GET BYTE TO OUTPUT
INC DX ; DATA PORT IS 1 GREATER THAN
; STATUS PORT
OUT DX,AL ; OUTPUT THE BYTE
POP CX ; RECOVER REGISTERS
POP DX
RET ; CY = 0 FROM TEST INSTRUCTION
NEC_OUTPUT ENDP
;-----

```

```

0331
0331 1E
0332 56
0333 2B C0
0335 32 FF
0337 8E D8

0339 C5 36 0078 R
033D D1 EB

033F 9C
0340 8A 20
0342 83 FB 01

```

```

; GET_PARM
; THIS ROUTINE FETCHES THE INDEXED POINTER FROM
; THE DISK_BASE BLOCK POINTED AT BY THE DATA
; VARIABLE DISK_POINTER
; A BYTE FROM THAT TABLE IS THEN MOVED INTO AH,
; THE INDEX OF THAT BYTE BEING THE PARM IN BX
; ENTRY --
; BL = INDEX OF BYTE TO BE FETCHED * 2
; IF THE LOW BIT OF BL IS ON, THE BYTE IS IMMEDIATELY
; OUTPUT TO THE NEC CONTROLLER
; EXIT --
; AH = THAT BYTE FROM BLOCK
; BX = DESTROYED
;-----
GET_PARM PROC NEAR
; SAVE SEGMENT
PUSH DS
; SAVE REGISTER
PUSH SI
; ZERO TO AX
SUB AX,AX
; ZERO BH
XOR BH,BH
MOV DS,AX
ASSUME DS:ABS0
LDS SI,DISK_POINTER ; POINT TO BLOCK
SHR BX,1 ; DIVIDE BX BY 2, AND SET FLAG FOR
; EXIT
; SAVE OUTPUT BIT
PUSHF
MOV AH,[SI+BX] ; GET THE BYTE
CMP BX,1 ; IS THIS THE PARM WITH DMA
; INDICATOR

```

Appendix A.

```

0345 75 05          JNZ     J27_1
0347 80 CC 01      DR      AH,1          ; TURN ON NO DMA BIT
034A EB 0C          JMP     SHORT J27_2
034C 83 FB 0A      J27_1: CMP     BX,10          ; MOTOR STARTUP DELAY?
034F 75 07          JNE     J27_2
0351 80 FC 04      CMP     AH,4          ; GREATER THAN OR EQUAL TO 1/2 SEC?
0354 7D 02          JGE     J27_2          ; YES, OKAY
0356 B4 04          MOV     AH,4          ; NO, FORCE 1/2 SECOND DELAY
0358 9D              POPF          ; GET OUTPUT BIT
0359 5E              POP     SI          ; RESTORE REGISTER
035A 1F              POP     DS          ; RESTORE SEGMENT
035B 72 AA          JC      NEC_OUTPUT   ; IF FLAG SET, OUTPUT TO CONTROLLER
035D C3              RET              ; RETURN TO CALLER
035E

```

```

GET_PARM          ENDP
;-----
; BOUND_SETUP
; THIS ROUTINE SETS UP BUFFER ADDRESSING FOR READ/WRITE/VERIFY
; OPERATIONS.
; INPUT
; ES HAS ORIGINAL BUFFER SEGMENT VALUE
; BP POINTS AT BASE OF SAVED PARAMETERS ON STACK
; OUTPUT
; ES HAS SEGMENT WHICH WILL ALLOW 64K ACCESS. THE
; COMBINATION ES:DI AND DS:SI POINT TO THE BUFFER. THIS
; CALCULATED ADDRESS WILL ALWAYS ACCESS 64K OF MEMORY.
; BX DESTROYED
DISKIO2.INC

```

```

035E          BOUND_SETUP      PROC      NEAR
035E          PUSH     CX          ; SAVE REGISTERS
035F 8B 5E 0C      MOV     BX,[BP+12]      ; GET OFFSET OF BUFFER FROM STACK
0362 53           PUSH     BX          ; SAVE OFFSET TEMPORARILY
0363 81 04          MOV     CL,4           ; SHIFT COUNT
0365 D3 EB          SHR     BX,CL         ; SHIFT OFFSET FOR NEW SEGMENT
; VALUE
0367 8C C1          MOV     CX,ES          ; PUT ES IN REGISTER SUITABLE FOR
; ADDING TO
0369 03 CB          ADD     CX,BX          ; GET NEW VALUE FOR ES
036B 8E C1          MOV     ES,CX          ; UPDATE THE ES REGISTER
036D 5B           POP     BX          ; RECOVER ORIGINAL OFFSET
036E 81 E3 000F    AND     BX,0000FH      ; NEW OFFSET
0372 8B F3          MOV     SI,BX          ; DS:SI POINT AT BUFFER
0374 8B FB          MOV     DI,BX          ; ES:DI POINT AT BUFFER
0376 59           POP     CX
0377 C3           RET
0378

```

```

BOUND_SETUP      ENDP
;-----
; CHK_STAT_2
; THIS ROUTINE HANDLES THE INTERRUPT RECEIVED AFTER
; A RECALIBRATE, SEEK, OR RESET TO THE ADAPTER.
; THE INTERRUPT IS WAITED FOR, THE INTERRUPT STATUS SENSED,
; AND THE RESULT RETURNED TO THE CALLER.
; INPUT
; NONE
; OUTPUT
; CY = 0 SUCCESS
; CY = 1 FAILURE -- ERROR IS IN DISKETTE_STATUS
; (AX) DESTROYED

```

```

0378          CHK_STAT_2      PROC      NEAR
0378          PUSH     BX          ; SAVE REGISTERS
0379 56           PUSH     SI
037A 33 DB          XOR     BX,BX          ; NUMBER OF SENSE INTERRUPTS TO
; ISSUE
037C BE 0391 R      MOV     SI,OFFSET J33_3 ; SET UP DUMMY RETURN FROM
; NEC_OUTPUT
037F 56           MOV     SI          ; PUT ON STACK
0380 B4 08          MOV     AH,08H        ; SENSE INTERRUPT STATUS
0382 E8 0307 R      CALL    NEC_OUTPUT    ; ISSUE SENSE INTERRUPT STATUS
0385 E8 03A9 R      CALL    RESULTS
0388 72 10          JC      J35           ; NEC TIME OUT, FLAGS SET IN
; RESULTS
038A 40 0042 R      MOV     AL,NEC_STATUS ; GET STATUS
038D A8 20          TEST    AL,SEEK_END   ; IS SEEK OR RECAL OPERATION DONE?
038F 75 0D          JNZ    J35_1         ; JUMP IF EXECUTION OF SEEK OR
; RECAL DONE
0391 4B           DEC     BX            ; DEC LOOP COUNTER
0392 75 EC          JNZ    J33_3         ; DO ANOTHER LOOP
0394 80 0E 0041 R 80  OR     J33_2          ; DISKETTE_STATUS, TIME OUT
0399 F9           STC
; RETURN ERROR INDICATION FOR
; CALLER
039A 5E           POP     SI            ; RESTORE REGISTERS
039B 5E           POP     SI
039C 5B           POP     BX
039D C3           RET

```

```

;-----SEEK END HAS OCCURED, CHECK FOR NORMAL TERMINATION
039E 24 C0          J35_1: AND     AL,0COH      ; MASK NORMAL TERMINATION BITS
03A0 74 F8          JZ      J35           ; JUMP IF NORMAL TERMINATION
03A2 80 0E 0041 R 40 OR     DISKETTE_STATUS,BAD_SEEK
03A7 EB F0          JMP     J34
03A9

```

```

CHK_STAT_2      ENDP
;-----
; RESULTS
; THIS ROUTINE WILL READ ANYTHING THAT THE NEC CONTROLLER
; HAS TO SAY FOLLOWING AN INTERRUPT.
; IT IS ASSUMED THAT THE NEC DATA PORT = NEC STATUS PORT + 1.
; INPUT
; NONE
; OUTPUT
; CY = 0 SUCCESSFUL TRANSFER
; CY = 1 FAILURE -- TIME OUT IN WAITING FOR STATUS

```

```

; NEC_STATUS AREA HAS STATUS BYTE LOADED INTO IT
; (AH) DESTROYED
;-----
03A9          PROC   NEAR
03A9          FC
03AA          BF 0042 R
03AD          51
03AE          52
03AF          53
03B0          B3 07
RESULTS      PROC   NEAR
              CLD
              MOV   DI,OFFSET NEC_STATUS ; POINTER TO DATA AREA
              PUSH  CX                    ; SAVE COUNTER
              PUSH  DX
              PUSH  BX
              MOV   BL,7                  ; MAX STATUS BYTES
;-----
;----- WAIT FOR REQUEST FOR MASTER
J38:          XOR   CX,CX                ; INPUT LOOP
              MOV   DX,NEC_STAT          ; COUNTER
              MOV   DX,NEC_STAT          ; STATUS PORT
J39:          IN   AL,DX                  ; WAIT FOR MASTER
              TEST  AL,080H              ; GET STATUS
              JNZ  J40A                  ; MASTER READY
              LOOP  J39                  ; TEST_DIR
              OR   DISKETTE_STATUS,TIME_OUT ; WAIT_MASTER
              OR   DISKETTE_STATUS,TIME_OUT ; RESULTS_ERROR
J40:          STC                          ; SET ERROR RETURN
;-----
;----- RESULT OPERATION IS DONE
J44:          POP  BX
              POP  DX
              POP  CX
              RET
;-----
;----- TEST THE DIRECTION BIT
J40A:         IN   AL,DX                  ; GET STATUS REG AGAIN
              TEST  AL,040H              ; TEST DIRECTION BIT
              JNZ  J42                    ; OK TO READ STATUS
J41:          JNZ  J42                    ; NEC_FAIL
              OR   DISKETTE_STATUS,BAD_NEC
              JMP  J40                    ; RESULTS_ERROR
;-----
;----- READ IN THE STATUS
J42:          INC  DX                      ; INPUT_STAT
              IN   AL,DX                  ; POINT AT DATA PORT
              MOV  [DI],AL                ; GET THE DATA
              INC  DI                      ; STORE THE BYTE
              INC  DI                      ; INCREMENT THE POINTER
              MOV  CX,10                   ; LOOP TO KILL TIME FOR NEC
J43:          LOOP J43
              DEC  DX                      ; POINT AT STATUS PORT
              IN   AL,DX                  ; GET STATUS
              TEST  AL,010H              ; TEST FOR NEC STILL BUSY
              JZ   J44                    ; RESULTS DONE
              DEC  BL                      ; DECREMENT THE STATUS COUNTER
              JNZ  J38                    ; GO BACK FOR MORE
              JMP  J41                    ; CHIP HAS FAILED
;-----
; NUM_TRANS
; THIS ROUTINE CALCULATES THE NUMBER OF SECTORS THAT
; WERE ACTUALLY TRANSFERRED TO/FROM THE DISKETTE
; INPUT
; (CH) = CYLINDER OF OPERATION
; (CL) = START SECTOR OF OPERATION
; OUTPUT
; (AL) = NUMBER ACTUALLY TRANSFERRED
; NO OTHER REGISTERS MODIFIED
;-----
03EA          PROC   NEAR
03EA          A0 0045 R
03ED          3A 46 0B
03F0          A0 0047 R
03F3          74 07
03F5          B3 08
03F7          MOV   BL,8
03FA          CALL  GET_PARM              ; GET EOT VALUE
03FC          MOV   AL,AH                  ; INTO AL
03FE          INC  AL                      ; USE EOT+1 FOR CALCULATION
0401          SUB  AL,[BP]+10              ; SUBTRACT START FROM END
0404          MOV  [BP+14],AL
0405          RET
NUM_TRANS     PROC   NEAR
RESULTS      PROC   NEAR
              ENDP
;-----
; DISABLE
; THIS ROUTINE WILL DISABLE ALL INTERRUPTS EXCEPT FOR
; INTERRUPT 6 SO WATCH DOG TIME OUT CAN OCCUR IN ERROR
; CONDITIONS.
; INPUT
; NONE
; OUTPUT
; NONE
; ALL REGISTERS REMAIN INTACT
;-----
0405          PROC   NEAR
0405          50
;-----
0406          PUSH  AX
0408          DISABLE ALL INTERRUPTS AT THE 8259 LEVEL EXCEPT DISKETTE
              IN   AL,INTA01              ; READ CURRENT MASK
              MOV  [BP+16],AX             ; SAVE MASK ON THE SPACE ALLOCATED
              MOV  AL,0BFH                ; ON THE STACK
040B          MOV   AL,0BFH                ; MASK OFF ALL INTERRUPTS EXCEPT
              MOV   AL,0BFH                ; DISKETTE
040D          OUT  INTA01,AL               ; OUTPUT MASK TO THE 8259
040F          CALL BOUND_SETUP             ; SETUP REGISTERS TO ACCESS BUFFER
0412          POP  AX
0413          RET
0414          ENDP
;-----
; ENABLE
; THIS PROC ENABLES ALL INTERRUPTS. IT ALSO SETS THE 8253 TO

```

Appendix A.

```

; THE MODE REQUIRED FOR KEYBOARD DATA DESERIALIZATION.
; BEFORE THE LATCH FOR KEYBOARD DATA IS RESET, BIT 0 OF THE
; 8255 IS READ TO DETERMINE WHETHER ANY KEYSTROKES OCCURED
; WHILE THE SYSTEM WAS MASKED OFF.
; INPUT
; NONE
; OUTPUT
; AL=1 MEANS A KEY WAS STRUCK DURING DISKETTE I/O. (OR NOISE
; ON THE LINE)
; AL=0 MEANS THAT NO KEY WAS PRESSED.
; AX IS DESTROYED. ALL OTHER REGISTERS REMAIN INTACT.
-----
0414          PROC    NEAR
0414 52        PUSH    DX                ; SAVE DX
;-----
0415 B0 76    RETURN   TIMER1 TO STATE NEEDED FOR KEYBOARD I/O
0417 E6 43    MOV     AL,01110110B
0419 50      OUT     TIM_CTL,AL
041A 58      PUSH    AX
;-----
041B B0 FF    MOV     AL,0FFH                ; WAIT FOR 8253 TO INITIALIZE
041D E6 41    OUT     TIMER+1,AL        ; ITSELF
041F 50      PUSH    AX                ; INITIAL VALUE FOR 8253
0420 58      POP     AX                ; LSB
0421 E6 41    OUT     TIMER+1,AL        ; WAIT
;-----
0423 8E 46 10 CHECK IF ANY KEYSTROKES OCCURED DURING DISKETTE TRANSFER
;-----
0426 E4 62    MOV     ES,[BP+16]            ; GET ORIGINAL ES VALUE FROM THE
0428 24 01    IN      AL,62H                ; READ PORT C OF 8255
042A 50      AND     AL,01H                ; BIT=1 MEANS KEYSTROKE HAS OCCURED
;-----
042B E4 A0    ENABLE NMI INTERRUPTS
042D B0 80    IN      AL,NMI_PORT        ; RESET LATCH
042F E6 A0    MOV     AL,80H                ; MASK TO ENABLE NMI
;-----
0431 8B 46 10 ENABLE ALL INTERRUPTS WHICH WERE ENABLED BEFORE TRANSFER
;-----
0434 E6 21    MOV     AX,[BP+16]            ; GET MASK FROM THE STACK
0436 58      OUT     INTA01,AL
0437 5A      POP     AX
0438 FB      POP     DX                ; PASS BACK KEY STROKE FLAG
0439 C3      STI
043A        RET

ENABLE      ENDP
-----
;CLOCK_WAIT
; THIS PROCEDURE IS CALLED WHEN THE TIME OF DAY
; IS BEING UPDATED. IT WAITS IF TIMER0 IS ALMOST
; READY TO WRAP UNTIL IT IS SAFE TO READ AN ACCURATE
; TIMER1.
; INPUT
; NONE.
; OUTPUT
; NONE. AX IS DESTROYED.
-----
043A        PROC    NEAR
043A 32 C0    XOR     AL,AL                ; READ MODE TIMER0 FOR 8253
043C E6 43    OUT     TIM_CTL,AL        ; OUTPUT TO THE 8253
043E 50      PUSH    AX
043F 58      POP     AX                ; WAIT FOR 8253 TO INITIALIZE
;-----
0440 E4 40    IN      AL,TIMER0            ; ITSELF
0442 86 C4    XCHG   AL,AH                ; READ LEAST SIGNIFICANT BYTE
0444 E4 40    IN      AL,AH                ; SAVE IT
0446 86 C4    XCHG   AL,AH                ; READ MOST SIGNIFICANT BYTE
0448 3D 012C XCHG   AX,THRESHOLD        ; REARRANGE FOR PROPER ORDER
044B 72 ED    CMP     AX,CLOCK_WAIT      ; IS TIMER0 CLOSE TO WRAPPING?
044D C3      JC      CLOCK_WAIT      ; JUMP IF CLOCK IS WITHIN THRESHOLD
044E        RET                ; OK TO READ TIMER1

CLOCK_WAIT ENDP
-----
;GET_DRIVE
; THIS ROUTINE WILL CALCULATE A BIT MASK FOR THE DRIVE WHICH
; IS SELECTED BY THE CURRENT INT 13 CALL. THE DRIVE SELECTED
; CORRESPONDS TO THE BIT IN THE MASK, I.E. DRIVE ZERO
; CORRESPONDS TO BIT ZERO AND A 01H IS RETURNED. THE BIT IS
; CALCULATED BY ACCESSING THE PARAMETERS PASSED TO INT 13
; WHICH WERE SAVED ON THE STACK.
; INPUT
; NONE.
; OUTPUT
; BYTE PTR[BP] MUST POINT TO DRIVE FOR SELECTION.
; AL CONTAINS THE BIT MASK. ALL OTHER REGISTERS ARE INTACT
-----
044E        PROC    NEAR
044E 51      PUSH    CX                ; SAVE REGISTER.
044F 8A 4E 00 MOV     CL,BYTE PTR[BP]    ; GET DRIVE NUMBER
0452 80 E1 3F AND     CL,OFF_DBL_TRK    ; IGNORE J SUPPORT PARAMETER
0455 80 01    MOV     AL,1                ; INITIALIZE AL WITH VALUE FOR
;-----
0457 D2 E0    SHL     AL,CL                ; SHIFTING
;-----
0459 24 0F    AND     AL,DRV_SUPPORT    ; SHIFT BIT POSITION BY DRIVE
;-----
045B 59      POP     CX                ; RANGE CHECK
045C C3      RET                ; RESTORE REGISTERS

GET_DRIVE  ENDP
-----
; SEEK
; THIS ROUTINE WILL MOVE THE HEAD ON THE NAMED DRIVE
; TO THE NAMED TRACK. IF THE DRIVE HAS NOT BEEN ACCESSED
; SINCE THE DRIVE RESET COMMAND WAS ISSUED, THE DRIVE WILL BE
; RECALIBRATED.
; INPUT
; (DL) = DRIVE TO SEEK ON

```

```

;      (CH) = TRACK TO SEEK TO
;      [BP] = BIT 6 :1 80 TRACK MEDIA
;           BIT 6 :0 40 TRACK
; OUTPUT
;      CY = 0 SUCCESS
;      CY = 1 FAILURE -- DISKETTE_STATUS SET ACCORDINGLY
;      (AX) DESTROYED
-----
045D 56          SEEK PROC NEAR
045E 53          PUSH SI          ; SAVE REGISTER
045F 51          PUSH BX          ; SAVE REGISTER
0460 BE 0074 R   MOV CX
0463 80 01      MOV SI,OFFSET TRACK0 ; BASE OF CURRENT HEAD POSITIONS
0465 8A CA      MOV AL,1          ; ESTABLISH MASK FOR RECAL
0467 81 E1 00FF AND CL,DL        ; USE DRIVE AS A SHIFT COUNT
0468 03 F1      ADD CX,0FFH      ; MASK OFF HIGH BYTE
046D D2 C0      ROL SI,CX          ; POINT SI AT CORRECT DRIVE
;           ; GET MASK FOR DRIVE
;----- SI CONTAINS OFFSET FOR CORRECT DRIVE, AL CONTAINS BIT MASK
; IN POSITION 0,1 OR 2
.046F 59          POP CX          ; RESTORE PARAMETER REGISTER
0470 BB 04F0 R   MOV BX,OFFSET PJ32 ; SET UP ERROR RECOVERY ADDRESS
0473 53          PUSH BX          ; NEEDED FOR ROUTINE NEC_OUTPUT
0474 84 06 003E R TEST SEEK_STATUS,AL ; TEST DRIVE FOR RECAL
0478 75 39      JNZ PJ28        ; NO RECAL
047A 08 06 003E R OR SEEK_STATUS,AL  ; TURN ON THE NO RECAL BIT IN FLAG
047E 80 3C 00   CMP BYTE PTR(SI),0 ; LAST REFERENCED TRACK=0?
0481 74 30      JZ PJ28        ; YES IGNORE RECAL
0483 B4 07      MOV AH,07H      ; RECALIBRATE COMMAND
0485 E8 0307 R   CALL NEC_OUTPUT
0488 8A E2      MOV AH,DL       ; RECAL REQUIRED ON DRIVE IN DL
048A E8 0307 R   CALL NEC_OUTPUT ; OUTPUT THE DRIVE NUMBER
;----- HEAD IS MOVING TO CORRECT TRACK
; HOOK FOR 40 TRACK TO 80 TRACK SUPPORT
048D E8 0378 R   CALL CHK_STAT_2 ; CHECK STATUS
0490 73 19      JNC RDY1        ; LEAVE ONE MORE CHANCE FOR RETRY
0492 C6 06 0041 R 00 MOV DISKETTE_STATUS,0 ; CLEAR STATUS
0497 B4 05      MOV AH,5        ; SET PARM AS 5 MILL
0499 E8 04F9 R   CALL DELAY_N_MS ; WAIT SUB
049C B4 07      MOV AH,07H      ; RECALIBRATE COMMAND
049E E8 0307 R   CALL NEC_OUTPUT ; OUT
.04A1 8A E2      MOV AH,DL       ; RECAL DRIVE IN DL
04A3 E8 0307 R   CALL NEC_OUTPUT ; OUT
04A6 E8 0378 R   CALL CHK_STAT_2 ; CHECK STATUS
04A9 72 48      JC PJ32_2      ; IF ERROR, JUMP TO SET CARRRY
RDY1:
04AB B4 12      MOV AH,18       ; RETRY OK WAIT UNTILL SETTLE
04AD E8 04F9 R   CALL DELAY_N_MS ; IX
; DRIVE IS SYNC WITH CONTROLLER, SEEK TO TRACK
; SEEK READY
; MOV BYTE PTR(SI),0
;----- DRIVE IS IN SYNCH WITH CONTROLLER, SEEK TO TRACK
PJ28:
04B3 8A 04      MOV AL,BYTE PTR(SI) ; GET THE PCN
04B5 2A C5     SUB AL,CH        ; GET SEEK_WAIT VALUE
04B7 74 36     JZ PJ31_1       ; ALREADY ON CORRECT TRACK
04B9 B4 0F      MOV AH,0FH      ; SEEK COMMAND TO NEC
04BB E8 0307 R   CALL NEC_OUTPUT
04BE 8A E2      MOV AH,DL       ; DRIVE NUMBER
04C0 E8 0307 R   CALL NEC_OUTPUT
04C3 51          PUSH CX          ; SAVE FOR DOS
04C4 F6 46 00 40 TEST BYTE PTR [BP],040H ; IF DOUBLE TRACK PARAMETER
04C8 75 02      JNE GO_SEEK     ; LEAVE PARAMETER AS IS
04CA D0 E5      SHL CH,1        ; MULTIPLY BY TWO WHEN 40 TRACK MEDIA
04CC 8A E5      MOV AH,CH       ; TRACK NUMBER
04CE E8 0307 R   CALL NEC_OUTPUT
04D1 59          POP CX          ; RESTORE FOR DOS
04D2 E8 0378 R   CALL CHK_STAT_2 ; GET ENDING INTERRUPT AND SENSE
; STATUS
;----- WAIT FOR HEAD SETTLE
04D5 9C          PUSHF           ; SAVE STATUS FLAGS
04D6 51          PUSH CX        ; SAVE REGISTER
04D7 B3 12      MOV BL,18     ; HEAD SETTLE PARAMETER
04D9 E8 0331 R   CALL GET_PARM
PJ29:
04DC B9 0226     MOV CX,550    ; HEAD SETTLE
04DF 0A E4      OR AH,AH     ; 1 MS LOOP
04E1 74 06     JZ PJ30      ; TEST FOR TIME EXPIRED
04E3 E2 FE     LOOP PJ30    ; DELAY FOR 1 MS
04E5 FE CC     DEC AH       ; DECREMENT THE COUNT
04E7 EB F3     JMP PJ29     ; DO IT SOME MORE
04E9 59          POP CX        ; RESTORE REGISTER
PJ31:
04EA 9D          POPF
04EB 72 06     JC PJ32_2
04ED 88 2C     MOV MOV      PJ32_2
04EF 5B          POP BX
PJ31_1:
04F0 5B          POP BX        ; GET RID OF DUMMY RETURN
04F1 5E          POP SI        ; SEEK_ERROR
04F2 C3          RET           ; RESTORE REGISTER
04F3 C6 04 FF   MOV PJ32_2, BYTE PTR(SI),0FFH ; UPDATE CORRECT
; RETURN TO CALLER
; UNKNOWN STATUS ABOUT SEEK
; OPERATION
04F6 5B          POP BX        ; GET RID OF DUMMY RETURN
04F7 EB F7     JMP SHORT PJ32
04F9          ENDP
SEEK
; INPUT AH: N MILL SEC WAIT
04F9 51          DELAY_N_MS PROC NEAR
04FA B9 0226     DE00: MOV CX,550 ; SOFTWARE TIMER
04FD 0A E4      OR AH,AH     ; JX
04FF 74 06     JZ DE40     ; JX
0501 E2 FE     LOOP DE20   ; JX

```

Appendix A.

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0503 FE CC          DEC      AH          ; JX
0505 EB F3        JMP      DE00        ; JX
0507 59          POP      CX          ; JX
0508 C3          RET
0509          DELAY_M_MS ENDP

;-----
; DISK_BASE
; THIS IS THE SET OF PARAMETERS REQUIRED FOR
; DISKETTE OPERATION. THEY ARE POINTED AT BY THE
; DATA VARIABLE DISK_POINTER. TO MODIFY THE PARAMETERS,
; BUILD ANOTHER PARAMETER BLOCK AND POINT AT IT
;-----
0509          DISK_BASE LABEL BYTE
0509 EF          DB      11101111B    ; SRT=E, HD UNLOAD=0F - 1ST SPECIFY
                                ; BYTE
050A 03          DB      3           ; HD LOAD=1, MODE=NO DMA - 2ND
                                ; SPECIFY BYTE
050B 25          DB      MOTOR_WAIT ; WAIT AFTER OPN TIL MOTOR OFF
050C 02          DB      2           ; 512 BYTES/SECTOR
050D 09          DB      9           ; EOT ( LAST SECTOR ON TRACK)
050E 2A          DB      02AH        ; GAP LENGTH
050F FF          DB      0FFH        ; DTL
0510 50          DB      050H        ; GAP LENGTH FOR FORMAT
0511 F6          DB      0F6H        ; FILL BYTE FOR FORMAT
0512 0F          DB      15          ; HEAD SETTLE TIME (MILLISECONDS)
0513 04          DB      4           ; MOTOR START TIME (1/8 SECONDS)

;-----
; DISK_INT
; THIS ROUTINE HANDLES THE DISKETTE INTERRUPT. AN INTERRUPT
; WILL OCCUR ONLY WHEN THE ONE-SHOT TIMER IS FIRED. THIS
; OCCURS IN AN ERROR SITUATION. THIS ROUTINE SETS ERRORS IN
; THE DISKETTE STATUS BYTE AND DISABLES THE ONE-SHOT TIMER.
; THEN THE RETURN ADDRESS ON THE STACK IS CHANGED TO RETURN
; TO THE OP_END LABEL.
; INPUT
; NONE.
; OUTPUT
; NONE. DS POINTS AT BIOS DATA AREA. CARRY FLAG IS SET SO
; THAT ERROR WILL BE CAUGHT IN THE ENVIRONMENT RETURNED TO.
;-----
0514          DISK_INT PROC FAR
0514 1E          PUSH     DS
0515 50          PUSH     AX
0516 52          PUSH     DX
0517 55          PUSH     BP
0518 E8 0000 E   CALL     D05          ; SAVE REGISTER
                                ; SAVE THE BP REGISTER
                                ; SETUP DS TO POINT AT BIOS DATA
;-----
; CHECK IF INTERRUPT OCCURED IN INT13 OR WHETHER IT IS A
; SPURIOUS INTERRUPT
051B 8B EC      MOV      BP,SP          ; POINT BP AT STACK
051D 0E          PUSH     CS          ; WAS IT IN THE BIOS AREA
051E 58          POP      AX
051F 3B 46 0A   CMP      AX,WORD PTR[BP+10] ; GET INTERRUPTED SEGMENT
0522 75 48      JNE      DI3          ; NOT IN BIOS, ERROR CONDITION
0524 8B 46 08   MOV      AX,WORD PTR[BP+8] ; GET IP ON THE STACK
0527 3D 01E2 R  CMP      AX,OFFSET VERIFY_LOOP ; RANGE CHECK IP FOR DISK
                                ; TRANSFER
052A 7C 40      JL       DI3          ; BELOW TRANSFER CODE
052C 3D 0228 R  CMP      AX,OFFSET OP_END+1 ; UPPER RANGE OF TRANSFER CODE
052F 7D 3B      JGE      DI3          ; ABOVE RANGE OF WATCHDOG TERRAIN
;-----
; VALID DISKETTE INTERRUPT CHANGE RETURN ADDRESS ON STACK TO
; PULL OUT OF LOOP
0531 C7 46 08 0227 R MOV      WORD PTR[BP+8],OFFSET OP_END
0536 81 4E 0C 0001 OR       WORD PTR[BP+12],1 ; TURN ON CARRY FLAG IN FLAGS ON
                                ; STACK
;-----
;*****
; A WRITE PROTECTED DISKETTE WILL ALWAYS GET STUCK IN WRITE LOOP
; WAITING FOR BEGINNING OF EXECUTION PHASE. WHEN THE WATCHDOG
; FIRES AND THE STATUS IN PORT NEC_STAT = DXH (X MEANS DON'T CARE)
; STATUS FROM THE RESULT PHASE IS AVAILABLE. THE STATUS IS READ
; AND WRITE PROTECT IS CHECKED FOR.
;-----
053B BA 00F4    MOV      DX,NEC_STAT
053E EC        IN       AL,DX          ; GET NEC STATUS BYTE
053F 24 F0     AND      AL,0F0H        ; MASK HIGH NIBBLE
0541 3C D0     CMP      AL,0D0H       ; MASK HIGH NIBBLE
0543 75 14     JNE      DI1          ; IS EXECUTION PHASE DONE
0545 E8 03A9 R  CALL     RESULTS       ; STUCK IN LOOP
                                ; GET STATUS OF OPERATION
0548 BE 0042 R  MOV      SI,OFFSET NEC_STATUS ; ADDRESS OF BYTES RETURNED BY
                                ; NEC
054B 8A 44 01  MOV      AL,[SI+1]       ; GET ST1
054E A8 02     TEST     AL,02H        ; WRITE PROTECT SIGNAL ACTIVE?
0550 74 07     JZ       DI1          ; TIME OUT ERROR
0552 80 0E 0041 R 03 OR       DISKETTE_STATUS,WRITE_PROTECT
0557 EB 13     JMP      SHORT DI3
;-----
; TIME OUT ERROR
DI1: OR       DISKETTE_STATUS,TIME_OUT
      OR       SEEK_STATUS,0 ; SET RECAL ON DRIVES
;-----
; RESET THE NEC AND DISABLE WATCHDOG
DI2: MOV      DX,NEC_CTL
      POP     BP
      MOV     SI,ADDRESS TO NEC CONTROL PORT
      CALL   GET_DRIVE
      PUSH   BP
      MOV     DX,AL
      OUT    DX,AL
      MOV     AL,EOI
      OUT    AL,EOI
      POP     BP
      POP     DX
      MOV     AL,INTA00,AL ; GIVE EOI TO 8259
;-----
0559 80 0E 0041 R 80 JZ       DI1
055E C6 06 003E R 00 OR       DI1,DI1
;-----
0563 BA 00F2    MOV      DX,NEC_CTL
0566 5D        POP     BP
;-----
0567 E8 044E R  CALL     GET_DRIVE
056A 55        PUSH   BP
056B EE        MOV     DX,AL
056C B0 20     OUT    DX,AL
056E E6 20     MOV     AL,EOI
0570 5D        OUT    AL,EOI
0571 5A        POP     BP
      POP     DX

```



0572 58  
 0573 1F  
 0574 CF  
 0575

```

    POP     AX
    POP     DS
    IRET                    ; RETURN FROM INTERRUPT
DISK_INT   ENDP

;-----INT 14-----
;RS232_IO
; THIS ROUTINE PROVIDES BYTE STREAM I/O TO THE COMMUNICATIONS
; PORT ACCORDING TO THE PARAMETERS:
;
; (AH)=0 INITIALIZE THE COMMUNICATIONS PORT
; (AL) HAS PARMS FOR INITIALIZATION
;
;-----7-----6-----5-----4-----3-----2-----1-----0-----
;-----BAUD RATE-----;-----PARITY-----;-----STOPBIT-----;-----WORD LENGTH-----
;
; 000 - 110          X0 - NONE          0 - 1    10 - 7 BITS
; 001 - 150          01 - ODD           1 - 2    11 - 8 BITS
; 010 - 300          11 - EVEN
; 011 - 600
; 100 - 1200
; 101 - 2400
; 110 - 4800
; 111 - 4800
;
; ON RETURN, THE RS232 INTERRUPTS ARE DISABLED AND
; CONDITIONS ARE SET AS IN CALL TO COMMO
; STATUS (AH=3)
;
; (AH)=1 SEND THE CHARACTER IN (AL) OVER THE COMMO LINE
; (AL) REGISTER IS PRESERVED
; ON EXIT, BIT 7 OF AH IS SET IF THE ROUTINE WAS
; UNABLE TO TRANSMIT THE BYTE OF DATA OVER
; THE LINE. IF BIT 7 OF AH IS NOT SET, THE
; REMAINDER OF AH IS SET AS IN A STATUS
; REQUEST, REFLECTING THE CURRENT STATUS OF
; THE LINE.
;
; (AH)=2 RECEIVE A CHARACTER IN (AL) FROM COMMO LINE BEFORE
; RETURNING TO CALLER
; ON EXIT, AH HAS THE CURRENT LINE STATUS, AS SET BY
; THE STATUS ROUTINE, EXCEPT THAT THE ONLY
; BITS LEFT ON, ARE THE ERROR BITS
; (7,4,3,2,1). IN THIS CASE, THE TIME OUT BIT
; INDICATES DATA SET READY WAS NOT RECEIVED.
; THUS, AH IS NOW ZERO ONLY WHEN AN ERROR
; OCCURRED. (NOTE: IF THE TIME-OUT BIT IS SET,
; OTHER BITS IN AH MAY NOT BE RELIABLE.)
;
; (AH)=3 RETURN THE COMMO PORT STATUS IN (AX)
; AH CONTAINS THE LINE CONTROL STATUS
; BIT 7 = TIME OUT
; BIT 6 = TRAN SHIFT REGISTER EMPTY
; BIT 5 = TRAN HOLDING REGISTER EMPTY
; BIT 4 = BREAK DETECT
; BIT 3 = FRAMING ERROR
; BIT 2 = PARITY ERROR
; BIT 1 = OVERRUN ERROR
; BIT 0 = DATA READY
; AL CONTAINS THE MODEM STATUS
; BIT 7 = RECEIVED LINE SIGNAL DETECT
;
; BIT 6 = RING INDICATOR
; BIT 5 = DATA SET READY
; BIT 4 = CLEAR TO SEND
; BIT 3 = DELTA RECEIVE LINE SIGNAL DETECT
; BIT 2 = TRAILING EDGE RING DETECTOR
; BIT 1 = DELTA DATA SET READY
; BIT 0 = DELTA CLEAR TO SEND
;
; (DX) = PARAMETER INDICATING WHICH RS232 CARD (0,1 ALLOWED)
; DATA AREA RS232_BASE CONTAINS THE BASE ADDRESS OF THE 8250 ON THE
; CARD. LOCATION 400H CONTAINS UP TO 4 RS232 ADDRESSES POSSIBLE
; DATA AREA RS232_TIM_OUT (BYTE) CONTAINS OUTER LOOP COUNT
; VALUE FOR TIMEOUT (DEFAULT=1)
;
; OUTPUT
;
; AX      MODIFIED ACCORDING TO PARMS OF CALL
; ALL OTHERS UNCHANGED
;
;-----

```

0575  
 0575 03F9  
 0577 02EA  
 0579 0175  
 057B 00BA  
 057D 005D  
 057F 002F  
 0581 0017  
 0583 0017  
 0585  
 0585 FB  
 0586 1E  
 0587 52  
 0588 56  
 0589 57  
 058A 51  
 058B 53  
 058C 8B F2  
 058E 8B FA  
 0590 D1 E6  
 0592 E8 0000 E  
 0595 8B 94 0000 R  
 0599 08 D2  
 059B 74 13  
 059D 0A E4  
 059F 74 16  
 05A1 FE CC

```

    ASSUME CS:CODE,DS:DATA
A1 LABEL WORD
    DW 1017 ; 110 BAUD ; TABLE OF INIT VALUE
    DW 746 ; 150
    DW 373 ; 300
    DW 186 ; 600
    DW 93 ; 1200
    DW 47 ; 2400
    DW 23 ; 4800
    DW 23 ; 4800
RS232_IO PROC FAR
;----- VECTOR TO APPROPRIATE ROUTINE
;-----
    STI ; INTERRUPTS BACK ON
    PUSH DS ; SAVE SEGMENT
    PUSH DX
    PUSH SI
    PUSH DI
    PUSH CX
    PUSH BX
    MOV SI,DX ; RS232 VALUE TO SI
    MOV DI,DX ; AND TO DI (FOR TIMEOUTS)
    SHL SI,1 ; WORD OFFSET
    CALL DDS ; POINT TO BIOS DATA SEGMENT
    MOV DX,RS232_BASE[SI] ; GET BASE ADDRESS
    OR DX,DX ; TEST FOR 0 BASE ADDRESS
    JZ A3 ; RETURN
    OR AH,AH ; TEST FOR (AH)=0
    JZ A4 ; COMMO INIT
    DEC AH ; TEST FOR (AH)=1

```

Appendix A.

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05A3 74 47      JZ      A5      ; SEND AL
05A5 FE CC      DEC     AH      ; TEST FOR (AH)=2
05A7 74 6C      JZ      A12     ; RECEIVE INTO AL
05A9 FE CC      DEC     AH      ; TEST FOR (AH)=3
05AB 75 03      JNZ     A3      ;
05AD E9 063F R  JMP     A18     ; COMMUNICATION STATUS
05B0             ; RETURN FROM RS232
05B0 5B        POP     BX
05B1 59        POP     CX
05B2 5F        POP     DI
05B3 5E        POP     SI
05B4 5A        POP     DX
05B5 1F        POP     DS
05B6 CF        IRET
05B7 8A E0      ;----- INITIALIZE THE COMMUNICATIONS PORT
05B9 83 C2 03   MOV     AH,AL   ; SAVE INIT PARMS IN AH
05BC 80 80      ADD     DX,3    ; POINT TO 8250 CONTROL REGISTER
05BE EE        MOV     AL,80H
05BF 8A D4      OUT     DX,AL   ; SET DLAB=1
05C1 B1 04      ;----- DETERMINE BAUD RATE DIVISOR
05C3 D2 C2     MOV     DL,AH   ; GET PARMS TO DL
05C5 81 E2 000E ROL     DL,CL
05C7 BF 0575 R  AND     DX,0EH  ; ISOLATE THEM
05C9 03 FA      MOV     DI,OFFSET A1 ; BASE OF TABLE
05CB 03 FA      ADD     DI,DX   ; PUT INTO INDEX REGISTER
05CD 8B 94 0000 R MOV     DX,RS232_BASE[SI] ; POINT TO HIGH ORDER OF DIVISOR
05D2 42        INC     DX
05D3 2E: 8A 45 01 MOV     AL,CS:[DI]+1 ; GET HIGH ORDER OF DIVISOR
05D7 EE        OUT     DX,AL  ; SET MS OF DIV TO 0
05D8 4A        DEC     DX
05D9 2E: 8A 05  MOV     AL,CS:[DI] ; GET LOW ORDER OF DIVISOR
05DC EE        OUT     DX,AL  ; SET LOW OF DIVISOR
05DD 83 C2 03  ADD     DX,3
05E0 8A C4      MOV     AL,AH   ; GET PARMS BACK
05E2 24 1F     AND     AL,01FH ; STRIP OFF THE BAUD BITS
05E4 EE        OUT     DX,AL  ; LINE CONTROL TO 8 BITS
05E5 4A        DEC     DX
05E6 4A        DEC     DX
05E7 B0 00     MOV     AL,0
05E9 EE        OUT     DX,AL  ; INTERRUPT ENABLES ALL OFF
05EA EB 53     JMP     SHORT A18 ; COM STATUS
05EC           ;----- SEND CHARACTER IN (AL) OVER COMMO LINE
05EC 50        PUSH    AX
05ED 83 C2 04  ADD     DX,4    ; SAVE CHAR TO SEND
05F0 80 03     MOV     AL,3    ; MODEM CONTROL REGISTER
05F2 EE        OUT     DX,AL  ; DTR AND RTS
05F3 42        INC     DX      ; DATA TERMINAL READY, REQUEST TO
05F4 42        INC     DX      ; SEND
05F5 B7 30     MOV     BH,30H ; MODEM STATUS REGISTER
05F7 E8 064E R  CALL    WAIT_FOR_STATUS ; DATA SET READY & CLEAR TO SEND
05FA 74 08     JE      A7      ; ARE BOTH TRUE?
05FC 59        POP     CX      ; YES, READY TO TRANSMIT CHAR
05FD 8A C1     MOV     AL,CL   ; RELOAD DATA BYTE
05FF 80 CC 80  OR     AH,80H  ; INDICATE TIME OUT
0602 EB AC     JMP     A3      ; RETURN
0604           ; CLEAR TO SEND
0606 4A        DEC     DX      ; LINE STATUS REGISTER
0608 B7 20     MOV     BH,20H ; IS TRANSMITTER READY
0609           ;
0607 E8 064E R  CALL    WAIT_FOR_STATUS ; TEST FOR TRANSMITTER READY
060A 75 F0     JNZ    A7      ; RETURN WITH TIME OUT SET
060C 83 EA 05  SUB     DX,5    ; DATA PORT
060F 59        POP     CX      ; RECOVER IN CX TEMPORARILY
0610 8A C1     MOV     AL,CL   ; MOVE CHAR TO AL FOR OUT, STATUS
0611           ; IN AH
0612 EE        OUT     DX,AL  ; OUTPUT CHARACTER
0613 EB 9B     JMP     A3      ; RETURN
0615 83 C2 04  ;----- RECEIVE CHARACTER FROM COMMO LINE
0618 B0 01     ADD     DX,4    ; MODEM CONTROL REGISTER
061A EE        MOV     AL,1    ; DATA TERMINAL READY
061B 42        OUT     DX,AL
061C 42        INC     DX      ; MODEM STATUS REGISTER
061D B7 20     INC     DX
061F E8 064E R  MOV     BH,20H ; DATA SET READY
0622 75 DB     CALL    WAIT_FOR_STATUS ; TEST FOR DSR
0624 4A        JNZ    A8      ; RETURN WITH ERROR
0625 EC        DEC     DX      ; LINE STATUS REGISTER
0626 A8 01     IN     AL,DX
0628 75 09     TEST   AL,DX
062A F6 06 0071 R 80 JNZ    A17     ; RECEIVE BUFFER FULL
062F 74 F4     TEST   BIODS_BREAK,80H ; TEST FOR REC. BUFF. FULL
0631 EB CC     JZ     A16     ; TEST FOR BREAK KEY
0633 24 1E     JMP     A8      ; LOOP IF NO BREAK KEY
0635 8A E0      AND    AL,00011110B ; SET TIME OUT ERROR
0637 8B 94 0000 R MOV     AH,AL   ; TEST FOR ERROR CONDITIONS ON REC'V
063B EC        MOV     DX,RS232_BASE[SI] ; CHAR
063C E9 05B0 R  IN     AL,DX   ; DATA PORT
063F 8B 94 0000 R JMP     A3      ; GET CHARACTER FROM LINE
0643 83 C2 05  ;----- COMMO PORT STATUS ROUTINE
0646 EC        MOV     DX,RS232_BASE[SI] ; RETURN
0647 8A E0      ADD     DX,5    ; CONTROL PORT
0649 42        IN     AL,DX   ; GET LINE CONTROL STATUS
064A EC        MOV     AH,AL  ; GET LINE CONTROL STATUS
064B 42        INC     DX     ; PUT IN AH FOR RETURN
064D 42        IN     AL,DX  ; POINT TO MODEM STATUS REGISTER
064E EC        JMP     A3      ; GET MODEM CONTROL STATUS
0648 E9 05B0 R  ; RETURN
;----- WAIT FOR STATUS ROUTINE

```

```

;ENTRY: BH=STATUS BIT(S) TO LOOK FOR,
;       DX=ADDR. OF STATUS REG
;EXIT:  ZERO FLAG ON = STATUS FOUND
;       ZERO FLAG OFF = TIMEOUT.
;       AH=LAST STATUS READ
;-----
064E      8A 9D 007C R
0652      2B C9
0654      EC
0655      8A ED
0657      22 C7
0659      3A C7

WAIT_FOR_STATUS PROC NEAR
MOV      BL,RS232_TIM_OUT[DI] ;LOAD OUTER LOOP COUNT
WFS0:    SUB      CX,CX
WFS1:    IN       AL,CX
MOV      AH,AL ;GET STATUS
AND      AL,BH ;MOVE TO AH
CMP      AL,BH ;ISOLATE BITS TO TEST
;         ;EXACTLY = TO MASK
RS232.INC

065B      74 08
065D      E2 F5
065F      FE CB
0661      75 EF
0663      0A FF
0665
0665      C3
0666
0666

JE       WFS_END ;RETURN WITH ZERO FLAG ON
LOOP    WFS1 ;TRY AGAIN
DEC     BL
JNZ    WFS0
OR     BH,BH ;SET ZERO FLAG OFF
WFS_END: RET
WAIT_FOR_STATUS ENDP
RS232_IO ENDP

;--- INT 15 ---
; CASSETTE I/O
; (AH) = 0 TURN CASSETTE MOTOR ON
; (AH) = 1 TURN CASSETTE MOTOR OFF
; (AH) = 2 READ 1 OR MORE 256 BYTE BLOCKS FROM CASSETTE
; (ES,BX) = POINTER TO DATA BUFFER
; (CX) = COUNT OF BYTES TO READ
; ON EXIT
; (ES,BX) = POINTER TO LAST BYTE READ + 1
; (DX) = COUNT OF BYTES ACTUALLY READ
; (CY) = 0 IF NO ERROR OCCURRED
;       = 1 IF ERROR OCCURRED
; (AH) = ERROR RETURN IF (CY)= 1
;       = 01 IF CRC ERROR WAS DETECTED
;       = 02 IF DATA TRANSITIONS ARE LOST
;       = 04 IF NO DATA WAS FOUND
; (AH) = 3 WRITE 1 OR MORE 256 BYTE BLOCKS TO CASSETTE
; (ES,BX) = POINTER TO DATA BUFFER
; (CX) = COUNT OF BYTES TO WRITE
; ON EXIT
; (EX,BX) = POINTER TO LAST BYTE WRITTEN + 1
; (CX) = 0
; (AH) = ANY OTHER THAN ABOVE VALUES CAUSES (CY)= 1
;       AND (AH)= 80 TO BE RETURNED (INVALID COMMAND).
;-----
0666      FB
0666      1E
0667      E8 0000 E
0668      80 26 0071 R 7F
0670      E8 0677 R
0673      1F
0674      CA 0002
0677
0677

ASSUME DS:DATA, ES:NOTHING, SS:NOTHING, CS:CODE
CASSETTE_IO PROC FAR
STI
PUSH DS ; INTERRUPTS BACK ON
CALL DDS ; ESTABLISH ADDRESSING TO DATA
AND BIOS_BREAK, 7FH ; MAKE SURE BREAK FLAG IS OFF
CALL W1 ; CASSETTE_IO_CONT
POP DS
RET 2 ; INTERRUPT RETURN
CASSETTE_IO ENDP
W1 PROC NEAR

; PURPOSE:
; TO CALL APPROPRIATE ROUTINE DEPENDING ON REG AH
; AH ROUTINE
;-----
; 0 MOTOR ON
; 1 MOTOR OFF
; 2 READ CASSETTE BLOCK
; 3 WRITE CASSETTE BLOCK
;-----
0677      0A E4
0679      74 13
067B      FE CC
067D      74 18
067F      FE CC
0681      74 1A

OR      AH,AH ; TURN ON MOTOR?
JZ     MOTOR_ON ; YES, DO IT
DEC     AH ; TURN OFF MOTOR?
JZ     MOTOR_OFF ; YES, DO IT
DEC     AH ; READ CASSETTE BLOCK?
JZ     READ_BLOCK ; YES, DO IT

0683      FE CC
0685      75 03
0687      E9 07A4 R
068A
068A      B4 80
068C      F9
068D      C3
068E
068E

DEC     AH ; WRITE CASSETTE BLOCK?
JNZ    W2 ; NOT_DEFINED
JMP    WRITE_BLOCK ; YES, DO IT
W2:
MOV     AH,080H ; COMMAND NOT_DEFINED
STC ; ERROR, UNDEFINED OPERATION
RET ; ERROR FLAG
W1 ENDP

MOTOR_ON PROC NEAR
; PURPOSE:
; TO TURN ON CASSETTE MOTOR
;-----
068E      E4 61
0690      24 F7
0692      E6 61
0694      2A E4
0696      C3
0697
0697

IN      AL,PORT_B ; READ CASSETTE OUTPUT
AND     AL,NOT_08H ; CLEAR BIT TO TURN ON MOTOR
OUT     PORT_B,AL ; WRITE IT OUT
SUB     AH,AH ; CLEAR AH
RET
MOTOR_ON ENDP
MOTOR_OFF PROC NEAR
; PURPOSE:
; TO TURN CASSETTE MOTOR OFF
;-----
0697      E4 61

IN      AL,PORT_B ; READ CASSETTE OUTPUT

```

Appendix A.

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0699 0C 08          OR      AL,08H          ; SET BIT TO TURN OFF
069B EB F5          JMP      M3              ; WRITE IT, CLEAR ERROR, RETURN
069D                ENDP
069D                PROC      NEAR
;-----
; PURPOSE:
; TO READ 1 OR MORE 256 BYTE BLOCKS FROM CASSETTE
;-----
; ON ENTRY:
; ES IS SEGMENT FOR MEMORY BUFFER (FOR COMPACT CODE)
; BX POINTS TO START OF MEMORY BUFFER
; CX CONTAINS NUMBER OF BYTES TO READ
; ON EXIT:
; BX POINTS 1 BYTE PAST LAST BYTE PUT IN MEM
; CX CONTAINS DECREMENTED BYTE COUNT
; DX CONTAINS NUMBER OF BYTES ACTUALLY READ
;-----
; CARRY FLAG IS CLEAR IF NO ERROR DETECTED
; CARRY FLAG IS SET IF CRC ERROR DETECTED
;-----
069D 53              PUSH     BX              ; SAVE BX
069E 51              PUSH     CX              ; SAVE CX
069F 56              PUSH     SI              ; SAVE SI
06A0 BE 0007        MOV      SI, 7          ; SET UP RETRY COUNT FOR LEADER
06A3 E8 085D R     CALL     BEGIN_OP       ; BEGIN BY STARTING MOTOR
06A6                ; SEARCH FOR LEADER
06A6 E4 62          IN       AL,PORT_C      ; GET INITIAL VALUE
06A8 24 10          AND     AL,010H         ; MASK OFF EXTRANEIOUS BITS
;-----
06AA A2 006B R     MOV      LAST_VAL,AL    ; SAVE IN LOC LAST_VAL
06AD BA 3F7A        MOV      DX,16250       ; # OF TRANSITIONS TO LOOK FOR
06B0                ; WAIT FOR EDGE
06B0 F6 06 0071 R 80  TEST     BIOS_BREAK, 80H ; CHECK FOR BREAK KEY
06B5 75 03          JNZ     W6A             ; JUMP IF NO BREAK KEY
;-----
06B7 4A              DEC      DX              ; JUMP IF BEGINNING OF LEADER
06B8 75 03          JNZ     W7              ; JUMP IF NO LEADER FOUND
06BA E9 073C R     JMP      W17            ; IGNORE FIRST EDGE
06BD E8 077C R     CALL     READ_HALF_BIT  ; JUMP IF NO EDGE DETECTED
06C0 E3 EE          JCXZ    W5              ; CHECK FOR HALF BITS
06C2 8A 0378        MOV      DX,0378H       ; MUST HAVE AT LEAST THIS MANY ONE
06C5 89 0200        MOV      CX,200H        ; SIZE PULSES BEFORE CHCKNG FOR
;-----
06C8 FA              CLI                     ; SYNC BIT (0)
06C9                ; DISABLE INTERRUPTS
06C9 F6 06 0071 R 80  TEST     BIOS_BREAK, 80H ; SEARCH-LDR
06CE 51              JNZ     W17            ; CHECK FOR BREAK KEY
06D0 51              PUSH     CX              ; JUMP IF BREAK KEY HIT
06D1 E8 077C R     CALL     READ_HALF_BIT  ; SAVE REG CX
06D4 0B C9          OR      CX, CX          ; GET PULSE WIDTH
06D6 59              POP      CX              ; CHECK FOR TRANSITION
06D7 74 CD          PDP     CX              ; RESTORE ONE BIT COUNTER
06D9 3B D3          JZ      W4              ; JUMP IF NO TRANSITION
06DB E3 04          CMP     DX,BX           ; CHECK PULSE WIDTH
06DD 73 C7          JNC     W4              ; IF CX=0 THEN WE CAN LOOK
;-----
06DF E2 E8          LOOP    W8              ; FOR SYNC BIT (0)
;-----
06E1 72 E6          JNC     W4              ; JUMP IF ZERO BIT (NOT GOOD
06E1                ; LEADER)
06E1                ; DEC CX AND READ ANOTHER HALF ONE
06E1                ; BIT
06E1                ; FIND-SYNC
06E3 E8 077C R     JC      W8              ; JUMP IF ONE BIT (STILL LEADER)
06E6 E8 074E R     ;----- A SYNCH BIT HAS BEEN FOUND. READ SYN CHARACTER:
06E9 3C 16          CALL     READ_HALF_BIT  ; READ SYN CHARACTER:
06EB 75 49          CALL     READ_BYTE     ; SKIP OTHER HALF OF SYNC BIT (0)
06ED 5E              CMP     AL,16H          ; READ SYNC BYTE
06EE 59              JNE     W16            ; SYNCHRONIZATION CHARACTER
06EF 5B              ;----- GOOD CRC SO READ DATA BLOCK(S)
;-----
;-----
; READ 1 OR MORE 256 BYTE BLOCKS FROM CASSETTE
; ON ENTRY:
; ES IS SEGMENT FOR MEMORY BUFFER (FOR COMPACT CODE)
; BX POINTS TO START OF MEMORY BUFFER
; CX CONTAINS NUMBER OF BYTES TO READ
; ON EXIT:
; BX POINTS 1 BYTE PAST LAST BYTE PUT IN MEM
; CX CONTAINS DECREMENTED BYTE COUNT
; DX CONTAINS NUMBER OF BYTES ACTUALLY READ
;-----
06F0 51              PUSH     CX              ; SAVE BYTE COUNT
06F1                ; COME HERE BEFORE EACH
06F1 C7 06 0069 R FFFF  MOV      CRC_REG,0FFFFH ; 256 BYTE BLOCK IS READ
06F7 BA 0100        MOV      DX,256         ; INIT CRC REG
06FA F6 06 0071 R 80  TEST     BIOS_BREAK, 80H ; SET DX TO DATA BLOCK SIZE
06FF 75 23          JNZ     W13            ; RD_BLK
0701 E8 074E R     CALL     READ_BYTE     ; CHECK FOR BREAK KEY
0704 72 1E          JZ      W13            ; JUMP IF BREAK KEY HIT
0706 E3 05          JC      W13            ; READ BYTE FROM CASSETTE
;-----
0708 26: 88 07        JCXZ    W12            ; CY SET INDICATES NO DATA
0708 43              ; TRANSITIONS
070C 49              ; IF WE'VE ALREADY REACHED
070D 4A              ; END OF MEMORY BUFFER
070E 7F EA          MOV      ES:[BX],AL    ; SKIP REST OF BLOCK
;-----
0708 43              INC     BX              ; STORE DATA BYTE AT BYTE PTR
070C 49              DEC     CX              ; INC BUFFER PTR
070D 4A              ; DEC BYTE COUNTER
070E 7F EA          ; LOOP UNTIL DATA
;-----
0708 43              DEC     DX              ; BLOCK HAS BEEN READ FROM CASSETTE
070C 49              JG      W11            ; DEC BLOCK CNT
070D 4A              ; RD_BLK

```



Appendix A.

```

;-----
; PURPOSE:
; TO COMPUTE TIME TILL NEXT DATA
; TRANSITION (EDGE)
; ON ENTRY:
; EDGE_CNT CONTAINS LAST EDGE COUNT
; ON EXIT:
; AX CONTAINS OLD LAST EDGE COUNT
; BX CONTAINS PULSE WIDTH (HALF BIT)
;-----
READ_HALF_BIT PROC NEAR
MOV CX, 100 ; SET TIME TO WAIT FOR BIT
MOV AH, LAST_VAL ; GET PRESENT INPUT VALUE
W22: ; RD-H-BIT
IN AL, PORT_C ; INPUT DATA BIT
AND AL, 010H ; MASK OFF EXTRANEIOUS BITS
CMP AL, AH ; SAME AS BEFORE?
LOOPE W22 ; LOOP TILL IT CHANGES
MOV LAST_VAL, AL ; UPDATE LAST VAL WITH NEW VALUE
MOV AL, 40H ; READ TIMER'S COUNTER COMMAND
OUT TIM_CTL, AL ; LATCH COUNTER
MOV BX, EDGE_CNT ; BX GETS LAST EDGE COUNT
IN AL, TIMER+1 ; GET LS BYTE
MOV AH, AL ; SAVE IN AH
IN AL, TIMER+1 ; GET MS BYTE
XCHG AL, AH ; XCHG AL, AH
SUB BX, AX ; SET BX EQUAL TO HALF BIT PERIOD
MOV EDGE_CNT, AX ; UPDATE EDGE COUNT;
RET
READ_HALF_BIT ENDP
;-----
; PURPOSE
; WRITE 1 OR MORE 256 BYTE BLOCKS TO CASSETTE.
; THE DATA IS PADDED TO FILL OUT THE LAST 256 BYTE BLOCK.
; ON ENTRY:
; BX POINTS TO MEMORY BUFFER ADDRESS
; CX CONTAINS NUMBER OF BYTES TO WRITE
; ON EXIT:
; BX POINTS 1 BYTE PAST LAST BYTE WRITTEN TO CASSETTE
; CX IS ZERO
;-----
WRITE_BLOCK PROC NEAR
PUSH BX
PUSH CX
IN AL, PORT_B ; DISABLE SPEAKER
AND AL, NOT 02H
OR AL, 01H ; ENABLE TIMER
OUT PORT_B, AL
MOV AL, 0B6H ; SET UP TIMER - MODE 3 SQUARE WAVE
OUT TIM_CTL, AL
CALL BEGIN_OP ; START MOTOR AND DELAY
MOV AX, 1184 ; SET NORMAL BIT SIZE
CALL W31 ; SET-TIMER
MOV CX, 0800H ; SET CX FOR LEADER BYTE COUNT
W23: ; WRITE LEADER
STC ; WRITE ONE BITS
CALL WRITE_BIT
LOOP W23 ; LOOP 'TIL LEADER IS WRITTEN
CLI ; DISABLE INTS.
CLC ; WRITE SYNC BIT (0)
CALL WRITE_BIT
POP CX ; RESTORE REGS CX, BX
POP BX
MOV AL, 16H ; WRITE SYNC CHARACTER
CALL WRITE_BYTE
;-----
; PURPOSE
; WRITE 1 OR MORE 256 BYTE BLOCKS TO CASSETTE
; ON ENTRY:
; BX POINTS TO MEMORY BUFFER ADDRESS
; CONTAINS NUMBER OF BYTES TO WRITE
; ON EXIT:
; BX POINTS 1 BYTE PAST LAST BYTE WRITTEN TO CASSETTE
; CX IS ZERO
;-----
WR_BLOCK:
MOV CRC_REG, 0FFFFH ; INIT CRC
MOV DX, 256 ; FOR 256 BYTES
W24: ; WR-BLK
MOV AL, ES:[BX] ; READ BYTE FROM MEM
CALL WRITE_BYTE ; WRITE IT TO CASSETTE
JCXZ W25 ; UNLESS CX=0, ADVANCE PTRS & DEC
; COUNT
INC BX ; INC BUFFER POINTER
DEC CX ; DEC BYTE COUNTER
W25: ; SKIP-ADV
DEC DX ; DEC BLOCK CNT
JG W24 ; LOOP TILL 256 BYTE BLOCK
; IS WRITTEN TO TAPE
; WRITE CRC
;
; WRITE 1'S COMPLEMENT OF CRC REG TO CASSETTE
; WHICH IS CHECKED FOR CORRECTNESS WHEN THE BLOCK IS READ
; REG AX IS MODIFIED
MOV AX, CRC_REG ; WRITE THE ONE'S COMPLEMENT OF THE
NOT AX ; TWO BYTE CRC TO TAPE
PUSH AX ; FOR 1'S COMPLEMENT
XCHG AH, AL ; SAVE IT
CALL WRITE_BYTE ; WRITE MS BYTE FIRST
; WRITE IT

```

```

077C B9 0064
077C B9 0064
077F 8A 26 006B R
0783
0783 E4 62
0785 24 10
0785 3A C4
0787 E1 F8
0789 A2 006B R
078B D0 40
078E E6 43
0790 8B 1E 0067 R
0792 E4 41
0796 E4 E0
0798 E4 41
079A 86 C4
079C 2B D8
079E A3 0067 R
07A0 C3
07A3
07A4

```

```

07A4
07A4 53
07A5 51
07A6 E4 61
07A8 24 FD
07AA 0C 01
07AC E6 61
07AE 80 86
07B0 E6 43
07B2 E8 085D R
07B5 B8 04A0
07B8 E8 0842 R
07BB B9 0800
07BE
07BE F9
07BF E8 082C R
07C2 E2 FA
07C4 FA
07C5 F8
07C6 E8 082C R
07C9 59
07CA 5B
07CB B0 16
07CD E8 0815 R

```

```

07D0
07D0 C7 86 0069 R FFFF
07D6 BA 0100
07D9
07D9 26: 8A 07
07DC E8 0815 R
07DF E3 02
07E1 43
07E2 49
07E3
07E3 4A
07E4 7F F3

```

```

07E6 A1 0069 R
07E9 F7 D0
07EB 50
07EC 86 E0
07EE E8 0815 R

```

```

07F1 58          POP      AX          ; GET IT BACK
07F2 E8 0815 R  CALL    WRITE_BYTE      ; NOW WRITE LS BYTE
07F5 0B C9      OR      CX,CX          ; IS BYTE COUNT EXHAUSTED?
07F7 75 D7      JNZ    WR_BLOCK        ; JUMP IF NOT DONE YET
07F9 51          PUSH   CX              ; SAVE REG CX
07FA FB          STI     STI             ; RE-ENABLE INTERRUPTS
07FB B9 0020     MOV     CX, 32         ; WRITE OUT TRAILER BITS
07FE           RET             ; TRAIL-LOOP
W26:           STC             ;
07FF E8 082C R  CALL    WRITE_BIT       ;
0802 E2 FA      LOOP   W26             ; WRITE UNTIL TRAILER WRITTEN
0804 59          POP     CX              ; RESTORE REG CX
0805 B0 B0      MOV     AL, 0B0H       ; TURN TIMER2 OFF
0807 E6 43      OUT    TIM_CTL, AL
0809 B8 0001     MOV     AX, 1
080C E8 0842 R  CALL    W31             ; SET TIMER
080F E8 0697 R  CALL    MOTOR_OFF      ; TURN MOTOR OFF
0812 2B C0      SUB    AX,AX          ; NO ERRORS REPORTED ON WRITE
0814 C3          RET             ; FINISHED
0815           WRITE_BLOCK  ENDP
;-----
; WRITE A BYTE TO CASSETTE.
; BYTE TO WRITE IS IN REG AL.
;-----
0815           WRITE_BYTE  PROC    NEAR
0815 51          PUSH   CX              ; SAVE REGS CX,AX
0816 50          PUSH   AX
0817 8A E8      MOV     CH,AL          ; AL=BYTE TO WRITE.
; (MS BIT WRITTEN FIRST)
; FOR 8 DATA BITS IN BYTE.
; NOTE: TWO EDGES PER BIT
; DISASSEMBLE THE DATA BIT
; ROTATE MS BIT INTO CARRY
; SAVE FLAGS.
; NOTE: DATA BIT IS IN CARRY
; WRITE DATA BIT
; RESTORE CARRY FOR CRC CALC
; COMPUTE CRC ON DATA BIT
; LOOP TILL ALL 8 BITS DONE
; JUMP IF NOT DONE YET
; RESTORE REGS AX,CX
; WE ARE FINISHED
0819 B1 08      MOV     CL,8
W27:           RCL     CH,1
081B           PUSHF
081B D0 D5      CALL   WRITE_BIT
081D 9C          POPF
081E E8 082C R  CALL   CRC_GEN
0821 9D          POPF
0822 E8 0849 R  CALL   CRC_GEN
0825 FE C9      DEC    CL
0827 75 F2      JNZ    W27
0829 58          POP    AX
082A 59          POP    CX
082B C3          RET
082C           WRITE_BYTE  ENDP
;-----
; WRITE BIT          PROC    NEAR
; PURPOSE:
; TO WRITE A DATA BIT TO CASSETTE
; CARRY FLAG CONTAINS DATA BIT
; I.E. IF SET DATA BIT IS A ONE
; IF CLEAR DATA BIT IS A ZERO
; NOTE: TWO EDGES ARE WRITTEN PER BIT
; ONE BIT HAS 500 USEC BETWEEN EDGES
; FOR A 1000 USEC PERIOD (1 MILLISEC)
; ZERO BIT HAS 250 USEC BETWEEN EDGES
; FOR A 500 USEC PERIOD (.5 MILLISEC)
; CARRY FLAG IS DATA BIT
;-----
082C B8 04A0     MOV     AX,1184        ;ASSUME IT'S A '1'
082F 72 03      JC     W28             ; SET AX TO NOMINAL ONE SIZE
0831 B8 0250     MOV     AX,592         ; JUMP IF ONE BIT
; NO, SET TO NOMINAL ZERO SIZE
; WRITE-BIT-AX
; WRITE BIT WITH PERIOD EQ TO VALUE
; AX
W28:           PUSH   AX
W29:           IN     AL,PORT_C    ;INPUT TIMER_0 OUTPUT
AND    AL,020H
JZ     W29
W30:           IN     AL,PORT_C    ;LOOP TILL HIGH
;NOW WAIT TILL TIMER'S OUTPUT IS
; LOW
AND    AL,020H
JNZ    W30
;RELOAD TIMER WITH PERIOD
;FOR NEXT DATA BIT
;RESTORE PERIOD COUNT
; SET TIMER
; SET LOW BYTE OF TIMER 2
; SET HIGH BYTE OF TIMER 2
0841 58          POP    AX
W31:           OUT    042H, AL
MOV    AL, AH
OUT    042H, AL
RET
0842           WRITE_BIT  ENDP
;-----
; CRC_GEN          PROC    NEAR
; UPDATE CRC REGISTER WITH NEXT DATA BIT
; CRC IS USED TO DETECT READ ERRORS
; ASSUMES DATA BIT IS IN CARRY
; REG AX IS MODIFIED
; FLAGS ARE MODIFIED
;-----
0849 A1 0069 R  MOV     AX,CRC_REG
;THE FOLLOWING INSTRUCTIONS
;WILL SET THE OVERFLOW FLAG
;IF CARRY AND MS BIT OF CRC
;ARE UNEQUAL
084C D1 D8      RCR    AX,1
084E D1 D0      RCL    AX,1
0850 F8          CLC
0851 71 04      JNO   W32             ;SKIP IF NO OVERFLOW

```

Appendix A.

```

0853 35 0810
0856 F9
0857 D1 D0
0859 A3 0869 R
085C C3
085D
-----
085D EB 088E R
085D B3 42
0862 B9 0700
0865 E2 FE
0867 FE CB
0869 75 F7
086B C3
086C
08D0
08D0

                                ; IF DATA BIT XORED WITH
                                ; CRC REG BIT 15 IS ONE
                                ; THEN XOR CRC REG WITH
                                ; 0810H
                                ; SET CARRY
                                ; ROTATE CARRY (DATA BIT)
                                ; INTO CRC REG
                                ; UPDATE CRC_REG
                                ; FINISHED
                                XOR      AX,0810H
W32:   STC
       RCL      AX,1
       MOV      CRC_REG,AX
       RET
CRC_GEN
-----
BEGIN_OP  PROC   NEAR
          CALL  MOTOR_ON
          MOV   BL,42H
          ; START TAPE AND DELAY
          ; TURN ON MOTOR
          ; DELAY FOR TAPE DRIVE
          ; TO GET UP TO SPEED (1/2 SEC)
          ; INNER LOOP= APPROX. 10 MILLISEC
W33:   MOV   CX,700H
W34:   LOOP  W34
          DEC  BL
          JNZ W33
          RET
BEGIN_OP  ENDP
          ORG  BEGIN+08D0H
CODE      ENDS
          END

```



```

XXXXXXXXXXXX
XXXXXXXXXXXX
X             X
X  MODULE 4  X
X             X
XXXXXXXXXXXX
XXXXXXXXXXXX

```

```

0000
0000 FA
0001 56
0002 57
0003 50
0004 53
0005 51
0006 52
0007 1E
0008 06

0009 BE 0008
000C 32 DB

000E 32 E4
0010 B9 0005
0013 E4 62
0015 A8 40
0017 74 02
0019 FE C4
001B E2 F6
001D 80 FC 03
0020 73 03
0022 E9 00B0 R

0025 B9 0032
0028 E4 62
002A A8 40
002C 74 05
002E E2 F8
0030 EB 7E 90

0033 B0 40
0035 E4 43
0037 90
0038 90
0039 E4 41
003B 8A E0
003D E4 41
003F 86 E0
0041 8B F8

0043 B9 0004
0046 E4 62
0048 A8 40
004A 75 64
004C E2 F8

004E BA 0220

0051 E8 00D3 R
0054 BA 020E
0057 50
0058 E8 00D3 R
005B 8A C8
005D 58
005E 3A C8
0060 74 59

0062 D0 EF
0064 0A F8
0066 4E
0067 75 EB

0069 E8 00D3 R
006C 50
006D E8 00D3 R
0070 8A C8
0072 58
0073 3A C8
0075 74 44

0077 80 E3 01
007A 74 3F

007C 8A C7
007E E8 0000 E
0081 F6 06 0338 R 04
0086 74 15
0088 A8 80
008A 74 05
008C FB
008D CD 48
008F EB 1F
0091
0091 DB 0040

```

```

-----
;
;
; KBDNMI - KEYBOARD NMI INTERRUPT ROUTINE
;
; THIS ROUTINE OBTAINS CONTROL UPON AN NMI INTERRUPT, WHICH
; OCCURS UPON A KEYSTROKE FROM THE KEYBOARD.
;
; THIS ROUTINE WILL DE-SERIALIZE THE BIT STREAM IN ORDER TO
; GET THE KEYBOARD SCAN CODE ENTERED. IT THEN ISSUES INT 41
; PASSING THE SCAN CODE IN AL TO THE KEY PROCESSOR. UPON RETURN
; IT RE-ENABLES NMI AND RETURNS TO SYSTEM (IRET).
;
-----
ASSUME CS:CODE,DS:DATA
KBDNMI PROC FAR
;-----DISABLE INTERRUPTS
CLI
;-----SAVE REGS & DISABLE NMI
PUSH SI
PUSH DI
PUSH AX
PUSH BX
PUSH CX
PUSH DX
PUSH DS
PUSH ES
;-----INIT COUNTERS
MOV SI,8 ; SET UP # OF DATA BITS
XOR BL,BL ; INIT. PARITY COUNTER
;-----SAMPLE 5 TIMES TO VALIDATE START BIT
XOR AH,AH
MOV CX,5 ; SET COUNTER
I1: IN AL,PORT_C ; GET SAMPLE
TEST AL,40H ; TEST IF 1
JZ I2 ; JMP IF 0
INC AH ; KEEP COUNT OF 1'S
I2: LOOP I1 ; KEEP SAMPLING
CMP AH,3 ; VALID START BIT ?
JNB I25 ; JUMP IF OK
JMP I8 ; INVALID (SYNC ERROR) NO AUDIO
;-----VALID START BIT, LOOK FOR TRAILING EDGE
I25: MOV CX,50 ; SET UP WATCHDOG TIMEOUT
I3: IN AL,PORT_C ; GET SAMPLE
TEST AL,40H ; TEST IF 0
JZ I5 ; JMP IF TRAILING EDGE FOUND
LOOP I3 ; KEEP LOOKING FOR TRAILING EDGE
JMP I8 ; SYNC ERROR (STUCK ON 1'S)
;-----READ CLOCK TO SET START OF BIT TIME
I5: MOV AL,40H ; READ CLOCK
OUT TIM_CTL,AL ;
NOP ;
NOP ;
IN AL,TIMER+1 ;
MOV AH,AL ;
IN AL,TIMER+1 ;
XCHG AH,AL ;
MOV DI,AX ; SAVE CLOCK TIME IN DI
;-----VERIFY VALID TRANSITION
MOV CX,4 ; SET COUNTER
I6: IN AL,PORT_C ; GET SAMPLE
TEST AL,40H ; TEST IF 0
JNZ I8 ; JMP IF INVALID TRANSITION (SYNC)
LOOP I6 ; KEEP LOOKING FOR VALID TRANSITION
;-----SET UP DISTANCE TO MIDDLE OF 1ST DATA BIT
MOV DX,544 ; 310 USEC AWAY (.838 US / CT)
;-----START LOOKING FOR TIME TO READ DATA BITS AND ASSEMBLY BYTE
I7: CALL I30
MOV DX,526 ; SET NEW DISTANCE TO NEXT HALF BIT
PUSH AX ; SAVE 1ST HALF BIT
CALL I30
MOV CL,AL ; PUT 2ND HALF BIT IN CL
POP AX ; RESTORE 1ST HALF BIT
CMP CL,AL ; ARE THEY OPPOSITES ?
JE I9 ; NO, PHASE ERROR
;-----VALID DATA BIT, PLACE IN SCAN BYTE
SHR BH,1 ; SHIFT PREVIOUS BITS
OR BH,AL ; OR IN NEW DATA BIT
DEC SI ; DECREMENT DATA BIT COUNTER
JNZ I7 ; CONTINUE FOR MORE DATA BITS
;-----WAIT FOR TIME TO SAMPLE PARITY BIT
CALL I30
PUSH AX ; SAVE 1ST HALF BIT
CALL I30
MOV CL,AL ; PUT 2ND HALF BIT IN CL
POP AX ; RESTORE 1ST HALF BIT
CMP CL,AL ; ARE THEY OPPOSITES ?
JE I9 ; NO, PHASE ERROR
;-----VALID PARITY BIT, CHECK PARITY
AND BL,1 ; CHECK IF ODD PARITY
JZ I9 ; JMP IF PARITY ERROR
;-----VALID CHARACTER, SEND TO CHARACTER PROCESSING
MOV AL,BH ; PLACE SCAN CODE IN AL
CALL D05
TEST JKB_FLAG_2,NMI_FLG
JZ I7_2
TEST AL,80H
JZ I7_1
STI ; ENABLE INTERRUPTS
INT 48H
JMP SHORT I8
I7_1: MOV BX,40H ; DURATION OF ERROR BEEP

```

Appendix A.

```

0094 89 0048      MOV     CX,48H           ; FREQUENCY OF TONE
0097 E8 0000 E    CALL    KB_NOISE        ; BUFFER FULL BEEP
009A FB          STI     SHORT I8       ; ENABLE INTERRUPTS
009B EB 13
009D
009D 80 0E 0338 R 04 I7_2: OR     JKB_FLAG_2,NMI_FLG
00A2 8A D8      MOV     BL,AL           ; STORE AL
00A4 E4 A0      IN     AL,0A0H        ; ENABLE NMI
00A6 8A C3      MOV     AL,BL         ; RESTORE AL
00A8 FB          STI     SHORT I8       ; ENABLE INTERRUPTS
00A9 CD 48      INT     48H          ; CHARACTER PROCESSING
00AB 80 26 0338 R FB AND     JKB_FLAG_2,NOT NMI_FLG
;-----RESTORE REGS AND RE-ENABLE NMI
I8: POP     ES           ; RESTORE REGS
POP     DS
POP     DX
POP     CX
POP     BX
IN     AL,0A0H      ; ENABLE NMI
POP     AX
POP     DI
POP     SI
IRET                    ; RETURN TO SYSTEM
;-----PARITY, SYNCH OR PHASE ERROR. OUTPUT MISSED KEY BEEP
I9: CALL    DDS         ; SETUP ADDRESSING
CMP     SI,8         ; ARE WE ON THE FIRST DATA BIT?
JE     I8            ; NO AUDIO FEEDBACK (MIGHT BE A
; ..GLITCH)
TEST    KB_FLAG_1,01H ; CHECK IF TRANSMISSION ERRORS
; ..ARE TO BE REPORTED
JNZ    I10          ; 1=DO NOT BEEP, 0=BEEP
CALL   ERROR_BEEP   ; CALL ERROR BEEP ROUTINE
JMP     SHORT I8    ; KEEP TRACK OF KEYBOARD ERRORS
; RETURN FROM INTERRUPT
KBDNMI ENDP
I30 PROC NEAR
I31: MOV     AL,40H      ; READ CLOCK
OUT     TIM_CTL,AL   ; M
NOP     ; M
NOP     ; M
IN     AL,TIMER+1   ; M
MOV     AH,AL        ; M
IN     AL,TIMER+1   ; M
XCHG   AH,AL        ; M
MOV     CX,DI        ; GET LAST CLOCK TIME
SUB     CX,AX        ; SUB CURRENT TIME
CMP     CX,DX        ; IS IT TIME TO SAMPLE ?
JNC    I31          ; NO, KEEP LOOKING AT TIME
SUB     CX,DX        ; UPDATE # OF COUNTS OFF
MOV     DI,AX        ; SAVE CURRENT TIME AS LAST TIME
ADD     DI,CX        ; ADD DIFFERENCE FOR NEXT TIME
;-----START SAMPLING DATA BIT (5 SAMPLES)
MOV     CX,5         ; SET COUNTER
;
;
; SAMPLE LINE
;
; PORT_C IS SAMPLED CX TIMES AND IF THERE ARE 3 OR MORE 1'S
; THEN 80H IS RETURNED IN AL, ELSE 00H IS RETURNED IN AL.
; PARITY COUNTER IS MAINTAINED IN ES.
;
;-----
I32: XOR     AH,AH       ; CLEAR COUNTER
IN     AL,PORT_C    ; GET SAMPLE
TEST   AL,40H      ; TEST IF 1
JZ     I33          ; JMP IF 0
INC     AH          ; KEEP COUNT OF 1'S
I33: LOOP  I32        ; KEEP SAMPLING
CMP     AH,3        ; VALID 1 ?
JB     I34          ; JMP IF NOT VALID 1
MOV     AL,080H     ; RETURN 80H IN AL (1)
INC     BL          ; INCREMENT PARITY COUNTER
RET                    ; RETURN TO CALLER
I34: XOR     AL,AL    ; RETURN 0 IN AL (0)
RET                    ; RETURN TO CALLER
I30 ENDP
;-----
;KEY62_INT
;
; THE PURPOSE OF THIS ROUTINE IS TO TRANSLATE SCAN CODES AND
; SCAN CODE COMBINATIONS FROM THE 62 KEY KEYBOARD TO THEIR
; EQUIVALENTS ON THE 83 KEY KEYBOARD. THE SCAN CODE IS
; PASSED IN AL. EACH SCAN CODE PASSED EITHER TRIGGERS ONE OR
; MORE CALLS TO INTERRUPT 9 OR SETS FLAGS TO RETAIN KEYBOARD
; STATUS. WHEN INTERRUPT 9 IS CALLED THE TRANSLATED SCAN
; CODES ARE PASSED TO IT IN AL. THE INTENT OF THIS CODE WAS
; TO KEEP INTERRUPT 9 INTACT FROM ITS ORIGIN IN THE PC FAMILY
; THIS ROUTINE IS IN THE FRONT END OF INTERRUPT 9 AND
; TRANSFORMS A 62 KEY KEYBOARD TO LOOK AS IF IT WERE AN 83
; KEY VERSION.
;
; IT IS ASSUMED THAT THIS ROUTINE IS CALLED FROM THE NMI
; DESERIALIZATION ROUTINE AND THAT ALL REGISTERS WERE SAVED
; IN THE CALLING ROUTINE. AS A CONSEQUENCE ALL REGISTERS ARE
; DESTROYED.
;-----
;EQUATES
BREAK_BIT EQU 80H
FN_KEY EQU 54H
PHK EQU FN_KEY+1
EXT_SCAN EQU PHK+1 ; BASE CODE FOR SCAN CODES
EXT_SCAN_END EQU ; EXTENDING BEYOND 85
AND_MASK EQU 06AH ; END OF EXTENDED SCAN CODE (=106)
CLEAR_FLAGS EQU 0FFH ; USED TO SELECTIVELY REMOVE BITS
;SCAN_CODES EQU AND_MASK - (FN_FLAG+FN_BREAK+FN_PENDING)
00B0 07
00B1 1F
00B2 5A
00B3 59
00B4 5B
00B5 E4 A0
00B7 58
00B8 5F
00B9 5E
00BA CF
00BB E8 0000 E
00BE 83 FE 08
00C1 74 ED
00C3 F6 06 0018 R 01
00C8 75 83
00CA EB 0AF8 R
00CD FE 86 0012 R
00D1 EB DD
00D3
00D3 80 40
00D5 E4 43
00D7 90
00D8 90
00D9 E4 41
00DB 8A E0
00DD E4 41
00DF 86 E0
00E1 8B CF
00E3 2B C8
00E5 3B CA
00E7 72 EA
00E9 2B CA
00EB 8B F8
00ED 03 F9
00EF B9 0005
00F2 32 E4
00F4 E4 62
00F6 A8 40
00F8 74 02
00FA FE C4
00FC E2 F6
00FE 80 FC 05
0101 72 05
0103 80 80
0105 FE C3
0107 C3
0108 32 C0
010A C3
010B

```

```

= 0010
= 0031
= 001C
= 0023
= 0014
= 0048
= 0050
= 004B
= 004D
= 000C
= 000D
= 000B

= 0018
= 0045
= 0047
= 004F
= 0049
= 0051
= 004A
= 004E
= 0093
= 0099
= 009A

010B
010B 10 31
010D 48 50 4B 4D
0111 1C 23 14
= 0009

0114
0114 10 45
0116 47 4F 49 51
011A 93 99 9A

011D
011D 72 73 74 75 76
0122 77 78 79 7A 70

0127
0127 14
0128 0048 0049 004D 0051
      0050 004F 004B 0047
      0039 001C
013C 0011 0012 001F 002D
      002C 002B 001E 0010
      000F 0001

0150
0150 FB
0151 FC
0152 E8 0000 E
0155 8A E0
0157 E8 0331 R

015A 73 01
015C CF

015D 3C FF
015F 74 48
0161 24 7F

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```

Q_KEY EQU 16
M_KEY EQU 49
ENTER_KEY EQU 28
H_KEY EQU 35
T_KEY EQU 20
UP_ARROW EQU 72
DOWN_ARROW EQU 80
LEFT_ARROW EQU 75
RIGHT_ARROW EQU 77
MINUS EQU 12
EQUALS EQU 13
NUM_0 EQU 11
;-----
;NEW TRANSLATED SCAN CODES
;-----
PAUSE EQU 16 ; HOLD FUNCTION IS SPECIAL CASE.
NUM_LOCK EQU 69
HOME EQU 71
END_KEY EQU 79
PAGE_UP EQU 73
PAGE_DOWN EQU 81
KEYPAD_MINUS EQU 74
KEYPAD_PLUS EQU 78
JITTKOU_KEY EQU 147
WARIKOMI_KEY EQU 153
SHURIYOU_KEY EQU 154
ASSUME CS:CODE,DS:DATA
;----TABLE OF VALID SCAN CODES
KBO LABEL BYTE
DB Q_KEY, M_KEY
DB UP_ARROW, DOWN_ARROW, LEFT_ARROW, RIGHT_ARROW
DB ENTER_KEY, H_KEY, T_KEY
KBOLEN EQU $ - KBO
;----TABLE OF NEW SCAN CODES
KB1 LABEL BYTE
DB PAUSE, NUM_LOCK
DB HOME, END_KEY, PAGE_UP, PAGE_DOWN
DB JITTKOU_KEY, WARIKOMI_KEY, SHURIYOU_KEY
;-----
;NOTE: THERE IS A ONE TO ONE CORRESPONDENCE BETWEEN
; THE SIZE OF KBO AND KB1.
;-----
;TABLE OF NUMERIC KEYPAD SCAN CODES
; THESE SCAN CODES WERE NUMERIC KEYPAD CODES ON
; THE 102 KEY KEYBOARD.
;-----
NUM_CODES LABEL BYTE
DB 72H,73H,74H,75H,76H
DB 77H,78H,79H,7AH,70H ; 10 NUMBERS ON KEYPAD
;-----
;EXTAB
; TABLE OF SCAN CODES FOR MAPPING EXTENDED SET
; OF SCAN CODES (SCAN CODES > 85). THIS TABLE
; ALLOWS OTHER DEVICES TO USE THE KEYBOARD INTERFACE.
; IF THE DEVICE GENERATES A SCAN CODE > 85 THIS TABLE
; CAN BE USED TO MAP THE DEVICE TO THE KEYBOARD. THE
; DEVICE ALSO HAS THE OPTION OF HAVING A UNIQUE SCAN
; CODE PUT IN THE KEYBOARD BUFFER (INSTEAD OF MAPPING
; TO THE KEYBOARD). THE EXTENDED SCAN CODE PUT IN THE
; BUFFER WILL BE CONTINUOUS BEGINNING AT 150. A ZERO
; WILL BE USED IN PLACE OF AN ASCII CODE. (E.G. A
; DEVICE GENERATING SCAN CODE 86 AND NOT MAPPING 86
; TO THE KEYBOARD WILL HAVE A [150,0] PUT IN THE
; KEYBOARD BUFFER)
; TABLE FORMAT:
; THE FIRST BYTE IS A LENGTH INDICATING THE NUMBER
; OF SCAN CODES MAPPED TO THE KEYBOARD. THE REMAINING
; ENTRIES ARE WORDS. THE FIRST BYTE (LOW BYTE) IS A
; SCAN CODE AND THE SECOND BYTE (HIGH BYTE) IS ZERO.
; A DEVICE GENERATING M SCAN CODES IS ASSUMED TO GENERATE THE
; FOLLOWING STREAM 86,87,88, ...,86*(N-1). THE SCAN CODE BYTES
; IN THE TABLE CORRESPOND TO THIS SET WITH THE FIRST DATA
; BYTE MATCHING 86. THE SECOND MATCHING 87 ETC.
; NOTES:
; (1) IF A DEVICE GENERATES A BREAK CODE, NOTHING IS
; PUT IN THE BUFFER.
; (2) A LENGTH OF 0 INDICATES THAT ZERO SCAN CODES HAVE BEEN
; MAPPED TO THE KEYBOARD AND ALL EXTENDED SCAN CODES WILL
; BE USED.
; (3) A DEVICE CAN MAP SOME OF ITS SCAN CODES TO THE KEYBOARD
; AND HAVE SOME ITS SCAN CODES IN THE EXTENDED SET.
;-----
EXTAB LABEL BYTE
DB 20 ; LENGTH OF TABLE
DW 72,73,77,81,80,79,75,71,57,28
,DW 17,18,31,45,44,43,30,16,15,1
;-----
KEY62_INT PROC FAR
STI
CLD ; FORWARD DIRECTION
CALL DDS ; SET UP ADDRESSING
MOV AH,AL ; SAVE SCAN CODE
CALL TPM ; ADJUST OUTPUT FOR USER
; MODIFICATION
JNC KBX0 ; JUMP IF OK TO CONTINUE
IRET ; RETURN FROM INTERRUPT.
;-----EXTENDED SCAN CODE CHECK
KBX0: CMP AL,0FFH ; IS THIS AN OVERRUN CHAR?
JE KBX_1 ; PASS IT TO INTERRUPT 9
AND AL,AND_MASK_BREAK_BIT ; TURN OFF BREAK BIT

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Appendix A.

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0163 3C 56          CMP     AL,EXT_SCAN    ; IS THIS A SCAN CODE > 85
0165 7C 38          JL      KBX4           ; REPLACE BREAK BIT
0167 3C 6A          CMP     AL,EXT_SCAN_END ; IS THIS A SCAN CODE < 106
0169 7D 37          JGE     KBX4           ; REPLACE BREAK BIT
;----SCAN CODE IS IN EXTENDED SET
0168 1E             PUSH    DS
016C 33 F6          XOR     SI,SI
016E 8E DE          MOV     DS,SI
;-----
0170 C4 3E 0124 R   LES     DI,DWORD PTR EXST ; GET THE POINTER TO THE EXTENDED
;-----
0174 26: 8A 0D      MOV     CL,BYTE PTR ES:[DI] ; GET LENGTH BYTE
0177 1F             POP     DS
;-----
;----DOES SCAN CODE GET MAPPED TO KEYBOARD OR TO NEW EXTENDED SCAN
; CODES?
0178 2C 56          SUB     AL,EXT_SCAN    ; CONVERT TO BASE OF NEW SET
017A FE C9          DEC     CL             ; LENGTH - 1
017C 3A C1          CMP     AL,CL         ; IS CODE IN TABLE?
017E 7F 14          JG     KBX1           ; JUMP IF SCAN CODE IS NOT IN TABLE
;----GET SCAN CODE FROM TABLE
0180 47             INC     DI             ; POINT DI PAST LENGTH BYTE
0181 8B D8          MOV     BX,AX
0183 32 FF          XOR     BH,BH         ; PREPARE FOR ADDING TO 16 BIT
;-----
;-----
0185 D1 E3          SHL     BX,1         ; REGISTER
0187 03 F8          ADD     DI,BX        ; OFFSET TO CORRECT TABLE ENTRY
0189 26: 8A 05      MOV     AL,BYTE PTR ES:[DI] ; TRANSLATED SCAN CODE IN AL
018C 3C 56          CMP     AL,EXT_SCAN    ; IS THIS A SCAN CODE > 85
018E 7C 12          JL      KBX4           ; REPLACE BREAK BIT
0190 3C 6A          CMP     AL,EXT_SCAN_END ; IS THIS A SCAN CODE < 106
0192 7F 0E          JG     KBX4           ; REPLACE BREAK BIT
;----SCAN CODE GETS MAPPED TO EXTENDED SCAN CODES
0194 F6 C4 80      KBO1:  TEST  AH,BREAK_BIT    ; IS THIS A BREAK CODE?
0197 74 01          JZ     KBX2           ; MAKE CODE, PUT IN BUFFER
0199 CF             IRET                    ; BREAK CODE, RETURN FROM INTERRUPT
019A 80 C4 40      KBO2:  ADD     AH,64         ; EXTENDED SET CODES BEGIN AT 150
019D 32 C0          XOR     AL,AL         ; ZERO OUT ASCII VALUE (NUL)
019F CD 78          INT     78H          ; KAMAKAN ROUTINE
01A1 CF             IRET                    ;
01A2 80 E4 80      KBO4:  AND     AH,BREAK_BIT  ; MASK BREAK BIT ON ORIGINAL SCAN
01A5 8A C4          OR      AL,AH         ; UPDATE NEW SCAN CODE
01A7 8A E0          MOV     AH,AL        ; SAVE AL IN AH AGAIN
;----83 KEY KEYBOARD FUNCTIONS SHIFT+PRISC AND CTRL+NUMLOCK
01A9 3C 45          KBO_1:  CMP     AL,NUM_KEY    ; IS THIS A NUMLOCK?
01AB 75 14          JNE     KBO_3         ; CHECK FOR PRISC
01AD F6 06 0017 R 04  TEST  KB_FLAG,CTL_SHIFT ; IS CTRL KEY BEING HELD DOWN?
01B2 74 0A          JZ     KBO_2         ; NUMLOCK WITHOUT CTRL, CONTINUE
01B4 F6 06 0017 R 08  TEST  KB_FLAG,ALT_SHIFT ; IS ALT KEY HELD CONCURRENTLY?
01B9 75 03          JNZ    KBO_2         ; PASS IT ON
01BB E9 02F0 R     JMP     KB16_1        ; PUT KEYBOARD IN HOLD STATE
01BE E9 027A R     JMP     CONT_INT      ; CONTINUE WITH INTERRUPT 48H
;----CHECK FOR PRISC
01C1 3C 37          KBO_2:  CMP     AL,55         ; IS THIS A PRISC KEY?
01C3 75 11          JNZ    KB2           ; NOT A PRISC KEY
01C5 F6 06 0017 R 03  TEST  KB_FLAG,LEFT_SHIFT+RIGHT_SHIFT ; EITHER SHIFT
;-----
;-----
01CA 74 F2          JZ     KBO_2         ; ACTIVE?
01CC F6 06 0017 R 04  TEST  KB_FLAG,CTL_SHIFT ; IS THE CTRL KEY PRESSED?
01D1 75 EB          JNZ    KBO_2         ; NOT A VALID PRISC (PC COMPATIBLE)
01D3 E9 030F R     JMP     PRISC        ; HANDLE THE PRINT SCREEN FUNCTION
;----FUNCTION KEY HANDLER
01D6 8A E0          KB2:   MOV     AH,AL        ; SAVE CHARACTER
01D8 24 7F          AND     AL,AND_MASK - BREAK_BIT ; MASK BREAK BIT
01DA 3C 54          CMP     AL,FN_KEY     ; CHECK FOR FUNCTION KEY
01DC 75 23          JNZ    KB4           ; JUMP IF NOT FUNCTION KEY
01DE F6 C4 80          TEST  AH,BREAK_BIT   ; IS THIS A FUNCTION BREAK
01E1 75 0B          JNZ    KB3           ; JUMP IF FUNCTION BREAK
01E3 80 26 0088 R 1F AND     KB_FLAG_2,CLEAR_FLAGS ; CLEAR ALL PREVIOUS
;-----
01EA 80 0E 0088 R A0 OR      KB_FLAG_2, FN_FLAG + FN_PENDING ; FUNCTIONS
01ED CF             IRET                    ; RETURN FROM INTERRUPT
;----FUNCTION BREAK
01EE F6 06 0088 R 20  KBO3:  TEST  KB_FLAG_2,FN_PENDING
01F3 75 06          JNZ    KB3_1        ; JUMP IF FUNCTION IS PENDING
01F5 80 26 0088 R 1F AND     KB_FLAG_2,CLEAR_FLAGS ; CLEAR ALL FLAGS
01FA CF             IRET                    ;
01FB 80 0E 0088 R 40 OR      KB_FLAG_2, FN_BREAK ; SET BREAK FLAG
0200 CF             IRET                    ; RETURN FROM INTERRUPT
;----CHECK IF FUNCTION FLAG ALREADY SET
0201 3C 55          KB4:   CMP     AL,PHK       ; IS THIS A PHANTOM KEY?
0203 74 FB          JZ     KB3_2        ; JUMP IF PHANTOM SEQUENCE
0205 F6 06 0088 R 90  KB4_0:  TEST  KB_FLAG_2, FN_FLAG+FN_LOCK ; ARE WE IN FUNCTION
;-----
;-----
020A 75 21          JNZ    KB5           ; STATE?
;----CHECK IF NUM_STATE IS ACTIVE
020C F6 06 0017 R 20  TEST  KB_FLAG,NUM_STATE
0211 74 16          JZ     KB4_1        ; JUMP IF NOT IN NUM STATE
0213 3C 0B          CMP     AL,NUM_0     ; ARE WE IN NUMERIC KEYPAD REGION?
0215 77 12          JA     KB4_1        ; JUMP IF NOT IN KEYPAD
0217 FE C8          DEC     AL           ; CHECK LOWER BOUND OF RANGE
0219 74 0E          JZ     KB4_1        ; JUMP IF NOT IN RANGE (ESC KEY)
;----TRANSLATE SCAN CODE TO NUMERIC KEYPAD
021B FE C8          DEC     AL           ; AL IS OFFSET INTO TABLE
021D BB 011D R     MOV     BX,OFFSET NUM_CODES
0220 2E: D7      CS:NUM_CODES
0222 80 E4 80      AND     AH,BREAK_BIT ; NEW SCAN CODE IS IN AL
;-----
0225 0A C4          OR      AL,AH         ; ISOLATE BREAK BIT ON ORIGINAL
0227 EB 51          JMP     AL,AH        ; SCAN CODE
0229 8A C4          MOV     SHORT CONT_INT ; UPDATE KEYPAD SCAN CODE
022B EB 4D          JMP     AL,AH        ; CONTINUE WITH INTERRUPT
;-----
022D             JMP     SHORT CONT_INT ; GET BACK BREAK BIT IF SET
;----CHECK FOR VALID FUNCTION KEY
022D             KBO5:

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022D 3C 01          CMP     AL,1          ; CHECK FOR ESC KEY (=1)
022F 75 25          JNE     KB7          ; NOT ESCAPE KEY
;----ESCAPE KEY, LOCK KEYBOARD IN FUNCTION LOCK
0231 F6 C4 80       TEST    AH,BREAK_BIT ; IS THIS A BREAK CODE?
0234 75 2C          JNZ     K88          ; NO PROCESSING FOR ESCAPE BREAK
0236 F6 06 0088 R 80 TEST    KB_FLAG_2,FN_FLAG ; TOGGLES ONLY WHEN FN HELD
; CONCURRENTLY
0238 74 25          JZ      K88          ; NOT HELD CONCURRENTLY
023D F6 06 0088 R 40 TEST    KB_FLAG_2,FN_BREAK ; HAS THE FUNCTION KEY BEEN
; RELEASED?
0242 75 1E          JNZ     K88          ; CONTINUE IF RELEASED. PROCESS AS
; ESC
0244 F6 06 0017 R 03 TEST    KB_FLAG,LEFT_SHIFT+RIGHT_SHIFT ; EITHER SHIFT?
0249 74 17          JZ      K88          ; NOT HELD DOWN
024B 80 36 0088 R 10 XOR     KB_FLAG_2,FN_LOCK ; TOGGLE STATE
0250 80 26 0088 R 1F AND     KB_FLAG_2,CLEAR_FLAGS ; TURN OFF OTHER STATES
0255 CF             IRET             ; RETURN FROM INTERRUPT
;----CHECK TABLE FOR OTHER VALID SCAN CODES
KB7:
0256             PUSH    CS          ;
0257 07             POP     ES          ; ESTABLISH ADDRESS OF TABLE
0258 BF 010B R      MOV     DI, OFFSET KB0 ; BASE OF TABLE
025B B9 0009        MOV     CX, KBOLEN    ; LENGTH OF TABLE
025E F2/ AE        REPNE  SCASB        ; SEARCH TABLE FOR A MATCH
0260 74 1D          JE      KB10        ; JUMP IF MATCH
;----ILLEGAL CHARACTER
0262 F6 06 0088 R 40 TEST    KB_FLAG_2,FN_BREAK ; HAS BREAK OCCURED?
0267 74 0F          JZ      KB9          ; FUNCTION KEY HAS NOT BEEN
; RELEASED
0269 F6 C4 80       TEST    AH,BREAK_BIT ; IS THIS A BREAK OF AN ILLEGAL
026C 75 0A          JNZ     KB9          ; DON'T RESET FLAGS ON ILLEGAL
; BREAK
026E 80 26 0088 R 1F AND     KB_FLAG_2,CLEAR_FLAGS ; NORMAL STATE
0273 C6 06 0087 R 00 MOV     CUR_FUNC,0    ; RETRIEVE ORIGINAL SCAN CODE
;----FUNCTION BREAK IS NOT SET
0278 8A C4          MOV     AL,AH        ; RETRIEVE ORIGINAL SCAN CODE
027A             CONT_INT:
027C E6 60          OUT     KBPORT,AL   ;
027E CD 09          INT     9H          ; ISSUE KEYBOARD INTERRUPT
027E CF             RET_INT:
;----BEFORE TRANSLATION CHECK FOR ALT+FN+M_KEY AS NUM LOCK
027F 3C 31          CMP     AL,M_KEY    ; IS THIS A POTENTIAL NUMLOCK?
0281 75 07          JNE     KB10_1      ; NOT A NUMKEY, TRANSLATE IT
0283 F6 06 0017 R 08 TEST    KB_FLAG,ALT_SHIFT ; ALT HELD DOWN ALSO?
0288 74 0B          JZ      K88          ; TREAT AS ILLEGAL COMBINATION
028A B9 010C R      MOV     CX, OFFSET KB0 + 1 ; GET OFFSET TO TABLE
028D 2B F9          SUB     DI, CX       ; UPDATE INDEX TO NEW SCAN CODE
; TABLE
028F 2E: 8A 85 0114 R MOV     AL, CS:KB1[DI] ; MOV NEW SCAN CODE INTO REGISTER
;----TRANSLATED
0294 F6 C4 80       TEST    AH,BREAK_BIT ; IS THIS A BREAK CHAR?
0297 74 31          JZ      KB13        ; JUMP IF MAKE CODE
;----CHECK FOR TOGGLE KEY
0299 3C 45          CMP     AL,NUM_LOCK ; IS THIS A NUM LOCK?
029B 75 08          JNZ     KB12_2      ; JUMP IF NOT A TOGGLE KEY
029D 0C 80          OR     AL,80H       ; TURN ON BREAK BIT
029F E6 60          OUT     KBPORT,AL   ;
02A1 CD 09          INT     9H          ; TOGGLE STATE
02A3 24 7F          AND     AL,AND_MASK-BREAK_BIT ; TURN OFF BREAK BIT
02A5 F6 06 0088 R 40 TEST    KB_FLAG_2,FN_BREAK ; HAS FUNCTION BREAK OCCURED?
02AA 74 11          JZ      KB12_3      ; JUMP IF BREAK HAS NOT OCCURED
02AC 3A 06 0087 R  CMP     AL,CUR_FUNC  ; IS THIS A BREAK OF OLD VALID
; FUNCTION
02B0 75 CC          JNE     RET_INT     ; ALLOW FURTHER CURRENT FUNCTIONS
02B2 80 26 0088 R 1F AND     KB_FLAG_2,CLEAR_FLAGS
02B7             KB12_20:
02B7 C6 06 0087 R 08 MOV     CUR_FUNC,0    ; CLEAR CURRENT FUNCTION
02BC CF             IRET             ; RETURN FROM INTERRUPT
02BD 3A 06 0087 R  CMP     AL,CUR_FUNC  ; IS THIS BREAK OF FIRST FUNCTION?
02C1 75 BB          JNE     RET_INT     ; IGNORE
02C3 80 26 0088 R DF AND     KB_FLAG_2,AND_MASK-FN_PENDING ; TURN OFF PENDING
; FUNCTION
02C8 EB ED          JMP     KB12_20     ; CLEAR CURRENT FUNCTION AND RETURN
;----VALID MAKE KEY HAS BEEN PRESSED
02CA F6 06 0088 R 40 TEST    KB_FLAG_2,FN_BREAK ; CHECK IF FUNCTION KEY HAS BEEN
; PRESSED
02CF 74 0D          JZ      KB14_1      ; JUMP IF NOT SET
;----FUNCTION BREAK HAS ALREADY OCCURED
02D1 80 3E 0087 R 08 CMP     CUR_FUNC,0    ; IS THIS A NEW FUNCTION?
02D6 74 06          JZ      KB14_1      ; INITIALIZE NEW FUNCTION
02D8 38 06 0087 R  CMP     CUR_FUNC,AL  ; IS THIS NON-CURRENT FUNCTION
02DC 75 90          JNZ     K885        ; JUMP IF NO FUNCTION IS PENDING
; . . . TO RETRIEVE ORIGINAL SCAN CODE
;----CHECK FOR SCAN CODE GENERATION SEQUENCE
02DE A2 0087 R      MOV     CUR_FUNC,AL  ; INITIALIZE CURRENT FN
02E1 3C 93          CMP     AL,JITTKOU_KEY ; IS THIS A ENTER,M OR T KEY ?
02E3 72 07          JB     KB16         ; NO. JMP KB16
02E5 8A E0          MOV     AH,AL        ; EXTENDED SET CODE
02E7 32 C0          XOR     AL,AL        ; CLEAR AL
02E9 CD 78          INT     78H         ; KANAKAN ROUTINE
02EB CF             IRET             ;
02EC             KB16:
02EC 3C 10          CMP     AL,PAUSE    ; IS THIS THE HOLD FUNCTION
02EE 75 8A          JNE     CONT_INT    ; NO. JMP CONT_INT
;----PUT KEYBOARD IN HOLD STATE
02F0 F6 06 0018 R 08 TEST    KB_FLAG_1,HOLD_STATE ; CANNOT GO IN HOLD STATE IF
; ITS ACTIVE
02F5 75 17          JNZ     KB16_2      ; DONE WITH INTERRUPT
02F7 80 0E 0018 R 08 OR     KB_FLAG_1,HOLD_STATE ; TURN ON HOLD FLAG
02FC 80 26 0338 R FB AND     KB_FLAG_2,0FBH ; RESET KEYBOARD LATCH
0301 E4 A0          AND     AL,HMI_PORT ;
0303 F6 06 0018 R 08 IN     KB_FLAG_1,HOLD_STATE ; STILL IN HOLD STATE?
0308 80 80          HOLD: TEST    AL,80H
; MOV

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Appendix A.

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030A E6 A0          OUT    NMI_PORT,AL    ; ENABLE NMI
030C 75 F5          JNZ    HOLD           ; CONTINUE LOOPING UNTIL KEY IS
                                ; PRESSED
030E CF            KB16_2: IRET          ; RETURN FROM INTERRUPT 48H
;-----PRINT SCREEN FUNCTION
030F F6 06 0018 R 08  PR7SC: TEST   KB_FLAG_1,HOLD_STATE ; IS HOLD STATE IN PROGRESS?
0314 74 06          JZ     KB16_3          ; OK TO CONTINUE WITH PR7SC
0316 80 26 0018 R F7  AND    KB_FLAG_1,0FFH-HOLD_STATE ; TURN OFF FLAG
0318 CF            IRET
031C              KB16_3:
0321 80 24 0338 R FB  AND    JKB_FLAG_2,0FBH
0324 07            ADD    SP,3*2          ; GET RID OF CALL TO INTERRUPT 48H
                                ; POP REGISTERS THAT AREN'T
                                ; MODIFIED IN INT5
0325 1F            POP    DS
0326 5A            POP    DX
0327 59            POP    CX
0328 5B            POP    BX
0329 E4 A0          IN     AL,NMI_PORT      ; RESET KEYBOARD LATCH
032B CD 05          INT    5H             ; ISSUE INTERRUPT
032D 58            POP    AX
032E 5F            POP    DI
032F 5E            POP    SI          ; POP THE REST
0330 CF            IRET
0331

KEY62_INT ENDP
;-----
; TYPAMATIC
; THIS ROUTINE WILL CHECK KEYBOARD STATUS BITS IN KB_FLAG_2
; AND DETERMINE WHAT STATE THE KEYBOARD IS IN. APPROPRIATE
; ACTION WILL BE TAKEN.
; INPUT
; AL= SCAN CODE OF KEY WHICH TRIGGERED NON-MASKABLE INTERRUPT
; OUTPUT
; CARRY BIT = 1 IF NO ACTION IS TO BE TAKEN.
; CARRY BIT = 0 MEANS SCAN CODE IN AL SHOULD BE PROCESSED
; FURTHER.
; MODIFICATIONS TO THE VARIABLES CUR_CHAR AND VAR_DELAY ARE
; MADE. ALSO THE PUTCHAR BIT IN KB_FLAG_2 IS TOGGLED WHEN
; THE KEYBOARD IS IN HALF RATE MODE.
;-----
0331 53            TPM PROC    NEAR
0332 38 06 0085 R    PUSH   BX
0336 74 31          CMP    CUR_CHAR,AL    ; IS THIS A NEW CHARACTER?
                                ; JUMP IF SAME CHARACTER
;-----NEW CHARACTER CHECK FOR BREAK SEQUENCES
0338 A8 80          TEST   AL,BREAK_BIT   ; IS THE NEW KEY A BREAK KEY?
033A 74 12          JZ     TP0            ; JUMP IF NOT A BREAK
033C 24 7F          AND    AL,07FH       ; CLEAR BREAK BIT
033E 38 06 0085 R    CMP    CUR_CHAR,AL    ; IS NEW CHARACTER THE BREAK OF
                                ; LAST MAKE?
0342 8A C4          MOV    AL,AH          ; RETRIEVE ORIGINAL CHARACTER
0344 75 05          JNZ   TP             ; JUMP IF NOT THE SAME CHARACTER
0346 C6 06 0085 R 00  MOV    CUR_CHAR,00    ; CLEAR CURRENT CHARACTER
0348 F8            TP:    CLC           ; CLEAR CARRY BIT
034C 5B            POP    BX
034D C3            RET
;-----INITIALIZE A NEW CHARACTER
034E A2 0085 R      TP0:    MOV    CUR_CHAR,AL    ; SAVE NEW CHARACTER
0351 80 26 0086 R F0  AND    VAR_DELAY,0F0H ; CLEAR VARIABLE DELAY
0356 80 26 0088 R FE  AND    KB_FLAG_2,0FEH ; INITIAL PUTCHAR BIT AS ZERO
035B F6 06 0088 R 02  TEST   KB_FLAG_2,INIT_DELAY ; ARE WE INCREASING THE
                                ; INITIAL DELAY?
0360 74 E9          JZ     TP             ; DEFAULT DELAY
0362 80 0E 0086 R 0F  OR     VAR_DELAY,DELAY_RATE ; INCREASE DELAY BY 2X
0367 EB E2          JMP    SHORT TP
;-----CHECK IF WE ARE IN TYPAMATIC MODE AND IF DELAY IS OVER
0369 F6 06 0088 R 08  TP2:    TEST   KB_FLAG_2,TYPE_OFF ; IS TYPAMATIC TURNED OFF?
036E 75 2B          JNZ   TP4            ; JUMP IF TYPAMATIC RATE IS OFF
0370 8A 1E 0086 R    MOV    BL,VAR_DELAY   ; GET VAR_DELAY
0374 80 E3 0F          AND    BL,0FH        ; MASK OFF HIGH ORDER(SCREEN RANGE)
0377 9A DB          OR     BL,BL          ; IS INITIAL DELAY OVER?
0379 74 0D          JZ     TP3            ; JUMP IF DELAY IS OVER
037B FE CB          DEC    BL             ; DECREASE DELAY WAIT BY ANOTHER
                                ; CHARACTER
037D 80 26 0086 R F0  AND    VAR_DELAY,0F0H
0382 08 1E 0086 R    OR     VAR_DELAY,BL
0386 EB 13          JMP    SHORT TP4
;-----CHECK IF TIME TO OUTPUT CHAR
0388 F6 06 0088 R 04  TP3:    TEST   KB_FLAG_2,HALF_RATE ; ARE WE IN HALF RATE MODE
038D 74 BC          JZ     TP             ; JUMP IF WE ARE IN NORMAL MODE
038F 80 36 0088 R 01  XOR    KB_FLAG_2,PUTCHAR ; TOGGLE BIT
0394 F6 06 0088 R 01  TEST   KB_FLAG_2,PUTCHAR ; IS IT TIME TO PUT OUT A CHAR
0399 75 B0          JNZ   TP4            ; NOT TIME TO OUTPUT CHARACTER
039B F9            TP4:    STC           ; SKIP THIS CHARACTER
039C 5B            POP    BX             ; SET CARRY FLAG
039D C3            RET
039E

TPM PAGE
;----- INT 16
; KEYBOARD I/O
; THESE ROUTINES PROVIDE KEYBOARD SUPPORT
; INPUT
; (AH)=0 READ THE NEXT JIS8BIT CHARACTER STRUCK FROM THE
; KEYBOARD, RETURN THE RESULT IN (AL), SCAN CODE IN
; (AH)
;
; (AH)=1 SET THE Z FLAG TO INDICATE IF AN JIS8BIT CHARACTER IS
; AVAILABLE TO BE READ.
; (ZF)=1 -- NO CODE AVAILABLE
; (ZF)=0 -- CODE IS AVAILABLE
; IF ZF = 0, THE NEXT CHARACTER IN THE BUFFER TO BE
; READ IS IN AX, AND THE ENTRY REMAINS IN THE BUFFER

```





Appendix A.

```

03BA F6 C4 7F          TEST    AH,7FH          ; AH=5 OR AH=85H
03BD 74 65             JZ     CHG_SHIFT       ; CHANGE THE KBD SHIFT STATUS
03BF FE CC             DEC    AH               ; AH=6
03C1 74 5E             JZ     RET_INT16       ; ILLEGAL FUNCTION CALL
03C3 FE CC             DEC    AH               ; AH=7
03C5 75 5A             JNZ   RET_INT16       ; ILLEGAL FUNCTION CALL
03C7 E9 04B9 R         JMP    CHG_SW          ; CHANGE THE KANAKAN AND INDICATOR SWITCH
;
;----- READ THE KEY TO FIGURE OUT WHAT TO DO
;
K1:
03CA FB               STI    ; ASCII READ
03CA FB               NOP    ; INTERRUPTS BACK ON DURING LOOP
03CB 90               CLI    ; ALLOW AN INTERRUPT TO OCCUR
03CC FA               MOV    BX,BUFFER_HEAD ; INTERRUPTS BACK OFF
03CD 8B 1E 001A R     CMP    BX,BUFFER_TAIL ; GET POINTER TO HEAD OF BUFFER
03D1 3B 1E 001C R     JZ     K1              ; TEST END OF BUFFER
03D5 74 F3             JZ     K1              ; LOOP UNTIL SOMETHING IN BUFFER
03D7 8B 07             MOV    AX,[BX]         ; GET SCAM CODE AND ASCII CODE
03D9 EB 04B9 R         CALL   K4              ; MOVE POINTER TO NEXT POSITION
03DC 89 1E 001A R     MOV    BUFFER_HEAD,BX ; STORE VALUE IN VARIABLE
03E0 EB 3F             JMP    SHORT RET_INT16
;
;----- ASCII STATUS
;
K2:
03E2 FA               CLI    ; INTERRUPTS OFF
03E2 FA               MOV    BX,BUFFER_HEAD ; GET HEAD POINTER
03E3 8B 1E 001A R     CMP    BX,BUFFER_TAIL ; IF EQUAL (Z=1) THEN NOTHING THERE
03E7 3B 1E 001C R     MOV    AX,[BX]
03EB 8B 07             STI    ; INTERRUPTS BACK ON
03ED FB               POP    BX              ; RECOVER REGISTER
03EE 5B               POP    DS              ; RECOVER SEGMENT
03EF 1F               RET    2               ; THROW AWAY FLAGS
;
;----- SHIFT STATUS
;
K3:
03F3 A0 0017 R         MOV    AL,KB_FLAG      ; GET THE SHIFT STATUS FLAGS
03F4 8A 26 0336 R     MOV    AH,JKB_FLAG    ; GET THE JKB_FLAG
03FA EB 25             JMP    SHORT RET_INT16
;
;----- SET TYPAMATIC
;
TRATE:
03FC 3C 04             CMP    AL,4            ; CHECK FOR CORRECT RANGE
03FE 7F 21             JG     RET_INT16       ; IF ILLEGAL VALUE IN AL IGNORE
0400 80 26 0088 R F1  AND    KB_FLAG_2,0F1H ; MASK OFF ANY OLD TYPAMATIC STATES
0405 D0 E0             SHL    AL,1           ; SHIFT TO PROPER POSITION
0407 08 06 0088 R     OR     KB_FLAG_2,AL
040B EB 14             JMP    SHORT RET_INT16
;
;----- ADJUST KEY CLICK
;
KCLICK:
040D 8A C8             OR     AL,AL           ; TURN OFF KEYBOARD CLICK?
040F 75 07             JNZ   KCLICK1         ; JUMP FOR RANGE CHECK
0411 80 26 0018 R FB  AND    KB_FLAG_1,AND_MASK-CLICK_ON ; TURN OFF CLICK
0416 EB 09             JMP    SHORT RET_INT16
;
KCLICK1:
0418 3C 01             CMP    AL,1           ; RANGE CHECK
041A 75 05             JNE   RET_INT16       ; NOT IN RANGE, RETURN
041C 80 0E 0018 R 04  OR     KB_FLAG_1,CLICK_ON ; TURN ON KEYBOARD CLICK
;
;----- INTERRUPT RETURN
;
RET_INT16:
0421 5B               POP    BX              ; RECOVER REGISTER
0422 1F               POP    DS              ; RECOVER REGISTER
0423 CF               IRET                   ; RETURN TO CALLER
;
;----- CHANGE KEYBOADR STATUS BY THE AL
;
CHG_SHIFT:
0424 50               PUSH   AX
0425 51               PUSH   CX
;
0426 8B C8             MOV    CX,AX          ; SAVE AX INTO CX
0428 24 C8             AND    AL,0C0H        ;
042A 3C C0             CMP    AL,0C0H        ;
042C 74 94             JE     ;              ; CHNGE KANAKAN ?
042E B4 FF             MOV    CH_CAP         ; IF AL=0C0H THEN CH_CAP
0430 CD 78             AND    AH,0FFH        ;
0432                   INT    78H           ; KANAKAM ROUTINE
;
CH_CAP:
0432 8A C1             MOV    AL,CL          ;
0434 24 30             AND    AL,30H         ;
0436 3C 30             CMP    AL,30H         ;
0438 74 1D             JE     ;              ;
043A 3C 20             CMP    AL,20H        ;
043C 74 14             JE     ;              ; TOGGLE ?
043E 3C 10             CMP    AL,10H        ;
0440 74 08             JE     ;              ; CAPS BIT ON ?
0442 80 26 0017 R BF  JE     CAP_ENT        ;
0447 EB 0E 98             AND    KB_FLAG,HOT_CAPS_STATE
044A                   JMP    CH_JAP
;
CAP_ENT:
044A 80 0E 0017 R 40  OR     KB_FLAG,CAPS_STATE
044F EB 06 90             JMP    CH_JAP
;
CAP_TOG:
0452 80 36 0017 R 40  XOR    KB_FLAG,CAPS_STATE
0457                   ;
;
CH_JAP:
0457 8A C1             MOV    AL,CL          ;
0459 24 0C             AND    AL,0CH         ;
045B 3C 0C             CMP    AL,0CH        ;
045D 74 08             JE     CH_H_Z         ;
045F D0 E8             SHR    CH_H_Z         ;
AL,1

```



```

0461 80 26 0336 R F9
0466 08 06 0336 R
046A
046A 8A C1
046C 24 03
046E 3C 03
0470 74 1D
0472 3C 02
0474 74 14
0476 3C 01
0478 74 08
047A 80 26 0336 R FE
047F EB 0E 90
0482
0482 80 0E 0336 R 01
0487 EB 06 90
048A
048A 80 36 0336 R 01
048F
048F F6 C5 80
0492 75 03
0494 EB 0EA9 R
0497
0497 59
0498 58
0499 EB 86

049B
049B 3C 03
049D 7F 17
049F 80 26 0338 R FC
04A4 08 06 0338 R
04A8 A8 01
04AA 74 05
04AC B8 FF02
04AF EB 03
04B1
04B1 B8 FF01
04B4
04B4 CD 78
04B6
04B6 E9 0421 R
04B9

04B9
04B9 43
04BA 43
04BB 3B 1E 0082 R
04BF 75 04
04C1 8B 1E 0080 R
04C5 C3
04C6

04C6 6B 6C 6D
04C9 3A 6E 6F
04CC 52
04CD 45 46 38 1D
04D1 2A 36
= 000D

04D3
04D3 80 20 10
04D6 40 02 04
04D9 80
04DA 20 10 08 04
04DE 02 01

04E0 1B FF 00 FF FF FF
1E FF
04E8 FF FF FF 1F FF 7F
FF 11
04F0 17 05 12 14 19 15
09 0F
04F8 10 1B 1D 0A FF 01
13
04FF 04 06 07 08 0A 0B
0C FF FF
0508 FF FF 1C 1A 18 03
16 02
0510 0E 0D FF FF FF FF
FF FF FF
0518 20 FF

051A
051A 5E 5F 60 61 62 63
64 65
0522 66 67 FF FF 77 FF
84 FF
052A 73 FF 74 FF 75 FF
76 FF
0532 FF

0533
0533 1B 31 32 33 34 35
36 37 38 39 30 2D
3D 08 09
0542 71 77 65 72 74 79
75 69 6F 70 5B 5D
0D FF 61 73 64 66

AND JKB_FLAG,NOT NOT_ALPHA_STATE
OR JKB_FLAG,AL
CH_H_Z:
MOV AL,CL
AND AL,03H
CMP AL,03H
JE CHG_SHIFT_R
CMP AL,02H ; TOGGLE ?
JE H_Z_T0G
CMP AL,01H
JE Z_ENT
AND JKB_FLAG,NOT ZENKAKU_STATE
JMP CHG_SHIFT_R
Z_ENT:
OR JKB_FLAG,ZENKAKU_STATE
JMP CHG_SHIFT_R
H_Z_T0G:
XOR JKB_FLAG,ZENKAKU_STATE
CHG_SHIFT_R:
TEST CH,80H
JNZ CHG_S_R
CALL IND
CHG_S_R:
POP CX
POP AX
JMP RET_INT16
;
;----- CHANGE KANAKH AND INDICATOR SWITCH
;
CHG_SW:
CMP AL,3
JG CHG_SW_3
AND JKB_FLAG_2,0FCH
OR JKB_FLAG_2,AL
TEST AL,1
JZ CHG_SW_1
MOV AX,0FF02H
JMP SHORT CHG_SW_2
CHG_SW_1:
MOV AX,0FF01H
CHG_SW_2:
INT 78H
CHG_SW_3:
JMP RET_INT16
KEYBOARD_IO
ENDP
;
;----- INCREMENT A BUFFER POINTER
;
K4 PROC NEAR
INC BX ; MOVE TO NEXT WORD IN LIST
INC BX
CMP BX,BUFFER_END ; AT END OF BUFFER?
JNE K5 ; NO, CONTINUE
MOV BX,BUFFER_START ; YES, RESET TO BUFFER BEGINNING
K5:
RET
K4 ENDP
;----- TABLE OF SHIFT KEYS AND MASK VALUES
K6 LABEL BYTE
DB KANJI_KEY,MUHEN_KEY,HENKAN_KEY
DB ALPHA_KEY,KATAKANA_KEY,HIRAGANA_KEY
DB INS_KEY ; INSERT KEY
DB NUM_KEY,SCROLL_KEY,ALT_KEY,CTL_KEY
DB LEFT_KEY,RIGHT_KEY
K6L EQU 9-K6
;----- SHIFT_MASK TABLE
K7 LABEL BYTE
DB KANJI_SHIFT,MUHEN_SHIFT,HENKAN_SHIFT
DB CAPS_SHIFT,KATAKANA_SHIFT,HIRAGANA_SHIFT
DB INS_SHIFT ; INSERT MODE SHIFT
DB NUM_SHIFT,SCROLL_SHIFT,ALT_SHIFT,CTL_SHIFT
DB LEFT_SHIFT,RIGHT_SHIFT
;----- SCAN CODE TABLES
K8 LABEL BYTE
DB 27,-1,0,-1,-1,-1,30,-1
DB -1,-1,-1,31,-1,127,-1,17
DB 23,5,18,20,25,21,9,15
DB 16,27,29,10,-1,1,19
DB 4,6,7,8,10,11,12,-1,-1
DB -1,-1,28,26,24,3,22,2
DB 14,13,-1,-1,-1,-1,-1,-1
DB ' ',-1
;----- CTL TABLE SCAN
K9 LABEL BYTE
DB 94,95,96,97,98,99,100,101
DB 102,103,-1,-1,119,-1,132,-1
DB 115,-1,116,-1,117,-1,118,-1
DB -1
;----- LC TABLE
K10 LABEL BYTE
DB 01BH,'1234567890-'',08H,09H
DB 'qwertyuiop[]',0DH,-1,'asdfghjkl;',027H

```

Appendix A.

```

        67 68 6A 6B 6C 3B
        27
055B 60 FF 5C 7A 78 63          DB      60H,-1,5CH,'zxcvbnm,./',-1,'X',-1,' '
        76 62 6E 6D 2C 2E
        2F FF 2A FF 20
056C FF
;----- UC TABLE
056D                                DB      -1
056D 1B 21 40 23 24 25          K11 LABEL BYTE
        5E 26 2A 28 29 5F          DB      27,'!@00',37,05EH,'&K()_+',08H,0
        2B 08 00
057C 51 57 45 52 54 59          DB
        55 49 4F 50 7B 7D          'QWERTYUIOP ',0DH,-1,'ASDFGHJKL:'
        0D FF 41 53 44 46
        47 48 4A 4B 4C 3A
        22
0595 7E FF 7C 5A 58 43          DB      7EH,-1,'|ZXCVBNM<>?',-1,0,-1,' ',-1
        56 42 4E 4D 3C 3E
        3F FF 00 FF 20 FF
;----- UC TABLE SCAN
05A7                                DB
05A7 54 55 56 57 58 59          K12 LABEL BYTE
        5A                          DB      84,85,86,87,88,89,90
05AE 5B 5C 5D                          DB      91,92,93
;----- ALT TABLE SCAN
05B1                                DB
05B1 68 69 6A 6B 6C          K13 LABEL BYTE
05B6 6D 6E 6F 70 71          DB      104,105,106,107,108
;----- NUM STATE TABLE
05BB                                DB
05BB 37 38 39 2D 34 35          K14 LABEL BYTE
        36 2B 31 32 33 30          DB      '789-456+1230.'
        2E
;----- BASE CASE TABLE
05C8                                DB
05C8 47 48 49 FF 4B FF          K15 LABEL BYTE
        4D                          DB      71,72,73,-1,75,-1,77
05CF FF 4F 50 51 52 53          DB      -1,79,80,81,82,83
;----- KEYBOARD INTERRUPT ROUTINE
;*****
KB_INT PROC FAR
        STI
        PUSH AX ; ALLOW FURTHER INTERRUPTS
        PUSH BX
        PUSH CX
        PUSH DX
        PUSH SI
        PUSH DI
        PUSH DS
        PUSH ES
        CLD ; FORWARD DIRECTION
        CALL DDS
        MOV BH,KB_FLAG
        MOV BL,KB_FLAG
        AND BX,4007H ; MASK KBD STATUS FLAG
        PUSH BX ; MEMORIZE KBD STATUS
        MOV AH,AL ; SAVE SCAN CODE IN AH
        TEST FOR OVERRUM SCAN CODE FROM KEYBOARD
        CMP AL,OFFH ; IS THIS AN OVERRUM CHAR?
        JNZ K16 ; NO, TEST FOR SHIFT KEY
        CALL ERROR_BEEP ; CALL ERROR BEEP ROUTINE
        JMP K26 ; END OF INTERRUPT
;----- TEST FOR SHIFT KEYS
K16:
        AND AL,07FH ; TEST_SHIFT
        PUSH CS ; TURN OFF THE BREAK BIT
        POP ES
        MOV DI,OFFSET K6 ; ESTABLISH ADDRESS OF SHIFT TABLE
        MOV CX,K6L ; SHIFT KEY TABLE
        REPNE SCASB ; LENGTH
        MOV AL,AH ; LOOK THROUGH THE TABLE FOR A
        JE K17 ; MATCH
        JMP K25 ; RECOVER SCAN CODE
        TEST FOR SHIFT KEYS
        JZ K25 ; JUMP IF MATCH FOUND
        JZ K25 ; IF NO MATCH, THEN SHIFT NOT FOUND
;----- SHIFT KEY FOUND
K17:
        SUB DI,OFFSET K6+1 ; ADJUST PTR TO SCAN CODE MATCH
        MOV AH,CS:K7[DI] ; GET MASK INTO AH
        TEST AL,80H ; TEST FOR BREAK KEY
        JZ K17_1
        JMP K23
;----- SHIFT MAKE FOUND, DETERMINE SET OR TOGGLE
K17_1:
        CMP DI,9 ; BREAK_SHIFT_FOUND
        JB K18 ; IS THIS A TOGGLE KEY
        ; YES, HANDLE TOGGLE KEY
;----- PLAIN SHIFT KEY, SET SHIFT ON
OR KB_FLAG,AH ; TURN ON SHIFT BIT
JMP K26 ; INTERRUPT_RETURN
;----- TOGGLED SHIFT KEY, TEST FOR 1ST MAKE OR NOT
K18:
        TEST KB_FLAG,CTL_SHIFT ; SHIFT-TOGGLE
        JZ K18_1 ; CHECK CTL SHIFT STATE
        JMP K25 ; JUMP IF CTL STATE
;----- ALTERNATE SHIFT
K18_1:
        TEST KB_FLAG,ALT_SHIFT ; CHECK FOR ALTERNATE SHIFT
        JZ K22 ; JUMP IF NOT ALTERNATE SHIFT
;
        CMP AL,ALPHA_KEY ; ALTERNATE-ALPHA_KEY = CAPS
        JNZ K19
        TEST AH,KB_FLAG_1 ; IS KEY ALREADY DEPRESSED

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0643 75 5B          JNZ K21          ; JUMP IF KEY ALREADY DEPRESSED
0645 08 26 0018 R  OR KB_FLAG_1,AH  ; INDICATE THAT THE KEY IS
                                ; DEPRESSED
0649 30 26 0017 R  XOR KB_FLAG,AH   ; TOGGLE THE SHIFT STATE
064D E9 07C9 R      JMP K26         ; INTERRUPT RETURN
0650
0650 3C 6E          K19: CMP AL,KATAKANA_KEY ; IS THIS THE KATAKANA_KEY
0652 75 14          JNZ K20         ; NOT KATAKANA_KEY
0654 F6 06 0337 R 10 TEST JKB_FLAG_1,HANKAKU_SHIFT ; IS KEY ALREADY DEPRESSED
0659 75 45          JNZ K21         ; JUMP IF KEY ALREADY DEPRESSED
065B 80 0E 0337 R 10 OR JKB_FLAG_1,HANKAKU_SHIFT ; INDICATE THAT THE KEY IS DEPRESSED
0660 80 26 0336 R FE AND JKB_FLAG,NOT ZENKAKU_STATE ; TOGGLE THE SHIFT STATE
0665 EB 39 90       JMP K21         ; INTERRUPT RETURN
0668
0668 3C 6F          K20: CMP AL,HIRAGANA_KEY ; IS THIS THE HIRAGANA_KEY
066A 74 03          JZ K20_1       ; NOT HIRAGANA_KEY
066C EB 15 90       JMP
066F
066F F6 06 0337 R 08 K20_1: TEST JKB_FLAG_1,ZENKAKU_SHIFT ; IS KEY ALREADY DEPRESSED
0674 75 2A          JNZ K21         ; JUMP IF KEY ALREADY DEPRESSED
0676 80 0E 0337 R 08 OR JKB_FLAG_1,ZENKAKU_SHIFT ; INDICATE THAT THE KEY IS DEPRESSED
067B 80 0E 0336 R 01 OR JKB_FLAG,ZENKAKU_STATE ; TOGGLE THE SHIFT STATE
0680 EB 1E 90       JMP K21         ; INTERRUPT RETURN
0683
0683 3C 6B          K20_2: CMP AL,KANJI_KEY   ; IS KNUM_KEY
0685 74 07          JZ K20_3       ;
0687 3C 52          CMP AL,INS_KEY  ; IS INS_KEY
0689 74 15          JE K21         ;
068B EB 7F 90       JMP K22_6       ;
068E
068E F6 06 0338 R 40 K20_3: TEST JKB_FLAG_2,KNUM_SHIFT ; IS KEY ALREADY DEPRESSED
0693 75 0B          JNZ K21         ; JUMP IF KEY ALREADY DEPRESSED
0695 80 0E 0338 R 40 OR JKB_FLAG_2,KNUM_SHIFT ; INDICATE THAT THE KEY IS DEPRESSED
069A 8A E0          MOV AH,AL      ;
069C 80 38          MOV AL,38H    ;
069E CD 78          INT 78H       ; KANAKAN ROUTINE
06A0 E9 07C9 R      K21: JMP K26         ; INTERRUPT RETURN
06A0
;----- NOT ALTERNATE SHIFT
;
06A3
06A3 3C 3A          K22: CMP AL,ALPHA_KEY  ; SHIFT TOGGLE KEY HIT; PROCESS IT
06A5 75 14          JNZ K22_1      ; IS THIS THE ALPHA_KEY
06A7 F6 06 0337 R 01 TEST JKB_FLAG_1,ALPHA_SHIFT ; NOT ALPHA_KEY
06AC 75 70          JNZ K22_7      ; IS KEY ALREADY DEPRESSED
06AE 80 0E 0337 R 01 OR JKB_FLAG_1,ALPHA_SHIFT ; JUMP IF KEY ALREADY DEPRESSED
06B3 80 26 0336 R F9 AND JKB_FLAG,ALPHA_STATE ; INDICATE THAT THE KEY IS DEPRESSED
06B8 E9 07C9 R      JMP K26         ; TOGGLE THE SHIFT STATE
06BB
06BB 3C 6E          K22_1: CMP AL,KATAKANA_KEY ; IS THIS THE KATAKANA_KEY
06BD 74 04          JZ K22_2       ; YES, KATAKANA_KEY
06BF 3C 6F          CMP AL,HIRAGANA_KEY ; IS THIS THE HIRAGANA_KEY
06C1 75 16          JNZ K22_3      ; NOT HIRAGANA_KEY
06C3
06C3 84 26 0337 R  K22_2: TEST AH,JKB_FLAG_1 ; IS KEY ALREADY DEPRESSED
06C7 75 55          JNZ K22_7      ; JUMP IF KEY ALREADY DEPRESSED
06C9 08 26 0337 R  OR JKB_FLAG_1,AH ; INDICATE THAT THE KEY IS DEPRESSED
06CD 80 26 0336 R F9 AND JKB_FLAG,ALPHA_STATE ; TOGGLE THE SHIFT STATE
06D2 08 26 0336 R  OR JKB_FLAG,AH   ; INTERRUPT RETURN
06D4 E9 07C9 R      JMP K26         ; INTERRUPT RETURN
06D9
06D9 3C 6B          K22_3: CMP AL,KANJI_KEY  ; IS THIS THE KANJI_KEY
06DB 74 0A          JZ K22_4       ; KANJI_KEY
06DD 3C 6C          CMP AL,MUHEN_KEY ; IS THIS THE MUHEN_KEY
06DF 74 06          JZ K22_4       ; MUHEN_KEY
06E1 3C 6D          CMP AL,HENKAN_KEY ; IS THIS THE HENKAN_KEY
06E3 74 02          JZ K22_4       ; HENKAN_KEY
06E5 EB 25          JMP SHORT K22_6 ;
06E7
06E7 84 26 0338 R  K22_4: TEST AH,JKB_FLAG_2 ; IS KEY ALREADY DEPRESSED
06EB 75 31          JNZ K22_7      ; JUMP IF KEY ALREADY DEPRESSED
06ED 08 26 0338 R  OR JKB_FLAG_2,AH ; INDICATE THAT THE KEY IS DEPRESSED
06F1 F6 06 0336 R 05 TEST JKB_FLAG,ZENKAKU_CHAR ; IF ZENKAKU CHARACTER
06F6 75 08          JNZ K22_5     ; SET AH SCANCODE
06F8 8A E0          MOV AH,AL     ; SET AH SCANCODE
06FA B0 20          MOV AL,20H   ; SPACE CHARACTER
06FC CD 78          INT 78H     ; KANAKAN ROUTINE
06FE EB 1E          JMP SHORT K22_7 ; INTERRUPT RETURN
0700
0700 8A E0          K22_5: MOV AH,AL     ; SET AH SCANCODE
0702 B0 81          MOV AL,81H   ; ZENKAKU SPACE 1ST BYTE
0704 CD 78          INT 78H     ; KANAKAN ROUTINE
0706 B0 40          MOV AL,40H   ; ZENKAKU SPACE 2ND BYTE
0708 CD 78          INT 78H     ; KANAKAN ROUTINE
070A EB 12          JMP SHORT K22_7 ; INTERRUPT RETURN
070C
070C 84 26 0018 R  K22_6: TEST AH,KB_FLAG_1 ; IS KEY ALREADY DEPRESSED
0710 75 0C          JNZ K22_7      ; JUMP IF KEY ALREADY DEPRESSED
0712 08 26 0018 R  OR KB_FLAG_1,AH ; INDICATE THAT THE KEY IS
                                ; DEPRESSED
0716 30 26 0017 R  XOR KB_FLAG,AH  ; TOGGLE THE SHIFT STATE
071C 3C 52          CMP AL,INS_KEY ; TEST FOR 1ST MAKE OF INSERT KEY
071E 74 03          JE
071E E9 07C9 R      K22_7: JMP K26         ; JUMP IF NOT INSERT KEY
0721
0721 B8 5200         K22_8: MOV AX,INS_KEY*256 ; SET SCAN CODE INTO AH. 0 INTO AL
0724 E9 0A53 R      JMP K57        ; PUT INTO OUTPUT BUFFER
0727
;----- BREAK SHIFT FOUND
;
0727
K23:

```

Appendix A.

```

0727 83 FF 09      CMP     DI,9           ; IS THIS ATOGGLE KEY
072A 72 1D          JB      K24           ; YES, HANDLE TOGGLE KEY
072C F6 D4          AND     AH           ; INVERT MASK
072E 3C 26 0017 R  AND     KB_FLAG,AH   ; TURN OFF SHIFT BIT
0732 30 B8          CMP     AL,ALT_KEY+80H ; IS THIS ALTERNATE SHIFT RELEASE
0734 75 10          JNE     K23_1        ; INTERRUPT_RETURN
;----- ALTERNATE SHIFT KEY RELEASED, GET THE VALUE INTO BUFFER
0736 A0 0019 R     MOV     AL,ALT_INPUT
0739 32 E4          XOR     AH,AH        ; SCAN CODE OF 0
073B 88 26 0019 R  MOV     AL,ALT_INPUT,AH ; ZERO OUT THE FIELD
073F 0A C0          OR      AL,AL        ; WAS THE INPUT=0?
0741 74 03          JE      K23_1        ; INTERRUPT_RETURN
0743 E9 0A9A R     JMP     K58          ; IT WASN'T, SO PUT IN BUFFER
0746 0746          K23_1: JMP     K26
0749 0749          K24:   JMP     ; BREAK-TOGGLE
0749 3C BA          CMP     AL,CAPS_KEY+BREAK_BIT ; SPECIAL CASE OF TOGGLE KEY
074B 75 19          JNE     K24_1        ; JUMP AROUND POTENTIAL UPDATE
074D 80 26 0337 R FE AND     JKB_FLAG_1,NOT ALPHA_SHIFT ; INDICATE NO LONGER DEPRESSED
0752 80 26 0018 R BF AND     KB_FLAG_1,NOT CAPS_SHIFT ; INDICATE NO LONGER DEPRESSED
0757 F6 06 0018 R 02 TEST    KB_FLAG_1,CLICK_SEQUENCE
075C 74 68          JZ      K26          ; INTERRUPT IS OVER
075E 80 26 0018 R FD AND     KB_FLAG_1,AND_MASK-CLICK_SEQUENCE ; MASK OFF MAKE
; OF CLICK
0763 EB 64 90      K24_1: JMP     K26          ; INTERRUPT IS OVER
0766 0766          K24_1: CMP     AL,KATAKANA_KEY+BREAK_BIT ; IS THIS THE KATAKANA_KEY
0768 75 0D          JNZ     K24_2        ; NOT KATAKANA_KEY
076A 80 26 0337 R FD AND     JKB_FLAG_1,NOT KATAKANA_SHIFT ; INDICATE NO LONGER DEPRESSED
076F 80 26 0337 R EF AND     JKB_FLAG_1,NOT HANKAKU_SHIFT ; INDICATE NO LONGER DEPRESSED
0774 EB 53 90      K24_2: JMP     K26          ; INTERRUPT IS OVER
0777 0777          K24_2: CMP     AL,HIRAGANA_KEY+BREAK_BIT ; IS THIS THE HIRAGANA_KEY
0779 75 0D          JNZ     K24_3        ; NOT HIRAGANA_KEY
077B 80 26 0337 R FB AND     JKB_FLAG_1,NOT HIRAGANA_SHIFT ; INDICATE NO LONGER DEPRESSED
0780 80 26 0337 R F7 AND     JKB_FLAG_1,NOT ZENKAKU_SHIFT ; INDICATE NO LONGER DEPRESSED
0785 EB 42 90      K24_3: JMP     K26          ; INTERRUPT IS OVER
0788 0788          K24_3: CMP     AL,KANJI_KEY+BREAK_BIT ; IS THIS THE KANJI_KEY
078A 75 0D          JNZ     K24_4        ; NOT KANJI_KEY
078C 80 26 0338 R 7F AND     JKB_FLAG_2,NOT KANJI_SHIFT ; INDICATE NO LONGER DEPRESSED
0791 80 26 0338 R BF AND     JKB_FLAG_2,NOT KNUM_SHIFT ; INDICATE NO LONGER DEPRESSED
0796 EB 31 90      K24_4: JMP     K26          ; INTERRUPT IS OVER
0799 0799          K24_4: CMP     AL,MUHEN_KEY+BREAK_BIT ; IS THIS THE MUHEN_KEY
079B 75 08          JNZ     K24_5        ; NOT MUHEN_KEY
079D 80 26 0338 R DF AND     JKB_FLAG_2,NOT MUHEN_SHIFT ; INDICATE NO LONGER DEPRESSED
07A2 EB 25 90      K24_5: JMP     K26          ; INTERRUPT IS OVER
07A5 07A5          K24_5: CMP     AL,HENKAN_KEY+BREAK_BIT ; IS THIS THE HENKAN_KEY
07A7 75 08          JNZ     K24_6        ; NOT HIRAGANA_KEY
07A9 80 26 0338 R EF AND     JKB_FLAG_2,NOT HENKAN_SHIFT ; INDICATE NO LONGER DEPRESSED
07AE EB 19 90      ;----- BREAK OF NORMAL TOGGLE
07B1 07B1          K24_6: NOT     AH           ; INVERT MASK
07B3 F6 D4          AND     KB_FLAG_1,AH ; INDICATE NO LONGER DEPRESSED
07B7 EB 10          JMP     SHORT K26    ; INTERRUPT_RETURN
;----- TEST FOR HOLD STATE
07B9 07B9          K25:   CMP     AL,80H       ; NO-SHIFT-FOUND
07BB 73 0C          JAE     K26          ; TEST FOR BREAK KEY
; NOTHING FOR BREAK CHARS FROM HERE
; ON
07BD F6 06 0018 R 08 TEST    KB_FLAG_1,HOLD_STATE ; ARE WE IN HOLD STATE?
07C2 74 24          JZ      K28          ; BRANCH AROUND TEST IF NOT
07C4 80 26 0018 R F7 AND     KB_FLAG_1,NOT HOLD_STATE ; TURN OFF THE HOLD STATE
; BIT
; INTERRUPT-RETURN
07C9 07C9          K26:   MOV     DH,KB_FLAG
07CD 8A 16 0336 R   MOV     DL,JKB_FLAG
07D1 81 E2 4007     AND     DX,4007H    ; MASK KBD STATUS FLAG
07D5 5B            POP     BX
07D6 33 DA          XOR     BX,DX
07D8 74 05          JZ      K26_1        ; MASK KBD STATUS FLAG
07DA B8 05FF       MOV     AX,05FFH
07DD CD 16          INT     16H
07DF 07DF          K26_1: POP     ES
07E0 1F            POP     DS
07E1 5F            POP     DI
07E2 5E            POP     SI
07E3 5A            POP     DX
07E4 59            POP     CX
07E5 5B            POP     BX
07E6 58            POP     AX
07E7 CF            IRET
; RESTORE STATE
; RETURN, INTERRUPTS BACK ON WITH
; FLAG CHANGE
07E8 07E8          ;----- NOT IN HOLD STATE, TEST FOR SPECIAL CHARS
07E8 F6 06 0017 R 08 K28:   TEST    KB_FLAG,ALT_SHIFT ; NO-HOLD-STATE
07ED 75 03          JNZ     K29          ; ARE WE IN ALTERNATE SHIFT
07EF E9 08E9 R     JMP     K38          ; JUMP IF ALTERNATE SHIFT
; JUMP IF NOT ALTERNATE
;----- TEST FOR ALT+CTRL KEY SEQUENCES
07F2 07F2          K29:   TEST    KB_FLAG,CTL_SHIFT ; TEST-RESET
07F7 74 69          JZ      K31          ; ARE WE IN CONTROL SHIFT ALSO
07F9 3C 53          CMP     AL,DEL_KEY   ; NO_RESET
07FB 75 09          JNE     K29_1        ; SHIFT STATE IS THERE, TEST KEY
; NO_RESET
;----- CTL-ALT-DEL HAS BEEN FOUND, DO I/O CLEANUP
07FD C7 06 0072 R 1234 ;----- CTL-ALT-DEL HAS BEEN FOUND, DO I/O CLEANUP
0803 E9 0000 E     MOV     RESET_FLAG,1234H ; SET FLAG FOR RESET FUNCTION
0806 3C 52          CMP     NEAR PTR RESET ; JUMP TO POWER ON DIAGNOSTICS
0808 75 09          JNE     K29_1        ; CHECK FOR RESET WITH DIAGNOSTICS
; CHECK FOR OTHER
;----- CTL-ALT-INS HAS BEEN FOUND

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080A C7 06 0072 R 3412      MOV    RESET_FLAG, 3412H ; SET FLAG FOR DIAGNOSTICS
0810 E9 0000 E              JMP    NEAR PTR RESET ; LEVEL 1 DIAGNOSTICS
0813 3C 3A                  CMP    AL,CAPS_KEY ; CHECK FOR KEYBORAD CLICK TOGGLE
0815 75 13                    JNE    K29_3 ; CHECK FOR SCREEN ADJUSTMENT
;-----
0817 F6 06 0018 R 02      ALT+CTRL+CAPSLOCK HAS BEEN FOUND
081C 75 AB                    TEST   KB_FLAG_1,CLICK_SEQUENCE
;-----
081E 80 36 0018 R 04      JNZ    K26 ; JUMP IF SEQUENCE HAS ALREADY
;-----
0823 80 0E 0018 R 02      XOR    KB_FLAG_1,CLICK_ON ; TOGGLE BIT FOR AUDIO KEYSTROKE
0828 EB 9F                    ; FEEDBACK
082A 3C 4D                  OR     KB_FLAG_1,CLICK_SEQUENCE ; SET CLICK_SEQUENCE STATE
082C 75 12                    JMP    SHORT K26 ; INTERRUPT IS OVER
082E E8 0AD1 R              K29_3: CMP    AL,RIGHT_ARROW ; ADJUST SCREEM TO THE RIGHT?
;-----
0831 3C FC                    JNE    K29_4 ; LOOK FOR RIGHT ADJUSTMENT
0833 7C 94                    CALL   GET_POS ; GET THE # OF POSITIONS SCREEN IS
0835 FE 0E 0089 R          ; SHIFTED
0839 FE C8                    CMP    AL,0-RANGE ; IS SCREEN SHIFTED AS FAR AS
083B E8 0ADD R              ; POSSIBLE?
083E EB 14                    JL     K26 ; OUT OF RANGE
0840 3C 4B                    DEC    HORZ_POS ; SHIFT VALUE TO THE RIGHT
0842 75 1E                    DEC    AL ; DECREASE RANGE VALUE
0844 E8 0AD1 R              CALL   PUT_POS ; RESTORE STORAGE LOCATION
;-----
0847 3C 04                    JMP    SHORT K29_5 ; ADJUST
0849 7F 14                    K29_4: CMP    AL,LEFT_ARROW ; ADJUST SCREEM TO THE LEFT?
084B FE 06 0089 R          JNE    K31 ; NOT AN ALT_CTRL SEQUENCE
084F FE C0                    CALL   GET_POS ; GET NUMBER OF POSITIONS SCREEN IS
;-----
0851 E8 0ADD R              ; SHIFTED
0854 B0 02                    CMP    AL,RANGE ; IS SCREEN SHIFTED AS FAR AS
0856 BA 03D4                 ; POSSIBLE?
0859 EE                      ; OUT OF RANGE
085A A0 0089 R              ; SHIFT VALUE TO THE RIGHT
085D 42                      ; DECREASE RANGE VALUE
085E EE                      ; RESTORE STORAGE LOCATION
085F E9 07C9 R              ; ADJUST
;-----
0862 3C 39                    K29_5: CALL   PUT_POS ; PUT POSITION BACK IN STORAGE
0864 74 0B                    MOV    AL,2 ; ADDRESS TO CRT CONTROLLER
0866 3C 6C                    MOV    DX,3D4H ; COLUMN POSITION
0868 74 07                    OUT    DX,AL ; POINT AT DATA REGISTER
086A 3C 6D                    MOV    AL,HORZ_POS ; MOV POSITION
086C 74 03                    INC    DX ; INCREMENT COLUMN POSITION
086E EB 2A 90                OUT    DX,AL ; MOV POSITION
0871 B0 20                    JMP    K26
0873 E9 0A53 R              ;-----
0876 70 72 73 74 75          K29_6: JMP    K26
087B 76 77 78 79 7A          ;-----
;-----
0880 10 11 12 13 14 15      K31: IN ALTERNATE SHIFT, RESET NOT FOUND
0888 18 19 1E 1F 20 21      ; NO-RESET
0890 24 25 26 2C 2D 2E      CMP    AL,57 ; TEST FOR SPACE KEY
0898 31 32                    JZ     K31_1 ; GO TO K31_1
;-----
089A BF 0876 R              K31: CMP    AL,108 ; TEST FOR MUHENKAN KEY
089D B9 000A                 JZ     K31_1 ; GO TO K31_1
08A0 F2/ AE                  CMP    AL,109 ; TEST FOR HENKAN KEY
08A2 75 13                    JZ     K31_1 ; GO TO K31_1
08A4 81 EF 0877 R          CMP    AL,109 ; TEST FOR HENKAN KEY
08A8 A0 0019 R              JZ     K31_1 ; GO TO K31_1
08AB B4 0A                  JMP    K32 ; GO TO K32
08AD F6 E4                    MOV    AL,' ' ; SET SPACE CHAR
08AF 03 C7                  JMP    K57 ; BUFFER_FILL
08B1 A2 0019 R              ;-----
08B4 E9 07C9 R              ;-----
08B7 C6 06 0019 R 00      K30: ALT-INPUT-TABLE
08B8 3C 02                    LABEL BYTE
08C0 72 0C                    DB 70H,72H,73H,74H,75H
08C2 3C 0E                    DB 76H,77H,78H,79H,7AH ; 10 NUMBERS ON KEYPAD
08C4 73 08                    ;-----
08D0 80 C4 76              ;-----
08D3 32 C0                    ;-----
08D5 E9 0A53 R              ;-----
08DB 3C 3D                    ;-----
08DD 73 03                    ;-----
08DF E9 07C9 R              ;-----
08E1 3C 47                    ;-----
;-----
089A BF 0876 R              K32: MOV    DI,OFFSET K30 ; ALT-KEY-PAD
089D B9 000A                 MOV    CX,10 ; ALT-INPUT-TABLE
08A0 F2/ AE                  MOV    REPNE SCASB ; LOOK FOR ENTRY USING KEYPAD
08A2 75 13                    JNE    K33 ; LOOK FOR MATCH
08A4 81 EF 0877 R          MOV    DI,OFFSET K30+1 ; NO_ALT_KEYPAD
08A8 A0 0019 R              SUB    AL,ALT_INPUT ; DI NOW HAS ENTRY VALUE
08AB B4 0A                  MOV    AH,10 ; GET THE CURRENT BYTE
08AD F6 E4                    MUL    AH,10 ; MULTIPLY BY 10
08AF 03 C7                  MOV    AX,DI ; ADD IN THE LATEST ENTRY
08B1 A2 0019 R              ADD    ALT_INPUT,AL ; STORE IT AWAY
08B4 E9 07C9 R              JMP    K26 ; THROW AWAY THAT KEYSTROKE
;-----
08B7 C6 06 0019 R 00      K33: LOOK FOR SUPERSHIFT ENTRY
08B8 3C 02                    ; NO-ALT-KEYPAD
08C0 72 0C                    ; ZERO ANY PREVIOUS ENTRY INTO
08C2 3C 0E                    ; INPUT
08C4 73 08                    MOV    CX,26 ; DI,ES ALREADY POINTING
08D0 80 C4 76              REPNE SCASB ; LOOK FOR MATCH IN ALPHABET
08D3 32 C0                    JNE    K34 ; NOT FOUND, FUNCTION KEY OR OTHER
08D5 E9 0A53 R          XOR    AL,AL ; ASCII CODE OF ZERO
08DB 3C 3D                    JMP    K57 ; PUT IT IN THE BUFFER
08DD 73 03                    ;-----
08DF E9 07C9 R              ;-----
08E1 3C 47                    ;-----
;-----
089A BF 0876 R              K34: LOOK FOR TOP ROW OF ALTERNATE SHIFT
089D B9 000A                 ; ALT-TOP-ROW
08A0 F2/ AE                  ; KEY WITH '1' ON IT
08A2 75 13                    ; NOT ONE OF INTERESTING KEYS
08A4 81 EF 0877 R          ; IS IT IN THE REGION?
08A8 A0 0019 R              ; ALT-FUNCTION
08AB B4 0A                  ; CONVERT PSUEDO SCAN CODE TO
08AD F6 E4                    ; RANGE
08AF 03 C7                  ; INDICATE AS SUCH
08B1 A2 0019 R              ; BUFFER_FILL
08B4 E9 07C9 R              ; PSEUDO_SCAN_CODES
08B7 C6 06 0019 R 00      ; ALT-FUNCTION
08B8 3C 02                    ; TEST FOR IN TABLE
08C0 72 0C                    ; ALT-CONTINUE
08C2 3C 0E                    ; ALT-CONTINUE
08C4 73 08                    ; CLOSE-RETURN
08D0 80 C4 76              ; IGNORE THE KEY
08D3 32 C0                    ; ALT-CONTINUE
08D5 E9 0A53 R          ; IN KEYPAD REGION
08DB 3C 3D                    ;-----
08DD 73 03                    ;-----
08DF E9 07C9 R              ;-----
08E1 3C 47                    ;-----
;-----
089A BF 0876 R              K35: CMP    AL,59
089D B9 000A                 JAE   K37
08A0 F2/ AE                  ;-----
08A2 75 13                    ;-----
08A4 81 EF 0877 R          K36: JMP    K26
08A8 A0 0019 R              ;-----
08AB B4 0A                  ;-----
08AD F6 E4                    ;-----
08AF 03 C7                  ;-----
08B1 A2 0019 R              ;-----
08B4 E9 07C9 R          K37: CMP    AL,71
08B7 C6 06 0019 R 00      ;-----
08B8 3C 02                    ;-----
08C0 72 0C                    ;-----
08C2 3C 0E                    ;-----
08C4 73 08                    ;-----
08D0 80 C4 76              ;-----
08D3 32 C0                    ;-----
08D5 E9 0A53 R          ;-----
08DB 3C 3D                    ;-----
08DD 73 03                    ;-----
08DF E9 07C9 R          ;-----
08E1 3C 47                    ;-----

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Appendix A.

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JAE K36 ; IF SO, IGNORE
MOV BX,OFFSET K13 ; ALT SHIFT PSEUDO SCAN TABLE
JMP K63 ; TRANSLATE THAT
NOT IN ALTERNATE SHIFT
K38: ; NOT-ALT-SHIFT
TEST KB_FLAG,CTL_SHIFT ; ARE WE IN CONTROL SHIFT?
JZ K45 ; NOT-CTL-SHIFT
;----- CONTROL SHIFT, TEST SPECIAL CHARACTERS
;----- TEST FOR BREAK AND PAUSE KEYS
CMP AL,SCROLL_KEY ; TEST FOR BREAK
JNE K41 ; NO-BREAK
MOV BX,BUFFER_HEAD ; GET CURRENT BUFFER HEAD
MOV BIOS_BREAK,80H ; TURN ON BIOS_BREAK BIT
INT 1BH ; BREAK INTERRUPT VECTOR
SUB AX,AX ; PUT OUT DUMMY CHARACTER
MOV [BX],AX ; PUT DUMMY CHAR AT BUFFER HEAD
CALL K4 ; UPDATE BUFFER POINTER
MOV BUFFER_TAIL,BX ; UPDATE TAIL
JMP K26 ; DONE WITH INTERRUPT
; NO-PAUSE
K41: ; TEST SPECIAL CASE KEY 55
;-----
CMP AL,55
JNE K42 ; NOT-KEY-55
MOV AX,114*256 ; START/STOP PRINTING SWITCH
JMP K57 ; BUFFER_FILL
;----- SET UP TO TRANSLATE CONTROL SHIFT
K42: ; NOT-KEY-55
MOV BX,OFFSET K8 ; SET UP TO TRANSLATE CTL
CMP AL,59 ; IS IT IN TABLE?
JGE K42_1
JMP K56 ; YES, GO TRANSLATE CHAR
K42_1: MOV BX,OFFSET K9 ; CTL TABLE SCAN
CMP AL,7EH ; IS NUMERIC PAD RETURN KEY
JNE K42_2 ; NO.
MOV AL,6AH ; SET 'LF' CODE
JMP K57_4
K42_2: CMP AL,6AH ; IS IT IN TABLE?
JAE K45_2
JMP K63 ; TRANSLATE_SCAN
;----- TEN KEYPAD SCAN CODE TABLE
K43 LABEL BYTE
DB 4AH,4EH,70H,71H,72H,73H
DB 74H,75H,76H,77H,78H,79H
DB 7AH,7BH,7CH,7DH,7EH
K43L EQU 8-K43
;----- TEN KEYPAD CHARACTER CODE TABLE
K44 LABEL BYTE
DB '+0.123456789*/, ',GDH
;----- NOT IN CONTROL SHIFT
K45: MOV DI,OFFSET K43 ; SCAN CODE TABLE
MOV CX,K43L ; LENGTH
REPNE SCASB ; LOOK THROUGH THE TABLE FOR A
; MATCH
JNE K45_1 ; IF NOT MATCH THEN K45_1
SUB DI,OFFSET K43+1 ; ADJUST PTR TO SCAN CODE MATCH
MOV AL,CS:[DI] ; GET CHARACTER CODE INTO AL
JMP K57 ; BUFFER_FILL
K45_1: CMP AL,71 ; TEST FOR KEYPAD REGION
JAE K48 ; HANDLE KEYPAD REGION
CMP AL,28 ; TEST FOR CR KEY
JNE K45_3
MOV AL,13 ; CR CODE
INT 78H ; KANAKAN ROUTINE
K45_2: JMP K26 ; INTERRUPT RETURN
K45_3: TEST KB_FLAG,LEFT_SHIFT+RIGHT_SHIFT
JZ K54 ; TEST FOR SHIFT STATE
;----- UPPER CASE, HANDLE SPECIAL CASES
CMP AL,15 ; BACK TAB KEY
JNE K46 ; NOT-BACK-TAB
MOV AX,15*256 ; SET PSEUDO SCAN CODE
JMP K57 ; BUFFER_FILL
K46: CMP AL,59 ; FUNCTION KEYS
JB K47 ; NOT-UPPER-FUNCTION
MOV BX,OFFSET K12 ; UPPER CASE PSEUDO SCAN CODES
JMP K63 ; TRANSLATE_SCAN
; NOT-UPPER-FUNCTION
K47: PUSH DX ;
MOV DX,AX ; STORE AX TO DX
TEST JKB_FLAG,HIRAGANA_STATE ; IS HIRAGANA STATE ?
JZ K47_1 ; NO. JMP K47_1
MOV BX,OFFSET H_Z_U_1ST ; ZENKAKU HIRAGANA UPPER SHIFT 1ST BYTE
CALL CK ; HANDLING
MOV AX,DX ; RESTORE DX TO AX
MOV BX,OFFSET H_Z_U_2ND ; ZENKAKU HIRAGANA UPPER SHIFT 2ND BYTE
CALL CK ; HANDLING
POP DX ;
JMP K26 ; INTERRUPT RETURN
K47_1: TEST JKB_FLAG,KATAKANA_STATE ; IS KATAKANA STATE ?
JZ K47_3 ; NO. JMP K47_3
TEST JKB_FLAG,ZENKAKU_STATE ; IS ZENKAKU STATE ?
JZ K47_2 ; NO. JMP K47_2
MOV BX,OFFSET K_Z_U_1ST ; ZENKAKU KATAKANA UPPER SHIFT 1ST BYTE
CALL CK ; HANDLING
MOV AX,DX ; RESTORE DX TO AX
MOV BX,OFFSET K_Z_U_2ND ; ZENKAKU KATAKANA UPPER SHIFT 2ND BYTE
CALL CK ; HANDLING
JMP K26 ; INTERRUPT RETURN

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09CB E8 0B6B R      CALL CK ; HANDLING
09CE 5A             POP DX ;
09CF E9 07C9 R      JMP K26 ; INTERRUPT RETURN
09D2
K47_2: MOV BX,OFFSET K_H_U ; HANKAKU KATAKANA UPPER SHIFT
09D5 E8 0B6B R      CALL CK ; HANDLING
09D8 5A             POP DX ;
09D9 E9 07C9 R      JMP K26 ; INTERRUPT RETURN
09DC
K47_3: POP DX
09DD BB 056D R      MOV BX,OFFSET K11 ; POINT TO UPPER CASE TABLE
09E0 EB 6D           JMP SHORT K56 ; OK, TRANSLATE THE CHAR
;-----
09E2
K48: ; KEYPAD-REGION
09E2 3C 53           CMP AL,83 ; IF SCAN CODE > 83
09E4 77 08           JA K49 ; THEN K49
09E6 2C 47           SUB AL,71 ; CONVERT ORIGIN
09E8 BB 05C8 R      MOV BX,OFFSET K15 ; BASE CASE TABLE
09EB E9 0AC9 R      JMP K64 ; CONVERT TO PSEUDO SCAN
09EE
K49: CMP AL,6AH ; IS SPECIAL KEY
09F0 75 06           JNE K50 ; NO. JMP K50
09F2 E8 0B13 R      CALL SK ; SPECIAL KEY HANDLING
09F5 E9 07C9 R      JMP K26 ; INTERRUPT RETURN
09F8
K50: JMP K26
09FA B0 20           MOV AL,' ' ;
EB 57 90         JMP K57 ;
;----- PLAIN OLD LOWER CASE
09FD
K54: ; NOT-SHIFT
09FD 3C 3B           CMP AL,59 ; TEST FOR FUNCTION KEYS
09FF 72 04           JB K55 ; NOT-LOWER-FUNCTION
0A01 32 C0           XOR AL,AL ; SCAN CODE IN AH ALREADY
0A03 EB 4E           JMP SHORT K57 ; BUFFER FILL
0A05 ; NOT-LOWER-FUNCTION
0A05 52             PUSH DX ;
0A06 8B D0           MOV DX,AX ; STORE AX TO DX
0A08 F6 06 0336 R 04 TEST JKB_FLAG,HIRAGANA_STATE ; IS HIRAGANA STATE ?
0A0D 74 12           JZ K55_1 ; NO. JMP K47_1
0A0F BB 0CD7 R      MOV BX,OFFSET H_Z_L_1ST ; ZENKAKU HIRAGANA LOWER SHIFT 1ST BYTE
0A12 E8 0B6B R      CALL CK ; HANDLING
0A15 8B C2           MOV AX,DX ; RESTORE DX TO AX
0A17 BB 0D11 R      MOV BX,OFFSET H_Z_L_2ND ; ZENKAKU HIRAGANA LOWER SHIFT 2ND BYTE
0A1A E8 0B6B R      CALL CK ; HANDLING
0A1D 5A             POP DX ;
0A1E E9 07C9 R      JMP K26 ; INTERRUPT RETURN
0A21
K55_1: TEST JKB_FLAG,KATAKANA_STATE ; IS KATAKANA STATE ?
0A26 74 23           JZ K55_3 ; NO. JMP K47_3
0A28 F6 06 0336 R 01 TEST JKB_FLAG,ZENKAKU_STATE ; IS ZENKAKU STATE ?
0A2D 74 12           JZ K55_2 ; NO. JMP K47_2
0A2F BB 0BEF R      MOV BX,OFFSET K_Z_L_1ST ; ZENKAKU KATAKANA LOWER SHIFT 1ST BYTE
0A32 E8 0B6B R      CALL CK ; HANDLING
0A35 8B C2           MOV AX,DX ; RESTORE DX TO AX
0A37 BB 0C29 R      MOV BX,OFFSET K_Z_L_2ND ; ZENKAKU KATAKANA LOWER SHIFT 2ND BYTE
0A3A E8 0B6B R      CALL CK ; HANDLING
0A3D 5A             POP DX ;
0A3E E9 07C9 R      JMP K26 ; INTERRUPT RETURN
0A41
K55_2: MOV BX,OFFSET K_H_L ; HANKAKU KATAKANA LOWER SHIFT
0A44 E8 0B6B R      CALL CK ; HANDLING
0A47 5A             POP DX ;
0A48 E9 07C9 R      JMP K26 ; INTERRUPT RETURN
0A4B
K55_3: POP DX ; LC TABLE
0A4B MOV BX,OFFSET K10 ; TRANSLATE THE CHARACTER
0A4C ; TRANSLATE THE CHARACTER
JA4F FE C8 ; CONVERT ORIGIN
0A4F 2E D7 ; CONVERT THE SCAN CODE TO ASCII
0A51
;----- PUT CHARACTER INTO BUFFER
0A53 ; BUFFER-FILL
0A53 3D 297E ; IS THIS A 'TIRDE'
0A56 75 12 ; NOT 'TIRDE'
0A58 F6 06 0336 R 01 TEST JKB_FLAG,ZENKAKU_STATE ; ZENKAKU STATE ?
0A5D 74 22           JZ K57 ; GO TO BEEP
0A5F B0 81           MOV AL,81H ; 1ST BYTE OF 'TIRDE'
0A61 CD 78           INT 78H ; KANAKAN
0A63 B0 60           MOV AL,60H ; 2ND BYTE OF 'TIRDE'
0A65 CD 78           INT 78H ; KANAKAN
0A67 E9 07C9 R      JMP K26 ; INTERRUPT RETURN
0A6A
K57_1: CMP AX,02B5CH ; IS THIS A 'REVERSE SLASH'
0A6D 75 22 ; NOT 'REVERSE SLASH'
0A6F F6 06 0336 R 01 TEST JKB_FLAG,ZENKAKU_STATE ; ZENKAKU STATE ?
0A74 74 0B           JZ K57 ; GO TO BEEP
0A76 B0 81           MOV AL,81H ; 1ST BYTE OF 'REVERSE SLASH'
0A78 CD 78           INT 78H ; KANAKAN
0A7A B0 5F           MOV AL,5FH ; 2ND BYTE OF 'REVERSE SLASH'
0A7C CD 78           INT 78H ; KANAKAN
0A7E E9 07C9 R      JMP K26 ; INTERRUPT RETURN
0A81
K57_3: PUSH BX ; DURATION OF ERROR BEEP
0A82 51             PUSH CX ; FREQUENCY OF TONE
0A83 BB 0080 ; BUFFER FULL BEEP
0A86 B9 0048 ;
0A89 E8 0000 E ;
0A8C 59             CALL KB_NOISE
0A8D 5B             POP CX ;
0A8E E9 07C9 R      JMP K26 ; INTERRUPT RETURN
0A91
K57_4: ; IS THIS AN IGNORE CHART?
0A91 3C FF           CMP AL,-1 ; YES, DO NOTHING WITH IT
0A93 74 1F           JE K59 ; LOOK FOR -1 PSEUDO SCAN
0A95 80 FC FF           CMP AH,-1

```



Appendix A.

```

0A98 74 1A
0A9A
0A9A F6 06 0017 R 00
0A9F 74 20
0AA1 F6 06 0017 R 03
0AA6 74 0F

0AAB 3C 41
0AAA 72 15
0AAC 3C 5A
0AAE 77 11
0AB0 04 20
0AB2 EB 0D
0AB4
0AB4 E9 07C9 R

0AB7
0AB7 3C 61
0AB9 72 06
0ABB 3C 7A
0ABD 77 02
0ABF 2C 20
0AC1
0AC1 E8 0DBF R
0AC4 E9 07C9 R

0AC7
0AC7 2C 3B
0AC9
0AC9 2E: D7
0ACB 8A E0
0ACD 32 C0
0ACF EB 82
0AD1

```

```

0AD1
0AD1 51
0AD2 A0 0086 R
0AD5 24 F0
0AD7 B1 04
0AD9 D2 F8
0ADB 59
0ADC C3
0ADD

```

```

0ADD
0ADD 51
0ADE B1 04
0AE0 D2 E0
0AE2 8A 0E 0086 R
0AE6 80 E1 0F
0AE9 0A C1
0AEB A2 0086 R
0AEE 59
0AEF C3
0AF0

```

```

0AF0
0AF0 BB 0080
0AF3 B9 0048
0AF6 E8 0000 E
0AF9 80 26 0017 R F0

0AFE 80 26 0018 R 0F

0B03 80 26 0088 R 1F
0B08 80 26 0337 R 00

0B0D 80 26 0338 R 0F
0B12 C3
0B13

```

```

JE K59 ; NEAR_INTERRUPT_RETURN
;----- HANDLE THE CAPS LOCK PROBLEM
K58: TEST KB_FLAG,CAPS_STATE ; BUFFER-FILL-NOTEST
JZ K61 ; SKIP IF NOT
;----- IN CAPS LOCK STATE
TEST KB_FLAG,LEFT_SHIFT+RIGHT_SHIFT ; TEST FOR SHIFT
JZ K60 ; STATE
; IF NOT SHIFT, CONVERT LOWER TO
; UPPER
;----- CONVERT ANY UPPER CASE TO LOWER CASE
CMP AL,'A' ; FIND OUT IF ALPHABETIC
JB K61 ; NOT_CAPS_STATE
CMP AL,'Z' ; NOT_CAPS_STATE
JA K61 ; NOT_CAPS_STATE
ADD AL,'a'-'A' ; CONVERT TO LOWER CASE
JMP SHORT K61 ; NOT_CAPS_STATE
K59: JMP K26 ; INTERRUPT_RETURN
;----- CONVERT ANY LOWER CASE TO UPPER CASE
K60: CMP AL,'a' ; LOWER-TO-UPPER
JB K61 ; FIND OUT IF ALPHABETIC
CMP AL,'z' ; NOT_CAPS_STATE
JA K61 ; NOT_CAPS_STATE
SUB AL,'a'-'A' ; CONVERT TO UPPER CASE
; NOT_CAPS_STATE
K61: CALL ZEN_AH ; TRANSLATE A/N TO ZENKAKU
JMP K26 ; INTERRUPT_RETURN
;----- TRANSLATE SCAN FOR PSEUDO SCAN CODES
K63: SUB AL,59 ; TRANSLATE-SCAN
; CONVERT ORIGIN TO FUNCTION KEYS
K64: XLAT CS:K9 ; TRANSLATE-SCAN-ORGD
MOV AH,AL ; CTL TABLE SCAN
XOR AL,AL ; PUT VALUE INTO AH
JMP K57 ; ZERO ASCII CODE
; PUT IT INTO THE BUFFER
KB_INT ENDP
;-----
;GET_POS
; THIS ROUTINE WILL SHIFT THE VALUE STORED IN THE HIGH NIBBLE
; OF THE VARIABLE VAR_DELAY TO THE LOW NIBBLE.
;INPUT NONE. IT IS ASSUMED THAT DS POINTS AT THE BIOS DATA AREA
;OUTPUT AL CONTAINS THE SHIFTED VALUE.
;-----
GET_POS PROC NEAR
PUSH CX ; SAVE SHIFT REGISTER
MOV AL,BYTE PTR VAR_DELAY ; GET STORAGE LOCATION
AND AL,0F0H ; MASK OFF LOW NIBBLE
MOV CL,4 ; SHIFT OF FOUR BIT POSITIONS
SAR AL,CL ; SHIFT THE VALUE SIGH EXTENDED
POP CX ; RESTORE THE VALUE
RET
GET_POS ENDP
;-----
;PUT_POS
; THIS ROUTINE WILL TAKE THE VALUE IN LOW ORDER NIBBLE IN
; AL AND STORE IT IN THE HIGH ORDER OF VAR_DELAY
;INPUT AL CONTAINS THE VALUE FOR STORAGE
;OUTPUT NONE.
;-----
PUT_POS PROC NEAR
PUSH CX ; SAVE REGISTER
MOV CL,4 ; SHIFT COUNT
SHL AL,CL ; PUT IN HIGH ORDER NIBBLE
MOV CL,BYTE PTR VAR_DELAY ; GET DATA BYTE
AND CL,0FH ; CLEAR OLD VALUE IN HIGH NIBBLE
OR AL,CL ; COMBINE HIGH AND LOW NIBBLES
MOV BYTE PTR VAR_DELAY,AL ; PUT IN POSITION
POP CX ; RESTORE REGISTER
RET
PUT_POS ENDP
;-----
;ERROR_BEEP
; THIS ROUTINE WILL ERROR BEEP
;INPUT NONE.
;OUTPUT NONE.
; NOTE:
; THIS ROUTINE DESTROY BX AND CX.
; THIS ROUTINE CALL KB_NOISE.
;-----
ERROR_BEEP PROC NEAR
MOV BX,80H ; DURATION OF ERROR BEEP
MOV CX,48H ; FREQUENCY OF TONE
CALL KB_NOISE ; BUFFER FULL BEEP
AND KB_FLAG,0F0H ; CLEAR ALT,CLRL,LEFT AND RIGHT
; SHIFTS
AND KB_FLAG_1,0FH ; CLEAR POTENTIAL BREAK OF INS,CAPS
; ,NUM AND SCROLL SHIFT
AND KB_FLAG_2,1FH ; CLEAR FUNCTION STATES
AND JKB_FLAG_1,00H ; CLEAR HANKAKU,ZENKAKU,HIRAGAMA,KATAKANA AND
; ALPHA SHIFT
AND JKB_FLAG_2,0FH ; CLEAR KANJI,KNUMBER,MUHEN AND HENKAN SHIFT
RET
ERROR_BEEP ENDP
;-----
; SPACIAL CASE KEY (06AH) HANDLING

```



```

0B13
0B13 F6 06 0336 R 05
0B18 75 24
0B1A F6 06 0017 R 03
0B1F 74 0D
0B21 F6 06 0336 R 02
0B26 74 02
0B2A EB 13
0B2A
0B2A B0 7E
0B2C EB 0D
0B2E
0B2E F6 06 0336 R 02
0B33 74 04
0B35 B0 80
0B37 EB 02
0B39
0B39 B0 5C
0B3B
0B3B CD 78
0B3D
0B3D C3
0B3E
0B3E 52
0B3F F6 06 0017 R 03
0B44 74 0C
0B46 F6 06 0336 R 06
0B4B 75 1C
0B4D BA 8150
0B50 EB 0F
0B52
0B52 F6 06 0336 R 06
0B57 75 05
0B59 BA 818F
0B5C EB 03
0B5E
0B5E BA 815B
0B61
0B61 8A C6
0B63 CD 78
0B65 8A C2
0B67 CD 78
0B69
0B69 5A
0B6A C3
0B6B

```

```

;
;
; MNOTEH
; THIS KEY IS 'JIS' UNIQUE KEY ('ASCII' NOT INCLUDE).
;-----
SK PROC HEAR
TEST JKB_FLAG,ZENKAKU_CHAR ; ZENKAKU CHARACTER ?
JNZ SK6 ; YES, ZENKAKU STATE
TEST KB_FLAG,RIGHT_SHIFT+LEFT_SHIFT ; UPPER CASE?
JZ SK2 ; NOT UPPER CASE
TEST JKB_FLAG,KATAKANA_STATE ; KATAKANA STATE ?
JZ SK1 ; NOT KATAKAMA STATE
JMP SHORT SK5 ; GO TO RETURN

SK1: MOV AL,07EH ; 'OVERSCORE'
JMP SHORT SK4 ; CALL KAHAKAH

SK2: TEST JKB_FLAG,KATAKANA_STATE ; KATAKANA STATE ?
JZ SK3 ; NOT KATAKANA STATE
MOV AL,0B0H ; 'PROLONGED SOUND'
JMP SHORT SK4 ; CALL KAHAKAH

SK3: MOV AL,05CH ; 'YEN SIGH'

SK4: INT 78H ; KAHAKAH ROUTINE

SK5: RET

SK6: PUSH DX ;
TEST KB_FLAG,RIGHT_SHIFT+LEFT_SHIFT ; UPPER CASE ?
JZ SK7 ; NOT UPPER CASE
TEST JKB_FLAG,NOT ALPHA_STATE ; ALPHA STATE
JNZ SK10 ; NOT ALPHA STATE
MOV DX,0B150H ; 'OVERSCORE'
JMP SHORT SK9 ; CALL KAHAKAH

SK7: TEST JKB_FLAG,NOT ALPHA_STATE ; ALPHA STATE
JNZ SK8 ; NOT ALPHA STATE
MOV DX,0B18FH ; 'YEN SIGH'
JMP SHORT SK9 ; CALL KAHAKAH

SK8: MOV DX,0B15BH ; 'PROLONGED SOUND'

SK9: MOV AL,DH ;
INT 78H ; KAHAKAH ROUTINE
MOV AL,DL ;
INT 78H ; KAHAKAH ROUTINE

SK10: POP DX ;
RET ;
ENDP
SK
PAGE
;
;
;-----

```

JAPANESE CHARACTER SET HANDLING

```

INPUT
AH = SCAN CODE
AL = OFFSET FROM TABLE'S FIRST BYTE + 1
BX = OFFSET OF TABLE'S FIST BYTE

OUTPUT
NONE
;-----

```

```

0B6B
0B6B FE C8
0B6D 2E D7
0B6F 3C FF
0B71 74 07
0B73 80 FC FF
0B76 74 02
0B78 CD 78
0B7A
0B7A C3
0B7B
0B7B
0B7B
0B7B
0B7B 1B C7 CC B1 B3 B4
      B5 D4 D5 D6 DC CE
0B89 CD 08
      09 C0 C3 B2 BD B6
      DD C5 C6 D7 BE DE
0B97 DF FF
      FF C1 C4 BC CA B7
      B8 CF C9 D8 DA B9
0BA4 D1
      FF DB C2 BB BF CB
      BA D0 D3 C8 D9 D2
0BB2 FF 2A
      FF 20 FF
0BB5
0BB5 1B FF FF A7 A9 AA
      AB AC AD AE A6 FF
0BC3 FF 08
      09 FF FF A8 FF FF
      FF FF FF FF FF FF
0BD1 A2 FF FF
      FF FF FF FF FF FF
      FF FF FF FF FF FF
0BDE A3
      FF FF AF FF FF FF
      FF FF FF A4 A1 A5
0BEC FF 20 FF

```

```

CK PROC HEAR
DEC AL ;
XLAT CS,K11 ;
CMP AL,-1 ;
JE CK1 ;
CMP AH,-1 ;
JE CK1 ;
INT 78H ;

CK1: RET ;
ENDP
CK
K_H_L LABEL BYTE ; KATAKANA HANKAKU LOWER CASE
DB 1BH,0C7H,0CCH,0B1H,0B3H,0B4H,0B5H,0D4H,0D5H,0D6H,0DCH,0CEH,0CDH,0BH
DB 09H,0C0H,0C3H,0B2H,0BDH,0B6H,0DDH,0C5H,0C6H,0D7H,0BEH,0DEH,0DFH,-1
DB -1,0C1H,0C4H,0BCH,0CAH,0B7H,0B8H,0CFH,0C9H,0D8H,0DAH,0B9H,0D1H
DB -1,0DBH,0C2H,0BBH,0BFH,0CBH,0BAH,0D0H,0D3H,0C8H,0D9H,0D2H,-1,0ZAH
DB -1,20H,-1
;
K_H_U LABEL BYTE ; KATAKANA HANKAKU UPPER CASE
DB 1BH,-1,-1,0A7H,0A9H,0AAH,0ABH,0ACH,0ADH,0AEH,0AGH,-1,-1,0BH
DB 09H,-1,-1,0ABH,-1,-1,-1,-1,-1,-1,-1,-1,0A2H,-1
DB -1,-1,-1,-1,-1,-1,-1,-1,-1,-1,-1,-1,0A3H
DB -1,-1,0AFH,-1,-1,-1,-1,-1,-1,0A4H,0A1H,0A5H,-1,-1
DB -1,20H,-1

```



```

0D82 FF 81 FF
0D85 FF FF FF 9F A3 A5
0D85 A7 E1 E3 E5 F0 92
58 FF
0D93 FF FF FF A1 FF FF
FF FF FF FF 77 FF
75 FF
0DA1 FF FF FF FF FF FF
FF FF FF FF 78 96
76
0DAE FF FF C1 FF FF FF
FF FF FF 41 42 45
FF FF
0DBC FF 40 FF

0DBF
0DBF 52
0DC0 8B D0
0DC2 F6 06 0336 R 81
0DC7 74 1E
0DC9 3C 20
0DCB 7C 1A
0DCD 3C 7E
0DCF 7F 16
0DD1 2C 20
0DD3 BB 0DEB R
0DD6 2E: D7

0DD3 CD 78
0DDA 8B C2
0DDC 2C 20
0DDE BB 0E4A R
0DE1 2E: D7

0DE3 CD 78
0DE5 5A
0DE6 C3
0DE7
0DE7 CD 78
0DE9 5A
0DEA C3
0DEB

0DEB 81 81 81 81 81 81
0DEB 81 81
0DF3 81 81 81 81 81 81
81 81
0DFB 82 82 82 82 82 82
82 82
0E03 82 82 81 81 81 81
81 81
0E0B 81 82 82 82 82 82
82 82
0E13 82 82 82 82 82 82
82 82
0E1B 82 82 82 82 82 82
82 82
0E23 82 82 82 81 81 81
81 81
0E2B 81 82 82 82 82 82
82 82
0E33 82 82 82 82 82 82
82 82
0E3B 82 82 82 82 82 82
82 82
0E43 82 82 82 81 81 81
81

0E4A 40 49 8D 94 90 93
0E4A 95 4C
0E52 69 6A 96 7B 43 7C
44 5E
0E5A 4F 50 51 52 53 54
55 56
0E62 57 58 46 47 83 81
84 48
0E6A 97 60 61 62 63 64
65 66
0E72 67 68 69 6A 6B 6C
6D 6E
0E7A 6F 70 71 72 73 74
75 76
0E82 77 78 79 6D 8F 6E
4F 51
0E8A 4D 81 82 83 84 85
86 87
0E92 88 89 8A 8B 8C 8D
8E 8F
0E9A 90 91 92 93 94 95
96 97
0EA2 98 99 9A 6F 62 70
50

```

```

DB -1,81H,-1
;
H_Z_U_2ND LABEL BYTE ; HIRAGANA ZENKAKU UPPER 2ND BYTE
DB -1,-1,-1,9FH,0A3H,0A5H,0A7H,0E1H,0E3H,0E5H,0F0H,92H,58H,-1
;
DB -1,-1,-1,0A1H,-1,-1,-1,-1,-1,-1,77H,-1,75H,-1
;
DB -1,-1,-1,-1,-1,-1,-1,-1,-1,-1,78H,96H,76H
;
DB -1,-1,0C1H,-1,-1,-1,-1,-1,-1,-1,41H,42H,45H,-1,-1
;
DB -1,40H,-1
;
;-----
;
; ZEN_AN
;
; TRANSLATE A/M TO ZENKAKU A/M
; INPUT AX
; AH = SCAN CODE
; AL = 1BYTE JIS CODE (20H-7EH)
;
; OUTPUT NONE
;-----
;
ZEN_AN PROC NEAR
;
; PUSH DX ; SAVE AX IN DX
; MOV DX,AX
; TEST JKB_FLAG,ZENKAKU_STATE
; JZ H_KAKU ; IF NOT ZENKAKU THEN H_KAKU
; CMP AL,20H ; IF AL<20H THEN H_KAKU
; JL H_KAKU
; CMP AL,7EH ; IF AL>7EH THEN H_KAKU
; JG H_KAKU
; SUB AL,20H ; CONVERT ORIGIN
; MOV BX,OFFSET Z_ALPHA1
; XLAT CS:Z_ALPHA1 ; CONVERT THE SCAN CODE TO 1ST BYTE OF
; ; CHARACTER CODE
; ; KANAKAH ROUTINE
; INT 78H
; MOV AX,DX ; RESTORE AX
; SUB AL,20H ; CONVERT ORIGIN
; MOV BX,OFFSET Z_ALPHA2
; XLAT CS:Z_ALPHA2 ; CONVERT THE SCAN CODE TO 2ND BYTE OF
; ; CHARACTER CODE
; ; KANAKAH ROUTINE
; INT 78H
; POP DX
; RET
;
H_KAKU:
; INT 78H
; POP DX
; RET
;
ZEN_AN ENDP
;
; Z_ALPHA1 LABEL BYTE ; ZENKAKU ALPHA 1ST BYTE
; DB 81H,81H,81H,81H,81H,81H,81H,81H
; DB 81H,81H,81H,81H,81H,81H,81H,81H
; DB 82H,82H,82H,82H,82H,82H,82H,82H
; DB 82H,82H,81H,81H,81H,81H,81H,81H
; DB 81H,82H,82H,82H,82H,82H,82H,82H
; DB 82H,82H,82H,82H,82H,82H,82H,82H
; DB 82H,82H,82H,82H,82H,82H,82H,82H
; DB 82H,82H,82H,81H,81H,81H,81H,81H
; DB 81H,82H,82H,82H,82H,82H,82H,82H
; DB 82H,82H,82H,82H,82H,82H,82H,82H
; DB 82H,82H,82H,81H,81H,81H,81H,81H
; DB 82H,82H,82H,82H,82H,82H,82H,82H
; DB 82H,82H,82H,82H,82H,82H,82H,82H
; DB 82H,82H,82H,81H,81H,81H,81H,81H
;
; Z_ALPHA2 LABEL BYTE ; ZENKAKU ALPHA 2ND BYTE
; DB 40H,49H,8DH,94H,90H,93H,95H,4CH
; DB 69H,6AH,96H,7BH,43H,7CH,44H,5EH
; DB 4FH,50H,51H,52H,53H,54H,55H,56H
; DB 57H,58H,46H,47H,83H,81H,84H,48H
; DB 97H,60H,61H,62H,63H,64H,65H,66H
; DB 67H,68H,69H,6AH,6BH,6CH,6DH,6EH
; DB 6FH,70H,71H,72H,73H,74H,75H,76H
; DB 77H,78H,79H,6DH,8FH,6EH,4FH,51H
; DB 4DH,81H,82H,83H,84H,85H,86H,87H
; DB 88H,89H,8AH,8BH,8CH,8DH,8EH,8FH
; DB 90H,91H,92H,93H,94H,95H,96H,97H
; DB 98H,99H,9AH,6FH,62H,70H,50H

```





**Appendix A.**

*[The following table content is extremely faint and largely illegible. It appears to be a multi-column table with various numerical and text entries.]*

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```

XXXXXXXXXXXX
XXXXXXXXXXXX
M           M
M  MODULE 5 M
M           M
XXXXXXXXXXXX
XXXXXXXXXXXX

```

```

----- INT 5 -----
THIS LOGIC WILL BE INVOKED BY INTERRUPT 05H TO PRINT THE
SCREEN.
50:0   THE STATUS OF THE PRINT SCREEN.
      = 0 : PRINT SCREEN ISN'T CALLED.
      = 1 : PRINT SCREEN IS IN PROGRESS
      =-1 : ERROR ENCOUNTED DURING PRINTING
-----

```

```

-----
VIDEO MODE TABLE
-----
BYTE 1  7 , 6 : 01 CHARACTER / 10 GRAPHIC
        5 - 0 : COLOR TABLE OFFSET
-----
BYTE 2  7 - 0 : VERTICAL SCREEN SIZE(SIZE/8 WHEN GRAPHIC)
-----
BYTE 3  7 - 0 : HORIZONTAL SCREEN SIZE
-----

```

```

0000
0000 40 19 28
0003 40 19 28
0006 40 19 50
0009 40 19 50
000C 84 19 28
000F 84 19 28
0012 80 19 50
0015 00 00 00
0018 8C 19 14
001B 8C 19 28
001E 84 19 50
0021 00 00 00
0024 00 00 00
0027 00 00 00
002A 00 00 00
002D 00 00 00
0030 40 0B 28
0033 40 0B 28
0036 40 0B 50
0039 40 0B 50
003C 84 19 28
003F 84 19 28
0042 80 19 50
0045 00 00 00
0048 8C 19 14
004B 8C 19 28
004E 84 19 50
0051 AC 19 50
0054 00
0055 00
0056 F8
0057 F8
0058 00
0059 00
005A 80
005B 80
005C 70
005D 70
005E F8
005F F8
0060 00
0061 00
0062 80
0063 00
0064 60
0065 00
0066 80
0067 10
0068 98
0069 00
006A 60
006B 10
006C 78
006D 00
006E 98
006F 10
0070 60
0071 18
0072 98
0073 60
0074 78
0075 80
0076 98
0077 E0
0078 78
0079 C0
007A 78
007B E0
007C 78
007D F0
007E F8
007F F8
0080 00
0081 00
0082 80
0083 80
0084 20
0085 20
0086 C0

```

```

TABLE PROC  NEAR
MODETBL DB 040H,019H,028H ; C (40,25) ;NATIVE
DB 040H,019H,028H ; C (40,25)
DB 040H,019H,050H ; C (80,25)
DB 040H,019H,050H ; C (80,25)
DB 084H,019H,028H ; G (320,200)
DB 084H,019H,028H ; G (320,200)
DB 080H,019H,050H ; G (640,200)
DB 000H,000H,000H ; NOT VALID
DB 08CH,019H,014H ; G (160,200)
DB 08CH,019H,028H ; G (320,200)
DB 084H,019H,050H ; G (640,200)
DB 000H,000H,000H ; NOT VALID
DB 000H,000H,000H ; NOT VALID
DB 000H,000H,000H ; NOT VALID
DB 000H,000H,000H ; NOT VALID
DB 040H,00BH,028H ; C (40,11)
DB 040H,00BH,028H ; C (40,11)
DB 040H,00BH,050H ; C (80,11)
DB 040H,00BH,050H ; C (80,11)
DB 084H,019H,028H ; G (320,200)
DB 084H,019H,028H ; G (320,200)
DB 080H,019H,050H ; G (640,200)
DB 000H,000H,000H ; NOT VALID
DB 08CH,019H,014H ; G (160,200)
DB 08CH,019H,028H ; G (320,200)
DB 084H,019H,050H ; G (640,200)
DB 0ACH,019H,050H ; G (640,200)
DOTTBL DB 000H ; 2 COLOR
DB 000H
DB 0F8H
DB 0F8H
DB 000H ; 4 COLOR
DB 000H
DB 080H
DB 080H
DB 070H
DB 070H
DB 0F8H
DB 0F8H
DB 000H ; 16 COLOR
DB 000H
DB 080H
DB 000H
DB 060H
DB 000H
DB 080H
DB 010H
DB 098H
DB 000H
DB 060H
DB 010H
DB 078H
DB 000H
DB 098H
DB 010H
DB 060H
DB 078H
DB 080H
DB 098H
DB 0E0H
DB 078H
DB 0C0H
DB 078H
DB 0E0H
DB 078H
DB 0F0H
DB 0F8H
DB 0F8H
DB 000H ; SUPER 16 COLOR
DB 000H
DB 080H
DB 080H
DB 020H
DB 020H
DB 0C0H

```

Appendix A.

0087 C0  
 0088 28  
 0089 28  
 008A E0  
 008B E0  
 008C 58  
 008D 58  
 008E F8  
 008F F8  
 0090 00  
 0091 00  
 0092 80  
 0093 80  
 0094 20  
 0095 20  
 0096 C0  
 0097 C0  
 0098 28  
 0099 28  
 009A E0  
 009B E0  
 009C 58  
 009D 58  
 009E F8  
 009F F8  
 00A0

DB 0C0H  
 DB 028H  
 DB 028H  
 DB 0E0H  
 DB 0E0H  
 DB 058H  
 DB 058H  
 DB 0F8H  
 DB 0F8H  
 DB 000H  
 DB 000H  
 DB 080H  
 DB 080H  
 DB 020H  
 DB 020H  
 DB 0C0H  
 DB 0C0H  
 DB 028H  
 DB 028H  
 DB 0E0H  
 DB 0E0H  
 DB 058H  
 DB 058H  
 DB 0F8H  
 DB 0F8H

```

TABLE ENDP
;-----;
; MAIN ROUTINE
;-----;
PRINT_SCREEN PROC FAR
    PUSH DS ;SAVE REGS.
    PUSH ES
    PUSH AX
    PUSH BX
    PUSH CX
    PUSH DX
    PUSH SI
    PUSH DI
    MOV AX,DSEGM ;SET DS
    MOV DS,AX
    MOV AX,XXDATA ;SET ES
    MOV ES,AX
    CMP STATUS17,1 ;IS INT17 IN PROGRESS ?
    JE RETURN
    CMP STATUS_BYTE,1 ;IS INT5 IN PROGRESS ?
    JE RETURN
    MOV STATUS_BYTE,1
    CALL INIT ;INITIAL PROCESS
    MOV SPSAVES,SP
    CALL CRLF
    MOV AH,CPI ;SAVE CPI,LPI
    MOV AL,LPI
    PUSH AX
    MOV AH,15 ;GET DISPLAY STATUS AND PAGE
    INT 10H
    MOV PAGENO,BH
    XOR AH,AH ;MODE CHECK
    MOV DI,AX
    ADD DI,AX
    ADD DI,AX
    MOV AL,MODETABL[DI] ;SET VIDEO MODE
    MOV VIDEO_MODE,AL
    CMP VIDEO_MODE,0
    JE ABEND
    JG CHAR ;CHARACTER
    JMP GRAPH ;GRAPHIC
EXIT: MOV SI,XXDATA ;SET NORMAL STATUS
    MOV ES,SI
    INC STATUS_BYTE
    POP BX ;RESTORE CPI,LPI
ABEND: CALL CPILPI ;RESTORE SP
    MOV SP,SPSAVES
    MOV SI,XXDATA ;SET ERROR STATUS
    MOV ES,SI
    SUB STATUS_BYTE,2 ;POST ROUTINE
RETURN: CALL POST
    POP DI
    POP SI
    POP DX
    POP CX
    POP BX
    POP AX
    POP ES
    POP DS
    IRET
PRINT_SCREEN ENDP
;-----;
; INT 17 CALL ROUTINE
;-----;
INT17 PROC NEAR
    PUSH DX
    XOR DX,DX
    INT 17H ;INT 17
    TEST AH,08H ;ERROR ?
    JNZ ABEND
    POP DX
    RET
INT17 ENDP
;-----;
; CHARACTER HARD COPY
;-----;
CHAR PROC NEAR
    MOV BX,7814H ;6 CPI , 6 LPI SET
    
```

00A0 1E  
 00A1 06  
 00A2 50  
 00A3 53  
 00A4 51  
 00A5 52  
 00A6 56  
 00A7 57  
 00A8 B8 ---- R  
 00AB 8E DB  
 00AD B8 ---- R  
 00B0 8E C0  
 00B2 80 3E 001E R 01  
 00B7 74 64  
 00B9 26: 80 3E 0000 R 01  
 00BF 74 5C  
 00C1 26: C6 06 0000 R 01  
 00C7 E8 0230 R  
 00CA 89 26 0069 R  
 00CE E8 033F R  
 00D1 8A 26 0003 R  
 00D5 A0 0004 R  
 00D8 50  
 00D9 B4 0F  
 00DB CD 10  
 00DD 88 3E 0068 R  
 00E1 32 E4  
 00E3 88 F8  
 00E5 03 F8  
 00E7 03 F8  
 00E9 2E: 8A 85 0000 R  
 00EE A2 006D R  
 00F1 80 3E 006D R 00  
 00F6 74 13  
 00F8 7F 38  
 00FA E9 01A8 R  
 00FD BE ---- R  
 0100 8E C6  
 0102 26: FE 06 0000 R  
 0107 58  
 0108 E8 0308 R  
 0108 88 26 0069 R  
 010F BE ---- R  
 0112 8E C6  
 0114 26: 80 2E 0000 R 02  
 011A E8 0242 R  
 011D 5F  
 011E 5E  
 011F 5A  
 0120 59  
 0121 58  
 0122 58  
 0123 07  
 0124 1F  
 0125 CF  
 0126

0126  
 0126 52  
 0127 33 D2  
 0129 CD 17  
 012B F6 C4 08  
 012E 75 DB  
 0130 5A  
 0131 C3  
 0132

0132  
 0132 B8 7814



```

0135 E8 0308 R          CALL      CPIPLI
0138 2E: 8A AD 0001 R   MOV       CH,MODETBL+1[DI]
013D 2E: 8A 8D 0002 R   MOV       CL,MODETBL+2[DI]
0142 32 F6              XOR       DH,DH          ;SET DISPLAY SIZE TO CX
0144 32 D2              XOR       DL,DL          ;0 -> V.POS
0146 33 F6              XOR       SI,SI          ;0 -> H.POS
0148 B4 02              MOV       AH,2          ;0 -> BUF.POS
014A 8A 3E 0068 R      MOV       BH,PAGENO     ;MOVE CURSOR
014E CD 10              INT       10H
0150 B4 08              MOV       AH,8
0152 8A 3E 0068 R      MOV       BH,PAGENO     ;READ CHARACTER & ATTRIBUTE
0156 CD 10              INT       10H
0158 3C 1C              CMP       AL,1CH
015A 77 22              JA        FORMAT        ;FORMAT CHARACTER ?
015C 3C 19              CMP       AL,19H
015E 77 1C              JA        CNTRL         ;
0160 3C 18              CMP       AL,18H
0162 77 1A              JA        FORMAT        ;
0164 3C 17              CMP       AL,17H
0166 77 14              JA        CNTRL         ;
0168 3C 14              CMP       AL,14H
016A 77 12              JA        FORMAT        ;
016C 3C 10              CMP       AL,10H
016E 77 0C              JA        CNTRL         ;
0170 3C 0F              CMP       AL,0FH
0172 77 0A              JA        FORMAT        ;
0174 3C 06              CMP       AL,06H
0176 77 04              JA        CNTRL         ;
0178 3C 00              CMP       AL,00H
017A 77 02              JA        FORMAT        ;
017C B0 20              MOV       AL,' '
017E 88 84 03D4 R      CNTRL:   MOV       CODE_INT5[SI],AL
0182 C6 84 0474 R 00   FORMAT:  MOV       ATTR_INT5[SI],0
0187 46                INC       SI
0188 FE C2              INC       DL
018A 3A D1              CMP       DL,CL
018C 75 BA              JNE      CHAR10
018E 51                PUSH     CX
018F B4 0B              MOV       AH,11
0191 8D 1E 03D4 R      MOV       BX,CODE_INT5
0195 8D 3E 0474 R      LEA      DI,ATTR_INT5
0199 8B CE              MOV       CX,SI
019B E8 0126 R          CALL     INT17
019E 59                POP      CX
019F FE C6              INC       DH
01A1 3A F5              CMP       DH,CH
01A3 75 9F              JNE      CHAR00
01A5 E9 00FD R          JMP      CHAR00
01A8

```

```

CHAR      ENDP
-----
; GRAPHIC HARD COPY
-----

```

```

01A8 BB 7810          GRAPH  PROC      NEAR
01AB E8 0308 R        MOV       BX,7810H
01AE B8 0C05        CALL     CPIPLI          ;6 CPI , 7.5 LPI SET
01B1 B7 00          MOV       AX,0C05H
01B3 E8 0126 R      MOV       BH,0          ;SET UNIDIRECTION
01B6 B8 001B        CALL     INT17
01B9 E8 0126 R      MOV       AX,001BH
01BC B8 0028        CALL     INT17          ;SET 3 BYTE TRANSMISSION
01BF E8 0126 R      MOV       AX,0028H
01C2 E8 025A R      CALL     INT17
01C5 33 C9          CALL     SIZESET
01C7 E8 0317 R      XOR       CX,CX
01CA 8B 16 0062 R   GR00:   CALL     ESCX1
01CE 4A              MOV       DX,VSIZE
01CF 8B 36 006E R   GR10:   DEC       DX
01D3 33 FF          XOR       SI,SLICE
01D5 51              MOV       DI,DI
01D6 E8 02BE R      GR20:   CALL     DOTSET
01D9 41              INC       CX
01DA 2B 36 0064 R   SUB      SI,HRATIO
01DE 75 F6          JNZ      GR20          ;MOVE POSITION RIGHT
01E0 8B 0E 0066 R   MOV       CX,VRATIO
01E4 51              GR40:   PUSH     CX
01E5 33 FF          XOR       DI,DI
01E7 8B 0E 0000 R   MOV       CX,PRINTER_ID
01EB 41              INC       CX
01EC 51              GR50:   XOR       CX,CX
01ED B9 0008          MOV       CX,8
01F0 8A 85 0080 R   GR60:   MOV       AL,DOT[DI]
01F4 D1 E0          SHL      AX,1
01F6 88 85 0080 R   MOV       DI,DOT[DI],AL
01FA 47              INC       DI
01FB E2 F3          LOOP    GR60
01FD 59              POP      CX
01FE 8A C4          MOV       AL,AH
0200 32 E4          XOR       AH,AH          ;PRINTING
0202 E8 0126 R      CALL     INT17
0205 E2 E5          LOOP    GR50
0207 59              POP      CX
0208 E2 DA          LOOP    GR40
020A 59              POP      CX
020B 4A              DEC       DX
020C 83 FA 00        CMP      DX,0
020F 7D BE          JNL      GR10
0211 E8 033F R      CALL     CRLF
0214 A1 006E R      MOV       AX,SLICE
0217 33 D2          XOR       DX,DX
0219 F7 36 0064 R   DIV     HRATIO
021D 03 C8          ADD      CX,AX
021F 3B 0E 0060 R   CMP      CX,HSIZE

```

Appendix A.

```

0223 7C A2          JL      GR00
0225 88 0C05      MOV     AX,0C05H          ;SET BIDIRECTION
0226 87 01        MOV     BH,1
022A E8 0126 R    CALL   INT17
022D E9 00FD R    JMP    EXIT
0230              GRAPH ENDP
;-----;
; INITIAL PROCESS
;-----;
0230 5E           INIT  PROC   NEAR
0231 1E           POP    SI                ;SAVE RETURN ADDRESS
0232 07           PUSH   DS                ;RESET ES
0233 A0 0026 R    POP    ES
0236 50           MOV    AL,LF_CT          ;SAVE LINE FEED COUNT
0237 B4 03       PUSH   AX
0239 8A 3E 0068 R MOV    AH,3             ;SAVE CURSOR POSITION
023D CD 10       MOV    BH,PAGENO
023F 52           INT    10H
0240 56           PUSH   DX
0241 C3           RET                    ;RESTORE RETURN ADDRESS
0242              INIT  ENDP
;-----;
; POST PROCESS
;-----;
0242 5E           POST  PROC   NEAR
0243 5A           POP    SI                ;SAVE RETURN ADDRESS
0244 80 3E 006D R 00  CMP    VIDEO_MODE,0    ;RESTORE CURSOR POSITION
0249 7C 08       JL     POST10
024B B4 02       MOV    AH,2
024D 8A 3E 0068 R MOV    BH,PAGENO
0251 CD 10       INT    10H
0253 58           POST10: POP    BX           ;RESTORE LINE FEED COUNT
0254 88 1E 0026 R MOV    LF_CT,BX
0258 56           PUSH   SI
0259 C3           RET
;-----;
; DOT SIZE SET
;-----;
025A 32 E4       SIZESET PROC   NEAR
025A 32 E4       XOR    AH,AH            ;SET VERTICAL DOT SIZE
025C 2E: 8A 85 0001 R MOV    AL,MODETBL+1[DI]
0261 81 03       MOV    CL,3
0263 D3 E0       SHL   AX,CL
0265 A3 0062 R   MOV    VSIZE,AX
0268 32 E4       XOR    AH,AH            ;SET HORIZONTAL DOT SIZE
026A 2E: 8A 85 0002 R MOV    AL,MODETBL+2[DI]
026F D3 E0       SHL   AX,CL
0271 A3 0060 R   MOV    HSIZE,AX
0274 C7 06 0066 R 0002 MOV    VRATIO,2        ;SET ENLARGE RATIO
027A 81 3E 0062 R 00C8 CMP    VSIZE,200
0280 7F 06       JG    VRATIO,5
0282 C7 06 0066 R 0005 MOV    VSIZE,5
0288 A1 0000 R   SIZE00: MOV    AX,PRINTER_ID
028B 81 3E 0060 R 0168 CMP    HSIZE,360
0291 7F 0C       JG    SIZE10
0293 D0 E0       SHL   AL,1
0295 81 3E 0060 R 00B4 CMP    HSIZE,180
029B 7F 02       JG    SIZE10
029D D0 E0       SHL   AL,1
029F A3 0064 R   SIZE10: MOV    HVRATIO,AX
02A2 2E: 8A 85 0000 R MOV    AL,MODETBL+0[DI] ;SET COLOR TABLE OFFSET
02A7 A3 006B R   MOV    COLORTB,AX
02AA 81 26 006B R 003F AND    COLORTB,003FH
02B0 8B 0E 0000 R MOV    CX,PRINTER_ID
02B4 80 08       MOV    AL,8
02B6 FE C1      INC    CL
02B8 F6 E1      MUL   CL
02BA A3 006E R   MOV    SLICE,AX
02BD C3           RET
02BE              SIZESET ENDP
;-----;
; DOT STORE
;-----;
02BE 51          DOTSET PROC   NEAR
02BF 52          PUSH   CX
02C0 B4 0D      PUSH   DX
02C2 CD 10      MOV    AH,13          ;READ DOT
02C4 3B 0E 0060 R 00 CMP    INT 10H
02C8 7C 02      CMP    CX,HSIZE
02CA 80 00      JL     DOT00
02CC 25 000F    MOV    AL,0
02CF 8B D8      AND    AX,000FH      ;SET VERTICAL MASK
02D1 D1 E3      MOV    BX,AX
02D3 03 1E 006B R 00 ADD    BX,COLORTB
02D7 2E: 8A 87 0054 R 00 MOV    DH,DOTTBL+0[BX]
02DC 2E: 8A 97 0055 R 00 MOV    DL,DOTTBL+1[BX]
02E1 8B 1E 0064 R 00 MOV    BX,RATIO      ;SET HORIZONTAL RATIO
02E5 D1 EB      SHR   BX,1
02E7 8B CB      MOV    CX,BX
02E9 0B DB      OR    BX,BX
02EB 75 04      JNZ   DOT20
02ED 8B 0E 0000 R 00 MOV    CX,PRINTER_ID
02F1 8B B5 0080 R 00 MOV    DOT[DI],DH
02F5 47          DOT20: MOV    DI
02F6 E2 F9      INC   DOT20
02F8 8B CB      MOV    CX,BX
02FA 0B DB      OR    BX,BX
02FC 74 07      JZ    DOT19          ;640 MODE ?

```

```

02FE 88 95 0080 R      DOT30: MOV     DOT[DI],DL
0302 47                INC     DI
0303 E2 F9            LOOP   DOT30
0305 5A                DOT99: POP     DX
0306 59                POP     CX
0307 C3                RET
0308                DOTSET ENDP
;-----;
; CPI LPI SET ROUTINE (BH : CPI , BL : LPI)
;-----;
0308                CPILPI PROC    NEAR
0308 B8 0C01           MOV     AX,0C01H           ;CPI SET
0308 E8 0126 R        CALL   INT17
030E 8A FB            MOV     BH,BL              ;LPI SET
0310 B8 0C02           MOV     AX,0C02H
0313 E8 0126 R        CALL   INT17
0316 C3                RET
0317                CPILPI ENDP
;-----;
; ESC + x + 1 + M1 + M2
;-----;
0317                ESCX1  PROC    NEAR
0317 B8 001B           MOV     AX,001BH           ;ESC
031A E8 0126 R        CALL   INT17
031D B8 0025           MOV     AX,0025H           ;X
0320 E8 0126 R        CALL   INT17
0323 B8 0031           MOV     AX,0031H           ;1
0326 E8 0126 R        CALL   INT17
0329 A1 0062 R        MOV     AX,VSIZE           ;M1M2 = VSIZE X VRATIO
032C F7 26 0066 R    MUL     VRATIO
0330 50                PUSH   AX
0331 8A C4            MOV     AL,AH
0333 32 E4            XOR     AH,AH
0335 E8 0126 R        CALL   INT17
0338 58                POP     AX
0339 32 E4            XOR     AH,AH
033B E8 0126 R        CALL   INT17
033E C3                RET
033F                ESCX1  ENDP
;-----;
; CR. LF.
;-----;
033F                CRLF  PROC    NEAR
033F B8 000D           MOV     AX,000DH           ;CR
0342 E8 0126 R        CALL   INT17
0345 83 3E 0000 R 01  CMP     PRINTER_ID,1       ;PT-1 ?
034A 75 07            JNE    CR10                ;NO
034C B8 000A           MOV     AX,000AH           ;LF (FOR PT-1)
034F E8 0126 R        CALL   INT17
0352 C3                RET
0353 B8 041B           MOV     AX,041BH           ;VERTICAL FEED (FOR PT-2)
0356 E8 0126 R        CALL   INT17
0359 B8 0425           MOV     AX,0425H
035C E8 0126 R        CALL   INT17
035F B8 0435           MOV     AX,0435H
0362 E8 0126 R        CALL   INT17
0365 B8 0400           MOV     AX,0400H
0368 E8 0126 R        CALL   INT17
036B B8 0410           MOV     AX,0410H
036E E8 0126 R        CALL   INT17
0371 C3                RET
0372                CRLF  ENDP
03C0                ORG     BEGIN+03C0H
03C0                CODE  ENDS
03C0                END

```



```

;          4-6 : DASHED IMAGE
;          7   : UNDERSCORE IMAGE
;          8   : FEED VALUE
;-----;
0000 1E
0001 FF FF F8
0004 F8 3F F0
0007 80
0008 07
0009 1E
000A FF FF FF
000D 0F FC 1F
0010 80
0011 07
0012 18
0013 FF F0 00
0016 FC 30 00
0019 10
001A 04
001B 18
001C FF FF FF
001F 3F FC 3F
0022 10
0023 04
0024 14
0025 FC 00 00
0028 F8 00 00
002B 02
002C 02
002D 14
002E FF FF FF
0031 0F FC 1F
0034 02
0035 02
0036 10
0037 00 00 00
003A 00 00 00
003D 01
003E 00
003F 10
0040 FF FF FF
0043 F0 FF 0F
0046 01
0047 00
V_VALUE DB 1EH ; 4 LPI (UPPER)
        DB 0FFH,0FFH,0F8H
        DB 0F8H,03FH,0F0H
        DB 080H
        DB 7
        DB 1EH ; 4 LPI (LOWER)
        DB 0FFH,0FFH,0FFH
        DB 00FH,0FCH,01FH
        DB 080H
        DB 7
        DB 18H ; 5 LPI (UPPER)
        DB 0FFH,0F0H,000H
        DB 0FCH,030H,000H
        DB 010H
        DB 4
        DB 18H ; 5 LPI (LOWER)
        DB 0FFH,0FFH,0FFH
        DB 03FH,0FCH,03FH
        DB 010H
        DB 4
        DB 14H ; 6 LPI (UPPER)
        DB 0FCH,000H,000H
        DB 0F8H,000H,000H
        DB 002H
        DB 2
        DB 14H ; 6 LPI (LOWER)
        DB 0FFH,0FFH,0FFH
        DB 00FH,0FCH,01FH
        DB 002H
        DB 2
        DB 10H ; 7.5 LPI (UPPER)
        DB 000H,000H,000H
        DB 000H,000H,000H
        DB 001H
        DB 0
        DB 10H ; 7.5 LPI (LOWER)
        DB 0FFH,0FFH,0FFH
        DB 0F0H,0FFH,00FH
        DB 001H
        DB 0

```

```

;-----;
;          HORIZONTAL GRID CONTROL VALUES
;          0 : KEY
;          1-3 : DASHED IMAGE
;          4 : HORIZONTAL SIZE
;          5 : SPACE BETWEEN CHARACTERS
;-----;

```

```

0048 90
0049 FC 0F C0
004C 12
004D 06
004E 78
004F F8 3E 00
0052 0F
0053 04
0054 6C
0055 F8 7C 00
0058 0D
0059 03
005A 60
005B F0 F0 00
005E 0C
005F 01
0060
H_VALUE DB 90H ; 10 CPI
        DB 0FCH,00FH,0C0H
        DB 18
        DB 6
        DB 78H ; 12 CPI
        DB 0F8H,03EH,000H
        DB 15
        DB 4
        DB 6CH ; 13.4 CPI
        DB 0F8H,07CH,000H
        DB 13
        DB 3
        DB 60H ; 15 CPI
        DB 0F0H,0F0H,000H
        DB 12
        DB 1

```

```

;-----;
;          TABLE ENDP
;-----;
;          MAIN ROUTINE
;-----;

```

```

0060
0061 FB
0062 1E
0063 06
0064 56
0065 57
0066 55
0067 52
0068 51
0069 53
006A 50
006B BE ---- R
006D 8E DE
006F C6 06 001E R 01
0074 89 26 0014 R
0078 0B D2
007A 75 3D
007C 83 3E 0000 R 00
0081 75 05
0083 50
0084 E8 0832 R
0087 58
0088 C6 06 0002 R 10
008D 80 0E 0002 R 80
0092 80 FC 01
0095 74 35
0097 80 FC 03
009A 74 30
009C F7 06 0000 R 0001
00A2 75 28
00A4 F7 06 0000 R 0002
00AA 74 03
PRINTER_ID PROC FAR
        STI
        PUSH DS ; INTERRUPTS BACK ON
        PUSH ES ; SAVE REGISTERS
        PUSH SI
        PUSH DI
        PUSH BP
        PUSH DX
        PUSH CX
        PUSH BX
        PUSH AX
        MOV SI,DSEGM ; SET DATA SEGMENT ADDRESS
        MOV DS,SI
        MOV STATUS17,1 ; SET PROGRESS FLAG ON
        MOV SPSAVE,SP ; SAVE STACK POINTER
        OR DX,DX ; CHECK PRINTER CLASS
        JNZ NO_USE ; IMPROPER DEVICE --RETURN--
        CMP PRINTER_ID,0 ; CHECK PRINTER-ID
        JNE 100 ; AVAILABLE
        PUSH AX ; INITIALIZE
        CALL INIT1
        POP AX
100: MOV RETURN_CODE,PR_SELECT ; SET SELECT TO RETURN CODE
     OR RETURN_CODE,PR_BUSY
     CMP AH,1 ; IF INIT IS REQUIRED,
     JE PTR1 ; JUMP TO PTR1 ROUTINE DIRECTLY.
     CMP AH,3 ; IF STATUS IS REQUIRED,
     JE PTR1 ; JUMP TO PTR1 ROUTINE DIRECTLY.
     TEST PRINTER_ID,1 ; CHECK PRINTER TYPE-1
     JNZ PTR1
     TEST PRINTER_ID,2 ; CHECK PRINTER TYPE-2
     JZ NO_DEV

```

Appendix A.

```

00AC E9 0136 R
00AF C6 06 0002 R 08
00B4 80 26 0002 R EF
00B9
00B9 58
00BA 8A 26 0002 R
00DE C6 06 001E R 00
00C3 58
00C4 59
00C5 5A
00C6 5D
00C7 5E
00C8 5E
00C9 07
00CA 1F
00CB CF

```

```

JMP PTR2
NO_DEV: MOV RETURN_CODE,PR_ERROR ; DEVICE IS INVALID
AND RETURN_CODE,0FFH-PR_SELECT
NO_USE:
RETURN: POP AX ; FUNCTION IS INVALID
MOV AH,RETURN_CODE ; SET RETURN CODE
MOV STATUS17,0 ; SET PROGRESS FLAG OFF
POP BX ; RECOVER REGISTERS
POP CX
POP DX
POP BP
POP DI
POP SI
POP ES
POP DS
IRET

```

```

;-----;
; FOLLOWING MAIN BRANCH IS PREPARED FOR PRINTER TYPE-1
;-----;

```

```

00CC 0A E4
00CE 74 32
00D0 FE CC
00D2 74 38
00D4 FE CC
00D6 74 39
00D8 FE CC
00DA 74 3C
00DC FE CC
00DE 74 3D
00E0 FE CC
00E2 74 3E
00E4 FE CC
00E6 74 18
00E8 FE CC
00EA 74 14
00EC FE CC
00EE 74 18
00F0 FE CC
00F2 74 0C
00F4 FE CC
00F6 74 08
00F8 FE CC
00FA 74 30
00FC FE CC
00FE 74 31

```

```

PTR1: OR AH,AH
JZ AH0_1 ; AH=0 PRINT A CODE IN AL
DEC AH ; AH=1 PRINTER INITIALIZATION
JZ AH1_1 ; AH=2 READ PRINTER STATUS
DEC AH ; AH=3 READ PRINTER STATUS-II
JZ AH2_1 ; AH=4 PRINT PASS THRU CODE IN AL
DEC AH ; AH=5 DOUBLE SIZE CHARACTER PRINT
JZ AH3_1 ; AH=6 IS NOT USED
DEC AHX_1 ; AH=7 IS NOT USED
JZ AHX_1 ; AH=8 IS NOT USED
DEC AH ; AH=9 IS NOT USED
JZ AHX_1 ; AH=A IS NOT USED
DEC AH ; AH=B PRINT CHARACTER IN SPECIAL BUFFER
JZ AHX_1 ; AH=C CHANGE PRINTER CONTROL PARAMETER
DEC AHC_1 ; AH=? INVALID FUNCTION IS SPECIFIED

```

```

0100 EB B7
0102 C6 06 000D R 00
0107 E8 019F R
010A EB AD
010C E8 0832 R
010F EB A8
0111 32 DB
0113 E8 0F54 R
0116 EB A1
0118 E8 0F91 R
011B EB 9C
011D E8 0F0A R
0120 EB 97
0122 C6 06 000D R 01
0127 E8 019F R
012A EB 8D
012C E8 0AB7 R
012F EB 88
0131 E8 08F8 R
0134 EB 83

```

```

;---- AH=? ( PTR1 : FUNCTION INVALID ) -----;
AHX_1: JMP NO_USE ; AH=? INVALID FUNCTION IS SPECIFIED
;---- AH=0 ( PTR1 : PRINT A CODE IN AL ) -----;
AH0_1: MOV SIZE,AH,NOR ; SET CHARACTER SIZE NORMAL
CALL CMD_CHK
JMP RETURN
;---- AH=1 ( PTR1 : PRINTER INITIALIZATION ) -----;
AH1_1: CALL INIT1
JMP RETURN
;---- AH=2 ( PTR1 : READ PRINTER STATUS INTO AH ) -----;
AH2_1: XOR BL,BL
CALL STATUS
JMP RETURN
;---- AH=3 ( PTR1 : READ PRINTER STATUS-II INTO AH ) -----;
AH3_1: CALL STATUS2
JMP RETURN
;---- AH=4 ( PTR1 : PRINT PASS THROUGH CODE IN AL ) -----;
AH4_1: CALL FIRE
JMP RETURN
;---- AH=5 ( PTR1 : DOUBLE SIZE CHARACTER PRINT ) -----;
AH5_1: MOV SIZE,AH,BAI ; SET CHARACTER SIZE DOUBLE
CALL CMD_CHK
JMP RETURN
;---- AH=B ( PTR1 : PRINT ATTRIBUTES AND CHARACTERS IN SPECIAL BUFFER )
AHB_1: CALL ATTR
JMP RETURN
;---- AH=C ( PTR1 : CHANGE PRINTER CONTROL PARAMETERS ) -----;
AHC_1: CALL CHG_PT1
JMP RETURN

```

```

;-----;
; FOLLOWING MAIN BRANCH IS PREPARED FOR PRINTER TYPE-2
;-----;

```

```

0136 0A E4
0138 74 33
013A FE CC
013C 74 35
013E FE CC
0140 74 37
0142 FE CC
0144 74 3B
0146 FE CC
0148 74 3D
014A FE CC
014C 74 3F
014E FE CC
0150 74 18
0152 FE CC
0154 74 14
0156 FE CC
0158 74 18
015A FE CC
015C 74 0C
015E FE CC
0160 74 08
0162 FE CC
0164 74 2D

```

```

PTR2: OR AH,AH
JZ AH0_2 ; AH=0 PRINT A CODE IN AL
DEC A1 ; AH=1 PRINTER INITIALIZATION
JZ AH1_2 ; AH=2 READ PRINTER STATUS
DEC AH ; AH=3 READ PRINTER STATUS-II
JZ AH2_2 ; AH=4 PRINT PASS THRU CODE IN AL
DEC AH ; AH=5 DOUBLE SIZE CHARACTER PRINT
JZ AH3_2 ; AH=6 IS NOT USED
DEC AHX_2 ; AH=7 IS NOT USED
JZ AHX_2 ; AH=8 IS NOT USED
DEC AH ; AH=9 IS NOT USED
JZ AHX_2 ; AH=A IS NOT USED
DEC AH ; AH=B PRINT ATTRIBUTES AND CHARACTERS
JZ AHB_2

```

```

0166 FE CC          DEC      AH
0168 74 2F          JZ      AHC_2
;----- AH=? ( PTR2 : FUNCTION INVALID ) ; AH=C CHANGE PRINTER CONTROL PARAMETERS
AHX_2: JMP      NO_USE
;----- AH=0 ( PTR2 : PRINT A CODE IN AL ) -----
HT 17

016D E8 07FA R     AH0_2: CALL   CHAR0
0170 E9 00B9 R     JMP     RETURN
;----- AH=1 ( PTR2 : PRINTER INITIALIZATION ) -----
0173 E8 0832 R     AH1_2: CALL   IMIT1
0176 E9 00B9 R     JMP     RETURN
;----- AH=2 ( PTR2 : READ PRINTER STATUS INTO AH ) -----
0179 32 DB          AH2_2: XOR    BL,BL
017B E8 0F54 R     CALL   STATUS
017E E9 00B9 R     JMP     RETURN
;----- AH=3 ( PTR2 : READ PRINTER STATUS-II INTO AH ) -----
0181 E8 0F91 R     AH3_2: CALL   STATUS2
0184 E9 00B9 R     JMP     RETURN
;----- AH=4 ( PTR2 : PRINT PASS THROUGH CODE IN AL ) -----
0187 E8 0F0A R     AH4_2: CALL   FIRE
018A E9 00B9 R     JMP     RETURN
;----- AH=5 ( PTR2 : DOUBLE SIZE CHARACTER PRINT ) -----
018D E8 0816 R     AH5_2: CALL   CHAR5
0190 E9 00B9 R     JMP     RETURN
;----- AH=B ( PTR2 : PRINT ATTRIBUTE AND CHARACTER IN SPECIAL BUFFER )
0193 E8 0AB7 R     AHB_2: CALL   ATTR
0196 E9 00B9 R     JMP     RETURN
;----- AH=C ( PTR2 : CHANGE PRINTER CONTROL PARAMETERS ) -----
0199 E8 0979 R     AHC_2: CALL   CHG_PT2
019C E9 00B9 R     JMP     RETURN
019F                PRINTER_IO ENDP
;-- CMD_CHK
; CHECKS A CODE JUST RECEIVED, AND RECOGNIZES IT AS A COMMAND OR
; A DATA.
;-----
CHD_CHK PROC      NEAR
TEST  FLG1,TWO_BYTE_FLG ; AL IS SECOND BYTE OF TWO BYTES CODE ?
JNZ  A51
TEST  FLG1,ESC_FLG     ; ESC SEQUENCE IS IN PROGRESS ?
JZ    A21
CALL  ESC_COD         ; JUMP TO ESC SEQ ANALYSE ROUTINE
JMP  ARET
A21:  CMP  AL,1BH      ; "ESC" ?
JNE  A4
OR   FLG1,ESC_FLG    ; SET "ESC" RECEIVED INDICATOR
MOV  CODEN,1         ; SET CODE COUNTER = 1
JMP  ARET
A4:   CMP  AL,80H     ; ++++++
JBE  A41             ; +
CMP  AL,9FH         ; + CHECK LEGALITY OF 1ST CODE OF
JBE  A5              ; +
CMP  AL,0DFH        ; + TWO BYTES CODE
JBE  A41             ; +
CMP  AL,0FCH        ; +
JBE  A5              ; +
A41:  CALL SINGLE    ; ++++++
JMP  ARET           ; OTHERS ARE SINGLE CODES
A5:   OR   FLG1,TWO_BYTE_FLG ; SET TWO BYTES CODE INDICATOR
A51:  AND  FLG1,0FFH-TWO_BYTE_FLG ; RESET TWO BYTES CODE INDICATOR
A6:   PUSH AX
CALL  MOD_C1        ; FORCE INTO CHARACTER MODE
OR   PRINT_MODE,EVEN_PR_FLG ; SET CHARACTER MODE
POP  AX
CALL  CHR_BUF       ; CODE IS STORED IN BUFFER
ARET: RET
CHD_CHK ENDP
;-- ESC COD
; CHECKS CODE COUNTER OF ESC-SEQUENCE, AND TRANSFERS CONTROL TO
; ANALYSE ROUTINE ACCORDING TO COUNTER VALUE.
;-----
01F6 ESC_COD PROC      NEAR
01F6 83 3E 0020 R 01  CMP  CODEN,1 ; + IN CASE OF CONTINUOUS "ESC" S +
01FB 75 04          JNE  BS1     ; + ARE RECEIVED, ONLY FIRST +
01FD 3C 1B          CMP  AL,1BH ; CHECK AL IS DUMMY "ESC"
01FF 74 56          JZ   BRET   ; IGNORE
0201 FF 06 0020 R   INC  CODEN ; CODE COUNT INCREMENT
0205 83 3E 0020 R 02  CMP  CODEN,2 ; CODE COUNT = 2 ?
020A 75 06          JNE  BS2
020C E8 0258 R     CALL  B100
020F EB 46 90       JMP  BRET
0212 83 3E 0020 R 03  CMP  CODEN,3 ; CODE COUNT = 3 ?
0217 75 06          JNE  BS3
0219 E8 02B7 R     CALL  B200
021C EB 39 90       JMP  BRET
021F 83 3E 0020 R 04  CMP  CODEN,4 ; CODE COUNT = 4 ?
0224 75 06          JNE  BS4
0226 E8 033B R     CALL  B300
0229 EB 2C 90       JMP  BRET
022C 83 3E 0020 R 05  CMP  CODEN,5 ; CODE COUNT = 5 ?
0231 75 06          JNE  BS5
0233 E8 036B R     CALL  B400
0236 EB 1F 90       JMP  BRET
0239 F6 06 002D R 01  TEST  FLG2,X1_FLG ; ESC X 1 IS IN PROGRESS ?
023E 74 06          JZ   B01
0240 E8 057C R     CALL  DX1
0243 EB 12 90       JMP  BRET
0246 F6 06 002D R 02  TEST  FLG2,X2_FLG ; ESC X 2 IS IN PROGRESS ?
024B 74 06          JZ

```

Appendix A.

```

024D EB 05D5 R          CALL DX2
0250 EB 05 90          JMP BRET
0253 EB 01 90          ER_RTNB: NOP
0254 90                BRET: RET
0257 C3                ESC_COD ENDP
0258

;--- B100 ---
; ANALYSES SECOND CODE OF ESC SEQUENCE.
;-----
B100 PROC NEAR
B1:  CMP AL,25H          ; CHECK AL="PERCENT"
     JNE B11
     OR  FLG1,X_FLG    ; SET "X" RECEIVED INDICATOR
     JMP B1RET
B11:  CMP AL,5BH          ; CHECK AL="POUND"
     JNE B12
     MOV SIZE_ESC,BAI  ; SET CHAR. SIZE TO DOUBLE
     AND FLG1,OFFH-ESC_FLG ; RESET "ESC" INDICATOR
     JMP B1RET
B12:  CMP AL,5DH          ; CHECK AL="VERTICAL BAR"
     JNE B13
     MOV SIZE_ESC,NOR  ; SET CHAR. SIZE TO NORMAL
     AND FLG1,OFFH-ESC_FLG ; RESET "ESC" INDICATOR
     JMP B1RET
B13:  CMP AL,46H          ; CHECK AL="F"
     JNE B14
     OR  FLG1,F_FLG    ; SET "F" INDICATOR
     JMP B1RET
B14:  CMP AL,28H          ; CHECK AL="("
     JE  B15
     CMP AL,29H          ; CHECK AL=")"
     JE  B15
     CMP AL,4FH          ; CHECK AL="O"
     JE  B15
     CMP AL,50H          ; CHECK AL="P"
     JE  B15
     CMP AL,53H          ; CHECK AL="S"
     JE  B15
     CMP AL,56H          ; CHECK AL="V"
     JE  B15
     JMP ER_B1
B15:  AND FLG1,OFFH-ESC_FLG ; RESET "ESC" RECEIVED INDICATOR
     JMP B1RET
ER_B1: NOP
B1RET: RET
B100 ENDP

;--- B200 ---
; ANALYSES THIRD CODE OF ESC SEQUENCE.
;-----
B200 PROC NEAR
B2:  TEST FLG1,X_FLG    ; "X" RECEIVED ?
     JNZ B201
     TEST FLG1,F_FLG    ; "F" RECEIVED ?
     JNZ B21
     JMP ER_B2
B21:  MOV N1,AL          ; AL IS N1 VALUE ( IF "F" RCVD )
     JMP B2RET
B201: CMP AL,31H          ; AL IS "1" ?
     JE  B202
     CMP AL,32H          ; AL IS "2" ?
     JE  B203
     CMP AL,33H          ; AL IS "3" ?
     JE  B204
     CMP AL,35H          ; AL IS "5" ?
     JE  B205
     CMP AL,36H          ; AL IS "6" ?
     JE  B206
     CMP AL,39H          ; AL IS "9" ?
     JE  B207
     CMP AL,34H          ; AL IS "4" ?
     JE  B208
     CMP AL,38H          ; AL IS "8" ?
     JE  B209
     CMP AL,42H          ; AL IS "B" ?
     JE  B209
     CMP AL,55H          ; AL IS "U" ?
     JE  B209
     JMP ER_B2
B202: OR  FLG2,X1_FLG    ; OTHERS ARE ILLEGAL
     JMP B2RET
B203: OR  FLG2,X2_FLG    ; SET "X2" INDICATOR
     JMP B2RET
B204: OR  FLG2,X3_FLG    ; SET "X3" INDICATOR
     JMP B2RET
B205: OR  FLG2,X5_FLG    ; SET "X5" INDICATOR
     JMP B2RET
B206: OR  FLG2,X6_FLG    ; SET "X6" INDICATOR
     JMP B2RET
B207: OR  FLG2,X9_FLG    ; SET "X9" INDICATOR
     JMP B2RET
B208: OR  FLG2,IGN_FLG   ; SET COMMAND IGNORE INDICATOR
     JMP B2RET
B209: AND FLG1,OFFH-X_FLG-ESC_FLG ; RESET "X" & "ESC" INDICATOR
     JMP B2RET
ER_B2: NOP
B2RET: RET
B200 ENDP

;--- B300 ---
; ANALYSES FOURTH CODE OF ESC SEQUENCE.
;-----
B300 PROC NEAR
B3:  TEST FLG2,7FH      ; X1,2,3,5,6,9 MODE IN PROGRESS ?

```



```

0340 74 06
0342 A2 0022 R
0345 EB 23 90
0348 F6 06 002C R 04
034D 74 10
034F A2 0023 R
0352 8A 26 0022 R
0356 A3 0024 R
0359 EB 07C0 R
035C EB 0C 90
035F F6 10 002D R 40
0364 75 04
0366 EB 01 90
0369 90
036A C3
036B

```

```

JZ B31
MOV N1,AL ; AL IS N1 VALUE
JMP B3RET
B31: TEST FLG1,F_FLG ; "F" MODE IN PROGRESS ?
JZ B32
MOV N2,AL ; AL IS N2 VALUE
MOV AH,N1
MOV N1M2,AX ; N1M2 VALUE GEN
CALL F_EM ; CALL ESC-F EMULATION ROUTINE
JMP B3RET
B32: TEST FLG2,IGN_FLG ; IGNORE INDICATOR ON ?
JNZ B3RET ; RETURN
JMP ER_B3
ER_B3: NOP
B3RET: RET
B300 ENDP

```

```

;--B400
;-----
; ANALYSES FIFTH CODE OF ESC SEQUENCE.
;-----
;

```

```

036B F6 06 002D R 40
0368 74 0D
0370
0372 80 26 002C R FC
0377 80 26 002D R BF
037C EB 5A 90
037F A2 0023 R
0382 8A 26 0022 R
0386 A3 0024 R
0389 F6 06 002D R 01
038E 74 06
0390 EB 0494 R
0393 EB 43 90
0396 F6 06 002D R 02
0398 74 06
039D EB 0494 R
03A0 EB 36 90
03A3 F6 06 002D R 04
03A8 74 06
03AA EB 052F R
03AD EB 29 90
03B0 F6 06 002D R 08
03B5 74 06
03B7 EB 0738 R
03BA EB 1C 90
03BD F6 06 002D R 10
03C2 74 06
03C4 EB 06EF R
03C7 EB 0F 90
03CA F6 06 002D R 20
03CF 74 06
03D1 EB 078E R
03D4 EB 02 90
03D7 90
03D8 C3
03D9

```

```

B400 PROC NEAR
B4: TEST FLG2,IGN_FLG ; IGNORE INDICATOR ON ?
JZ B41
AND FLG1,0FFH-ESC_FLG-X_FLG ; RESET "ESC" & "X" INDICATOR
AND FLG2,0FFH-IGN_FLG ; RESET IGNORE INDICATOR
JMP B4RET
B41: MOV N2,AL ; AL IS N2
MOV AH,N1
MOV N1M2,AX ; N1M2 VALUE GEN
TEST FLG2,X1_FLG ; "X1" IS IN PROGRESS ?
JZ B42
CALL X1X2_EM ; CALL X1 X2 EMULATION ROUTINE
JMP B4RET
B42: TEST FLG2,X2_FLG ; "X2" IS IN PROGRESS ?
JZ B43
CALL X1X2_EM ; CALL X1 X2 EMULATION ROUTINE
JMP B4RET
B43: TEST FLG2,X3_FLG ; "X3" IS IN PROGRESS ?
JZ B44
CALL X3_EM ; CALL X3 EMULATION ROUTINE
JMP B4RET
B44: TEST FLG2,X5_FLG ; "X5" IS IN PROGRESS ?
JZ B45
CALL X5_EM ; CALL X5 EMULATION ROUTINE
JMP B4RET
B45: TEST FLG2,X6_FLG ; "X6" IS IN PROGRESS ?
JZ B46
CALL X6_EM ; CALL X6 EMULATION ROUTINE
JMP B4RET
B46: TEST FLG2,X9_FLG ; "X9" IS IN PROGRESS ?
JZ ER_B4
CALL X9_EM ; CALL X9 EMULATION ROUTINE
JMP B4RET
ER_B4: NOP
B4RET: RET
B400 ENDP

```

```

;-- SINGLE
;-----
; ANALYSES ONE BYTE CONTROL CODE WHICH INCLUDES
; CANCEL, CARRIAGE RETURN, LINE FEED, FORM FEED, SPACE, DC1 OR DC3.
;-----
;

```

```

03D9
03D9 3C 18
03DB 74 1F
03DD 3C 0A
03DF 74 21
03E1 3C 0C
03E3 74 3B
03E5 3C 1C
03E7 74 56
03E9 3C 0D
03EB 74 65
03ED 3C 08
03EF 74 61
03F1 3C 11
03F3 74 5D
03F5 3C 13
03F7 74 59
03F9 EB 4A 90
03FC EB 08E1 R
03FF EB 51 90
0402 EB 047D R
0405 51
0406 33 C9
0408 0A 0E 0026 R
040C 75 02
040E B1 01
0410 B0 0A
0412 EB 0F0A R
0415 E2 F9
0417 59
0418 C6 06 0009 R 00
041D EB 33 90
0420 EB 047D R
0423 B0 1B
0425 EB 0F0A R
0428 B0 32
042A EB 0F0A R
042D B0 0C
042F EB 0F0A R

```

```

SINGLE PROC NEAR
CMP AL,18H ; "CAN" ?
JE C1
CMP AL,0AH ; "LF" ?
JE C3
CMP AL,0CH ; "FF" ?
JE C4
CMP AL,1CH ; "FS" ?
JE C5
CMP AL,0DH ; "CR" ?
JE CRET
CMP AL,08H ; "BS" ?
JE CRET
CMP AL,11H ; "DC1" ?
JE CRET
CMP AL,13H ; "DC3" ?
JE CRET
JMP C6 ; JUMP TO ONE BYTE CHR CODE
;+++++
C1: CALL RESET CANCEL
JMP CRET
;+++++
C3: CALL PRINT2 LINE FEED
PUSH CX
XOR CX,CX ; CLEAR CX
OR CL,LF_CT ; CL=0 ?
JNZ C31
MOV CL,1
C31: MOV AL,0AH ; "LF"
CALL FIRE
LOOP C31
C32: POP CX
MOV PRINT_MODE,0 ; RESET PRINT MODE
JMP CRET
;+++++
C4: CALL PRINT2 FORM FEED
MOV AL,18H ; THIS SEQUENCE KEEPS CORRECT
CALL FIRE ; PAGE LENGTH FEEDITH BY
MOV AL,32H ; LINE FEED MOVEMENT OF
CALL FIRE ; ONE-SIXTH-INCH
MOV AL,0CH ; "FF"
CALL FIRE

```



```

04F6          MOD_C1 PROC    NEAR
04F6 F6 06 0009 R 02    TEST PRINT_MODE,LOW_PR_FLG ; GRAPHIC MODE ?
04FB 74 17              JZ    C1END
04FD E8 0699 R          CALL LOW_PRT
0500 80 26 0009 R FD   AND  PRINT_MODE,OFFH-LOW_PR_FLG ; CALL LOW PART PRINT
0505 80 0D              MOV  AL,ODH ; RESET GRAPHIC MODE
0507 E8 0F0A R          CALL FIRE ; FORCE CR + LF OUT TO PRINTER
050A 80 0A              MOV  AL,DAH
050C E8 0F0A R          CALL FIRE
050F 80 0A              MOV  AL,DAH
0511 E8 0F0A R          CALL FIRE
0514 C3                  RET
0515          C1END: RET
MOD_C1 ENDP
;----- MOD_C2 -----
; CHECKS CURRENT PRINT MODE, AND IF CHARCATER MODE IS ACTIVE,
; FORCES CHARACTER DATA OUT TO PRINTER AND
; FORCES INTO GRAPHIC MODE.
;-----
0515          MOD_C2 PROC    NEAR
0515 F6 06 0009 R 01    TEST PRINT_MODE,EVEN_PR_FLG ; CHARACTER MODE ?
051A 74 12              JZ    C2END
051C E8 0AA7 R          CALL PRINT ; CALL CHAR. PRINT ROUTINE
051F 80 26 0009 R FE   AND  PRINT_MODE,OFFH-EVEN_PR_FLG ; RESET CHARACTER MODE
0524 80 0D              MOV  AL,ODH ; FORCE CR + LF OUT TO PRINTER
0526 E8 0F0A R          CALL FIRE
0529 80 0A              MOV  AL,DAH
052B E8 0F0A R          CALL FIRE
052E C3                  RET
052F          C2END: RET
MOD_C2 ENDP
;----- X3_EM -----
; EMULATES ESC-X3 ( HORIZONTAL SKIP ), AND
; CONVERTS ESC-X3 TO ESC-L + NULL DATA .
;-----
052F          X3_EM PROC    NEAR
052F 8B 0E 0024 R      MOV  CX,H1N2 ; GET H1N2 VALUE
0533 83 F9 00          CMP  CX,0 ; CHECK ZERO
0536 76 39              JBE  N33
0538 81 F9 0460        CMP  CX,MAX ; CHECK < 1128
053C 77 33              JA   N33
053E 51                PUSH CX
053F E8 0515 R          CALL MOD_C2 ; FORCE INTO GRAPHIC MODE
0542 59                POP  CX
0543 A1 0010 R          MOV  AX,CSP ; GET CURRENT SLICE POSITION
0546 50                PUSH AX
0547 5E                POP  SI
0548 03 C1            ADD  AX,CX ; ADJUST MAX SLICE POSITION
054A 3D 0460          CMP  AX,MAX
054D 77 17              JA   N22
054F 80 0E 0009 R 02   OR   PRINT_MODE,LOW_PR_FLG ; INTO GRAPHIC MODE
0554 A3 0010 R          MOV  CSP,AX
0557 8B C1            MOV  AX,CX
0559 E8 0E88 R          CALL ESCL ; CALL ESC-L OUT
055C B0 00              MOV  AL,0 ; ZERO OUT TO PRINTER
055E E8 0F0A R          CALL FIRE ; FOR MAKING BLANK
0561 E2 F9              LOOP N11
0563 EB 0C 90          JMP  N33
0566 E8 0699 R          CALL LOW_PRT ; CALL LOW PART GRAPHIC PRINT
0569 B0 0D              MOV  AL,ODH ; FORCE "CR" OUT
056B E8 0F0A R          CALL FIRE
056E EB 01 90          JMP  N33
0571 80 26 002C R FC   AND  FLG1,OFFH-ESC_FLG-X_FLG ; RESET "ESC" & "X" INDICATOR
0574 80 26 002D R FB   AND  FLG2,OFFH-X3_FLG ; RESET "X3" INDICATOR
057B C3                  RET
057C          X3_EM ENDP
;----- DX1 -----
; CONTROLS ESC-X1 ( IMAGE TRANSMISSION ) DATA STREAM.
; IF DATA COUNT IS ODD, THE DATA IS OUT TO PRINTER,
; IF DATA COUNT IS EVEN, THE DATA IS STORED IN IMAGE BUFFER FOR LOW
; PART PRINTING.
;-----
057C          DX1 PROC    NEAR
057C 8B 0E 0020 R      MOV  CX,CODEN ; GET CODE COUNT
0580 83 E9 05          SUB  CX,5 ; CALCULATE CORRECT DATA COUNT
0583 F7 C1 0001        TEST CX,01H ; CX IS ODD NUMBER ?
0587 74 28              JZ    F2 ; IF EVEN, GO TO F1
0589 F6 06 002E R 10   TEST FLG3,SL_FUL_FLG ; CHECK SLICE COUNT EXCEEDS MAX
058E 75 44              JNZ  F4
0590 FF 06 0010 R      INC  CSP ; UPDATE CSP
0594 81 3E 0010 R 0460 CMP  CSP,MAX ; CHECK MAX POSITION
059A 77 06              JA   F1
059C E8 0F0A R          CALL FIRE ; AL (DATA) IS OUT
059F EB 33 90          JMP  F4
05A2 F6 06 002E R 10 F1: TEST FLG3,SL_FUL_FLG ; CHECK SLICE COUNT EXCEEDS MAX
05A7 75 2B              JNZ  F4
05A9 80 0E 002E R 10 OR   FLG3,SL_FUL_FLG ; IF EXCEEDS, SET SLICE FULL IND.
05AE EB 24 90          JMP  F4
05B1 F6 06 002E R 10 F2: TEST FLG3,SL_FUL_FLG ; IF SLICE POSITION EXCEEDS MAX.
05B6 75 0A              JNZ  F3 ; IGNORE FURTHER DATA
05B8 33 F6              XOR  SI,SI ; CLEAR SI
05BA 03 36 0010 R      ADD  SI,CSP ; GET CSP TO SI
05BE 08 84 00A0 R      OR   IMAGE_BUFFER[SI],AL ; DATA IS STORED FOR SECONDARY PRINT
05C2 D1 E9              SHR  CX,1 ; DIVIDE BY 2
05C4 3B 0E 0024 R      CMP  CX,H1N2 ; REACHED H1N2 COUNT ?
05C8 75 0A              JNE  F4
05CA 80 26 002C R FC   AND  FLG1,OFFH-ESC_FLG-X_FLG ; RESET "ESC" & "X" INDICATOR

```

Appendix A.

05CF 80 26 002D R FE  
 05D4 C3  
 05D5

```

F4:      AND      FLG2,0FFH-X1_FLG      ; RESET "X1" FLAG
DX1:    RET
        ENDP
;----- DX2 -----
; CONTROLS ESC-X2 (IMAGE TRANSMISSION AND ENLARGE) DATA STREAM.
; IF DATA COUNT IS ODD, THE DATA IS OUT TO PRINTER TWICE,
; IF DATA COUNT IS EVEN, THE DATA IS STORED TWICE IN IMAGE BUFFER
; FOR LOW PART PRINTING.
;-----

```

05D5  
 05D5 8B 0E 0020 R  
 05D9 83 E9 05  
 05DC F7 C1 0001  
 05E0 74 3A  
 05E2 F6 06 002E R 10  
 05E7 75 75  
 05E9 FF 06 0010 R  
 05ED 81 3E 0010 R 0460  
 05F3 77 5D  
 05F5 50  
 05F6 E8 0F0A R  
 05F9 58  
 05FA FF 06 0010 R  
 05FE 81 3E 0010 R 0460  
 0604 77 0A  
 0606 E8 0F0A R  
 0609 FF 0E 0010 R  
 060D EB 4F 90  
 0610 80 0E 002E R 20  
 0615 FF 0E 0010 R  
 0619 EB 43 90  
 061C F6 06 002E R 10  
 0621 75 1A  
 0623 33 F6  
 0625 03 36 0010 R  
 0629 08 84 00A0 R  
 062D F6 06 002E R 20  
 0632 75 1E  
 0634 46  
 0635 08 84 00A0 R  
 0639 89 36 0010 R  
 063D D1 E9  
 063F 3B 0E 0024 R  
 0643 75 19  
 0645 80 26 002C R FC  
 064A 80 26 002D R FD  
 064F EB 0D 90  
 0652 F6 06 002E R 10  
 0657 75 05  
 0659 80 0E 002E R 10  
 065E C3  
 065F

```

DX2:    PROC      NEAR
        MOV      CX,CODEN      ; GET CODE COUNT TO CX
        SUB      CX,5          ; CALCULATE CORRECT DATA COUNT
        TEST     CX,01H       ; CX IS ODD NUMBER ?
        JZ       G2
        TEST     FLG3,SL_FUL_FLG ; CHECK SLICE COUNT EXCEEDS MAX
        JNZ     G5
        INC      CSP          ; UPDATE CSP
        CMP      CSP,MAX      ; CHECK MAX POSITION
        JA       G4
        PUSH     AX           ; SAVE N1N2
        CALL    FIRE
        POP      AX          ; GET N1N2
        INC      CSP          ; UPDATE CSP
        CMP      CSP,MAX      ; CHECK MAX POSITION
        JA       G1
        CALL    FIRE
        DEC      CSP          ; DECREMENT CSP FOR ADJUSTING
        JMP     G5           ; LOW PART DATA COUNT
G1:     OR       FLG3,BAI_FUL_FLG ; SET SLICE FULL INDICATOR
        DEC      CSP          ; DECREMENT CSP FOR ADJUSTING
        JMP     G5           ; LOW PART DATA COUNT
G2:     TEST     FLG3,SL_FUL_FLG ; IF SLICE POSITION EXCEEDS MAX.
        JNZ     G3          ; IGNORE FURTHER DATA
        XOR      SI,SI       ; CLEAR SI
        ADD     SI,CSP        ; GET CSP TO SI
        OR      IMAGE_BUFFER[SI],AL ; DATA IS STORE IN IMAGE BUFFER
        TEST     FLG3,BAI_FUL_FLG ; CHECK SLICE FULL INDICATOR
        JNZ     G4
        INC      SI          ; UPDATE SLICE POSITION
        OR      IMAGE_BUFFER[SI],AL
        MOV      CSP,SI      ; CURRENT SLICE POSITION STORE
        SHR     CX,1         ; DIVIDE BY 2
        CMP     CX,N1N2     ; REACHED N1N2 COUNT ?
        JHE     G5
        AND     FLG1,0FFH-ESC_FLG-X_FLG ; RESET "ESC" & "X" INDICATOR
        AND     FLG2,0FFH-X2_FLG ; RESET "X2" INDICATOR
        JMP     G5
G4:     TEST     FLG3,SL_FUL_FLG ; CHECK SLICE FULL INDICATOR
        JNZ     G5
        OR      FLG3,SL_FUL_FLG ; SET SLICE FULL INDICATOR
G5:     RET
DX2:    ENDP
;----- FS_EM -----
; EMULATES FIXED DATA IMAGE TRANSMISSION DATA STREAM.
; TRANSMITTED DATA COUNT IS STORED IN FS_N.
;
; THIS COMMAND USES MOST RECENT ESC-X1 OR ESC-X2 CONTROL VALUES.
;-----

```

065F  
 065F 83 3E 0029 R 00  
 0664 74 32  
 0666 E8 0515 R  
 0669 80 0E 0009 R 02  
 066E 80 0E 002C R 03  
 0673 80 26 002E R EF  
 0678 A1 0029 R  
 067B C7 06 0020 R 0005  
 0681 E8 0E88 R  
 0684 F6 06 0028 R 02  
 0689 74 08  
 068B 80 0E 002D R 02  
 0690 EB 06 90  
 0693 80 0E 002D R 01  
 0698 C3  
 0699

```

FS_EM:  PROC      NEAR
        CMP      FS_N,0      ; CHECK TX COUNT
        JZ       H2          ; IF ZERO, GO TO END
        CALL    MOD_C2      ; ALREADY IMAGE MODE ?
        OR      PRINT_MODE,LOW_PR_FLG ; INTO IMAGE MODE
        OR      FLG1,ESC_FLG+X_FLG ; ESC X ON
        AND     FLG3,0FFH-SL_FUL_FLG ; RESET SLICE FULL INDICATOR
        MOV     AX,FS_N      ; TX COUNT INTO AX
        MOV     CODEN,5      ; ADJUST CODE COUNTER
        CALL    ESCL        ; ESC L AND M1 M2 OUT
        TEST    IM_MOD,02H   ; NOW ESC X 2 ?
        JZ       H1         ; IF NOT THEN ESC X1
        OR      FLG2,X2_FLG ; ESC X2 FLG ON
        JMP     H2
H1:     OR      FLG2,X1_FLG ; ESC X1 FLG ON
H2:     RET
FS_EM:  ENDP
;----- LOW_PRT -----
; PRINTS LOW PART OF 8-BIT GRAPHIC IMAGE IN IMAGE BUFFER
;-----

```

0699  
 0699 80 1B  
 069B E8 0F0A R  
 069E 80 30  
 06A0 E8 0F0A R  
 06A3 80 0D  
 06A5 E8 0F0A R  
 06A8 80 0A  
 06AA E8 0F0A R  
 06AD A1 0010 R  
 06B0 3B 06 0012 R  
 06B4 77 03  
 06B6 A1 0012 R  
 06B9 3D 0460  
 06BC 76 03  
 06BE B8 0460  
 06C1 50  
 06C2 E8 0E88 R  
 06C5 59  
 06C6 33 F6  
 06C8 46  
 06C9 8A 84 00A0 R

```

LOW_PRT PROC      NEAR
        MOV      AL,1BH      ; THIS SEQUENCE SETS LINE FEED
        CALL    FIRE        ; PITCH FOR ORDINARY USING
        MOV     AL,30H      ; VALUE OF ONE-NINTH-INCH.
        CALL    FIRE
        MOV     AL,0DH      ; CR
        CALL    FIRE
        MOV     AL,0AH      ; LF
        CALL    FIRE
        MOV     AX,CSP      ; GET CSP TO AX
        CMP     AX,CSPMAX
        JA     E1
        MOV     AX,CSPMAX
        CMP     AX,MAX      ; CHECK CSP EXCEEDS MAX. ?
        JBE     E2
        MOV     AX,MAX      ; IF EXCEEDS, AX = 1120
        PUSH    AX
        CALL    ESCL        ; CALL ESC-L OUT ROUTINE
        POP     CX
        XOR     SI,SI       ; CLEAR SI
        INC     SI          ; ADJUST SI
        MOV     AL,IMAGE_BUFFER[SI] ; GET PRINT DATA FROM BUFFER
E1:     OR      AX,MAX
        JBE     E2
E2:     CALL    ESCL
        POP     CX
        XOR     SI,SI
        INC     SI
E3:     MOV     AL,IMAGE_BUFFER[SI]

```

```

06CD E8 0F0A R          CALL FIRE
06D0 C6 84 00A0 R 00   MOV IMAGE_BUFFER[S1],0
06D5 F6 C4 08          TEST AH,PR_ERROR ; CLEAR IMAGE BUFFER
06D8 75 03            JNZ E4 ; ERROR HAS OCCURED ?
06DA 46              INC S1
06DB E2 EC            LOOP E3
06DD 80 26 0009 R FD   AND PRINT_MODE,0FFH-LOW_PR_FLG ; RESET GRAPHIC MODE IND.
06E2 C7 06 0010 R 0000 MOV CSP,0 ; CLEAR CSP
06E8 C7 06 0012 R 0000 MOV CSPMAX,0 ; CLEAR CSPMAX
06EE C3              RET
06EF                LOW_PRT ENDP
;--- X6_EM ---
; EMULATES ESC-X6 (CR TO SPECIFIED DOT POSITION).
;-----
X6_EM PROC NEAR
MOV CX,N1N2 ; GET N1N2 VALUE TO CX
CMP CX,0 ; CHECK ZERO
JBE J3
CMP CX,MAX ; CHECK EXCEEDS MAX
JAE J3
PUSH CX
MOV CX,CSP
CMP CX,CSPMAX
JB J1
MOV CSPMAX,CX
J1: CALL MOD_C2 ; CALL CHECK CURRENT MODE
MOV PRINT_MODE,LOW_PR_FLG ; FORCE INTO GRAPHIC MODE
MOV AL,0DH ; "CR"
CALL FIRE
POP CX
MOV AX,CX ; GET N1N2 VALUE
CALL ESCL ; CALL ESC-L OUT ROUTINE
MOV AL,0 ; ZERO OUT TO PRINTER
J2: CALL FIRE ; FOR MAKING BLANK
LOOP J2
MOV AX,N1N2 ; SAVE NEW CURRENT SLICE POSITION
MOV CSP,AX
J3: AND FLG1,0FFH-ESC_FLG-X_FLG ; RESET "ESC" & "X" INDICATOR
AND FLG2,0FFH-X6_FLG ; RESET "X6" INDICATOR
RET
X6_EM ENDP
;--- X5_EM ---
; EMULATES ESC-X5 (FORWARD VERTICAL STEP FEED)
;-----
X5_EM PROC NEAR
CMP N1N2,0 ; CHECK N1N2 = ZERO
JBE L4
CMP N1N2,100H ; CHECK EXCEEDS MAX
JA L4
MOV CX,CSP
PUSH CX
CALL PRINT2 ; CALL SECONDARY PRINT
MOV CX,N1N2
XOR DX,DX
MOV AX,CX ;+++++ THIS SEQUENCE CALCULATES +
MOV CX,13 ;+ BY FOLLOWING FORMULA: +
DIV CX ;+
MOV CX,AX ;+ CX = 1/13 * CX +
CMP CX,0
JNE L1
INC L1
L1: MOV AL,1BH ; "ESC"
CALL FIRE
MOV AL,30H ; "0"
CALL FIRE
L2: MOV AL,0AH ; "LF" OUT
CMP CX,0
JE L3
CALL FIRE
DEC CX
JMP L2
L3: POP CX
MOV N1N2,CX
CALL X6_EM
L4: AND FLG1,0FFH-ESC_FLG-X_FLG ; RESET "ESC" & "X" INDICATOR
AND FLG2,0FFH-X5_FLG ; RESET "X5" INDICATOR
RET
X5_EM ENDP
;--- X9_EM ---
; EMULATES ESC-X9 (LINE FEED PITCH).
; FOLLOWING CONVERSION IS PROCEEDED:
; 0 <= N1N2 < 13 ..... LINE FEED COUNT = 1 x 1/9 INCH
; 13 <= N1N2 < 26 ..... LINE FEED COUNT = 2 x 1/9 INCH
; 26 <= N1N2 < 39 ..... LINE FEED COUNT = 3 x 1/9 INCH
; 39 <= N1N2 < 52 ..... LINE FEED COUNT = 4 x 1/9 INCH
; 52 <= N1N2 < 65 ..... LINE FEED COUNT = 5 x 1/9 INCH
; 65 <= N1N2 < 78 ..... LINE FEED COUNT = 6 x 1/9 INCH
; 78 <= N1N2 < 91 ..... LINE FEED COUNT = 7 x 1/9 INCH
; 91 <= N1N2 < 104 ..... LINE FEED COUNT = 8 x 1/9 INCH
; 104 <= N1N2 ..... LINE FEED COUNT = 9 x 1/9 INCH
;-----
X9_EM PROC NEAR
PUSH BX
PUSH CX
MOV AX,N1N2 ; GET N1N2 VALUE TO AX
XOR DX,DX
MOV CX,13
DIV CX
MOV CX,AX
INC CX
CMP CX,9

```



```

0863 BA 037A      MOV     DX,PR_CMD_PORT      ; SET INIT LINE HIGH
0866 B0 0C      MOV     AL,0CH
0868 EE      OUT     DX,AL
0869 E8 0896 R   CALL    DELAY
086C J2 E0      XOR     AH,AL              ; SET PRINTER ID
086E 74 1E      JZ     R9
0870 D0 E8      SHR     AL,1
0872 D0 E8      SHR     AL,1
0874 25 0001    AND     AX,01H
0877 35 0001    XOR     AX,01H
087A 40      INC     AX
087B A3 0000 R   MOV     PRINTER_ID,AX      ;
087E E8 08B9 R   R3:    CALL    DEFAULT        ; SET DEFAULT VALUE
0881 B3 00      MOV     BL,0              ; CALL STATUS READ ROUTINE
0883 E8 0F54 R   CALL    STATUS            ; GET LAST STATUS
0886 E8 0A34 R   CALL    GETVAL           ; RESET CPI , LPI CONTROL VALUE
0889 5E      POP     SI
088A 5A      POP     DX
088B 59      POP     CX
088C 5B      POP     BX
088D C3      RET
088E BA 037A      R9:    MOV     DX,PR_CMD_PORT      ; NOT ATTACH
0891 B0 0C      MOV     AL,0CH
0893 EE      OUT     DX,AL
0894 EB E8      JMP     R3
0896 EB E8      INI1  ENDP
;-- DELAY -----
; WAIT FOR INITIALIZE
;
; CARRY : PRINTER STATUS (0:ATTACHED /1:NOT ATTACHED)
;-----
0896 51      PROC    NEAR
0896 53      PUSH   CX
0897 53      PUSH   BX
0898 J3 C9      XOR     CX,CX
089A E2 FE      DEL00: LOOP  DEL00
089C BA 0379    MOV     DX,PR_STATUS_PORT  ; GET STATUS
089F EC      IN     AL,DX
08A0 6A D8      MOV     BL,AL
08A2 80 E3 FC  AND     BL,0FCH
08A5 B9 0032    MOV     CX,50              ; GET STATUS
08A8 EC      DEL10: IN     AL,DX
08A9 24 FC      AND     AL,0FCH
08AB 3A C3      CMP     AL,BL
08AD 75 06      JNE     DEL50
08AF E2 F7      LOOP   DEL10
08B1 5B      POP     BX
08B2 59      POP     CX              ; PRINTER IS ATTACHED
08B3 F8      CLC
08B4 C3      RET
08B5 5B      DEL50: POP     BX
08B6 59      POP     CX              ; PRINTER ISN'T ATTACHED
08B7 F9      STC
08B8 C3      RET
08B9 C3      DELAY ENDP
;-- DEFAULT -----
; SETS PRINTER CONTROL PARAMETERS TO DEFAULT VALUES.
;
; DEFAULT PARAMETERS : 5/10 CPI (PT-1)
;                     6/12 CPI (PT-2)
;                     4.5 LPI
;                     50 LPP
;                     SMALL TYPE
;-----
08B9 C6 06 0003 R 90  DEF00: PROC    NEAR
08BB 83 3E 0000 R 01  MOV     CPI,90H            ; CPI DEFAULT SET
08C3 75 05      CMP     PRINTER_ID,1
08C5 JNE DEF10    JNE     DEF10
08C5 C6 06 0003 R 78  MOV     CPI,78H            ; CPI DEFAULT SET
08CA C6 06 0004 R 14  MOV     LPI,14H           ; LPI DEFAULT SET
08CF C7 06 0007 R 0042 MOV     LPP,42H           ; LPP DEFAULT SET
08D5 C6 06 0026 R 01  MOV     LF_CT,1           ; SET DEFAULT LINE FEED COUNT ( =1 )
08DA C7 06 000A R 0002 MOV     CHAR_TYPE,2       ; SET SMALL TYPE CHAR MODE
08E0 C3      DEF99: RET
08E1 C3      DEFAULT ENDP
;-- RESET -----
; RESETS BIOS CONTROL PARAMETERS AND CLEARS BUFFER
;-----
08E1 50      RESET: PROC    NEAR
08E1 33 C0      PUSH   AX
08E2 XOR     AX,AX              ; CLEAR AX
08E4 MOV     CCP,AX            ; CLEAR CCP
08E7 MOV     CSP,AX           ; CLEAR CSP
08EA MOV     CODEN,AX         ; CLEAR CODE COUNTER
08ED MOV     FLG1,AL          ; CLEAR FLG1
08F0 MOV     FLG2,AL         ; CLEAR FLG2
08F3 MOV     FLG3,AL         ; CLEAR FLG3
08F6 POP     AX
08F7 RET
08F8 C3      RESET ENDP
;-- CHG-PT1 -----
; SETS PRINTER CONTROL PARAMETERS FOR PRINTER-1.
;
; CONTROLLED PARAMETERS ARE AS FOLLOWS:
;
; AL = 0 : DEFAULT SET
; AL = 1 : CHANGE CPI (CHARACTER / INCH)
; AL = 2 : CHANGE LPI (LINE / INCH)
; AL = 3 : CHANGE LPP (LINE / PAGE)
; AL = 6 : CHANGE TYPE (LARGE / SMALL)
;-----

```

Appendix A.

```

08F8
08F8 50
08F9 53
08FA 0A C0
08FC 74 13
08FE FE C8
0900 74 15
0902 FE C8
0904 74 29
0906 FE C8
0908 74 50
090A 3C 03
090C 74 56
090E EB 63 90
0911 E8 08B9 R
0914 EB 5D 90
0917 80 FF 60
091A 74 0C
091C 80 FF 6C
091F 74 07
0921 80 FF 78
0924 74 02
0926 B7 90
0928 88 3E 0003 R
092C EB 45 90
092F C6 06 0026 R 01
0934 80 FF 10
0937 74 10
0939 FE 06 0026 R
093D 80 FF 14
0940 74 07
0942 80 FF 18
0945 74 02
0947 B7 1E
0949 88 3E 0004 R
094D 80 1B
094F E8 0F0A R
0952 B8 30
0954 E8 0F0A R
0957 EB 1A 90
095A 89 1E 0024 R
095E E8 07C0 R
0961 EB 10 90
0964 80 FF 01
0967 77 0A
0969 FE C7
096B 8A DF
096D 32 FF
096F 89 1E 000A R
0973 EB 0A34 R
0976 58
0977 58
0978 C3
0979

```

```

;-----;
CHG_PT1 PROC NEAR
; SAVE AX
PUSH AX
; SAVE BX
PUSH BX
OR AL,AL
; DEFAULT VALUE SET
JZ P0
DEC AL
; CHANGE CPI
JZ P1
DEC AL
; CHANGE LPI
JZ P2
DEC AL
; CHANGE LPP
JZ P3
CMP AL,3
; CHANGE TYPE
JZ P6
JMP P99
P0: CALL DEFAULT
; CALL DEFAULT SET ROUTINE
JMP P99
P1: CMP BH,60H
; 7.5/15 CPI
JE P11
CMP BH,6CH
; 6.7/13.4 CPI
JE P11
CMP BH,78H
; 6/12 CPI
JE P11
MOV BH,90H
; IF OTHER, FORCE 5/10 CPI
MOV CPI,BH
; STORE TO DATA AREA
JMP P99
P2: MOV LF_CT,1
; 7.5 LPI CONVERT TO 4.5 LPI
CMP BH,10H
JE P21
INC LF_CT
; 6 LPI CONVERT TO 3 LPI
CMP BH,14H
JE P21
CMP BH,18H
; 5 LPI CONVERT TO 3 LPI
JE P21
MOV BH,1EH
; IF OTHER, FORCE 3 LPI
P21: MOV LPI,BH
MOV AL,1BH
; "ESC"
CALL FIRE
; "0"
MOV AL,30H
CALL FIRE
P3: MOV MIN2,BX
; SET MIN2 VALUE FROM BX
CALL F_EM
; CALL PAGE LENGTH SET ROUTINE
JMP P99
P6: CMP BH,1
; SET CHAR TYPE
JA P99
INC BH
MOV BL,BH
XOR BH,BH
MOV CHAR_TYPE,BX
P99: CALL GETVAL
; RESET CPI , LPI CONTROL VALUE
POP BX
POP AX
RET
CHG_PT1 ENDP
;-----;
;--CHG_PT2
; SETS PRINTER CONTROL PARAMETERS FOR PRINTER-2.
;
; CONTROLLED PARAMETERS ARE AS FOLLOWS:
;
; AL = 0 : DEFAULT SET
; AL = 1 : CHANGE CPI (CHARACTER / INCH)
; AL = 2 : CHANGE LPI (LINE / INCH)
; AL = 3 : CHANGE LPP (LINE / PAGE)
; AL = 5 : CHANGE DIRECTION
;-----;
CHG_PT2 PROC NEAR
OR AL,AL
; AL=0 ( DEFAULT SET )
JZ XG1
DEC AL
; AL=1 ( CPI CHANGE )
JZ XG2
DEC AL
; AL=2 ( LPI CHANGE )
JZ XG3
DEC AL
; AL=3 ( LPP CHANGE )
JZ XG4
DEC AL
; AL=5 ( PRINT DIRECTION CHANGE )
JZ XG6
JMP XRET
XG1: CALL DEFAULT
; JUMP TO DEFAULT SET ROUTINE
JMP XRET
XG2: CALL XCFI
; JUMP TO CPI CHANGE ROUTINE
JMP XRET
XG3: CALL XLP1
; JUMP TO LPI CHANGE ROUTINE
JMP XRET
XG4: CALL XLPP
; JUMP TO LPP SET ROUTINE
JMP XRET
XG6: CALL XDIR
; JUMP TO DIRECTION SET ROUTINE
XRET: CALL GETVAL
; RESET CPI , LPI CONTROL VALUE
RET
CHG_PT2 ENDP
;--XCPI
; SETS CPI VALUE TO PRINTER-2.
;
;-----;
XCPI PROC NEAR
MOV BL,75
CMP BH,60H
; CHECK BH INDICATES 7.5/15 CPI
JE XCPF
MOV BL,67
CMP BH,6CH
; CHECK BH INDICATES 6.7/13.4 CPI

```

```

0979
0979 0A C0
097B 74 15
097D FE C8
097F 74 17
0981 FE C8
0983 74 19
0985 FE C8
0987 74 1B
0989 FE C8
098B FE C8
098D 74 1B
098F EB 1C 90
0992 E8 08B9 R
0995 EB 16 90
0998 E8 09B1 R
099B EB 10 90
099E E8 09F0 R
09A1 EB 0A 90
09A4 E8 0A0E R
09A7 EB 04 90
09AA E8 0A27 R
09AD E8 0A34 R
09B0 C3
09B1

```

```

09B1
09B1 B3 4B
09B3 80 FF 60
09B6 74 15
09B8 B3 43
09BA 80 FF 6C

```



```

09BD 74 0E          JE          XCPF
09BF 83 3C          MOV         BL,60
09C1 80 FF 78       CMP         BH,78H          ; CHECK BH INDICATES 6/12 CPI
09C4 74 07          JE          XCPF
09C6 83 32          MOV         BL,50
09C8 80 FF 90       CMP         BH,90H          ; CHECK BH INDICATES 5/10 CPI
09CB 75 22          JNE        XCP9
09CD 88 3E 0003 R   XCPF: MOV     CPI,BH          ; BH VALUE IS STORED
09D1 B0 1B          MOV         AL,1BH         ; ESC + 7E + 02 + 00 + 01 + N
09D3 E8 0F0A R     CALL        FIRE
09D6 B0 7E          MOV         AL,7EH
09D8 E8 0F0A R     CALL        FIRE
09DB B0 02          MOV         AL,02H
09DD E8 0F0A R     CALL        FIRE
09E0 B0 00          MOV         AL,00H
09E2 E8 0F0A R     CALL        FIRE
09E5 B0 01          MOV         AL,01H
09E7 E8 0F0A R     CALL        FIRE
09EA 8A C3          MOV         AL,BL
09EC E8 0F0A R     CALL        FIRE
09EF C3
09F0
XCP9: RET
XCP1: ENDP
;-- XLPI -----
; SETS LPI VALUE TO PRINTER-2.
;-----
XLPI PROC          NEAR
09F0 88 3E 0004 R   MOV         LPI,BH          ; BH VALUE IS STORED
09F4 B0 1B          MOV         AL,1BH         ; ESC + X + 9 + M12
09F6 E8 0F0A R     CALL        FIRE
09F9 B0 25          MOV         AL,25H
09FB E8 0F0A R     CALL        FIRE
09FE B0 39          MOV         AL,39H
0A00 E8 0F0A R     CALL        FIRE
0A03 B0 00          MOV         AL,00H
0A05 E8 0F0A R     CALL        FIRE
0A08 8A C7          MOV         AL,BH
0A0A E8 0F0A R     CALL        FIRE
0A0D C3
0A0E
XLPI: RET
XLP1: ENDP
;-- XLPP -----
; SETS LPP VALUE TO PRINTER-2.
;-----
XLPP PROC          NEAR
0A0E 89 1E 0007 R   MOV         LPP,BX          ; BX VALUE IS STORED
0A12 B0 1B          MOV         AL,1BH         ; ESC + F + N1 + N2
0A14 E8 0F0A R     CALL        FIRE
0A17 B0 46          MOV         AL,46H
0A19 E8 0F0A R     CALL        FIRE
0A1C 8A C7          MOV         AL,BH
0A1E E8 0F0A R     CALL        FIRE
0A21 8A C3          MOV         AL,BL
0A23 E8 0F0A R     CALL        FIRE
0A26 C3
0A27
XLPP: RET
XLP2: ENDP
;-- XDIR -----
; SETS DIRECTION MODE TO PRINTER-2.
;-----
XDIR PROC          NEAR
0A27 B0 42          MOV         AL,42H
0A29 80 FF 01       CMP         BH,1
0A2C 74 02          JE          XDIR1
0A2E B0 55          MOV         AL,55H
0A30 E8 0ECE R     XDIR1: CALL   DIRSET
0A33 C3
0A34
XDIR: RET
XDIR: ENDP
;-- GETVAL -----
; GETS GRID LINE AND CHAR TYPE CONTROL VALUE
;-----
GETVAL PROC        NEAR
0A34 06          PUSH        ES
0A35 51          PUSH        CX
0A36 56          PUSH        SI
0A37 57          PUSH        DI
0A38 50          PUSH        AX
0A39 1E          PUSH        DS
0A3A 07          POP         ES          ; ES SET FOR MOVSB
0A3B 8D 36 0000 R   LEA        SI,V_VALUE          ; SEARCH V_VALUE TABLE
0A3F 8A 2E 0004 R   MOV         CH,LPI
0A43 2E: 3A 2C     CMP         CH,CS:[SI]
0A46 74 05          JE          GET10
0A48 83 C6 12     ADD         SI,18
0A4B EB F2          JMP         GET00
0A4D B9 0012     MOV         CX,18
0A50 8D 3E 0040 R   LEA        DI,GVALUE          ; MOVE TO VALUE AREA
0A54 1E          PUSH        DS
0A55 0E          PUSH        CS
0A56 1F          POP         DS
0A57 FC          CLD
0A58 F3/ A4         REP         MOVSB
0A5A 1F          POP         DS
0A5B 8D 36 0048 R   LEA        SI,H_VALUE          ; SEARCH V_VALUE TABLE
0A5F 8A 2E 0003 R   MOV         CH,CPI
0A63 2E: 3A 2C     CMP         CH,CS:[SI]
0A66 74 05          JE          GET30
0A68 83 C6 06     ADD         SI,6
0A6B EB F2          JMP         GET20
0A6D B9 0006     MOV         CX,6
0A70 8D 3E 0052 R   LEA        DI,GVALUE+18
0A74 1E          PUSH        DS
0A75 0E          PUSH        CS

```

Appendix A.

```

0A76 1F          POP     DS
0A77 FC          CLD
0A78 F3/ A4      REP     MOVSB
0A7A 1F          POP     DS
0A7B C7 06 005B R 0000  MOV     TVALUE+UPPER,0      ; SET CHAR TYPE CONTROL VALUE
0A81 C7 06 005A R 0001  MOV     TVALUE+LOWER,1
0A87 83 JE 000A R 02    CMP     CHAR_TYPE,2
0A8C 74 0C        JE      GET40
0A8E C7 06 005B R 0000  MOV     TVALUE+UPPER,0
0A94 C7 06 005A R 0008  MOV     TVALUE+LOWER,8
0A9A CD 11      GET40: INT     11H          ; SET SYSTEM IDENT
0A9C 24 20      AND     AL,20H
0A9E A2 001F R   MOV     SYSTEM_ID,AL
0AA1 58          POP     AX
0AA2 5F          POP     DI
0AA3 5E          POP     SI
0AA4 59          POP     CX
0AA5 07          POP     ES
0AA6 C3          RET
0AA7            GETVAL ENDP
;-- PRINT
; PRINTS CODES IN CODE BUFFER.
; INPUT
; CODE_BUFFER : CHARACTER STRING
; ATTR_BUFFER : CHARACTER SIZE
; CCP         : CHARACTER LENGTH
;-----
PRINT PROC NEAR
PUSH DS          ; SET ES
POP ES
LEA BX, CODE_BUFFER ; PRINT
MOV CSIZE, 0001H
CALL IMAGE
RET
;-----
PRINT ENDP
;-- ATTR
; PRINTS CODES IN BUFFER WITH ATTRIBUTE.
; INPUT
; ES:BX : CHARACTER STRING
; ES:DI : ATTRIBUTE STRING
; CX    : CHARACTER LENGTH
;-----
ATTR PROC NEAR
OR CX, CX        ; LAST CODE CHECK
JZ AT99
MOV SI, CX
CMP BYTE PTR ES:[BX+SI-1], 0DH
JE AT10
CMP BYTE PTR ES:[BX+SI-1], 0AH
JE AT10
CMP PRINTER_ID, 1 ; WHICH PRINTER IS ATTACHED ?
JE AT100
JMP AT200
AT10: DEC CX
JMP ATTR
AT99: RET
;-----
; PRINTER-1 IS ATTACHED
;-----
AT100: PUSH ES          ; OUT STRING
PUSH BX
PUSH DI
PUSH CX
MOV AL, ES:[BX]
CALL CMD_CHK
POP CX
POP DI
POP BX
POP ES
INC BX
LOOP AT100      ; LINE FEED
MOV AL, 0AH
CALL CMD_CHK
RET
;-----
; PRINTER-2 IS ATTACHED
;-----
AT200: CALL MODESET      ; 3 BYTE MODE SET
CALL UGRID      ; UPPER GRID LINE
CALL FEED       ; FEED
CALL CODEOUT    ; OUT CODE
CALL FEED       ; FEED
CALL LGRID      ; LOWER GRID LINE
MOV DL, GVALUE+V_SIZE ; FEED
MOV GVALUE+V_SIZE, 16
CALL FEED
MOV GVALUE+V_SIZE, DL
RET
;-----
ATTR ENDP
;-- IMAGE
; PRINTS THE CODE STRING
; INPUT
; ES:BX : CHARACTER STRING
; ES:BX+240 : CHARACTER SIZE
; CCP   : CHARACTER LENGTH
; CSIZE : ATTRIBUTE VALID FLAG (0:INVALID / 1:VALID)
;-----
IMAGE PROC NEAR
MOV AL, 1BH          ; SET LPI 1/9

```

```

0B15 E8 0F0A R      CALL FIRE
0B18 B0 30          MOV AL,30H
0B1A E8 0F0A R      CALL FIRE
0B1D 8B 36 0058 R  MOV SI,TVALUE+UPPER      ; PRINT UPPER IMAGE
0B21 E8 0B75 R      CALL ROTATE
0B24 83 3E 005A R 01 CMP TVALUE+LOWER,1      ; EVEN IMAGE ?
0B29 75 17          JNE IM10                ; NO
0B2B B0 1B          MOV AL,1BH
0B2D E8 0F0A R      CALL FIRE
0B30 B0 4A          MOV AL,4AH
0B32 E8 0F0A R      CALL FIRE
0B35 B0 01          MOV AL,01H
0B37 E8 0F0A R      CALL FIRE
0B3A B0 0D          MOV AL,0DH
0B3C E8 0F0A R      CALL FIRE
0B3F EB 10 90        JMP IM20
0B42 B0 1B          MOV AL,1BH                ; THIS SEQUENCE SETS LINE FEED
0B44 E8 0F0A R      CALL FIRE                ; PITCH FOR ORDINARY USING
0B47 B0 30          MOV AL,30H                ; VALUE OF ONE-NINTH-INCH.
0B49 E8 0F0A R      CALL FIRE
0B4C B0 0A          MOV AL,0AH
0B4E E8 0F0A R      CALL FIRE                ; LINE FEED
0B51 8B 36 005A R 01 CMP SI,TVALUE+LOWER      ; PRINT LOWER IMAGE
0B55 E8 0B75 R      CALL ROTATE
0B58 83 3E 000A R 01 CMP CHAR_TYPE,1          ; CHECK CHARACTER TYPE
0B5D 74 05          JLE IM99                 ; IF SIZE IS LARGE, JUMP
0B5F B0 0A          MOV AL,0AH                ; IF SIZE IS SMALL,
0B61 E8 0F0A R      CALL FIRE                ; LF OUT TO PRINTER
0B64 C7 06 000E R 0000 CCP,0                    ; CLEAR CURRENT CHARACTER POSITION
0B6A B9 0474        MOV CX,1140               ; CLEAR IMAGE BUFFER
0B6D 8D 36 00A0 R  LEA SI,IMAGE_BUFFER
0B71 E8 0E6D R      CALL CLEAR
0B74 C3
0B75

IMAGE ENDP
;-- ROTATE
; ROTATES FONT IMAGE AND OUT
; INPUT
; ES:BX : CODE BUFFER
; ES:BX+240 : CHARACTER SIZE
; CCP : CODE LENGTH
; CSIZE : ATTRIBUTE VALID FLAG (0:INVALID / 1:VALID)
;-----
ROTATE PROC NEAR
PUSH BX
PUSH CCP
MOV AL,GVALUE+H_SPACE ; (8 + SPACE) * CCP -> AX
ADD AL,8
MOV DX,CCP
MUL DL
CALL ESCL
ROT00: CMP CCP,0
JNG ROT99
MOV BP,ES:240[BX] ; MOVE CHARACTER SIZE TO BP
AND BP,CSIZE
CALL GETFONT ; GET FONT INTO FONT WORK
OR DH,DH ; HALF ?
JZ HALF
JMP NORMAL
ROT99: POP CCP
POP BX
RET
;-----
; NORMAL SIZE PRINT
;-----
NORMAL: MOV AH,GVALUE+H_SPACE ; SPACING
CALL SPOUT
CALL HVCONV ; LEFT IMAGE OUT
CMP CCP,0
JZ ROT00
ADD SI,16 ; RIGHT IMAGE OUT
CALL HVCONV
SUB SI,16
MOV AH,GVALUE+H_SPACE ; SPACING
CALL SPOUT
JMP ROT00
;-----
; HALF SIZE PRINT
;-----
HALF: MOV AH,1 ; SPACING
CALL SPOUT
CALL HVCONV ; LEFT IMAGE OUT
MOV AH,0 ; SPACING
CALL SPOUT
JMP ROT00
;-----
ROTATE ENDP
;-- GETFONT
; GETS FONT TO FONT WORK
;-----
;-----
GETFONT PROC NEAR
XOR DX,DX
CALL GETCODE ; GET THE FIRST CODE TO DL
; ++++++
CMP DL,80H
JBE ONE
; +
CMP DL,9FH
; + CHECK LEGALITY OF 1ST CODE OF
; +
JBE TWO
; +
CMP DL,0DFH
; + TWO BYTES CODE
; +
JBE ONE
; +
CMP DL,0FCH
; +
JBE TWO
; +
JMP ONE
; ++++++
TWO: MOV DH,DL ; GET THE SECOND CODE TO DL
CALL GETCODE

```

Appendix A.

```

08FB 53
08FC 06
08FD 56
08FE 09 0010
0C01 8D 36 0280 R
0C05 E8 0E6D R
0C08 B4 13
0C0A 80 3E 001F R 00
0C0F 74 02
0C11 B4 10
0C13 32 C0
0C15 8D 1E 0280 R
0C19 8B CA
0C1B 1E
0C1C 07
0C1D CD 10
0C1F 5E
0C20 07
0C21 5B
0C22 C3
0C23

ONE:  PUSH  BX           ; GET FONT IMAGE
      PUSH  ES
      PUSH  SI
      MOV   CX,16
      LEA  SI,Font
      CALL CLEAR
      MOV  AH,19
      CMP  SYSTEM_ID,00H
      JE   FONT10
      MOV  AH,16
FONT10: XOR  AL,AL
      LEA  BX,Font
      MOV  CX,DX
      PUSH DS
      POP  ES
      INT  10H
      POP  SI
      POP  ES
      POP  BX
      RET

GETFONT ENDP
;-- GETCODE
; GETS CODE AND UPDATE INDEX
;-----
GETCODE PROC    NEAR
      MOV  DL,ES:[BX]
      ADD  BX,BP
      INC  BX
      RET
; GET CODE INTO DL
; INDEX UPDATE IF DOUBLE SIZE
; INDEX UPDATE
GETCODE ENDP
;-- SPOUT
; OUTPUTS SPACES BETWEEN CHARACTERS
;
; INPUT
; VALUE+H_SPACE : SPACING VALUE
;-----
SPOUT  PROC    NEAR
      ADD  AH,GVALUE+H_SPACE
      SHR  AH,1
      MOV  CX,BP
      SHL  AH,CL
      MOV  CL,AH
      XOR  AL,AL
      OR   CL,CL
      JZ   SP99
SP00:  CALL  FIRE
      LOOP SP00
; OUT SPACE
SP99:  RET
SPOUT  ENDP
;-- HVCONV
; ROTATES FONT IMAGE AND PRINTS IT
;
; INPUT
; SI : FLAG (0:UPPER/EVEN 1:ODD 8:LOWER)
; CHAR_TYPE : TYPE (1:SMALL TYPE 2:LARGE TYPE)
; BP : SIZE (0:NORMAL SIZE 1:DOUBLE SIZE)
;-----
HVCONV PROC    NEAR
      PUSH  SI
      PUSH  BX
      PUSH  CX
      PUSH  DX
      MOV  DI,CHAR_TYPE
      MOV  AL,Font[SI]
      ADD  SI,DI
      MOV  CL,Font[SI]
      ADD  SI,DI
      MOV  DL,Font[SI]
      ADD  SI,DI
      MOV  BL,Font[SI]
      ADD  SI,DI
      MOV  AH,Font[SI]
      ADD  SI,DI
      MOV  CH,Font[SI]
      ADD  SI,DI
      MOV  DH,Font[SI]
      ADD  SI,DI
      MOV  BH,Font[SI]
      ADD  SI,DI
      MOV  SI,DI
      MOV  SI,8
HV00:  SHL  AL,1
      RCL  DI,1
      SHL  CL,1
      RCL  DI,1
      SHL  DL,1
      RCL  DI,1
      SHL  BL,1
      RCL  DI,1
      SHL  AH,1
      RCL  DI,1
      SHL  CH,1
      RCL  DI,1
      SHL  DH,1
      RCL  DI,1
      SHL  BH,1
      RCL  DI,1
      SHL  AX
      PUSH  CX
      MOV  CX,BP
      INC  CX
      ; LOAD FONT TO REGISTER
      ; LOAD DATA
      ; FIRE IMAGE
      ; LOOP FOR DOUBLE SIZE

```

```

0CA2 8B C7
0CA4 E8 0F0A R
0CA7 E2 F9
0CA9 59
0CAA 58
0CAB 4E
0CAC 75 CF
0CAE 29 2E 000E R
0CB2 FF 0E 000E R
0CB6 5A
0CB7 59
0CB8 5B
0CB9 5E
0CBA C3
0CBB

```

```

0CBB B0 1B
0CBB E8 0F0A R
0CBD B0 28
0CC0 E8 0F0A R
0CC2 C3
0CC5
0CC6

```

```

0CC6
0CC6 50
0CC7 51
0CC8 57
0CC9 53
0CCA C7 06 001C R 0003
0CD0 B0 0F
0CD2 8B F7
0CD4 E8 0E74 R
0CD7 72 28
0CD9 B0 55
0CDB E8 0ECE R
0CDE E8 0E9F R
0CE1 51
0CE2 8D 36 02A0 R
0CE6 B9 0036
0CE9 E8 0E6D R
0CEC 59
0CED BE 0000
0CF0 E8 0DD2 R
0CF3 E8 0D06 R
0CF6 E8 0E42 R
0CF9 47
0CFA E2 E5
0CFC B0 42
0CFE E8 0ECE R
0D01 5B
0D02 5F
0D03 59
0D04 58
0D05 C3
0D06

```

```

0D06
0D06 26 8B 03
0D09 25 0003
0D0C 74 09
0D0E 48
0D0F 74 07
0D11 48
0D12 74 11
0D14 EB 1C 90
0D17 C3

```

```

0D18 B6 FF
0D1A B2 FF
0D1C B7 FF
0D1E B4 80
0D20 E8 0D45 R
0D23 EB F2

```

```

0D25 B6 FF
0D27 B2 FF
0D29 B7 FF
0D2B B4 C0
0D2D E8 0D45 R
0D30 EB E5

```

```

0D32 8A 36 0053 R
0D36 8A 16 0054 R
0D3A 8A 3E 0055 R
0D3E B4 80
0D40 E8 0D45 R
0D43 EB D2

```

```

HV10: MOV AX,DI
CALL FIRE
LOOP HV10
POP CX
POP AX
DEC SI
JNZ HV00
SUB CCP,BP ; CCP UPDATE
DEC CCP
POP DX
POP CX
POP BX
POP SI
RET

```

```

HVCONV ENDP

```

```

;-- MODESET
; SETS 3 BYTE TRANS MODE

```

```

MODESET PROC NEAR
MOV AL,1BH ; ESC + (
CALL FIRE
MOV AL,28H
CALL FIRE
RET

```

```

MODESET ENDP

```

```

;-- UGRID
; PRINTS UPPER GRID LINE
; INPUT
; ES:DI : ATTRIBUTE BUFFER
; CX : ATTRIBUTE LENGTH

```

```

UGRID PROC NEAR
PUSH AX
PUSH CX
PUSH DI
PUSH BX
MOV CPIMASK,3 ; SET MASK FOR 13.5 CPI
MOV AL,0FH ; CHECK UPPER ATTRIBUTE EXISTS
MOV SI,DI
CALL CHECK
JC UG99
MOV AL,55H ; UNIDIRECTION SET
CALL DIRSET
CALL ESCX1 ; OUT ESC+X+1+N1+N2
PUSH CX ; CLEAR IMAGE BUFFER
LEA SI,GRID
MOV CX,54
CALL CLEAR
POP CX
MOV SI,0 ; SET VERTICAL GRID (UPPER)
CALL VGRID
CALL HGRID ; SET HORIZONTAL GRID
CALL PGRID ; OUT GRID IMAGE
INC DI
LOOP UG00
MOV AL,42H ; BIDIRECTION SET
CALL DIRSET
POP BX
POP DI
POP CX
POP AX
RET

```

```

UGRID ENDP

```

```

;-- HGRID
; PRINTS HORIZONTAL GRID LINE

```

```

HGRID PROC NEAR
MOV AX,ES:[DI] ; GET ATTRIBUTE BYTE
AND AX,0003H ; NO GRID ?
JZ HG99
DEC AX ; SINGLE ?
JZ HG100
DEC AX ; DOUBLE ?
JZ HG200
JMP HG300 ; DASHED

```

```

HG99: RET

```

```

; SINGLE

```

```

HG100: MOV DH,0FFH ; LOAD LINE MASK
MOV DL,0FFH
MOV BH,0FFH
MOV AH,080H ; LOAD LINE IMAGE
CALL HGSET ; SET GRID IMAGE
JMP HG99

```

```

; DOUBLE

```

```

HG200: MOV DH,0FFH ; LOAD LINE MASK
MOV DL,0FFH
MOV BH,0FFH
MOV AH,0C0H ; LOAD LINE IMAGE
CALL HGSET ; SET GRID IMAGE
JMP HG99

```

```

; DASHED

```

```

HG300: MOV DH,GVALUE+H_DASH ; LOAD LINE MASK
MOV DL,GVALUE+H_DASH+1
MOV BH,GVALUE+H_DASH+2
MOV AH,080H ; LOAD LINE IMAGE
CALL HGSET ; SET GRID IMAGE
JMP HG99

```

Appendix A.

```

0D45          HGRID ENDP
;-- HGSET
; SETS GRID LINE IMAGE
; INPUT
; DH,DL,BH : GRID LINE MASK
;-----
0D45          HGSET PROC NEAR
0D45 33 F6      XOR SI,SI
0D47 D0 D7      RCL BH,1
0D49 D1 D2      RCL DX,1
0D4B 73 04      JNC SKIP
0D4D 08 A4 02A0 R OR GRID[SI],AH
0D51 83 C6 03   ADD SI,3
0D54 83 FE 36   CMP SI,54
0D57 75 EE      JNE HG500
0D59 C3        RET
0D5A          HGSET ENDP
;-- CODEOUT
; PRINTS CODES
;-----
0D5A          CODEOUT PROC NEAR
0D5A 50         PUSH AX
0D5B 51         PUSH CX
0D5C 56         PUSH SI
0D5D 57         PUSH DI
0D5E B0 DF      MOV AL,0DFH
0D60 8B F3      MOV SI,BX
0D62 E8 0E74 R CALL CHECK
0D65 72 0B      JC CODE00
0D67 8B F3      MOV SI,BX
0D69 26 8A 04   MOV AL,ES:[SI]
0D6C E8 07FA R CALL CHAR0
0D6F 46         INC SI
0D70 E2 F7      LOOP CODE00
0D72 5F         POP DI
0D73 5E         POP SI
0D74 59         POP CX
0D75 58         POP AX
0D76 C3        RET
0D77          CODEOUT ENDP
;-- LGRID
; PRINTS LOWER GRID LINE
; INPUT
; ES:DI : ATTRIBUTE BUFFER
; CX : ATTRIBUTE LENGTH
;-----
0D77          LGRID PROC NEAR
0D77 50         PUSH AX
0D78 51         PUSH CX
0D79 57         PUSH DI
0D7A C7 06 001C R 0003 MOV CPIMASK,3
0D80 B0 4C      MOV AL,4CH
0D82 8B F7      MOV SI,DI
0D84 E8 0E74 R CALL CHECK
0D87 72 28      JC LG99
0D89 B0 55      MOV AL,55H
0D8B E8 0ECE R CALL DIRSET
0D8E E8 0E9F R CALL ESC%1
0D91 51         PUSH CX
0D92 8D 36 02A0 R LEA SI,GRID
0D96 B9 0036    MOV CX,54
0D99 E8 0E6D R CALL CLEAR
0D9C 59         POP CX
0D9D BE 0009     MOV SI,9
0DA0 E8 0DD2 R CALL VGRID
0DA3 E8 0D85 R CALL UNDER
0DA6 E8 0E42 R CALL PGRID
0DA9 47         INC DI
0DAA E2 E5      LOOP LG00
0DAC B0 42      MOV AL,42H
0DAE E8 0ECE R CALL DIRSET
0DB1 5F         POP DI
0DB2 59         POP CX
0DB3 58         POP AX
0DB4 C3        RET
0DB5          LGRID ENDP
;-- UNDER
; SETS UNDERSCORE IMAGE
;-----
0DB5          UNDER PROC NEAR
0DB5 26 F6 05 40 TEST BYTE PTR ES:[DI],40H
0DB9 74 07      JZ UN99
0DBB 8A 26 0047 R MOV AH,GVALUE+V_UNDER
0DBF E8 0DC3 R CALL UNSET
0DC2 C3        RET
0DC3          UN99: UNDER ENDP
;-- UNSET
; SETS UNDESCORE IMAGE
; INPUT
; AH : UNDERSCORE IMAGE
;-----
0DC3          UNSET PROC NEAR
0DC3 33 F6      XOR SI,SI
0DC5 08 A4 02A2 R OR GRID*2[SI],AH
0DC9 83 C6 03   ADD SI,3
0DCC 83 FE 36   CMP SI,54
0DCF 75 F4      JNE UN500
0DD1 C3        RET
0DD2          UNSET ENDP
;-- VGRID
; SETS VERTICAL GRID LINE
; INPUT
; ES:DI : ATTRIBUTE BUFFER
;-----

```

```

0DD2
0DD2 26: 8B 05
0DD5 25 000C
0DD6 74 0D
0DDA 2D 0004
0DDD 74 09
0DDF 2D 0004
0DE2 74 1E
0DE4 EB 42 90
0DE7 C3

0DE8 8A 84 0041 R
0DEC 08 06 02A0 R
0DF0 8A 84 0042 R
0DF4 08 06 02A1 R
0DF8 8A 84 0043 R
0DFC 03 06 02A2 R
0E00 EB E5

0E02 8A 84 0041 R
0E06 08 06 02A0 R
0E0A 08 06 02A3 R
0E0E 8A 84 0042 R
0E12 08 06 02A1 R
0E16 08 06 02A4 R
0E1A 8A 84 0043 R
0E1E 08 06 02A2 R
0E22 08 06 02A5 R
0E26 EB 8F

0E28 8A 84 0044 R
0E2C 08 06 02A0 R
0E30 8A 84 0045 R
0E34 08 06 02A1 R
0E38 8A 84 0046 R
0E3C 08 06 02A2 R
0E40 EB A5
0E42

0E42 51
0E42 06
0E43 06
0E44 32 ED
0E46 8A 0E 0056 R
0E4A 88 C1
0E4C 03 C8
0E4E 03 C8
0E50 80 3E 0052 R 6C
0E55 75 0A
0E57 03 0E 001C R
0E5B 81 36 001C R 0003
0E61 1E
0E62 07
0E63 8D 36 02A0 R
0E67 E8 0EFE R
0E6A 07
0E6B 59
0E6C C3
0E6D

0E6D C6 04 00
0E70 46
0E71 E2 FA
0E73 C3
0E74

0E74 51
0E74 0B C9
0E77 74 08
0E79 26: 84 04
0E7C 75 07
0E7E 46
0E7F E2 F8
0E81 F9
0E82 EB 02 90
0E85 F8
0E86 59
0E87 C3
0E88

```

```

; SI : 0:UPPER 9:LOWER
;-----;
VGRID PROC NEAR
MOV AX,ES:[DI] ; GET ATTRIBUTE BYTE
AND AX,000CH ; NO GRID ?
JZ VG99
SUB AX,4 ; SINGLE ?
JZ VG100
SUB AX,4 ; DOUBLE ?
JZ VG200
JMP VG300 ; DASHED
VG99: RET
;-----;
; SINGLE
;-----;
VG100: MOV AL,GVALUE+V_SOLID+0[SI] ; SET GRID IMAGE
OR GRID+0,AL
MOV AL,GVALUE+V_SOLID+1[SI]
OR GRID+1,AL
MOV AL,GVALUE+V_SOLID+2[SI]
OR GRID+2,AL
JMP VG99
;-----;
; DOUBLE
;-----;
VG200: MOV AL,GVALUE+V_SOLID+0[SI] ; SET GRID IMAGE
OR GRID+0,AL
OR GRID+3,AL
MOV AL,GVALUE+V_SOLID+1[SI]
OR GRID+1,AL
OR GRID+4,AL
MOV AL,GVALUE+V_SOLID+2[SI]
OR GRID+2,AL
OR GRID+5,AL
JMP VG99
;-----;
; DASHED
;-----;
VG300: MOV AL,GVALUE+V_DASH+0[SI] ; SET GRID IMAGE
OR GRID+0,AL
MOV AL,GVALUE+V_DASH+1[SI]
OR GRID+1,AL
MOV AL,GVALUE+V_DASH+2[SI]
OR GRID+2,AL
JMP VG99
VGRID ENDP
;-- PGRID
; PRINTS GRID IMAGE
;-----;
PGRID PROC NEAR
PUSH CX
PUSH ES
XOR CH,CH ; OUT GRID IMAGE
MOV CL,GVALUE+H_SIZE
MOV AX,CX ; CX = 3
ADD CX,AX
ADD CX,AX
CMP GVALUE+H_KEY,6CH ; 13.5 CPI ?
JNE PG00
ADD CX,CPIMASK ; CPI ADJUST
XOR CPIMASK,03H
PG00: PUSH DS
POP ES
LEA SI,GRID
CALL FIRES
POP ES
POP CX
RET
PGRID ENDP
;-- CLEAR
; FILLS THE AREA WITH 0
; INPUT
; SI : CLEAR ADDRESS
; CX : CLEAR COUNT
;-----;
CLEAR PROC NEAR
MOV BYTE PTR [SI],0
INC SI
LOOP CLEAR
RET
CLEAR ENDP
;-- CHECK
; CHECKS STRINGS
; INPUT
; AL : CODE MASK
; CX : CODE LENGTH
; ES:SI : CODE BUFFER
;-----;
CHECK PROC NEAR
PUSH CX
OR CX,CX
JZ CH10
TEST ES:[SI],AL ; ATTRIBUTE EXISTS ?
JNZ CH20
INC SI
LOOP CH00
CH10: STC ; NO
JMP CH99
CH20: CLC ; YES
CH99: POP CX
RET
CHECK ENDP
;-- ESCL
; OUTPUTS ESC + L + N1 + N2

```

Appendix A.

```

; INPUT
; AX : N1N2
;-----
0E88      PROC   NEAR
0E88      PUSH  AX
0E89      PUSH  AX
0E8A      MOV   AL,1BH           ; "ESC"
0E8C      CALL  FIRE
0E8F      MOV   AL,4CH         ; "L"
0E91      CALL  FIRE
0E94      POP   AX             ; N1
0E95      CALL  FIRE
0E98      POP   AX             ; N2
0E99      XCHG AL,AH
0E9B      CALL  FIRE
0E9E      RET
0E9F      ENDP
; ESCX1
; OUTPUTS ESC + X + 1 + N1 + N2
; INPUT
; CX : CHARACTER LENGTH
;-----
0E9F      PROC   NEAR
0E9F      PUSH  CX
0EA0      MOV   AL,1BH           ; ESC+X+1
0EA2      CALL  FIRE
0EA5      MOV   AL,25H
0EA7      CALL  FIRE
0EAA      MOV   AL,31H
0EAC      CALL  FIRE
0EAF      MOV   AL,GVALUE+H_SIZE ; DOT SIZE - CHAR SIZE
0EB2      MUL   CL              ; 13.5 CPI ?
0EB4      CMP   GVALUE+H_KEY,6CH ; AX = CX / 2
0EB9      JNE  ESCX1X
0EBB      CLC
0EBC      RCR   CX,1
0EBE      ADC  AX,CX
0EC0      MOV  CX,AX
0EC2      MOV  AL,CH           ; N1+N2
0EC4      CALL  FIRE
0EC7      MOV  AL,CL
0EC9      CALL  FIRE
0ECC      POP  CX
0ECD      RET
0ECE      ENDP
; DIRSET
; OUTPUTS ESC + X + DIRECTION (U OR D)
; INPUT
; AL : DIRECTION
;-----
0ECE      PROC   NEAR
0ECE      PUSH  AX
0ECF      MOV   AL,1BH           ; ESC+X+U,D
0ED1      CALL  FIRE
0ED4      MOV   AL,25H
0ED6      CALL  FIRE
0ED9      POP   AX
0EDA      CALL  FIRE
0EDD      RET
0EDE      ENDP
; FEED
; EXECUTES LINE FEED
;-----
0EDE      PROC   NEAR
0EDE      MOV   AL,0DH           ; CR.
0EE0      CALL  FIRE
0EE3      MOV   AL,1BH           ; ESC+X+5
0EE5      CALL  FIRE
0EE8      MOV   AL,25H
0EEA      CALL  FIRE
0EED      MOV   AL,35H
0EEF      CALL  FIRE
0EF2      MOV   AL,0             ; N1N2
0EF4      CALL  FIRE
0EF7      MOV   AL,GVALUE+V_SIZE
0EFA      CALL  FIRE
0EFD      RET
0EFE      ENDP
; FIRES
; OUTPUTS STRING DATA TO PRINTER PORT
; INPUT
; ES:SI : ADDRESS OF PRINT STRING
; CX : LENGTH OF STRING
; OUTPUT
; AH : STATUS
;-----
0EFE      PROC   NEAR
0EFE      PUSH  CX
0EFF      MOV  AL,ES:[SI]       ; SAVE REGS.
0F02      CALL  FIRE           ; OUT DATA
0F05      INC  SI
0F06      LOOP FIRE00
0F08      POP  CX
0F09      RET
0F0A      ENDP
; FIRE
; OUTPUTS DATA TO PRINTER PORT
; INPUT
; AL : OUTPUT DATA
; OUTPUT
; AH : PRINTER STATUS
;-----

```



```

0F0A      52
0F0A      53
0F0B      53
0F0C      53
0F0D      50
0F0E      BA 0378
0F11      EE
0F12      B3 01
0F14      E8 0F54 R
0F17      F6 C4 08
0F1A      75 17
0F1C      BA 037A
0F1F      B0 0D
0F21      EE
0F22      B0 0C
0F24      EE
0F25      B3 00
0F27      E8 0F54 R
0F2A      58
0F2B      8A 26 0002 R
0F2F      58
0F30      59
0F31      5A
0F32      C3
0F33      8B 26 0014 R
0F37      8D 36 0003 R
0F3B      B9 005D
0F3E      E8 0E6D R
0F41      8D 36 00A0 R
0F45      B9 0474
0F48      E8 0E6D R
0F4B      E8 08B9 R
0F4E      E8 0A34 R
0F51      E9 00B9 R
0F54

```

```

FIRE      PROC      NEAR
          PUSH      DX
          PUSH      CX          ; SAVE REGS.
          PUSH      BX
          PUSH      AX
          MOV      DX,PR_DATA_PORT
          OUT      DX,AL        ; OUT DATA IN AL
          MOV      BL,1        ; BUSY CHECK
          CALL     STATUS
          TEST     AH,PR_ERROR
          JNZ     ABEND
          MOV      DX,PR_CMD_PORT
          MOV      AL,0DH        ; SET STROBE
          OUT      DX,AL
          MOV      AL,0CH
          OUT      DX,AL
          MOV      BL,0        ; GET LAST STATUS
          CALL     STATUS
          POP      AX          ; RESTORE REGS.
          MOV      AH,RETURN_CODE
          POP      BX
          POP      CX
          POP      DX
          RET

ABEND:    MOV      SP,SPSAVE    ; ABEND EXIT
          LEA     SI,PRINTER_ID+3 ; CLEAR CONTROL AREA
          MOV     CX,93
          CALL     CLEAR
          LEA     SI,IMAGE_BUFFER ; CLEAR IMAGE_BUFFER
          MOV     CX,1140
          CALL     CLEAR
          CALL     DEFAULT        ; SET DEFAULT VALUE
          CALL     GETVAL        ; RESET CPI , LPI CONTROL VALUE
          JMP     RETURN

```

```

FIRE      ENDP
;-- STATUS
; GETS STATUS 1
;
; INPUT
; BL : BUSY WAIT FLAG (0:NOT WAIT / 1:WAIT)
;
; OUTPUT
; AH : PRINTER STATUS 1
; RETURN_CODE : PRINTER STATUS 1
;
;-----

```

```

0F54      52
0F55      51
0F56      86 E0

0F58      BA 0379
0F5B      C7 06 0016 R 0004
0F61      C7 06 001A R 000B
0F67      33 C9

0F69      EC
0F6A      24 FE
0F6C      0A DB
0F6E      74 13
0F70      A8 80
0F72      75 0F
0F74      E8 0FE5 R
0F77      E2 F0
0F79      FF 0E 001A R
0F7D      75 EA
0F7F      0C 01
0F81      24 F7
0F83      34 08
0F85      86 C4
0F87      80 E4 B9
0F8A      88 26 0002 R
0F8E      59
0F8F      5A
0F90      C3
0F91

```

```

STATUS    PROC      NEAR
          PUSH      DX
          PUSH      CX
          XCHG     AH,AL

; ST00:   MOV      DX,PR_STATUS_PORT ; SET STATUS PORT
          MOV      PR_TIME1,4        ; RESET BEEP TIMER
          MOV      PR_TIME3,11      ; SET WAIT TIME
          XOR      CX,CX

; ST10:   IN      AL,DX              ; GET STATUS
          AND     AL,0FEH           ; RESET TIMEOUT BIT
          OR      BL,BL             ; BUSY NO CHECK ?
          JZ      ST99
          TEST    AL,PR_BUSY        ; BUSY ?
          JNZ     ST99              ; NO
          CALL    BEEP              ; BEEP ROUTINE
          LOOP   ST10
          DEC     PR_TIMES
          JNZ     ST10
          OR      AL,PR_TIMEOUT     ; SET TIMEOUT STATUS
          AND     AL,0FFH-PR_ERROR
          XOR     AL,PR_ERROR
; ST99:   XCHG     AL,AH              ; STATUS TO AH
          XCHG     AH,0B9H          ; RESET UNUSED FLAG
          MOV     RETURN_CODE,AH
          POP     CX
          POP     DX
          RET

```

```

STATUS    ENDP
;-- STATUS2
; GETS STATUS 2
;
; OUTPUT
; AH : PRINTER STATUS 2
; RETURN_CODE : PRINTER STATUS 2
;
;-----

```

```

0F91      52

0F92      B4 80
0F94      83 JE 0000 R 02
0F99      74 02
0F9B      B4 01
0F9D      80 CC 80
0FA0      80 E4 81
0FA3      80 JE 0003 R 90
0FA8      74 17
0FAA      80 C4 02
0FAD      80 JE 0003 R 78
0FB2      74 0D
0FB4      80 C4 02
0FB7      80 JE 0003 R 6C
0FBC      74 03
0FBE      80 C4 02
0FC1      80 JE 0004 R 1E

```

```

STATUS2   PROC      NEAR
          PUSH      DX

;
; MOV     AH,0                    ; SET PRINTER_ID
; CMP     PRINTER_ID,2
; JE      ST20
; MOV     AH,1
; ST20:  OR      AH,80H            ; SET ASF ON
          AND     AH,81H            ; RESET UNUSED BIT
          CMP     CPI,90H          ; CPI=5 ?
          JE      LPISET
          ADD     AH,02H
          CMP     CPI,78H          ; CPI=6 ?
          JE      LPISET
          ADD     AH,02H
          CMP     CPI,6CH          ; CPI=6.7 ?
          JE      LPISET
          ADD     AH,02H
          CMP     LPI,1EH         ; CPI=7.5
          LPISET: CMP     LPI,1EH  ; LPI=4 ?
          RET

```

Appendix A.

```

0FC6 74 17          JE      ST999          ; LPI=5 ?
0FC8 80 C4 08      ADD     AH,08H
0FCB 80 3E 0004 R 18 CMP     LPI,18H
0FD0 74 0D          JE      ST999          ; LPI=6 ?
0FD2 80 C4 08      ADD     AH,08H
0FD5 80 3E 0004 R 14 CMP     LPI,14H
0FDA 74 03          JE      ST999          ; LPI=7.5
0FDC 80 C4 08      ADD     AH,08H
0FDF 88 26 0002 R  ST999: MOV     RETURN_CODE,AH
0FE3 5A             POP     DX
0FE4 C3             RET
0FES

STATUS2 ENDP
;-- BEEP
; BEEP WHEN BUSY
;
;
;
BEEP PROC NEAR
0FES FF 0E 0018 R   DEC     PR_TIME2
0FE9 75 11        JNZ     BEEP99
0FEB FF 0E 0016 R   DEC     PR_TIME1
0FEF 75 0B        JNZ     BEEP99
0FF1 B8 0E07      MOV     AX,0E07H ; BEEP
0FF4 CD 10        INT     10H
;
; MOV     PR_TIME1,1
;
BEEP99: RET
;
BEEP ENDP
1040 ORG     BEGIN+1040H
1040 CODE ENDS
END

```

```

XXXXXXXXXXXXX
XXXXXXXXXXXXX
X          X
X MODULE 7 X
X          X
XXXXXXXXXXXXX
XXXXXXXXXXXXX
;=== INT 19 =====
; BOOT STRAP LOADER
; TRACK 0, SECTOR 1 IS READ INTO THE
; BOOT LOCATION (SEGMENT 0, OFFSET 7C00)
; AND CONTROL IS TRANSFERRED THERE.
;
; IF THE DISKETTE IS NOT PRESENT OR HAS A
; PROBLEM LOADING (E.G., NOT READY), AN INT.
; 18H IS EXECUTED. IF A CARTRIDGE HAS VECTORED
; INT. 18H TO ITSELF, CONTROL WILL BE PASSED TO
; THE CARTRIDGE.
;
; THIS ROUTINE SET INITIAL CARTRIDGE PROCESS
; AND ALSO CHECK DISKETTE IS BOOTABLE OR NOT
;-----
= 001E
0000 EQU CS:CODE,DS:ABS0
0000 FB BOOT_STRAP EQU 1EH
; ENABLE EXTERNAL INTERRUPTS
;
0001 B4 01 MOV AH,1 ; INITIALIZE PRINTER
0003 33 D2 XOR DX,DX
0005 CD 17 INT INT_17
;
0007 CD 7B INT 7BH ; GO SYSTEM CARTRIDGE HANDLING
;
0009 CD 11 H1: INT INT_11 ; CHECK APPLICATION MODE
000B 24 30 AND AL,30H ;
000D 3C 20 CMP AL,20H ; EXTENSION MODE ?
000F 74 15 JE H2 ; YES-SKIP FOLLOWING VIDEO SET MODE
;
0011 B8 0011 MOV AX,0011H ; SET 20X11 COLOR MODE FOR DEFAULT
0014 CD 10 INT INT_10 ;
0016 B8 1000 MOV AX,1000H ; SET 20X11 DEFAULT COLOR BLUE
0019 B8 0100 MOV BX,0100H ; FOREGROUND TO BLUE
001C CD 10 INT INT_10 ;
001E B8 1001 MOV AX,1001H ; INCLUDING BORDER COLOR TO BLUE
0021 B8 0100 MOV BX,0100H ;
0024 CD 10 INT INT_10 ;
;
0026 B8 FF20 H2: MOV AX,KKNINIT ; INITIALIZE KANA-KAN CONVERSION
0029 CD 7B INT 7BH ;
;
002B E8 0000 E ASSUME DS:DATA
002E 8B 3E 0302 R CALL DDS
MOV DI,JEQUIP_FLAG ; GET DRIVE CONFIGURATION
;
0032 80 0E 0336 R 08 OR JKB_FLAG,08H ; FLAG TO KANA-KAN INITIALIZED
0037 B8 0500 MOV AX,0500H ; INITIALIZE KEYBOARD
003A CD 16 INT INT_16 ;
;-----
003C 2B C0 ASSUME DS:ABS0
003E 8E D8 SUB AX,AX ; ESTABLISH ADDRESSING
0040 8E C0 MOV DS,AX
0042 BB 7C00 R MOV ES,AX ; SET DISKETTE BUFFER ADDR
MOV BX,OFFSET BOOT_LOCN ;
;-----
0045 C7 06 0078 R 0000 E ;----- RESET THE DISK PARAMETER TABLE VECTOR
004B 8C 0E 007A R MOV WORD PTR DISK_POINTER,OFFSET DISK_BASE
MOV WORD PTR DISK_POINTER+2,CS
;-----
004F 33 F6 ;----- LOAD SYSTEM FROM DISKETTE
XOR SI,SI ; RESET DISKETTE INSTALLED FLAG
;
0051 33 D2 XOR DX,DX ; HEAD 0, DRIVE 0
0053 D1 CF ROR DI,1 ; DISKETTE DRIVE ATTACHED ?
0055 73 2F JNC H7 ; NO -GOTO NEXT DRIVE CHECK
;
0057 B9 0004 MOV CX,4 ; SET RETRY COUNT
005A 51 PUSH CX ; SAVE RETRY COUNT
005B B4 00 MOV AH,0 ; RESET THE DISKETTE SYSTEM
005D CD 13 INT INT_13 ;
005F 72 08 JC H5 ; IF ERROR, TRY AGAIN
;
0061 B8 0201 MOV AX,0201H ; READ OP., 1 SECTOR
0064 B9 0001 MOV CX,0001H ; TRACK 0, SECTOR 1
0067 CD 13 INT INT_13 ; READ IN THE SINGLE SECTOR
0069 59 POP CX ; RESTORE RETRY COUNT
006A 73 09 JNC H6 ; IF GOOD, GOTO FORMAT CHECK
006C F6 C4 80 TEST AH,80H ; TIMEOUT ERROR ?
006F 75 15 JNZ H7 ; YES-GOTO NEXT DRIVE CHECK
0071 E2 E7 LOOP H4 ; DO IT FOR RETRY TIMES
0073 EB 11 JMP SHORT H7 ;
;
0075 BE FFFF ;----- CHECK DISKETTE FORMAT
H6: MOV SI,0FFFFH ; SET DISKETTE INSTALLED FLAG
;
0078 81 7F 03 4249 CMP DS:WORD PTR BOOT_LOCN1[BX], 'BI' ; FORMAT GOOD ?
007D 75 07 H7 ; NO -
007F 81 7F 05 4A4D CMP DS:WORD PTR BOOT_LOCN2[BX], 'JM'
0084 74 22 JE GO_BOOT ; YES-GOTO BOOT PROGRAM
;
0086 42 H7: INC DX ; SET DRIVE 0 FOR NEXT DRIVE
;
0087 80 FA 04 CMP DL,4 ; END OF DRIVES ?
008A 72 C7 JB H3 ; NO -
;
008C 0B F6 OR SI,SI ; ANY DISKETTE INSTALLED ?
008E 74 16 JZ H9 ; NO -
;
0090 BE 00B4 R ;----- DISKETTE WAS INSTALLED, BUT SYSTEM DISKETTE WAS NOT INSTALLED.
MOV SI,OFFSET BOOT_ERR ; DISPLAY ERROR MESSAGE

```

Appendix A.

```

0093 89 003E
0096 2E 8A 04
0099 46
009A E8 0000 E
009D E2 F7

009F 84 00
00A1 CD 16

00A3 E9 0009 R

00A6 CD 18

00A8
00A8 59

00A9 80 CA 40
00AC 88 57 1E
00AF EA 7C00 ---- R

00B4 90 B3 82 B5 82 A2
      20 83 56 83 58 83
      65 83 80 20 83 65
      DE 82 83 58 83 50
      AF 83 67 82 F0 8D
      B7 82 B5 8D 9E 82
      DD 2C

00DA 0A0D
00DC 89 FC 8D 73 83 4C
      81 58 82 F0 89 9F
      82 B5 82 C4 89 BA
      82 B3 82 A2

00F2

0100
0100

HB:   MOV     CX,62           ;
      MOV     AL,CS:[SI]     ;
      INC     SI             ;
      CALL    PRT_HEX        ;
      LOOP   HB              ;

      MOV     AH,0           ; WAIT ANY KEY INPUT
      INT    INT_16         ;

      JMP     HI             ; RETRY FROM 1ST DRIVE

;----- UNABLE TO IPL FROM THE DISKETTE
H9:   INT    INT_18         ; GO TO BASIC OR APPLICATION CARTRIDGE

;----- IPL WAS SUCCESSFUL
GO_BOOT:
      POP     CX             ; ADJUST STACK POINTER

      OR     DL,40H          ; SET DOUBLE TRACK SYSTEM DISKETTE
      MOV    DS:BYTE PTR LBOOT[BX],DL ; OR LOGICAL BOOT DISKETTE
      JMP    BOOT_LOCH      ; GO BOOT LOCATION

;-----
; MESSAGE AREA
;-----
BOOT_ERR DB "正しい システム ディスケットを差し込み。"

      DW     0A0DH
      DB     "改行キーを押して下さい"

BOOT_STRAP      ENDP

CODE      ORG     BEGIN+100H
          ENDS
          END

```

```

XXXXXXXXXXXX
X          X
X  MODULE 8  X
X          X
XXXXXXXXXXXX

```

```

0000
= 0000

```

```

0000 ??
= 0001
= 0002
= 0004
= 0008
= 0010
= 0020
= 0080

```

```

0001 ???
= 0000
= 0002
= 0004
= 0001
= 0000
= 0006
= 0040
= 0010
= 00C0

```

```

=
=
0003 ??
0004 ??
0005 ??
0006 ??
0007 ??
0008 ??

```

```

0009 6C [ ?? ]

```

```

0075 6C [ ?? ]

```

```

= 0000
00E1 ??
= 0001
= 0080
00E2 ??
= 0003
= 0088

```

```

00E3 ??
= 0000
= 0001

```

```

00E4 ???

```

```

00E6 ???
00E8 ???
= 000F

```

```

00EA ???
00EC ???

```

```

00EE ???
= 0030
= 0015

```

```

00F0 ??
00F1 ???

```

```

00F3 ???
= 183C
= 0A11
= 1811

```

```

00F5 ???
= 8140

```

```

00F7 ???
= 0000
= 0002
= 0004
= 0008

```

```

00FA ??
= 0001
= 0002
= 0003

```

```

;XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
;M
;M          KANA-KANJI DATA DEFINITION
;M
;XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
;
;KKDATA SEGMENT AT 80H
;
;DSTART EQU 6
;
;XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
;M          DATA AEAR
;XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
;
;M
;DSTAT DB ? ;DATA STATUS
;HANKAF EQU 01H ;HANKAKU
;ZENZF EQU 02H ;ZENKAKU
;FUNCF EQU 04H ;FUNCTION
;ZENMENF EQU 08H ;ZENMEN
;KANJIF EQU 10H ;KANJI
;ZENIF EQU 20H ;ZENKAKU HIGH BYTE
;DAKUF EQU 80H ;DAKUTEN/HANDAKUTEN
;
;KBDSTAT DW ? ;KEYBOARD STASAS
;ESHIFT EQU 00H ;EISU SHIFT
;KSHIFT EQU 02H ;KATAKANA SHIT
;HSHIFT EQU 04H ;HIRAGANA SHIFT
;ZMODE EQU 01H ;ZENKAKU MODE
;HMODE EQU 00H ;HANKAKU MODE
;KBDMSK EQU 06H ;KBD STATUS MASK PATTERN
;CAPSST EQU 40H ;CAPS STATUS
;CAPSON EQU 10H ;CAPS LOCK
;KKNOCHG EQU 0C0H ;KANA-KAN REQUIREMENT NOT CHANGED
;
;KKKYDCD EQU CHAR1 ;KEY DATA CODE(6BYTE)
;KKINKEY EQU CHAR1
;CHAR1 DB ? ;CHARACTER 1
;ATTR1 DB ? ;ATTRIBUTE 1
;SCAN1 DB ? ;SCAN CODE 1
;CHAR2 DB ? ;CHARACTER 2
;ATTR2 DB ? ;ATTRIBUTE 2
;SCAN2 DB ? ;SCAN CODE 2
;
;-----
;
;KKINBUF DB 108 DUP(?) ;YOMI INPUT BUFFER(120BYTE)
;
;KKINBFSV DB 108 DUP(?) ; YOMI INPUT BUFFER SAVE
;
;
;HANATTR EQU 0 ;HANKAKU ATTRIBUTE
;ZENATTR1 DB ? ;HIGH BYTE OF ZENKAKU ATTRIBUTE
;ZATTR15 EQU 01H ;80x25
;ZATTR1N EQU 80H ;80x11,40x11
;ZENATTR2 DB ? ;LOW BYTE OF ZENKAKU ATTRIBUTE
;ZATTR25 EQU 03H ;80x25
;ZATTR2H EQU 88H ;80x11,40x11
;
;ROATCHT DB ? ;ROTATE ATTRIBUTE COUNTER
;ROT25 EQU 0 ;80x25
;ROT11 EQU 1 ;80x11,40x11
;
;SPACE DW ? ;SPACE HANKAKU DATA
;
;KKWCCA DW ? ; BUFFER CONTECUTAL CURSOR
;KKEOPMAX DW ? ; END OF YOMI POINTER ( MAX )
;BEOPMAX EQU 15 ;BANGOU MAXIMUM EOP
;
;KKWEOP DW ? ; END OF YOMI POINTER
;KKWEOPSV DW ? ; SAVE AREA FOR END OF YOMI POINTER
;
;BASE DW ? ; START COLUMN OF DISPLAY INPUT BUFF.
;BBASE25 EQU 48 ;BANGOU 80x25
;BBASE11 EQU 21 ;BANGOU 80x11/40x11
;
;KKWINST DB ? ; INSERT MODE FLAG TABLE
;KKINP DW ? ;DISPLAY POINTER
;
;KKCURSR DW ? ;KANA-KAN CURSOR POSITION
;KCSRFP25 EQU 183CH ;KANJI CURSOR POSITION (80x25)
;KCSRFP11 EQU 0A11H ; (X 11)
;KCSRFP4025 EQU 1811H ; (40x25)
;
;KKCHRSP DW ? ;CLEAR DATA OF INPUT BUFFER
;ZENSP EQU 8140H ;ZENKAKU SPACE DATA
;
;KKFORPIT DW ? ;MOVE FORWARD POINTER
;
;KKMODE DB ? ;KANA-KANJI MODE
;NORMALM EQU 0 ;NORMAL MODE
;KANJIM EQU 2 ;KANJI MODE
;SELECTM EQU 4 ;KANJI SELECT MODE
;CODEM EQU 8 ;KANJI BAGOU MODE
;
;TVMODE DB ? ;TV MODE
;TV8025 EQU 1 ;TV MODE 80x25
;TV8011 EQU 2 ; 80x11
;TV4011 EQU 3 ; 40x11

```

Appendix A.

```

00FB ??          ;
= 0050          TVLINE DB ? ;NUMBERS OF CHARACTER IN 1 LINE
= 0028          TVL80 EQU 80 ;80x25
                TVL40 EQU 40 ;80x11/40x11
                ;
00FC ??          ;
= 0018          KKOILP DB ? ;OIL POSITION
= 000A          OILP25 EQU 24 ;80x25 POSITION
                OILP11 EQU 10 ;80x11/40x11 POSITION
                ;
00FD ??          ;
00FE ??          KHBLK DB ? ;KOUHO BLOCK NO.
                KHBLKMX DB ? ;MAXIMUM BLOCK NO.
                ;
00FF ?????      ;
                APKBDST DW ? ;SAVE KBD STATUS FOR APPLICATION
                ;
0101 ??          ;
= 001F          DOILP DB ? ;DATA POSITION OF OIL
= 000C          DOILP25 EQU 31 ;80x25 POSITION
= 000C          DOILP11 EQU 12 ;80x11 POSITION
= 000C          DOILP11K EQU 12 ;40x11 POSITION (KANJI MODE)
= 0000          DOILP11S EQU 00 ;40x11 POSITION (SELECT MODE)
= 0000          CLINDI1P EQU 00 ;40x11 INDICATOR POSITION
0102 ??          DOILN DB ? ;NUMBER OF OIL DATA
= 0031          DOILN25 EQU 49 ;80x25 NUMBER
= 0044          DOILN11 EQU 68 ;80x11 NUMBER
= 001C          DOILN11K EQU 28 ;40x11 NUMBER (KANJI MODE)
= 0028          DOILN11S EQU 40 ;40x11 NUMBER (SELECT MODE)
= 000C          CLINDI1N EQU 12 ;40x11 NUMBER OF INDICATOR
                ;
0103 ??          ;
= 0027          BANGGUP DB ? ;BANGOU DATA POSITION
= 000C          BANP25 EQU 39 ;80x25 POSITION
                BANP11 EQU 12 ;80x11/40x11 POSITION
                ;
0104 ??          ;
= 002F          BASTERP DB ? ;ASTERISK POSITION IN KKOUTBUF
= 001B          BAST25 EQU 47 ;80x25 POSITION
                BAST11 EQU 27 ;80x11/40x11 POSITION
                ;
0105 ?????      ;
0107 ?????      DCRSRP DW ? ;CURSOR POSITION (KKDISP)
0109 ?????      DSTRTP DW ? ;START POSITION (KKDISP)
                DENDP DW ? ;END POSITON (KKDISP)
                ;
010B 07 [       ;-----M
                KKGVHYM DB 7 DUP(?) ; YOMI WORK
                ]
                ;
0112 ?????      ;
0114 ?????      KKDICLN DW ? ; DICTIONARY INDEX LENGTH
0116 ?????      KKDICFF DW ? ; DICTIONARY TOP OFFSET
                KKLENYM DW ? ; YOMI LENGTH ON INDEX
                ;
= 0000          ;
= 0001          KKECNRML EQU 0 ; NORMAL
= 0004          KKECNOTF EQU 1 ; YOMI NOT FOUND
= 0008          KKECYMLN EQU 4 ; YOMI LENGTH EXCEPTION
= 0010          KKECYMHR EQU 8 ; HIRAGANA YOMI EXCEPTION
                KKECDICT EQU 16 ; DICTIONARY FORMAT ERROR
                ;
0118 ??          ;
0119 25 [       KKYOMIL DB ? ; LENGTH OF YOMI
                KKYOMI DB 37 DUP(?) ; YOMI BUFFER
                ]
                ;
013E ?????      ;
0140          ;
0140 ?????      KKOHOSUU DW ? ; TOTAL NUMBER OF KOUHO
0142 ?????      KKDICADR LABEL DWORD
                KKDICOFF DW ? ; DICTIONARY OFFSET
                KKDICSEG DW ? ; DICTIONARY SEGMENT ADDRESS
                ;
0144 ?????      KKOHOZAN DW ? ; ZAN
0146 ?????      KKOHOCHT DW ? ; DISPLAY KOUHO COUNTER
0148 ?????      KKHODSP DW ? ; DISPLAY KOUHO PRINTER
                ;
014A 44 [       ;-----M
                KKHANQUE DB 17x4 DUP(?) ; QUEING BUFFER
                ]
                ;
018E ?????      ;
0190 ??          ;
= 0001          KKCHRM DB ?
= 0002          MDZENNUM EQU 01 ; ZENKAKU NUMERIC CHARACTER
= 0003          MDZENHIR EQU 02 ; ZENKAKU HIRAGANA CHARACTER
= 0004          MDZENKAT EQU 03 ; ZENKAKU KATAKANA CHARACTER
= 0005          MDHANNUM EQU 04 ; HANKAKU NUMERIC CHARACTER
                MDHANKAT EQU 05 ; HANKAKU KATAKANA CHARACTER
                ;
0191 ??          ;
= 0001          KKYMSTS DB ?
= 0002          KKYMSTE EQU 00000001B ; PROCESS END FLAG
= 0004          KKYMSTM EQU 00000010B ; MOVE FORWARD FLAG
= 0008          KKYMSTD EQU 00000100B ; DAKUTEN, HANDAKUTEN FLAG
= 0080          KKYMSTH EQU 00001000B ; NUMERIC KEY CONVERSION FLAG
                KKYMSTER EQU 10000000B ; ERROR FLAG
                ;
0192 ??          ;
0193 ??          ;
= 00FC          ;
= 0077          ATTRMSK DB ?
                ATMSK25 EQU 0FCH ;ATTRIBUTE MASK
                ATMSK11 EQU 77H ;80x25
                ;
0194 ??          ;
                OLDMODE DB ? ;OLD KANA-KAN MODE
                ;
= 0195          ;
                DEND EQU 6-DSTART
                ;

```











0131  
0131 88 1E 0003 R  
0135 88 26 0004 R  
0139 88 3E 0005 R  
013D A2 0000 R  
0140 EB 1B 90

0143  
0143 E8 0166 R  
0146 3C 00  
0148 75 13  
014A 88 1E 0006 R  
014E A0 00E2 R  
0151 A2 0007 R  
0154 88 3E 0008 R  
0158 C6 06 0000 R 02

015D  
015D B4 02  
015F CD 16  
0161 A3 0001 R  
0164 5A  
0165 C3  
0166

0166  
0166 8B 3E 00E6 R  
016A 83 EF 06  
016D 72 78  
016F 8A 45 0009 R  
0173 8A 85 000A R  
0177 3A 06 00E1 R  
017B 75 6A  
017D 80 FC 82  
0180 74 05  
0182 80 FC 83  
0185 75 60  
0187  
0187 8A 85 000C R  
018B 81 FB 1ADE  
018F 74 1C  
0191 81 FB 1A4A  
0195 74 16  
0197 81 FB 1BDF  
019B 74 06  
019D 81 FB 1B4B  
01A1 75 44

01A3  
01A3 BE 0208 R  
01A6 B9 000A  
01A9 B6 02  
01AB EB 08

01AD  
01AD BE 01EA R  
0180 B9 0028  
0183 B6 01  
0185  
0185 2E 3A 04  
0188 74 05  
018A 46  
018B E2 F8  
018D EB 28  
018F  
018F 02 C6  
01C1 88 26 0003 R  
01C5 A2 0006 R  
01C8 A0 00E1 R  
01CB A2 0004 R  
01CE A0 00E2 R  
01D1 A2 0007 R  
01D4 8A 85 000B R  
01D8 A2 0005 R  
01DB A2 0008 R  
01DE C6 06 0000 R 82  
01E3 80 01  
01E5 EB 02  
01E7  
01E7 32 C0  
01E9  
01E9 C3  
01EA

01EA A9 AB AD AF B1  
01EF B3 B5 B7 B9 BB  
01F4 BD BF C2 C4 C6  
01F9 4A 4C 4E 50 52  
01FE 54 56 58 5A 5C

```

CSC070:
MOV CHAR1,BL
MOV ATTR1,AH
MOV SCAN1,BH
MOV DSTAT,AL
JMP CSC090
;SET HANKAKU CHARACTER
;SET ATTRIBUTE
;SET SCAN CODE
;SET DATA STATUS
;-----
;M ZENKAKU PROCESS
;M
CSC080:
CALL HANDAKU
CMP AL,0
JNE CSC090
MOV CHAR2,BL
MOV AL,ZENATTR2
MOV ATTR2,AL
MOV SCAN2,BH
MOV DSTAT,ZEN2F
;DAKUTEN OR HANDAKUTEN PROCESS
;DAKUTEN OR HANDAKUTEN ?
; YES. GOTO
;SET ZENKAKU CHARACTER
;SET ZENKAKU-2 ATTRIBUTE
;SET SCAN CODE
;ZENKAKU FLAG ON
;-----
;M RETURN TO CALLER
;M
CSC090:
MOV AH,D02
INT 16H
MOV KBDSTAT,AX
POP DX
RET
;READ KBD STATUS
;SET KANA-KAN KBD STATUS
;RETURN TO CALLER
CSCAN ENDP
;***** HANDAKU *****
;M
;M DAKUTEN/HANDAKUTEN PROCESS
;M
;M *****
HANDAKU PROC
MOV DI,KKWCCA
SUB DI,D06
JB HAN080
MOV AH,KKINBUF[DI]
MOV AL,KKINBUF[DI]+1
CMP AL,ZENATTR1
JNE HAN080
CMP AH,ZENHIRA
JE HAN000
CMP AH,ZENKATA
JNE HAN080
;LOAD CURSOR POINTER IN KKINBUF
;CURSOR POINTER IS AT TOP ?
; YES. GOTO
;LOAD HIGH BYTE OF ZENKAKU
;LOAD ATTRIBUTE-1
;DATA IS ZENKAKU ?
; NO. GOTO
;HIRAGANA ?
; YES. GOTO
;KATAKANA ?
; NO. GOTO
HAN000:
MOV AL,KKINBUF[DI]+3
CMP BX,KYDAKU1
JE HAN040
CMP BX,KYDAKU2
JE HAN040
CMP BX,KYHAND1
JE HAN005
CMP BX,KYHAND2
JNE HAN080
;LOAD LOW BYTE OF ZENKAKU
;DAKUTEN ?
; YES. GOTO
;HANDAKUTEN ?
; NO. GOTO
;-----
;M HANDAKUTEN PROCESS
;M
;M
HAN005:
MOV SI,OFFSET HAGYOH
MOV CX,HANDNUM
MOV DH,D02
JMP SHORT HAN050
;LOAD HA GYOU DATA OFFSET
;LOAD NUMBERS OF HANDAKUTEN CHARACTER
;GOTO CHARACTER CHECK
;-----
;M DAKUTEN PROCESS
;M
;M
HAN040:
MOV SI,OFFSET KAGYOH
MOV CX,DAKUNUM
MOV DH,D01
HAN050:
CMP AL,CS:[SI]
JE HAN060
INC SI
LOOP HAN050
JMP SHORT HAN080
;LOAD KA GYOU DATA OFFSET
;LOAD NUMBERS OF DAKUTEN CHARACTER
;CHECK DAKUTEN CHARACTER ?
; YES. GOTO
;UNMATCH. GOTO
HAN060:
ADD AL,DH
MOV CHAR1,AH
MOV CHAR2,AL
MOV AL,ZENATTR1
MOV ATTR1,AL
MOV AL,ZENATTR2
MOV ATTR2,AL
MOV AL,KKINBUF[DI]+2
MOV SCAN1,AL
MOV SCAN2,AL
MOV DSTAT,ZEN2F+DAKUF
MOV AL,D01
JMP SHORT HANRTH
;MAKE DAKUTEN/HANDAKUTEN CHARACTER
;SET HIGH BYTE OF ZENKAKU
;SET LOW BYTE OF ZENKAKU
;SET ATTRIBUTE-1
;SET ATTRIBUTE-2
;LOAD SCAN CODE
;SET SCAN CODE
;SET SCAN CODE
;SET ZENKAKU DAKUTEN/HANDAKUTEN FLAG
;SET RETURN CODE OF DAKUTEN/HANDAKUTEN
HAN080:
XOR AL,AL
HANRTH:
RET
HANDAKU ENDP
;-----
;M DAKUTEN/HANDAKUTEN CHARACTER
;M
;M
KAGYOH: DB 0A9H,0ABH,0ADH,0AFH,0B1H
SAGYOH: DB 0B3H,0B5H,0B7H,0B9H,0BBH
TAGYOH: DB 0BDH,0BFH,0C2H,0C4H,0C6H
KAGYOK: DB 4AH,4CH,4EH,50H,52H
SAGYOK: DB 54H,56H,58H,5AH,5CH

```

Appendix A.

```

0203 5E 60 63 65 67
0208 CD D0 D3 D6 D9
020D 6E 71 74 77 7A

0212
0212 80 07
0214 B4 0E
0216 CD 10
0218 C3
0219

TAGYOK: DB 5EH,60H,63H,65H,67H
HAGYOH: DB 0CDH,0D0H,0D3H,0D6H,0D9H
HAGYOK: DB 6EH,71H,74H,77H,7AH
;**** SPEAKER ****
;M SOUND SPEAKER FOR INVALID OPERATION
;M
;M *****
SPEAKER PROC AL,BEL
MOV AH,DOE ;SPEAKER ON
MOV INT 10H ;RETURN TO CALLER
RET
SPEAKER ENDP
;M *****
;M PROGRAM NAME: KKINIT
;M
;M DESCRIPTIVE NAME: KANA-KAN INITIALIZATION
;M
;M FUNCTION: THIS ROUTINE IS INITIALIZATION FOR KANA-KANJI HENKAN.
;M IT SETS TELEVISION MODE.
;M
;M LINKAGE: CALL
;M
;M INPUT: NONE
;M
;M OUTPUT: KANA-KAN COMMON TABLES
;M
;M RETURN CODES: NONE
;M
;M EXTERNAL REFERENCES:
;M
;M ROUTINES: NONE
;M
;M TABLES: KANA-KAN COMMON TABLES
;M
;M REGISTERS: BX,CX,DX,DI - WORK
;M ALL OTHERS UNCHANGED
;M
;M CHANGE ACTIVITY: VERSION 00.00
;M
;M *****
;M ***** KKINIT *****
;M
;M ENTRY OF KKINIT
;M
;M *****
KKINIT PROC NEAR
PUSH AX
PUSH AX
;-----
;M GET TV MODE
;M -----
MOV AH,DOF
INT 10H ;READ DISPLAY STATUS
MOV DX,AX
INT 11H ;READ EQUIPMENT
POP BX
AND AL,D30 ;IGNORE MEANINGLESS BIT

0226 3C 20 CMP AL,D20 ;SYSTEM MODE IS 5550 EM ?
0228 74 0D JE INI002 ; YES. GOTO
022A 80 FE 50 CMP DH,80 ;TV MODE IS 80*11 ?
022D 74 04 JE INI000 ; YES. GOTO
022F B2 03 MOV DL,TV4011 ;LOAD 40*11 MODE
0231 EB 23 JMP SHORT INI006
0233
0233 B2 02 INI000: MOV DL,TV8011 ;LOAD 80*11 MODE
0235 EB 1F JMP SHORT INI006
0237
0237 80 26 0195 R FD AND KKFLAG,0FDH ;
023C 80 FA 0B CMP DL,D0B ;COLOR GRAPHIC MODE ?
023F 74 0E JE INI004 ; YES. GOTO
0241 80 FA 0E CMP DL,DOE ;COLOR TEXT MODE ?
0244 75 05 JNE INI003 ; NO. GOTO
0246 80 0E 0195 R 02 OR KKFLAG,CLRF25 ;SET 80*25 COLOR FLAG
024B
024B B2 01 INI003: MOV DL,TV8025 ;LOAD 80*25 MODE
024D EB 07 JMP SHORT INI006
024F
024F 80 0E 0195 R 02 INI004: OR KKFLAG,CLRF25 ;SET 80*25 COLOR FLAG
0254 B2 03 MOV DL,TV4011 ;LOAD 40*25 MODE
0256
0256 81 FB FF20 INI006: CMP BX,KINITX ;REQUIREMENT IS INITIAL ?
025A 74 09 JE INI008 ; YES. GOTO
025C 38 16 00FA R CMP TVMODE,DL ;CHANGE TV MODE ?
0260 75 03 JNE INI008 ; YES. GOTO
0262 E9 033C R JMP INI008
0265
0265 BF 0000 R INI008: MOV DI,OFFSET DSTART ;LOAD KANA-KAN CONTROL TABLE OFFSET
0268 B9 0195 MOV CX,DEND ;CAPACITY OF KANA-KAN CONTROL TABLE
026B 32 C0 XOR AL,AL ;LOAD 00H
026D
026D AA INI009: STOSB ;CLEAR KANA-KAN CONTROL TABLE
026E E2 FD LOOP
0270 80 26 0195 R 03 AND KKFLAG,03H ;CLEAR KKFLAG WITHOUT KANA-KAN ON/OFF FLAG
0275 88 16 00FA R MOV TVMODE,DL ;SET TV MODE
0279 C7 06 00E4 R 0020 MOV SPACE,BLANK ;SET HANKAKU SPACE DATA
027F 80 FA 03 CMP DL,TV4011 ;TV MODE IS 40*11 ?
0282 74 3F JE TV4011L ; YES. GOTO
0284 80 FA 02 CMP DL,TV8011 ;TV MODE IS 80*11 ?

```



Appendix A.

034D 74 1D  
 034F 3D 6B20  
 0352 74 18  
 0354 3D 6B40  
 0357 74 13  
 0359  
 0359 80 FC FF  
 035C 74 18  
 035E 3D 6B00  
 0361 74 05  
 0363 80 FC 6B  
 0366 74 0E  
 0368  
 0368 CD 79  
 036A EB 0A  
 036C  
 036C B3 80  
 036E E8 1139 R  
 0371 E8 0CA2 R  
 0374 EB 02  
 0376  
 0376 33 C0  
 0378  
 0378 C3  
 0379

```

        JE      NOR010      ;ENTER KANJI MODE ?
        CMP    AX,KYKANJX   ; YES. GOTO
        JE      NOR010      ;ENTER KANJI MODE ?
        CMP    AX,KYKANJ2   ; YES. GOTO
        JE      NOR010
NOR000:      JE      ;OTHER KANJI REQUIREMENT ?
        CMP    AH,KANJAH    ; YES. GOTO
        JE      NOR020
        CMP    AX,SCAN6B0
        JE      NOR001
        CMP    AH,SCAN6B
        JE      NOR020
NOR001:      INT     79H      ;BUFFER QUEUING
        JMP    SHORT NOR020
NOR010:      MOV    BL,D80
        CALL   KKWOIL      ;INITIALIZE INDICATOR
        CALL   KKZINT      ;KANJI MODE INITIALIZTION
        JMP    SHORT NORRTH
NOR020:      XOR    AX,AX    ;SET NORMAL RETURN CODE
NORRTH:      RET          ;RETURN TO CALLER
KKNOML ENDP
    
```

```

;*****
;M
;M PROGRAM NAME: KKBCTL
;M
;M DESCRIPTIVE NAME: BANGOU CONTROL CSECT
;M
;M FUNCTION: THIS ROUTINE BRANCHES TO EACH PROCESS THAT IS THE
;M GRAPHIC, CURSOR OR FUNCTION.
;M
;M LINKAGE: CALL
;M
;M INPUT: KANA-KAN COMMON TABLES
;M
;M OUTPUT: NONE
;M
;M RETURN CODES: (AX)
;M
;M 0 - SUCCESSFUL
;M 1 - INVALID OPERATION
;M
;M EXTERNAL REFERENCES:
;M
;M ROUTINES: KKBGRP - BANGOU GRAPHIC KEYS
;M KKCSR - BANGOU CURSOR KEYS
;M KKBFNC - BANGOU FUNCTION KEYS
;M
;M TABLES: KANA-KAN COMMON TABLES
;M
;M REGISTERS: AX - RETURN CODE
;M ALL OTHERS UNCHANGED
;M
;M CHANGE ACTIVITY: VERSION 00.00
;M
;M
;M *****
;M ***** KKBCTL *****
;M
;M ENTRY OF KKBCTL
;M
;M *****
    
```

0379  
 0379 F6 06 0000 R 83  
 037E 74 05  
 0380 E8 039F R  
 0383 EB 19  
 0385  
 0385 8A 26 0005 R  
 0389 A0 0003 R  
 038C 3D 4B00  
 038F 74 0A  
 0391 3D 4D00  
 0394 74 05  
 0396 E8 03DE R  
 0399 EB 03  
 039B  
 039B E8 0C2E R  
 039E  
 039E C3  
 039F

```

KKBCTL PROC
TEST DSTAT,HANKAF+ZEN2F+DAKUF ;GRAPHIC KEY ?
JZ BCT010 ; NO. GOTO
CALL KKBGRP ;GRAPHIC KEY PROCESS
JMP SHORT BCTRTH
BCT010: MOV AH,SCAN1
        MOV AL,CHAR1 ;LOAD SCAN CODE/CHARACTER
        CMP AX,KYCSRLX ;CURSOR LEFT KEY ?
        JE BCT020 ; YES. GOTO
        CMP AX,KYCSRRX ;CURSOR RIGHT KEY ?
        JE BCT020 ; YES. GOTO
        CALL KKBFNC ;FUNCTION KEY PROCESS
        JMP SHORT BCTRTH
BCT020: CALL KKCSR ;CUSOR KEY PROCESS
BCTRTH: RET ;RETURN TO CALLER
KKBCTL ENDP
    
```

```

;*****
;M
;M PROGRAM NAME: KKBGRP
;M
;M DESCRIPTIVE NAME: BANGOU GRAPHIC KEYS
;M
;M FUNCTION: THIS ROUTINE DISPLAYS NUMERICAL DATA AND STACKS IT
;M IN INPUT BUFFER.
;M
;M LINKAGE: CALL
;M
;M INPUT: KANA-KAN COMMON TABLES
;M
;M OUTPUT: KANA-KAN COMMON TABLES
;M
;M RETURN CODES: (AX)
;M
;M 0 - SUCCESSFUL
;M 1 - INVALID OPERATION
;M
    
```

039F  
 039F A0 0005 R  
 03A2 3C 02  
 03A4 72 34  
 03A6 3C 0A  
 03A8 77 04

03AA 04 2F  
 03AC EB 1A  
 03AE  
 03AE 3C 0B  
 03B0 75 04  
 03B2 04 25  
 03B4 EB 12  
 03B6  
 03B6 3C 72  
 03B8 72 08  
 03BA 3C 7A  
 03BC 77 1C  
 03BE 2C 41  
 03C0 EB 06  
 03C2  
 03C2 3C 70  
 03C4 75 14  
 03C6 2C 40

03C8  
 03C8 A2 0003 R  
 03CB C6 06 0004 R 00  
 03DD C6 06 0000 R 01  
 03D5 EB 1209 R  
 03D8 EB 03

03DA  
 03DA B8 0001  
 03DD  
 03DD C3  
 03DE

```

;M
;M EXTERNAL REFERENCES: ;M
;M ;M
;M ROUTINES: KKGRP - GRAPHIC KEYS PROCESS ;M
;M ;M
;M TABLES: KANA-KAN COMMON TABLES ;M
;M ;M
;M REGISTERS: AX - RETURN CODE ;M
;M ALL OTHERS UNCHANGED ;M
;M ;M
;M CHANGE ACTIVITY: VERSION 00.00 ;M
;M ;M
;M ***** ;M
;M ***** KKBGRP ***** ;M
;M ;M
;M ENTRY OF KKBGRP ;M
;M ;M
;M ***** ;M
KKBGRP PROC ;M
MOV AL,SCAN1 ;LOAD SCAN CODE ;M
CMP AL,SCAN02 ;M
JB BGR050 ;M
CMP AL,SCAN0A ;NUMERICAL DATA (1<-->9) ? ;M
JA BGR010 ;NO. GOTO ;M
;M-----;M
;M MAKE DISPLAY DATA ( 0 <--> 9 ) ;M
;M-----;M
ADD AL,HUM19M1 ;MAKE 1<-->9 ;M
JMP SHORT BGR040 ;M
BGR010: ;M
CMP AL,SCAN0B ;NUMERICAL DATA (0) ? ;M
JHE BGR020 ;NO. GOTO ;M
ADD AL,HUM0M1 ;MAKE 0 ;M
JMP SHORT BGR040 ;M
BGR020: ;M
CMP AL,SCAN72 ;M
JB BGR030 ;M
CMP AL,SCAN7A ;NUMERICAL DATA OF TEN-KEY (1<-->9) ? ;M
JA BGR050 ;NO. GOTO ;M
SUB AL,HUM19M2 ;MAKE 1<-->9 ;M
JMP SHORT BGR040 ;M
BGR030: ;M
CMP AL,SCAN70 ;NUMERICAL DATA OF TEN-KEY (0) ;M
JHE BGR050 ;NO. GOTO ;M
SUB AL,NUM0M2 ;MAKE 0 ;M
;M-----;M
;M STACK AND DISPLAY ;M
;M-----;M
BGR040: ;M
MOV CHAR1,AL ;SET NUMERICAL DATA ;M
MOV ATTR1,HANATTR ;SET HANKAKU ATTRIBUTE ;M
MOV DSTAT,HANKAF ;SET HANKAKU STATUS ;M
CALL KKGRP ;GRAPHIC KEY PROCESS ;M
JMP SHORT BGRRTN ;M
;M-----;M
;M ABNORMAL RETURN ;M
;M-----;M
BGR050: ;M
MOV AX,D01 ;SET ABNORMAL RETURN CODE ;M
BGRRTN: ;M
RET ;RETURN TO CALLER ;M
KKBGRP ENDP ;M
;M ***** ;M
;M PROGRAM NAME: KKBFC ;M
;M ;M
;M DESCRIPTIVE NAME: BANGOU FUNCTION KEY ;M
;M ;M
;M FUNCTION: THIS ROUTINE TREATS ALL FUNCTION KEYS. ;M
;M THESE KEYS ARE THE KANJI, ESCAPE, BACK, DELETE, INSERT, ;M
;M ERASE EOF. THE OTHERS ARE INVALID. ;M
;M ;M
;M LINKAGE: CALL ;M
;M ;M
;M INPUT: KANA-KAN COMMON TABLES ;M
;M ;M
;M OUTPUT: KANA-KAN COMMON TABLE ;M
;M KANJI QUEUING ;M
;M ;M
;M RETURN CODES: (AX) ;M
;M ;M
;M 0 - SUCCESSFUL ;M
;M 1 - INVALID OPERATION ;M
;M ;M
;M EXTERNAL REFERENCES: ;M
;M ;M
;M ROUTINES: KKZINT - ZENKOUHO INITIALIZATION ;M
;M KKBINT - BANGOU INITIALIZATION ;M
;M KKBACK - BACK KEY PROCESS ;M
;M KKDEL - DELETE KEY PROCESS ;M
;M KKINST - INSERT KEY PROCESS ;M
;M KKEOF - ERASE EOF KEY PROCESS ;M
;M KKWOIL - WRITE OIL ;M
;M ;M
;M TABLES: KANA-KAN COMMON TABLES ;M
;M ;M
;M REGISTERS: AX - RETURN CODE ;M
;M ALL OTHERS UNCHANGED ;M
;M ;M
;M CHANGE ACTIVITY: VERSION 00.00 ;M
;M ;M
;M ;M

```





```

;M-----
;M      NORMAL RETURN
;M-----
0473      33 C0
0473      33 C0
0475
0475      5B
0476      59
0477      5A
0478      5E
0479      C3
047A
;M-----
;M      BFN090:
;M      XOR      AX,AX          ;SET NOMAL RETURN CODE
;M      BFNRTM:
;M      POP      BX          ;RESTORE REGISTER
;M      POP      CX
;M      POP      DX
;M      POP      SI
;M      RET
;M      ;RETURN TO CALLER
;M      KKBFFC  ENDP
;M      ;***** RINST *****
;M      ;M
;M      ;M      RESET INSERT MODE
;M      ;M
;M      ;M*****
;M      RINST  PROC
;M      XOR      AX,AX
;M      CALL  KKINST
;M      RET
;M      RINST  ENDP
;M      ;***** BHENKAN *****
;M      ;M
;M      ;M      BANGOU HENKAN
;M      ;M
;M      ;M*****
;M      BHENKAN PROC
;M      MOV      BX,KKHEOP
;M      CMP      BL,BNUMMIN
;M      JB      BHE030
;M      CMP      BL,BNUMMAX
;M      JA      BHE030
;M      MOV      SI,OFFSET KKBINBUF
;M      ;9 <= BANGOU NUM. => F ?
;M      ; NO. GOTO
;M      ;LOAD INPUT BUFFER OFFSET
;M-----
;M      CHECK TEN
;M-----
0491      E8 0506 R
0494      8A D0
0496      E8 0506 R
0499      B6 0A
049B      F6 E6
049D      02 D0
049F      80 FA 01
04A2      72 5E
04A4      80 FA 5E
04A7      77 59
;M-----
;M      CALL  CHRLD          ;LOAD TEN CHARACTER 1
;M      MOV   DL,AL
;M      CALL  CHRLD          ;LOAD TEN CHARACTER 2
;M      MOV   DH,10
;M      MUL  DH
;M      ADD  DL,AL          ;CHANGE DECIMAL TEN INTO BINARY
;M      CMP  DL,TEMMIN
;M      JB  BHE030
;M      CMP  DL,TEMMAX
;M      JA  BHE030
;M      ;1 <= TEN => 94 ?
;M      ; NO. GOTO
;M-----
;M      CHECK KU
;M-----
04A9      E8 0506 R
04AC      8A C8
04AE      32 ED
04B0      83 FB 00
04B3      74 15
04B5      E8 0506 R
04B8      F6 E6
04BA      03 C8
04BC      83 FB 00
04BF      74 09
04C1      E8 0506 R
04C4      B6 64
04C6      F6 E6
04C8      03 C8
04CA
04CA      83 F9 01
04CD      72 33
04CF      83 F9 78
04D2      77 2E
;M-----
;M      CALL  CHRLD          ;LOAD KU CHARACTER 1
;M      MOV   CL,AL
;M      XOR   CH,CH
;M      CMP  BX,D00
;M      JE  BHE000
;M      CALL  CHRLD          ;LOAD KU CHARACTER 2
;M      MUL  DH
;M      ADD  CX,AX
;M      CMP  BX,D00
;M      JE  BHE000
;M      CALL  CHRLD          ;LOAD KU CHARACTER 3
;M      MOV   DH,100
;M      MUL  DH
;M      ADD  CX,AX
;M      ;CHANGE DECIMAL KU INTO BINARY
;M      BHE000:
;M      CMP  CX,KUMIN
;M      JB  BHE030
;M      CMP  CX,KUMAX
;M      JA  BHE030
;M      ;01 <= KU => 120 ?
;M      ; NO. GOTO
;M-----
;M      CHANGE KU/TEN INTO INTERNAL CODE
;M-----
04D4      F6 C1 01
04D7      74 11
;M-----
;M      TEST  CL,D01          ;KU IS EVEN ?
;M      JZ   BHE010
;M      ; YES. GOTO
;M-----
;M      ODD KU
;M-----
04D9      FE C1
04DB      E8 050E R
04DE      80 C2 3F
04E1      80 FA 7F
04E4      72 0A
04E6      FE C2
04E8      EB 06
;M-----
;M      INC   CL
;M      CALL  CHGKU
;M      ADD  DL,3FH
;M      CMP  DL,7FH
;M      JB  BHE020
;M      INC  DL
;M      JMP  SHORT BHE020
;M-----
;M      EVEN  KU
;M-----
04EA
04EA      E8 050E R
04ED      80 C2 9E
;M-----
;M      BHE010:
;M      CALL  CHGKU
;M      ADD  DL,9EH
;M-----
;M      BUFFER QUEUING
;M-----
04F0
04F0      B4 FF
04F2      8A C1
04F4      CD 79
04F6      8A C2
04F8      CD 79
;M-----
;M      BHE020:
;M      MOV  AH,KANJAH
;M      MOV  AL,CL
;M      INT  79H
;M      MOV  AL,DL
;M      INT  79H
;M      ;SET INTERNAL CODE
;M      ;BUFFER QUEUING
;M      ;SET INTERNAL CODE
;M      ;BUFFER QUEUING
;M-----
;M      INITIALIZE BANGOU MODE
;M-----
04FA      E8 047A R
04FD      E8 051E R
;M-----
;M      CALL  RINST
;M      CALL  KKBINT

```



```

055A 32 E4
055C 8B F8
055E B0 2A
0560 E8 1139 R

0563 B8 FFFF
0566 8B 16 00EE R
056A 89 16 0105 R
056E A3 0107 R
0571 A3 0109 R
0574 E8 10ED R
0577 5B
0578 59
0579 5A
057A 5F
057B 5E
057C C3
057D BA B0 C4 DE 3D 3D
      3D 3E 20
0586 A0 A0 A0 A0 A0 2A
= 000F
058C

```

```

XOR AH,AH
MOV DI,AX
MOV AL,ASTER
CALL KKWOIL ;SET ASTERISK DATA
;-----
;M SET CURSOR
;-----
MOV AX,0FFFFH
MOV DX,BASE ;LOAD BAGOU BASE POSITION
MOV DCRSRP,DX
MOV DSTRTP,AX
MOV DENDP,AX
CALL KKDISP ;DISPLAY CURSOR
POP BX ;RESTORE REGISTER
POP CX
POP DX
POP DI
POP SI
RET ;RETURN TO CALLER
BMARK DB 0BAH,0B0H,0C4H,0DEH,3DH,3DH,3DH,3EH,20H
BNUMS EQU 15 ;NUMBER OF BMARK
KKBINT ENDP
;*****
;M PROGRAM NAME: KKZCTL
;M
;M DESCRIPTIVE NAME: KEY CONTROL FOR KANA-KAN ZENKOUHO MODE
;M
;M FUNCTION:
;M
;M THIS MODULE ANALYZE INPUT KEY FOR KANA-KANJI CONVERSION,
;M AND CALLS EACH FUNCTION ROUTINES.
;M
;M LINKAGE: CALLED BY KKKFDM
;M
;M INPUT:
;M KANA-KAN COMMON TABLES
;M
;M OUTPUT:
;M LINK EACH FUNCTION MODULE
;M
;M BP : FUNCTION KEY NUMBER
;M
;M RETURN CODES: (AX)
;M
;M 0 - SUCCESSFUL
;M 1 - INVALID OPERATION
;M
;M EXTERNAL REFERENCES:
;M
;M ROUTINES:
;M
;M KKZGRP ... GRAPHIC KEY DISPLAY
;M KKCSR ... CURSOR KEY PROCESS
;M KKZFNC ... FUNCTION KEY CONTROL
;M
;M TABLES:
;M
;M KANA-KAN COMMON TABLES
;M
;M REGISTERS: AX - RETURN CODE
;M BX,CX,DX,DI,SI,BP - WORK
;M ALL OTHERS UNCHANGED
;M
;M CHANGE ACTIVITY: VERSION 00.00
;M
;M
;M
;*****
;+-----+
;+ INPUT FUNCTION KEY CODE & PROC.ENTRY ADDRESS TABLE
;+-----+
KKCCDTB:

```

```

058C 4B00 063D R
0590 4D00 063D R
0594 6B20 060D R
0598 6B40 060D R
059C FF00 060D R
05A0 6B38 060D R
05A4 FF80 060D R
05A8 6D20 060D R
05AC 6D40 060D R
05B0 6C20 060D R
05B4 6C40 060D R
05B8 011B 060D R
05BC 5200 060D R
05C0 5300 060D R
05C4 0E08 060D R
05C8 7700 060D R
05CC 1C0D 060D R
05D0 7E0D 060D R
05D4 9300 060D R
05D8 FFFF 0641 R
= 004C
= 0000
= 0001
= 0002
= 0004
05DC 00
05DD 00

```

```

KKCCDTB:
DW KYCSRLX,KZCCSR ; CURSOR LEFTE
DW KYCSRRX,KZCCSR ; CURSOR RIGHT
DW KYKANJX,KZCFNC ; KANJI
DW KYKANJ2,KZCFNC ; KANJI
DW KEXITX,KZCFNC ; KANJI EXIT ( = KANJI )
DW KYCODEX,KZCFNC ; KANJI BANGOU
DW KCHNGX,KZCFNC ; KANJI MODE CHANGE ( = KANJI BANGOU )
DW KYHENKX,KZCFNC ; HENKAN
DW KYHENK2,KZCFNC ; HENKAN (ZENKAKU)
DW KYMUHEX,KZCFNC ; MUHENKAN
DW KYMUHE2,KZCFNC ; MUHENKAN (ZENKAKU)
DW KYESCPX,KZCFNC ; ESCAPE
DW KYINSTX,KZCFNC ; INSERT
DW KYDELTX,KZCFNC ; DELETE
DW KYBACKX,KZCFNC ; BACKSPACE
DW KYEEOFX,KZCFNC ; ERASE EOF
DW KYCRRTX,KZCFNC ; CARRIER RETURN
DW KYCRRTY,KZCFNC ; CARRIER RETURN
DW KYENTRX,KZCFNC ; ENTER
KKCCDTBE DW 0FFFFH,KZCGRP
KKCCDTBL EQU KKCCDTBE-KKCCDTB ; LENGTH OF KKCCDTB
FLGNL EQU 00H ; NULL
FLGVK EQU 01H ; NOT QUEING KEY CODE: IF THERE AER NO YOMI CHAR.
FLGRI EQU 02H ; RESET INSERT MODE: WHEN THIS FUNC. KEY INPUT
FLGNQ EQU 04H ; NOT FUNCTION, NOT QUEING
KPRCSTS:
DB FLGNL ; CURSOR LEFTE
DB FLGHL ; CURSOR RIGHT

```

Appendix A.

```

05DE 01          DB      FLGNL+FLGVK      ; KANJI
05DF 01          DB      FLGNL+FLGVK      ; KANJI
05E0 01          DB      FLGNL+FLGVK      ; KANJI EXIT ( = KANJI )
05E1 01          DB      FLGNL+FLGVK      ; KANJI BANGOU
05E2 01          DB      FLGNL+FLGVK      ; KANJI MODE CHANGE ( = KANJI BANGOU )
05E3 04          DB      FLGNL+FLGNQ      ; HENKAN
05E4 04          DB      FLGNL+FLGNQ      ; HENKAN (ZENKAKU)
05E5 04          DB      FLGNL+FLGNQ      ; MUHENKAN
05E6 04          DB      FLGNL+FLGNQ      ; MUHENKAN (ZENKAKU)
05E7 00          DB      FLGNL            ; ESCAPE
05E8 02          DB      FLGNL+FLGRI      ; INSERT
05E9 00          DB      FLGNL            ; DELETE
05EA 02          DB      FLGNL+FLGRI      ; BACKSPACE
05EB 00          DB      FLGNL            ; ERASE EOF
05EC 02          DB      FLGNL+FLGRI      ; CARRIER RETURN
05ED 02          DB      FLGNL+FLGRI      ; CARRIER RETURN
05EE 03          DB      FLGNL+FLGVK+FLGRI ; ENTER
05EF            PROC      NEAR
05EF 2B ED        SUB      BP,BP            ; CLEAR FUNC.KEY TABLE POINTER
05F1 A0 0003 R    MOV      AL,KKINKEY      ; GET INPUT KEY (CHR. CODE)
05F4 8A 26 0005 R MOV      AH,KKINKEY+2      ; GET INPUT KEY (SCAN CODE)
05F8            KZC010:  CMP      AX,WORD PTR KKCCDTB[BP] ; COMPARE INPUT KEY WITH FUNC.KEY
05FD 74 08        JE      KZC020          ; RETURN THE POINTER
05FF 83 C5 04     ADD      BP,4            ; SCAN TERMINATE CHECK
0602 83 FD 4C     CMP      BP,KKCCDTBL    ; NO... REREAT SCAN
0605 75 F1        JNE     KZC010          ; NO... REREAT SCAN
0607            KZC020:  CALL     WORD PTR KKCCDTB[BP][2] ; BRANCH TO EACH FUNCTION
060C C3          RET

;*****
;M      FUNCTION KEY CONTROL
;*****
KZCFNC:
        SHR      BP,1            ; BP <-- BP / 4
        SHR      BP,1
        MOV      BL,BYTE PTR KPRCSTS[BP]
        CMP      KKWEOP,0
        JNE     KZCFNC_CHK
        TEST     BL,FLGVK
        JNZ     KZCFNC_CHK
KZCFNC_QUEING:
        TEST     BL,FLGNQ
        JNZ     QUEURTH
        INT      79H            ; QUEING INPUT FUNCTION KEY CODE
QUEURTH:
        XOR      AX,AX
        RET
KZCFNC_CHK:
        TEST     BL,FLGRI
        JNZ     KZCFNC_GO
        XOR      AX,AX
        CALL     KKINST          ; RESET INSERT MODE
KZCFNC_GO:
        SUB      BP,2            ; BP <-- BP - 2
        CALL     KKZFNC
        RET
;*****
;M      CURSOR KEY MOTION
;*****
KZCCSR:
        CALL     KKCSR          ; LINK CURSOR PROC. ROUTINE
        RET
;*****
;M      GRAPHIC KEY
;*****
KZCGRP:
        TEST     DSTAT,HANKAF+ZEN2F+DAKUF
        JZ      KZCGRP_CHK
        CALL     KKZGRP          ; GRAPHIC KEY INPUT OPERATION
        RET
KZCGRP_CHK:
        CMP      KKWEOP,0
        JNE     KZCGRP_RET
KZCGRP_QUEING:
        INT      79H            ; QUEING CURSOR KEY CODE
        XOR      AX,AX
        RET
KZCGRP_RET:
        MOV      AX,1
        RET
KKZCTL      ENDP
;*****
;M
;M      PROGRAM NAME: KKZGRP
;M
;M      DESCRIPTIVE NAME: KEY CONTROL FOR KANA-KAN ZENKOUHO MODE
;M
;M      FUNCTION:
;M
;M      THIS MODULE ANALYZE INPUT KEY FOR KANA-KANJI CONVERSION,
;M      AND CALLS EACH FUNCTION ROUTINES.
;M
;M      LINKAGE:      CALLED BY KKZCTL
;M
;M      INPUT:      KANA-KAN COMMON TABLES
;M
;M      OUTPUT:     KANA-KAN COMMON TABLES
;M
;M      RETURN CODES: (AX)
;M

```



Appendix A.

```
06F9 77 E8
06FB E8 1291 R
06FE E8 1355 R
0701 C6 06 0192 R 01
0706
0706 2B C0
0708 C3
0709
```

```

JA      KZG360      ; INITIALIZE O.I.L.
CALL   KROUTINIT   ; DISPLAY CURSOR TO EOP POSITION
CALL   KCSRDSP
CALL   KHENFST,01H
MOV
KZG370: SUB      AX,AX
      RET
KKZGRP ENDP
;*****
;
; PROGRAM NAME: KKZFNC
;
; DESCRIPTIVE NAME: KANA-KAN MAIN FUNCTION
;
; FUNCTION:
; THIS MODULE HAS THE FOLLOWING FUNCTION;
; . KANA - KANJI CONVERSION
; . DISPLAY SELECT NUMBER AND KANJI
; . EDIT INPUT BUFFER ( INSERT,DELETE,BACKSPACE,ERASE-EOF )
; . REGISTERD YOMI & BANGOU
;
; LINKAGE:
;
; INPUT:
; BP : FUNCTION KEY NUMBER
;      KANA-KAN COMMON TABLES
;
; OUTPUT:
;      KANA-KAN COMMON TABLES
;
; RETURN CODES: (AX)
;
; 0 - SUCCESSFUL
; 1 - INVALID OPERATION
;
; EXTERNAL REFERENCES:
;
; ROUTINES:
;      KKKNDR      KKBINT      KKZINT      KKBACK
;      KKDEL      KKINST     KKEEOF      KKGRP
;      KKWOIL     KKDISP     KROUTINIT
;      CDCHCK1    CDCHCK2    KKOHOEDT
;      KCALEM     KCSRDSP
;
; TABLES: KANA-KAN COMMON TABLES
;
; REGISTERS: AX - RETURN CODE
;            BX,CX,DX,DI,SI,BP - WORK
;            ALL OTHERS UNCHANGED
;
; CHANGE ACTIVITY: VERSION 00.00
;
;*****
FNCBR: DW      FNCKANJ      ; KANJI
      DW      FNCKANJ      ; KANJI
      DW      FNCKANJ      ; KANJI EXIT
      DW      FNCCODE      ; KANJI BANGO
      DW      FNCKANJ      ; KANJI MODE CHENGE
      DW      FNCHENK      ; HENKAN
      DW      FNCHENK      ; HENKAN
      DW      FNCMUHE      ; MUHENKAN
      DW      FNCMUHE      ; MUHENKAN
      DW      FNCESCP      ; ESCAPE
      DW      FNCINST      ; INSERT
      DW      FNCDEL      ; DELETE
      DW      FNCKANJ      ; BACKSPACE
      DW      FNCCEOF      ; ERASE-EOF
      DW      FNCCRRT      ; CARRIER RETURN
      DW      FNCCRRT      ; CARRIER RETURN
      DW      FNCENTR      ; ENTER
KKZFNC PROC
      HEAR
      MOV     AX,1
      SHL    BP,1          ; BP * 2
      JMP    WORD PTR FNCBR[BP] ; BRANCH TO EACH FUNCTION
KKZFNC ENDP
;*****
;
;      KANJI
;
;*****
FNCKANJ PROC
      MOV     KKMODE,NORMALM ; CHANGE TO NORMAL MODE
      MOV     BL,1
      CALL   KKWOIL          ; CLEAR OPERATOR INFORMATION LIME
      SUB     AX,AX
      RET
FNCKANJ ENDP
;*****
;
;      KANJI - BANGOU
;
;*****
FNCCODE PROC
      MOV     KKMODE,CODEM   ; CHANGE TO CODE MODE
      CALL   KKBINT          ; LINK KKBINT MODULE
      SUB     AX,AX
      RET
FNCCODE ENDP
;*****
;
;      HENKAN
;
;*****

```

```
0709 0735 R
070B 0735 R
070D 0735 R
070F 0742 R
0711 0735 R
0713 0894 R
0715 0894 R
0717 0833 R
0719 0833 R
071B 0875 R
071D 0894 R
071F 08AE R
0721 08B9 R
0723 08C4 R
0725 08CF R
0727 08CF R
0729 08F4 R
072B
072B B8 0001
072E D1 E5
0730 2E: FF A6 0709 R
0735
```

```
0735
0735 C6 06 00F9 R 00
073A B3 01
073C E8 1139 R
073F 2B C0
0741 C3
0742
```

```
0742
0742 C6 06 00F9 R 08
0747 E8 051E R
074A 2B C0
074C C3
074D
```







```

08CF 8A 84 000C R      MOV AL,KKINBUF[SI][3]      ; GET ZENKAKU CODE
08D3 3D 8140           CMP AX,8140H              ; SPACE " " KEY
08D6 74 4E            JZ HEN1090
08D8 F6 06 0191 R 08  TEST KKYMSTS,KKYMSTH
08DD 75 56            JNZ HEN1110
08DF 3D 814A           CMP AX,814AH              ; DAKUTEN KEY
08E2 74 2C            JZ HEN1020
08E4 3D 814B           CMP AX,814BH              ; HANDAKUTEN KEY
08E7 74 27            JZ HEN1020
08E9 B3 2D             MOV BL,2DH
08EB 3D 817C           CMP AX,817CH              ; MINUS "-" KEY
08EE 74 09            JZ HEN1010
08F0 B3 B0             MOV BL,0B0H
08F2 3D 815B           CMP AX,815BH              ; CHOUON KEY
08F5 74 02            JZ HEN1010
08F7 EB 44             JMP SHORT HEN1200         ; THE OTHER KEY
08F9
08FF 3E: C6 86 0119 R F4  MOV KKYOMI[BP],0F4H      ; SET CHOUON YOMI CODE
0901 8A C3             MOV AL,BL
0905 8A A4 000B R      MOV AH,KKINBUF[SI][2]    ; GET CHARACTER CODE & SCAN CODE
0908 E8 0B2A R         CALL QUEBUF               ; SET CODES
090D E9 09C0 R         AND KKYMSTS,0FFH-KKYMSTD ; DAKUTEN,HANDAKUTEN BIT OFF
0910
0910 8B D8             MOV BX,AX
0912 8A 36 0190 R      MOV DH,KKCHRMD           ; GET PEVIOUS CHARACTER MODE
0916 80 FE FF          CMP DH,0FFH
0919 74 02            JZ HEN1021
091B 86 02            MOV DH,MDZENHIR         ; SET ZENKAKU HIRAGANA MODE
091D
091D 81 EB 814A         SUB BX,814AH
0921 83 C3 53          ADD BX,KZENDAKU         ; CALCULATE OFFSET OF CONVERSION TABLE
0924 EB 68             JMP SHORT HEN1250
0926
0926 83 C6 06          ADD SI,6
0929
0929 80 3E 0118 R 00  CMP KKYOMIL,0
092E 75 05            JNZ HEN1110
0930 80 0E 0191 R 80  OR KKYMSTS,KKYMSTER     ; TURN ON ERROR BIT
0935
0935 80 0E 0191 R 03  OR KKYMSTS,KKYMSTE+KKYMSTM
093A E9 09C4 R         JMP HEN1280             ; SET END & MOVE FORWARD FLAGS
093D
093D 8B D8             MOV BX,AX                ; BX <--- 2 BYTE KANJI CODE
093F B0 01             MOV AL,1
0941 E8 1707 R      CALL CDCHCK2             ; ZENKAKU NUMERIC CODE ?
0944 84 C0            TEST AL,AL
0946 74 14            JZ HEN1210              ; JUMP IF YES
0948 B0 02             MOV AL,2
094A E8 1707 R      CALL CDCHCK2             ; ZENKAKU HIRAGANA CODE ?
094D 84 C0            TEST AL,AL
094F 74 1B            JZ HEN1220              ; JUMP IF YES
0951 B0 03             MOV AL,3
0953 E8 1707 R      CALL CDCHCK2             ; ZENKAKU KATAKANA CODE ?
0956 84 C0            TEST AL,AL
0958 74 1A            JZ HEN1230              ; JUMP IF YES
095A EB CD            JMP SHORT HEN1100        ; THE OTHER CODE ( CAN'T CONV. YOMI )
095C
095C B6 01             MOV DH,MDZENNUM
095E 81 EB 824F      SUB BX,824FH
0962 83 C3 56          ADD BX,KZENNUMR         ; CALCULATE OFFSET OF CONVERSION TABLE
0965 80 0E 0191 R 8A  OR KKYMSTS,KKYMSTM+KKYMSTM
                                ; SET END FLAG & MOVE FORWARD FLAG
096A EB 15             JMP SHORT HEN1240
096C
096C B6 02             MOV DH,MDZENHIR
096E 81 EB 829F      SUB BX,829FH
0972 EB 0D            JMP SHORT HEN1240
0974
0974 B6 03             MOV DH,MDZENKAT
0976 81 FB 837F      CMP BX,837FH
097A 72 01            JB HEN1231
097C 4B             DEC BX
097D
097D 81 EB 8340         SUB BX,8340H
0981
0981 80 3E 0190 R FF  CMP KKCHRMD,0FFH
0986 74 04            JZ HEN1250
0988 38 36 0190 R    CMP KKCHRMD,DH          ; COMPARE CURRENT MODE WITH PREVIOUS
098C 75 A7            JNZ HEN1110
098E
098E 88 36 0190 R    MOV KKCHRMD,DH
0992 8B C3             MOV AX,BX
0994 05 009F          ADD AX,9FH
0997 3E: 88 86 0119 R  MOV KKYOMI[BP],AL      ; SET YOMI CONVERSION CODE
099C D1 E3             SHL BX,1                 ; BX * 2
099E 2E: 8B 87 074D R  MOV AX,WORD PTR KZENCONV[BX] ; GET ZEN. KATAKANA & DAKUTEN
09A3 50             PUSH AX
09A4 8A A4 000B R    MOV AH,KKINBUF[SI][2]    ; GET KEY SCAN CODE
09A8 E8 0B2A R      CALL QUEBUF              ; SET ONE
09AB 58             POP AX
09AC 84 E4           TEST AH,AH
09AE 74 10           JZ HEN1270
09B0 47             INC DI
09B1 47             INC DI
09B2 B0 1A           MOV AL,1AH               ; SET DAKUTEN SCAN CODE
09B4 80 FC DF        CMP AH,0DFH              ; HANDAKUTEN CODE ?
09B7 75 02           JNZ HEN1260
09B9 B0 1B           MOV AL,1BH
09BB
09BB 86 E0             XCHG AH,AL
09BD E8 0B2A R        CALL QUEBUF              ; SET DAKUTEN,HANDAKUTEN CODE
09C0
09C0 FE 06 0118 R    INC KKYOMIL              ; INCREMENT YOMI LENGTH

```

Appendix A.

```

09C4
09C4 F6 06 0191 R 02
09C9 74 04
09CB 89 36 00F7 R
09CF
09CF 83 C6 06
09D2 83 C7 02
09D5 45
09D6 E9 0AA9 R

09D9
09D9 2B DB
09DB 8A 9C 0009 R
09DF 80 FB 20
09E2 74 34
09E4 F6 06 0191 R 08
09E9 75 3C
09EB 80 FB 2D
09EE 74 17
09F0 80 FB B0
09F3 74 12
09F5 B7 01
09F7 80 FB DE
09FA 74 33
09FC B7 02
09FE 80 FB DF
0A01 74 2C
0A03 B7 00
0A05 EB 42
0A07
0A07 E8 0B22 R
0A0A 3E: C6 86 0119 R F4
0A10 80 26 0191 R FB
0A15 EB 7C 90
0A18
0A18 83 C6 03
0A1B
0A1B 80 3E 0118 R 00
0A20 75 85
0A22 80 0E 0191 R 80
0A27
0A27 80 0E 0191 R 03
0A2C EB 69 90
0A2F
0A2F F6 06 0191 R 04
0A34 74 0B
0A36 4D
0A37 3E: 00 BE 0119 R
0A3C E8 0B22 R
0A3F EB 56
0A41
0A41 2A FF
0A43 81 EB 009D
0A47 EB 37
0A49
0A49 B0 03
0A4B E8 16CF R
0A4E 84 C0
0A50 74 0B
0A52 B0 04
0A54 E8 16CF R
0A57 84 C0
0A59 74 0E
0A5B EB BE
0A5D
0A5D B6 04
0A5F 83 EB 30
0A62 80 0E 0191 R 0A

0A67 EB 06
0A69
0A69 B6 05
0A6B 81 EB 009D
0A6F
0A6F 80 3E 0190 R FF
0A74 74 06
0A76 38 36 0190 R
0A7A 75 AB
0A7C
0A7C 88 36 0190 R
0A80
0A80 D1 E3
0A82 2E: 8B 87 080D R
0A87 3E: 88 86 0119 R
0A8C 08 26 0191 R
0A90 E8 0B22 R
0A93
0A93 FE 06 0118 R
0A97
0A97 F6 06 0191 R 02
0A9C 74 04
0A9E 89 36 00F7 R
0AA2
0AA2 83 C6 03
0AA5 83 C7 02
0AA8 45
0AA9
0AA9 A1 00EA R
0AAC 2B C6
0AAE 7E 0A
0AB0 F6 06 0191 R 01
0AB5 75 03

```

```

HEN1280: TEST KKYMSTS, KKYMSTM
          JZ HEN1290
          MOV KKFORPIT, SI ; SET MOVE FORWARD POINTER

HEN1290: ADD SI, 6 ; RENEW KKINBUF POINTER
          ADD DI, 2 ; RENEW KKHANQUE POINTER
          INC BP ; RENEW KKYOMI POINTER
          JMP HEN3000

-----
; HANKAKU CODE CONVERSION
;-----
HEN2000: SUB BX, BX
          MOV BL, KKINBUF[SI] ; GET HANKAKU CHARACTER CODE
          CMP BL, 20H ; SPACE " " KEY ?
          JE HEN2090
          TEST KKYMSTS, KKYMSTM
          JNZ HEN2110
          CMP BL, 2DH ; MINUS KEY ?
          JE HEN2010
          CMP BL, 0B0H ; CHOUON KEY ?
          JE HEN2010
          MOV BH, 1
          CMP BL, 0DEH ; DAKUTEN KEY ?
          JE HEN2120
          MOV BH, 2
          CMP BL, 0DFH ; HAN-DAKUTEN KEY ?
          JE HEN2120
          MOV BH, 0
          JMP SHORT HEN2200

HEN2010: CALL SETQUEBF ; SET CHARACTER CODE & SCAN CODE
          MOV KKYOMI[BP], 0F4H ; SET CHOUON YOMI CODE
          AND KKYMSTS, 0FFH-KKYMSTD ; DAKUTEN, HANDAKUTEN BIT OFF
          JMP HEN2270

HEN2090: ADD SI, 3

HEN2100: CMP KKYOMI, 0
          JNZ HEN2110
          OR KKYMSTS, KKYMSTER ; TURN ON ERROR BIT

HEN2110: OR KKYMSTS, KKYMSTE+KKYMSTM
          JMP HEN2280 ; SET END FLAG & MOVE FORWARD FLAG

HEN2120: TEST KKYMSTS, KKYMSTD
          JZ HEN2130
          DEC BP
          ADD KKYOMI[BP], BH ; ADJUST PREVIOUS YOMI CODE
          CALL SETQUEBF ; SET CHARACTER CODE & SCAN CODE
          JMP SHORT HEN2280

HEN2130: SUB BH, BH
          SUB BX, 9DH
          JMP SHORT HEN2260

HEN2200: MOV AL, 3
          CALL CDCHCK1 ; HANKAKU NUMERIC CODE ?
          TEST AL, AL
          JZ HEN2210 ; JUMP IF YES
          MOV AL, 4
          CALL CDCHCK1 ; HANKAKU KATAKANA CODE ?
          TEST AL, AL
          JZ HEN2220 ; JMP IF YES
          JMP SHORT HEN2100 ; THE OTHER CODE ( CAN'T CONV. YOMI )
          ; ***** HANKAKU NUMERIC *****
HEN2210: MOV DH, MDHANHUM
          SUB BX, 30H ; CALCULATE OFFSET OF CONVERSION TABLE
          OR KKYMSTS, KKYMSTM+KKYMSTM ; SET END FLAG & MOVE FORWARD FLAG
          JMP SHORT HEN2240

HEN2220: MOV DH, MDHANKAT
          SUB BX, 9DH

HEN2240: CMP KKCHRM, 0FFH
          JZ HEN2250
          CMP KKCHRM, DH ; COMPARE CURRENT MODE WITH PREVIOUS
          JNZ HEN2110

HEN2250: MOV KKCHRM, DH

HEN2260: SHL BX, 1 ; BX * 2
          MOV AX, WORD PTR KHANCONV[BX]
          MOV KKYOMI[BP], AL ; SET YOMI CONVERSION CODE
          OR KKYMSTS, AH ; SET DAKUTEN, HANDAKUTEN CODE
          CALL SETQUEBF ; SET CHARACTER CODE & SCAN CODE

HEN2270: INC KKYOMI ; INCREMENT YOMI LENGTH

HEN2280: TEST KKYMSTS, KKYMSTM
          JZ HEN2290
          MOV KKFORPIT, SI ; SET MOV FORWARD POINTER

HEN2290: ADD SI, 3 ; RENEW KKINBUF POINTER
          ADD DI, 2 ; RENEW KKHANQUE POINTER
          INC BP ; RENEW KKYOMI POINTER

HEN3000: MOV AX, KKWEOP
          SUB AX, SI
          JLE HEN3010 ; ALL INPUT CHARACTER CONVERTED ?
          OR KKYMSTS, KKYMSTE ; YES ...
          JNZ HEN3010 ; ENDFLAG CHECK

```

```

0AB7 E9 08BE R
0ABA
0ABF F6 06 0191 R 80
0AC1 75 58
0AC4 BE 0118 R
0AC5 1E
0AC8 E8 0E89 R
0AC9 1F
0ACA 06
0ACE 8F 06 0142 R
0AD2 89 3E 0140 R
0AD4 48 08
0AD6 75 43
0AD7 41
0ADB 89 0E 013E R
0ADD 88 C1
0AE2 2E: F6 36 0893 R
0AE4 84 E4
0AE6 74 04
0AE8 32 E4
0AEA EB 02
0AEC FE C8
0AEC 86 C4
0AEE A3 00FD R
0AF1 33 C0
0AF3 A2 0192 R
0AF6 EB 1B
0AF8
0AF8 A0 00FD R
0AFB 3A 06 00FE R
0AFF 74 08
0B01 FE 06 00FD R
0B05 33 C0
0B07 EB 0A
0B09
0B09 83 3E 0144 R 01
0B0E 75 06
0B10 B8 0001
0B13
0B13 E8 1396 R
0B16
0B16 2B C0
0B18 C3
0B19
0B19 C6 06 00F9 R 02
0B1E B8 0001
0B21 C3

```

```

0B22
0B22 8A 84 0009 R
0B26 8A A4 000B R
0B2A
0B2A 89 85 014A R
0B2E FF 06 018E R
0B32 C3
0B33

```

```

0B33
0B33 80 3E 00F9 R 02
0B38 74 1B

```

```

0B3A
0B3A A1 0148 R
0B3D 3B 06 013E R
0B41 74 0B
0B43 80 3E 00FD R 00
0B48 74 27
0B4A FE 0E 00FD R
0B4E
0B4E 33 C0
0B50 E8 1396 R
0B53 EB 1C
0B55
0B55 2B ED
0B57
0B57 3B 2E 00EA R
0B58 7D 11
0B5D 3E: 8A 86 0009 R
0B62 3E: 8A A6 000B R
0B67 CD 79
0B69 83 C5 03
0B6C EB E9
0B6E
0B6E E8 0CA2 R
0B71
0B71 2B C0
0B73 C3
0B74

```

```

JMP HEN0020 ; WHEN NOT END, REPEAT PROC.
;-----
;***** LINK TO DICTIONARY ACCESS ROUTINE, *****
;***** AND EDIT CANDIDATE FORMAT. *****
;-----
HEN3010:
TEST KKYMSTS,KKYMSTER
JNZ HEN4020
MOV SI,OFFSET KKYOMIL
PUSH DS
CALL KKKNDR ; LINK KANA-KAN CONVERSION ROUTINE
POP DS
PUSH ES
POP KKDICSEG ; SAVE SEGMENT OF DICTIONARY
MOV KKDICOFF,DI ; SAVE OFFSET OF DICTIONARY
TEST AL,00001000B ; INVALID YOMI CODE ?
JNZ HEN4020 ; YES... ERROR RETURN
INC CX
MOV KKOHOOSU,CX ; SAVE TOTAL KOUHO COUNT
MOV AX,CX
DIV DATA07 ; CALCULATE MAX KOUHO BLOCK NUMBER
TEST AH,AH
JZ HEN3020
XOR AH,AH
JMP SHORT HEN3030
HEN3020:
DEC AL
HEN3030:
XCHG AL,AH
MOV WORD PTR KHLK,AX ; SET INITIAL KOUHO BLOCK NUMBER
XOR AX,AX
MOV KHEMFST,AL ;
JMP SHORT HEN4000
HEN3900:
MOV AL,KHLK ; CHECK END BLOCK NUMBER
CMP AL,KHLKMX
JE HEN3910 ; JUMP IF END BLOCK
INC KHLK ; NEXT BLOCK
XOR AX,AX
JMP SHORT HEN4000
HEN3910:
CMP KKOHOZAN,1 ;
JNE HEN4010
MOV AX,1
HEN4000:
CALL KKOHOEDT ; EDIT OUTPUT BUFFER
HEN4010:
SUB AX,AX
RET
HEN4020:
MOV KKM0DE,KANJIM ; CHANGE TO KANJI MODE
MOV AX,1
RET
;*****
;M INTERNAL SUB ROUTINE <SETQUEBF>, <QUEBUF> ;
;*****
SETQUEBF:
MOV AL,KKINBUF[SI]
MOV AH,KKINBUF[SI][2] ; GET CHR. CODE & SCAN CODE
QUEBUF:
MOV WORD PTR KKHANQUE[DI],AX ; SET ONE TO QUEING BUFFER
INC KKHLEN
RET
FNCHENK ENDP
;*****
;M MUHENKAN ;
;M ;
;*****
FNCMUHE PROC
CMP KKM0DE,KANJIM ; CURRENT MODE CHECK
JE MUH020
;*****
SELECT MODE *****
MUH010:
MOV AX,KKOHODSP
CMP AX,KKOHOSUU ; DISPLAY LAST KOUHO NUMBER ?
JE MUH011
CMP KHLK,0 ; CHECK FIRST KOUHO BLOCK
JE MUH050 ; PREVIOUS BLOCK
DEC KHLK
MUH011:
XOR AX,AX
CALL KKOHOEDT ; EDIT KOUHO
JMP SHORT MUH050
MUH020:
SUB BP,BP
MUH030:
CMP BP,KKWEOP
JGE MUH040
MOV AL,KKINBUF[BP] ; GET CHARACTER CODE
MOV AH,KKINBUF[BP][2] ; GET KEY SCAN CODE
INT 79H ; QUEING
ADD BP,3 ; RENEW POINTER
JMP SHORT MUH030
MUH040:
CALL KKZINT ; INITIALIZE ZENKOH0 MODE
MUH050:
SUB AX,AX
RET
FNCMUHE ENDP
;*****
;M ESCAPE ;
;M ;

```



```

0BF3 C3
0BF4

0BF4
0BF4 80 3E 00F9 R 02
0BF9 74 04
0BFB 88 0001
0BFE C3
0BFF
0BFF 1E
0C00 07
0C01 8B 16 00EC R
0C05 85 D2
0C07 74 22
0C09 BE 0075 R
0C0C
0C0C BF 0003 R
0C0F 8A 44 01
0C12 E8 1341 R
0C15 8B C8
0C17 2B D1
0C19 FC
0C1A F3/ A4
0C1C 56
0C1D 52
0C1E 8D 06 0003 R
0C22 E8 1209 R
0C25 5A
0C26 5E
0C27 0B D2
0C29 75 E1
0C2B
0C2B 2B C0
0C2D C3
0C2E

```

```

RET
FNCCRRT ENDP
;*****
;M
;M ENTER
;M
;M*****

FNCENTR PROC NEAR
CMP KKMODE,KANJIM
JZ ENT010
MOV AX,1 ; ERROR RETURN
RET

ENT010:
PUSH DS
POP ES
MOV DX, KKWEOPSV
TEST DX, DX
JZ ENT030
MOV SI, OFFSET KKINBFSV

ENT020:
MOV DI, OFFSET KKKYDCD
MOV AL, DS:[SI][1]
CALL KCALELM
MOV CX, AX
SUB DX, CX
CLD
REP MOVSB ; COPY TO KKKYDCD FROM KKINBFSV
PUSH SI
PUSH DX
LEA AX, KKKYDCD
CALL KKGRP ; INPUT BUFFER EDIT & DISPLAY
POP DX
POP SI
OR DX, DX ; CHARACTER CHANGE END ?
JNZ ENT030

ENT030:
SUB AX, AX
RET
FNCENTR ENDP
;*****
;M
;M PROGRAM NAME: KKCSR
;M
;M DESCRIPTIVE NAME: CURSOR KEY SUPPORT ROUTINE
;M
;M FUNCTION:
;M
;M THIS MODULE HAS IN PRINCIPLE A FUNCTION OF CURSOR MOTION.
;M WHITH HAS ONLY TWO KINDS, LEFT AND RIGHT MOTION.
;M
;M LINKAGE:
;M
;M INPUT: KANA-KAN COMMON TABLES
;M
;M OUTPUT: KANA-KAN COMMON TABLES
;M MOVE CURSOR
;M
;M RETURN CODES: (AX)
;M
;M 0 - SUCCESSFUL
;M 1 - INVALID OPERATION
;M
;M EXTERNAL REFERENCES:
;M
;M ROUTINES:
;M KKINST
;M KKDISP
;M
;M TABLES: KANA-KAN COMMON TABLES
;M
;M REGISTERS: AX - RETURN CODE
;M BP - WORK
;M ALL OTHERS UNCHANGED
;M
;M CHANGE ACTIVITY: VERSION 00.00
;M
;M*****
;M
;M*****

```

```

0C2E
0C2E 80 3E 00F9 R 04
0C33 74 69
0C35
0C35 83 3E 00EA R 00
0C3A 75 05
0C3C
0C3C CD 79
0C3E 33 C0
0C40 C3
0C41
0C41 2B C0
0C43 E8 0CF2 R
0C46 A0 0003 R
0C49 8A 26 0005 R
0C4D 3D 4D00
0C50 74 1D

```

```

KKCSR PROC NEAR
CMP KKMODE, SELECTM ; TEST CURRENT MODE
JE KCS210

KCS010:
CMP KKWEOP, 0 ; THERE AER NO YOMI CHARACTER
JNE KZCCSR_GO ; IF THEN QUEING CODE

KZCCSR_QUEING:
INT 79H ; QUEING CURSOR KEY CODE
XOR AX, AX
RET

KZCCSR_GO:
SUB AX, AX ; IF INSERT MODE
CALL KKINST ; THEN RESET INSERT MODE
MOV AL, KKINKEY ; GET INPUT KEY CODE
MOV AH, KKINKEY+2 ; GET INPUT KEY CODE
CMP AX, KYCSR_X ; CHECK CURSOR MOTION
JE KCS100

;*****
;M
;M CURSOR LEFT
;M
;M*****

KCS000:
MOV BP, KKWCCA ; GET CONTECTUAL CURSOR ADDRESS
TEST BP, BP ; IF CONTECTUAL CURSOR IS ZERO
JZ KCS200 ; THEN RETURN

```

```

0C52
0C52 8B 2E 00E6 R
0C56 85 ED
0C58 74 41

```



0CEA C6 06 0192 R 01  
 0CEF  
 0CEF 2B C0  
 0CF1 C3  
 0CF2

```

MOV     KHENFST,01H
KZI900: SUB     AX,AX
        RET
KKZINT  ENDP
;*****
;M
;M PROGRAM NAME: KKINST
;M
;M DESCRIPTIVE NAME: KAKAKAH INSERT PROCESS ROUTINE
;M
;M FUNCTION: WHEN ANY KEY IS PRESSED,THIS ROUTINE EXECUTS FOLLOWING
;M FUNCTION BY CURRENT MODE
;M 1) INSERTS CHARACTERS FRONT OF THE CURSOR
;M 2) SETS INSERT_MODE IN CURRENT MODE
;M 3) RESETS INSERT_MODE CURRENT MODE
;M
;M LINKAGE: CALL KKINST
;M
;M INPUT: AX-- OFFSET OF DATA TO BE INSERTED
;M KANA-KAN COMMON TABLES
;M
;M OUTPUT: KANA-KAN COMMON TABLES
;M
;M RETURN CODES: (AX)
;M
;M 0 - SUCCESSFUL
;M 1 - INVALID OPERATION
;M
;M EXTERNAL REFERENCES:
;M
;M ROUTINES: KKDISP -- DISPLAY DATA OF KAKAKAH INPUT BUFFER
;M DTINST -- INSERT DATA TO INPUT BUFFER POINTED BY AX
;M
;M TABLES: KANA-KAN COMMON TABLES
;M
;M REGISTERS: AX - RETURN CODE
;M ALL OTHERS UNCHANGED
;M
;M CHANGE ACTIVITY: VERSION 00.00
;M
;*****
INDEX  EQU  03H ;CONSTANT VALUE
;*****<KKINST>*****
;M
;M WHEN INSERT KEY HASPRESSED,THIS ROUTINE IS CALLED
;M
;M INPUT AX: POINTS INSERT DATA POSITION
;M
;*****<KKINST>*****
KKINST PROC NEAR
        PUSH BX ; SAVE REGISTERS
        PUSH CX
        PUSH DX
        PUSH SI
        PUSH DI
        MOV BL,KKWINST ; GET CURRENT MODE
        TEST BL,80H ; TEST DUMMY INSERT FLAG
        JNZ KKIS_D
        TEST BL,01H ; INSERT_MODE ?
        JZ KKIS_H ; NO,NOT INSERT_MODE
        MOV BX,AX ; YES,INSERT_MODE
        CMP AX,0H ; GET AX ( INSERT DATA )
        JZ KKIS_G ; FUNCTION KEY PRESSED?
        CALL INSTPRC ; YES,THEN INSERT_MODE OFF
        JMP SHORT KKIS_K ; NO,CALL INSERT_DATA PROC.
        ; GOTO RETURN
        CALL OFINST ; INSERT_MODE OFF
        JMP SHORT KKIS_L ; RESET INSERT_MODE
        ; NOT INSERT_MODE
        MOV AL,KKINKEY
        MOV AH,KKINKEY+2
        CMP AX,KYINSTX ; GET FUNCTION KEY CODE
        JNZ KKIS_L ; INSERT KEY PRESSED ?
        ; NO,RETURN TO CALLER
        CALL SETINST ; SET INSERT_MODE
        MOV AX,0 ; SET RETURN CODE ( NORMAL )
        POP DI ; RESTORE REGISTERS
        POP SI
        POP DX
        POP CX
        POP BX
KKIS_990: RET ; RETURN TO CALLER
KKINST ENDP
;*****<INSTPRC>*****
;M
;M WHEN NOT INSERT KEY PRESSED ON INSERT MODE
;M THIS SUBROUTINE IS CALLED
;M
;M BX: POINTS INSERT DATA POSITION
;M
;*****<INSTPRC>*****
INSTPRC PROC NEAR
        LEA SI,KKINBUF ; GET OFFSET OF INPUT BUFFER
        MOV AL,BYTE PTR DS:[BX+1]
        ; GET ATTR OF INSERT DATA
        CALL KCALELM ; GET DATA LENGTH FOR INSERTION
        MOV DX,AX ; SAVE DATA LENGTH FOR INSERTION

```

= 0003

0CF2  
 0CF3 53  
 0CF4 51  
 0CF5 52  
 0CF6 56  
 0CF7 57  
 0CF8 8A 1E 00F0 R  
 0CF9 F6 C3 80  
 0CFE 75 05  
 0D00 F6 C3 01  
 0D03 74 11  
 0D05  
 0D05 8B D8  
 0D07 3D 0000  
 0D0A 74 05  
 0D0C E8 0D2E R  
 0D0F EB 17  
 0D11  
 0D11 E8 0D7C R  
 0D14 EB 0F  
 0D16  
 0D16 A0 0003 R  
 0D19 8A 26 0005 R  
 0D1D 3D 5200  
 0D20 75 03  
 0D22  
 0D22 E8 0D87 R  
 0D25  
 0D25 8B 0000  
 0D28  
 0D28 5F  
 0D29 5E  
 0D2A 5A  
 0D2B 59  
 0D2C 5B  
 0D2D  
 0D2D C3  
 0D2E  
 0D2E 8D 36 0009 R  
 0D32 8A 47 01  
 0D35 E8 1341 R  
 0D38 8B D0

Appendix A.

```

0D3A      0D3A A1 00E6 R
0D3D      0D3D 03 C2
0D3F      0D3F 3B 06 00E8 R
0D43      0D43 77 33
0D45      0D45 E8 1573 R
0D48      0D48 8B C2
0D4A      0D4A B1 03
0D4C      0D4C F6 F1
0D4E      0D4E 32 E4
0D50      0D50 03 06 00F3 R
0D54      0D54 A3 0105 R
0D57      0D57 E8 16A3 R
0D5A      0D5A E8 16B0 R
0D5D      0D5D
0D5D      0D5D E8 10ED R
0D60      0D60 A1 00EA R
0D63      0D63 3B 06 00E8 R
0D67      0D67 72 06
0D69      0D69 A1 00E8 R
0D6C      0D6C A3 00EA R
0D6F      0D6F
0D6F      0D6F 01 16 00E6 R
0D73      0D73 B8 0000
0D76      0D76 EB 03
0D78      0D78
0D78      0D78 B8 0001
0D7B      0D7B
0D7B      0D7B C3
0D7C      0D7C

INPRC_050:
MOV      AX,KKWCCA      ; GET CURRENT CURSOR POSITION
ADD      AX,DX          ;
CMP      AX,KKEOPMAX   ; CHECK CURSOR POSITION
JA       INPRC_120     ; ERROR RETURN
CALL     DTINST        ; INSERT DATA FROM TABLE TO INPUT BUFFE
MOV      AX,DX          ; GET INSERT CHARACTER ELEMENT (3 OR 6)
MOV      CL,INDEX
DIV      CL
XOR      AH,AH
ADD      AX,KKCUSR
DCRSR,AX      ; SET CURSOR POSITION
MOV      SETSTRCL      ; SET START POSITION
CALL     SETENDCL     ; SET END POSITION

INPRC_090:
CALL     KKDISP       ; CALL DISPLAY ROUTINE
MOV      AX,KKWEOP    ; GET END POSITION
CMP      AX,KKEOPMAX  ; CHECK END POSITION
JB       INPRC_100   ;
MOV      AX,KKEOPMAX  ; GET BUFFER END POSITION
MOV      KKWEOP,AX   ; RESET END POSITION

INPRC_100:
ADD      KKWCCA,DX    ; UPDATE CURRENT CURSOR POSITION
MOV      AX,0H        ; SET RETURN CODE(NORMAL)
JMP     SHORT INPRC_990

INPRC_120:
MOV      AX,01H      ; SET RETURN CODE ( ERROR )

INPRC_990:
RET       ; RETURN TO CALLER

INSTPRC ENDP
;*****<OFINST>*****
;M
;M RESET INSERT_MODE,WHEN INSERT KEY IS PRESSED ON INSERT_MODE M
;M
;*****<OFINST>*****
OFINST   PROC NEAR
AND      KKWINST,0FEH ; RESET INSERT MODE FLAG
;
; CMP      TVMODE,TV8025
; JNE     OFINS010
; TEST   KKFLAG,CLRF25 ; TV MODE IS 80*25 COLOR ?
; JNZ    OFINS010      ; YES. GOTO
; MOV    DSTRTP,OFFFFH
; CALL   KKDISP       ; DISPLAY CURSOR
; RET    ; RETURN TO CALLER
;OFINS010:
MOV      AL,ASTER
CALL     SETMARK      ; DISPLAY INSERTION MARK
RET
OFINST ENDP
;*****<SETINST>*****
;M
;M SET INSERT_MODE,WHEN INSERT KEY IS PRESSED ON NOT INSERT_MODE M
;M
;*****<SETINST>*****
SETINST  PROC NEAR
OR       KKWINST,01H ; SET INSERT MODE
;
; CMP      TVMODE,TV8025
; JNE     STINS010
; TEST   KKFLAG,CLRF25 ; TV MODE IS 80*25 COLOR ?
; JNZ    STINS010      ; YES. GOTO
; MOV    DSTRTP,OFFFFH
; CALL   KKDISP       ; DISPLAY CURSOR
; RET    ; RETURN TO CALLER
;STINS010:
MOV      AL,CAPS
CALL     SETMARK      ; DISPLAY INSERTION MARK
RET
SETINST ENDP
;*****<SETMARK>*****
;M
;M DISPLAY INSERT MARK OR ASTER MARK M
;M
;*****<SETMARK>*****
SETMARK  PROC NEAR
MOV      AH,HANATTR   ; GET HANKAKU ATTRIBUTE
MOV      BX,KKEOPMAX
MOV      WORD PTR KKINBUF[BX],AX ; SET CHARACTER CODE
MOV      KKINP,BX     ; SET DISPLAY POINTER
MOV      AX,BX
CALL     GETCULM      ; CALCULATE COLUMN COUNT
MOV      DSTRTP,AX    ; SET COLUMN POSITION
MOV      DENDP,AX
CALL     KKDISP
RET       ; RETURN TO CALLER
SETMARK ENDP
;*****
;M
;M PROGRAM NAME: KKEEOF M
;M
;M DESCRIPTIVE NAME: KANA-KAN ERASE EOF PROCESS M
;M
;M FUNCTION: WHEN ERASE EOF KEY IS PRESSED, THIS ROUTINE ERASE THE M
;M CHARACTERS AFTER THE CURSOR POSITION AND SET SPECIAL M
;M CHARACTER. M
;M
;M LINKAGE: CALL M
;M
;M INPUT: KANA-KAN COMMON TABLES M
;M
;M OUTPUT: KANA-KAN COMMON TABLES M
;M
;M RETURN CODES: (AX) M
;M

```



```

;M      0 - SUCCESSFUL
;M      1 - INVALID OPERATION
;M
;M      EXTERNAL REFERENCES:
;M
;M      ROUTINES: KKDISP - DISPLAY YOMI AND BANGOU
;M                EOPCHECK - CHECK END POSITION OF CHARACTER
;M
;M      TABLES: KANA-KAN COMMON TABLES
;M
;M      REGISTERS: AX - RETURN CODE
;M                ALL OTHERS UNCHANGED
;M
;M      CHANGE ACTIVITY: VERSION 00.00
;M
;M
;M *****
ZENLEN EQU 06H ;ZENKAKU DATA LENGTH
;M *****
;M      ENTRY OF KKEEOF
;M
;M *****
KKEEOF PROC NEAR
;M *****
;M      ;SAVE REGISTER
;M *****
;M      KKEE_020:
;M      MOV AX, KKWCCA ; GET CURRENT CURSOR POSITION
;M      CMP AX, KKWEOP ; CHECK CURRENT CURSOR POSITION
;M      JAE KKEE_090 ; CURSOR > DATA END, GOTO RETURN
;M      CALL POINTST ; SET PARAMETERS FOR SETCHR ROUTINE
;M      CALL SETCHR ; SET CHARACTERS AFTER THE CURSOR
;M *****
;M      KKEE_060:
;M      CALL SETSTRCL ; SET START POSITION
;M      CALL SETENDCL ; SET END POSITION
;M      CALL KKDISP ; CALL DISPLAY ROUTINE
;M      MOV AX, KKWCCA ; GET CURSOR POSITION
;M      MOV KKWEOP, AX ; SET NEW END POSITION
;M *****
;M      KKEE_090:
;M      MOV AX, 0H ; SET RETURN CODE ( NORMAL )
;M      POP DI ; RESTORE REGISTERS
;M      POP SI
;M      POP DX
;M      POP CX
;M      POP BX
;M *****
;M      KKEE_990:
;M      RET ; RETURN TO CALLER
;M *****
;M      KKEEOF ENDP
;M *****
;M      SET SPECIAL CHARACTER AFTER THE CURSOR
;M      INPUT:
;M      CX -- LOOP COUNT
;M      DX -- START POSITION TO SET CHR.
;M *****
;M *****
;M      SETCHR PROC NEAR
;M      LEA DI, KKINBUF ; GET OFFSET OF INPUT BUFFER
;M      ADD DI, DX ; SI POINTS START POSITION TO SET
;M *****
;M      ST_010:
;M      CMP CX, 0H ; LOOP END ?
;M      JBE ST_990 ; YES, GOTO RETURN
;M      MOV AX, KKCHRSP ; GET SPECIAL CHARACTER CODE
;M      STOSW ; SET CHARACTER CODE
;M      XOR AL, AL
;M      STOSB ; CLEAR SCAN CODE AREA
;M      DEC CX
;M      JMP SHORT ST_010 ; LOOP PROC.
;M *****
;M      ST_990:
;M      RET ; RETURN TO CALLER
;M *****
;M      SETCHR ENDP
;M *****
;M      POINTST PROC NEAR
;M *****
;M      SET START & END POSITION AND LOOP_CNT TO BE SET SPECIAL CHR.
;M      OUTPUT:
;M      CX -- LOOP COUNT
;M      DX -- START POSITION TO SET CHR.
;M *****
;M *****
;M      POINTST PROC NEAR
;M      MOV DX, KKWCCA ; GET CURRENT CURSOR POSITION
;M      MOV AX, KKWEOP ; GET END POSITION
;M      SUB AX, DX
;M      MOV CL, INDEX
;M      DIV CL
;M      XOR AH, AH ; CLEAR AH
;M      MOV CX, AX ; SET LOOP CNT.
;M      RET ; RETURN TO CALLER
;M *****
;M      POINTST ENDP
;M *****
;M      PROGRAM NAME: KKBACK
;M
;M      DESCRIPTIVE NAME: KANAKAN BACK SPACE PROCESS ROUTINE
;M
;M      FUNCTION: WHEN BACK SPACE KEY IS PRESSED, THIS ROUTINE DELETES
;M                THE CHARACTER FRONT OF THE CURSOR AND SHIFTS CHARACTERS
;M                AFTER THE CURSOR POSITION LEFT
;M
;M

```

= 0006

```

ODAF
ODAF 53
ODB0 51
ODB1 52
ODB2 56
ODB3 57
ODB4
ODB4 A1 00E6 R
ODB7 3B 06 00EA R
ODBB 73 15
ODBD E8 0DF2 R
ODC0 E8 0DD8 R
ODC3
ODC3 E8 16A3 R
ODC6 E8 16B0 R
ODC9 E8 10ED R
ODCC A1 00E6 R
ODCF A3 00EA R
ODD2
ODD2 8B 0000
ODD5 5F
ODD6 5E
ODD7 5A
ODD8 59
ODD9 5B
ODDA
ODDA C3
ODDB

```

```

ODDB
ODDB 8D 3E 0009 R
ODDF 03 FA
ODE1
ODE1 83 F9 00
ODE4 76 0B
ODE4 FC
ODE7 A1 00F5 R
ODEA AB
ODEB 32 C0
ODED AA
ODEE 49
ODEF EB F0
ODF1
ODF1 C3
ODF2

```

```

ODF2
ODF2 8B 16 00E6 R
ODF6 A1 00EA R
ODF9 2B C2
ODFB B1 03
ODFD F6 F1
ODFF 32 E4
OE01 8B C8
OE03 C3
OE04

```

Appendix A.

```

;M LINKAGE: CALL KKBACK
;M
;M INPUT: KANA-KAN COMMON TABLES
;M
;M OUTPUT: KANA-KAN COMMON TABLES
;M
;M RETURN CODES: (AX)
;M
;M 0 - SUCCESSFUL
;M 1 - INVALID OPERATION
;M
;M EXTERNAL REFERENCES:
;M
;M ROUTINES: KKDISP -- DISPLAY DATA OF KANAKAN INPUT BUFFER
;M DTSHFT -- SHIFT DATA FO INPUT BUFFER AND CHECK
;M END POSITION
;M
;M TABLES: KANA-KAN COMMON TABLES
;M
;M REGISTERS: AX - RETURN CODE
;M ALL OTHERS UNCHANGED
;M
;M CHANGE ACTIVITY: VERSION 00.00
;M
;M *****
;M
;M <KKBACK> *****
;M
;M WHEN BACK SPACE KEY IS PRESSED, THIS ROUTINE IS CALLED
;M
;M <KKBACK> *****
;M
KKBACK PROC NEAR
;M
;M ; SAVE REGISTERS
;M
;M PUSH BX
;M PUSH CX
;M PUSH DX
;M PUSH SI
;M PUSH DI
;M
;M KKBK_020:
;M ;
;M ; GET OFFSET OF INPUT BUFFER
;M LEA SI, KKINBUF ; GET CURRENT CURSOR POSITION
;M MOV AX, KKWCCA ; CURSOR = BUFFER HEAD ?
;M CMP AX, 0H ; NO, GOTO...
;M JNZ KKBK_070 ; CURSOR IS BUFFER HEAD
;M
;M KKBK_040:
;M MOV BX, 0
;M JMP SHORT KKBK_137
;M
;M KKBK_070:
;M ; CURSOR ISN'T BUFFER HEAD
;M MOV BX, KKWCCA ; GET CURRENT CURSOR POSITION
;M ADD BX, SI
;M MOV AL, BYTE PTR DS:[BX-2] ; GET ATTR. OF PREVIOUS DATA
;M ; GET DATA LENGTH
;M CALL KCALEM
;M MOV BX, AX
;M
;M KKBK_137:
;M CALL BKSHFT
;M CALL SETENDCL ; SET END POSITION
;M CALL KKDISP ; CALL DISPLAY ROUTINE
;M
;M KKBK_150:
;M SUB KKWEOP, DX ; UPDATE END POINT
;M
;M KKBK_160:
;M MOV AX, 0 ; SET RETURN CODE (NORMAL)
;M POP DI ; RESTORE REGISTERS
;M POP SI
;M POP DX
;M POP CX
;M POP BX
;M
;M KKBK_990:
;M RET ; RETURN TO CALLER
;M
;M KKBACK ENDP
;M *****<BKSHFT>*****
;M
;M SHIFT PROCESS OF BACK SPACE PROC. ROUTINE
;M
;M <BKSHFT>*****
;M
BKSHFT PROC NEAR
;M
;M ; SET PARAMETER TO DTSHFT
;M MOV AX, KKWCCA ; AX CONTAIN DATA POSITION
;M SUB AX, BX ; SHIFT DATA & CHECK EOP
;M CALL DTSHFT ; UPDATE CURRENT CURSOR POSITION
;M SUB KKWCCA, BX ; GET DATA LENGTH
;M MOV AX, BX
;M MOV CL, INDEX
;M DIV CL
;M XOR AH, AH ; CLEAR AH
;M SUB DCRSRP, AX ; SET CURSOR POSITION
;M CALL SETSTRCL ; SET START POSITION
;M RET
;M
;M BKSHFT ENDP
;M *****
;M
;M PROGRAM NAME: KKDELT
;M
;M DESCRIPTIVE NAME: KANAKAN DELETE PROCESS ROUTINE
;M
;M FUNCTION: WHEN DELETE KEY IS PRESSED, THIS ROUTINE DELETES
;M THE CHARACTER ON THE CURSOR POSITION AND SHFT CHARACTERS
;M AFTER THE CURSOR LEFT
;M
;M LINKAGE: CALL KKDELT
;M
;M INPUT: KANA-KAN COMMON TABLES
;M
;M OUTPUT: KANA-KAN COMMON TABLES
;M
;M RETURN CODES: (AX)

```

```

;M
;M      0 - SUCCESSFUL
;M      1 - INVALID OPERATION
;M
;M EXTERNAL REFERENCES:
;M
;M      ROUTINES: KKDISP -- DISPLAY DATA OF KAHAKAN INPUT BUFFER
;M                  DTSHFT -- SHIFT DATA OF INPUT BUFFER AND CHECK
;M                      END POSITION
;M
;M      TABLES: KANA-KAN COMMON TABLES
;M
;M      REGISTERS: AX - RETURN CODE
;M                  ALL OTHERS UNCHANGED
;M
;M      CHANGE ACTIVITY: VERSION 00.00
;M
;M *****
;M
;M <KKDEL> *****
;M
;M      THIS ROUTINE DELETES A CHARACTER ON THE CURSOR
;M
;M <KKDEL> *****
;M
KKDEL  PROC      HEAR          ;
;M      PUSH      BX          ; SAVE REGISTERS
;M      PUSH      CX
;M      PUSH      DX
;M      PUSH      SI
;M      PUSH      DI
;M      MOV       AX, KKWCCA    ; GET CURRENT CURSOR POSITION
;M      CMP       AX, KKKEOPMAX ; CHECK BUFFER END
;M      JAE       KKDL_200
;M      CMP       AX, KKWEOP    ; CHECK CURSOR POINT
;M      JAE       KKDL_110     ; NOP
;M      CALL      DTSHFT       ; SHIFT DATA & CHECK EOP
;M      CALL      SETSTRCL     ; SET START POSITION
;M      CALL      SETENDCL     ; SET END POSITION
;M      CALL      KKDISP       ; CALL DISPLAY ROUTINE
;M
;M      KKDL_100:
;M      SUB       KKWEOP, DX    ; UPDATE ENDPOSITION
;M      JMP       SHORT KKDL_110
;M
;M      KKDL_200:
;M      KKDL_110:
;M      MOV       AX, 0H        ; SET RETURN CODE
;M      POP       DI          ; RESTORE REGISTERS
;M      POP       SI
;M      POP       DX
;M      POP       CX
;M      POP       BX
;M
;M      KKDL_990:
;M      RET          ; RETURN TO CALLER
;M
;M      KKDEL  ENDP
;M *****
;M      PROGRAM NAME: KKNDR
;M *****
;M      DISCRIPTIVE NAME: READ DICTIONARY
;M *****
;M      FUNCTION: ACCESS TANKANJI DICTIONARY
;M *****
;M      LINKAGE
;M      INPUT:
;M          1. LENGTH OF YOMI ( DS,SI ) + 0
;M          2. YOMI          ( DS,SI ) + 1
;M      OUTPUT:
;M          1. NUMBER OF CANDIDATES ( CX )
;M          2. CANDIDATDS          ( ES,DI )
;M          3. RETURN CODE         ( AX )
;M
;M      RETURN CODES: (AX)
;M
;M      0 - SUCCESSFUL
;M      1 - YOMI NOT FOUND
;M      4 - YOMI LENGTH EXCEPTION
;M      8 - HIRAGANA CODE EXCEPTION
;M      16 - DICTIONARY FORMAT EXCEPTION
;M
;M      EXTERNAL REFERENCES:
;M      ROUTINES: NOM
;M      TABLES: TANKANJI DICTIONARY
;M
;M      REGISTERS: AX - RETURN CODE
;M                  CX - NUMBER OF CANDIDATES
;M                  ( ES,DI ) - FIRST CANDIDATE TOP ADDRESS
;M                  ALL OTHERS UNCHANGED
;M
;M      RESTRICTIONS:
;M      YOMI LENGTH INDICATION:
;M      DICTIONARY CONTAINS YOMI LENGTH INFORMATIONS
;M      BY FOLLOWING CONVENTION ,
;M      1. 81H INDICATES THAT YOMI LENGTH IS 1
;M      2. 80H INDICATES THAT NOLENGTH 1 YOMI IS CONTAINED IN
;M      THIS DICTIONARY. AFTER 80H, NEXT YOMI IS CONTAINED.
;M      3. IF YOMI LENGTH IS GRATER THAN 2 OR EQUAL TO 2 THEN
;M      LENGTH IS NOT CONTAINED, BUT YOMI DELIMITER INDICATE
;M      THAT LAST CHARACTER OF THAT YOMI.
;M
;M      CHANGE ACTIVITY: VERSION 00.00
;M      KJ CODE COMPACTION REJECTED
;M
;M *****
;M      DIC LABEL BYTE          ; 1 BYTE MEMORY ON DICTIONARY

```

```

0E5A
0E5A 53
0E5B 51
0E5C 52
0E5D 56
0E5E 57
0E5F A1 00E6 R
0E62 3B 06 00E8 R
0E66 73 18
0E68 3B 06 00EA R
0E6C 73 12
0E6E E8 162F R
0E71 E8 16A3 R
0E74 E8 16B0 R
0E77 E8 10ED R
0E7A
0E7A 29 16 00EA R
0E7E EB 00
0E80
0E80
0E80 B8 0000
0E83 5F
0E84 5E
0E85 5A
0E86 59
0E87 5B
0E88
0E88 C3
0E89

```



```

0F11 EB 10D1 R
0F14 E8 0F2A R
0F17 85 C0
0F19 75 0E
0F1B E8 0F60 R
0F1E 85 C0
0F20 75 07
0F22 E8 0FC4 R
0F25 8B 3E 0140 R
0F29 C3
0F2A
0F2A 2B C0
0F2C A0 0119 R
0F2F 24 7F
0F31 2C 1E
0F33 D0 E0
0F35 57
0F36 03 F8
0F38 26 8B 05
0F3B 86 E0
0F3D 5F
0F3E 3D FFFF
0F41 74 14
0F43 3D 00C2
0F46 7C 14
0F48 3D 7FFF
0F4B 7F 0F
0F4D 03 06 0140 R
0F51 8B D0
0F53 2B C0
0F55 EB 08
0F57 8B 0001
0F5A EB 03
0F5C 8B 0010
0F5F C3
0F60
0F60
0F60 53
0F61 56
0F62 8B FA
0F64 E8 1080 R
0F67 8B 0E 0118 R
0F6B 3A 0E 0118 R
0F6F 7D 23
0F71 03 F9
0F73 47
0F74 83 F9 01
0F77 75 01
0F79 47
0F7A 26 80 3D 81
0F7E 74 0F
0F80 26 80 3D 80
0F84 74 09
0F86 E8 1080 R
0F89 8B 0E 0118 R
0F8D EB DC
0F8F 8B 0001
0F92 EB 0D
0F94 3A 0E 0118 R
0F98 7F 04
0F9A 2B C0
0F9C EB 03
0F9E 8B 0001
0FA1 3D 0000
0FA4 75 1B
0FA6 8B DF
0FAB 83 F9 01
0FAB 74 01
0FAD 49

```

```

;*****
CALL GETDICT
;*****
;M SEARCH DICTIONARY
;*****
CALL CALAD96 ; DICT 96-ON-SAKUIN
TEST AX,AX ; AX --- RETURN CODE
JNZ ACCD09
CALL NDXSRC ; SEARCH YOMI IN INDEX
;M IN : DX YOMI ID IN INDEX
;M OUT : DI YOMI ID IN DATA
; AX --- RETURN CODE
ACCD09: RET
ACCDICT ENDP
;*****
;M PROCEDURE NAME: CALAD96
;M FUNCTION: CALCULATE 96 ON INDEX ADDRESS
;M
;*****
CALAD96 PROC NEAR
SUB AX,AX ;
MOV AL,KKYOMI ; FIRST CHARACTER OF GIVEN YOMI
AND AL,7FH ; RESET DELIMITTER
;M------( SAKUIN ADDRESS CALCULATION )-----M
SUB AL,1EH ;
SHL AL,1 ; MULTIPLY 2
;M------( END OF 96 ON SAKUIN ADDRESS CALCULATION LOGIC )-----M
PUSH DI ;
ADD DI,AX ; TOP AND DISPLACEMENT ---> ADDRESS
MOV AX,ES:[DI] ; AX: INDEX ENTRY DISPLACEMENT (A)
XCHG AH,AL ;
POP DI ; ES+DI-->TOP OF DICTIONARY
;
;*****
;M LIMIT CHECK OF 96 INDEX POINTER
;*****
CMP AX,-1 ; NO ENTRY OF POINTER
JE CALA02 ;
CMP AX,00C2H ; LOWER LIMIT CHECK
JL CALA03 ;
CMP AX,7FFFH ; UPPER LIMIT CHECK
JG CALA03 ;
ADD AX,KKDICOFF ; ADD DICTIONRY TOP OFFSET
MOV DX,AX ; DX : INDEX OFFSET SAVE REGISTER
SUB AX,AX ; NORMAL RETURN
JMP SHORT CALA09
CALA02: MOV AX,KKECHOTF ; EXCEPTION ' YOMI NOT FOUND '
JMP SHORT CALA09
CALA03: MOV AX,KKECDICT ; EXCEPTION ' DICTIONARY FORMAT ERROR '
CALA09: RET
CALAD96 ENDP
;*****
;M PROCEDURE NAME: NDXSRC
;M FUNCTION: SEARCH YOMI IN INDEX
;M
;*****
NDXSRC PROC NEAR
;
PUSH BX
;
PUSH SI
;*****
;M SEARCH YOMI INDEX
;*****
MOV DI,DX ; OFFSET OF YOMI ID IN INDEX
CALL YMLENC ; GET YOMI LENGTH IN DICTIONARY
MOV CX,KKLENYM ; ( CX ) YOMI LENGTH ON DICTIONARY
NDXIF: CMP CL,KKYOMIL ; KKYOMI IS ' GIVEN YOMI LENGTH '
JGE NDX2CAS ;
ADD DI,CX ;
INC DI ; DI -- NEXT YOMI ON INDEX
; (DI)+L+2-1 --> (DI)
CMP CX,1
JNE NDXSKP
INC DI
NDXSKP: CMP BYTE PTR ES:[DI],81H ; NEW FIRST YOMI
JE NDX2ELSE
CMP BYTE PTR ES:[DI],80H ; NEW FIRST YOMI
JNE NDX2ELSE
CALL YMLENC ; GET YOMI LENGTH IN DICTIONARY
MOV CX,KKLENYM ; YOMI LENGTH --> CX
JMP SHORT NDXIF
;*****
NDX2ELSE: MOV AX,KKECHOTF ; YOMI NOT FOUND
JMP SHORT NDXECAS
;*****
NDX2CAS: CMP CL,KKYOMIL ; COMPARE GIVEN YOMI LENGTH WITH
JG NDX3CAS
;M------( INDEX YOMI LEN. = GIVEN YOMI LEN. )-M
SUB AX,AX ; YOMI FOUND IN DICTIONARY
JMP SHORT NDXECAS
;*****
NDX3CAS: MOV AX,KKECHOTF ; EXCEPTION ' YOMI NOT FOUND '
NDXECAS: CMP AX,0 ; RETURN CODE -- ( AX )
JNE NDXNEXT
MOV BX,DI
CMP CX,1
JE NDX000
DEC CX

```

Appendix A.

```

0FAE
0FAE 03 D9
0FB0 26: 8B 07
0FB3 86 E0
0FB5 8B F8
0FB7 2B C0
0FB9 03 3E 0112 R
0FBD 03 3E 0140 R
0FC1 5E
0FC2 5B
0FC3 C3
0FC4

0FC4
0FC4 52
0FC5 56
0FC6 80 3E 0118 R 01
0FCB 75 0B

0FCD 26: 80 3D 81
0FD1 75 52
0FD3 E8 1058 R
0FD6 EB 50

0FD8 26: 80 3D 80
0FDC 75 03
0FDE 83 C7 01
0FE1 26: 80 3D 81
0FE5 75 05
0FE7 E8 103E R
0FEA EB F5
0FEC 26: 80 3D 80
0FF0 75 05
0FF2 88 0001
0FF5 EB 31
0FF7 26: 80 3D 81
0FFB 74 28
0FFD 8B D7
0FFF E8 102D R
1002 76 13

1004 8B FA
1006 E8 103E R
1009 26: 80 3D 81
100D 74 16
100F 26: 80 3D 80
1013 74 10
1015 EB E6

1017 8B FA
1019 E8 102D R
101C 75 07
101E 8B FA
1020 E8 1058 R
1023 EB 03

1025 88 0001
1028 8B FA
102A 5E
102B 5A
102C C3
102D

102D
102D 2B C9
102F 2B F6
1031 8D 36 010B R
1035 46
1036 8A 0E 0118 R
103A 49
103B F3/ A6
103D C3
103E

103E
103E EB 10C1 R
1041 3D 0000
1044 75 01
1046 40
1047 03 F8
1049 26: F6 05 80
104D 75 05

104F 83 C7 02
1052 EB F5

NDX000: ADD BX,CX ; ( ES,BX ) --> POINTER IN INDEX
MOV AX,ES:[BX] ; FETCH POINTER FROM INDEX
XCHG AH,AL
MOV DI,AX ; DI --- POINTER DATA OF DICTIONARY
SUB AX,AX
ADD DI,KKDICLN ; DICTIONARY INDEX LENGTH
ADD DI,KKDICOFF ; ( ES,DI ) --> YOMI IN DICTIONARY DATA
NDXNEXT: POP SI
POP BX
RET
NDXSRC ENDP
;*****
;M PROCEDURE NAME: YMSRCH
;M FUNCTION: YOMI SEARCH IN DATA
;*****
YMSRCH PROC NEAR
PUSH DX
PUSH SI
CMP KKYOMIL,1 ; YOMI LENGTH COMPARISON
JNE YMSR05
;*****
;M GIVEN YOMI LENGTH = 1
;*****
CMP BYTE PTR ES:[DI],81H ; YOMI ID COMPARE
JNE YMSRER
CALL SETCAND ; GET FIRST CANDIDATE OFFSET AND
JMP SHORT YMSR99
;*****
;M GIVEN YOMI LENGTH NOT 1
;*****
YMSR05: CMP BYTE PTR ES:[DI],80H
JNE YMSRLOOP
ADD DI,1 ; POINT TO NEXT ID
YMSRLOOP: CMP BYTE PTR ES:[DI],81H ; CF. YOMI LENGTH INDICATION ( L=1 )
JNE YMSR07
CALL YMNEXT ; SEARCH NEXT ENTRY ( YOMI )
JMP SHORT YMSRLOOP
YMSR07: CMP BYTE PTR ES:[DI],80H ; CF. YOMI LENGTH INDICATION
JNE YMSR08
MOV AX,KKECNOTF ; EXCEPTION ' YOMI NOT FOUND '
JMP SHORT YMSR99
YMSR08: CMP BYTE PTR ES:[DI],81H ; CF. YOMI LENGTH INDICATION ( L=1 )
JNE YMSRER ; EXCEPTION ' YOMI NOT FOUND '
YMSRLOOP2: MOV DX,DI ; SAVE YOMI ID
CALL YOMIC ; COMPARE GIVEN YOMI AND ON DICTIONARY
JBE YMSR09
;M----- ( GIVEN YOMI > DICTIONARY YOMI )-----M
MOV DI,DX ; RETRIEVE YOMI ID ADDRESS
CALL YMNEXT ; SEARCH NEXT YOMI ENTRY
CMP BYTE PTR ES:[DI],81H ; CF. YOMI LENGTH IN
JNE YMSRER ; EXCEPTION ' YOMI NOT FOU
CMP BYTE PTR ES:[DI],80H ; CF. YOMI LENGTH IN
JNE YMSRER ; EXCEPTION ' YOMI NOT FOU
JMP SHORT YMSRLOOP2 ; CONTINUE SEARCH YOMI AND COMPARE
;M----- ( END OF GIVEN YOMI > DICTIONARY YOMI )-----M
YMSR09: MOV DI,DX ; RETRIEVE YOMI ID
CALL YOMIC ; COMPARE GIVEN YOMI AND ON DICTIONARY
JNE YMSRER ; GIVEN YOMI < DICTIONARY YOMI
MOV DI,DX ; RETRIEVE YOMI ID
CALL SETCAND ; SET CANDIDATES TO OUTPUT BUFFER
JMP SHORT YMSR99
;M-----
YMSRER: MOV AX,KKECNOTF ; EXCEPTION ' YOMI NOT FOUND '
YMSR99: MOV DI,DX ; RETRIEVE YOMI ID ADDRESS
POP SI
POP DX
RET
YMSRCH ENDP
;*****
;M PROCEDURE NAME: YOMIC
;M FUNCTION: YOMI COMPARISON ( GIVEN YOMI AND ON DICTIONARY )
;*****
YOMIC PROC NEAR
SUB CX,CX
SUB SI,SI
LEA SI,KKGVNYM ; KKGVNYM CONTAINS DICTIONARY LIKE YOMI
INC SI ; COMPARE FROM SECOND CHARACTER OF YOMI
MOV CL,KKYOMIL ; GIVEN YOMI LENGTH
DEC CX ; ***
REPE CMPSB
RET
YOMIC ENDP
;*****
;M PROCEDURE NAME: YMNEXT
;M FUNCTION: SEARCH NEXT YOMI IN DATA
;*****
YMNEXT PROC NEAR
CALL LENGYM ; GET YOMI LENGTH ON DICTIONARY
CMP AX,0
JNE YMSKP
INC AX
YMSKP: ADD DI,AX ; ES+DI--> NEXT OF LAST CHARACTER OF
YMNXLOOP: TEST BYTE PTR ES:[DI],80H ; CHECK DELIMITER
JNZ YMNX04
;*****
;M NON LAST CANDIDATE
;*****
ADD DI,2
JMP SHORT YMNXLOOP ; POINT TO NEXT CANDIDATE

```

```

;
;*****
;M LAST CANDIDATE
;*****
1054 83 C7 02
1057 C3
1058
YMNX04: ADD DI,2 ; POINT TO SECOND CHARACTER OF YOMI
RET
YMNEXT ENDP
;
;*****
;M PROCEDURE NAME: SETCAND
;M FUNCTION: SET CANDIDATES TO OUTPUT BUFFER
;*****
1058
1058 2B C0 SETCAND PROC NEAR
105A A0 0118 R SUB AX,AX ;
105D 3D 0002 MOV AL,KKYOMIL ; ( AX ) GIVEN YOMI LENGTH
1060 7C 01 CMP AX,2
1062 48 JL SETSKP
1063 03 F8 DEC AX ; CF. YOMI LENGTH INDICATE
1065 89 3E 0140 R SETSKP: ADD DI,AX ; ( ES,DI ) POINT TOP OF CAMIDATES
1069 8C 06 0142 R MOV KKDICOFF,DI ; SAVE OFFSET OUTPUT OF THIS ROUTINE
106D 2B F6 MOV KKDICSEG,ES ; SAVE SEGMENT OUTPUT OF THIS ROUTINE
106F 26: F6 05 80 SUB SI,SI ; TOP OF OUTPUT BUFFER
1073 75 05 SETCLOOP: TEST BYTE PTR ES:[DI],80H ; IF LAST CANDIDATE FOR THAT YOMI
JNZ SETC04 ; THEN JUMP
;*****
;M NON LAST CANDIDATE
;*****
1075 E8 10C9 R CALL SET2BKJ
1078 EB F5 JMP SHORT SETCLOOP
;*****
;M LAST CANDIDATE
;*****
107A E8 10C9 R SETC04: CALL SET2BKJ
107A 2B C0 SUB AX,AX ; NORMAL RETURN
107D C3 RET
107F C3
1080
SETCAND ENDP
;*****
;M PROCEDURE NAME: YMLENC
;M FUNCTION: COUNT LENGTH OF YOMI ON DICTIONARY
;*****
1080
1080 57 YMLENC PROC NEAR
1081 51 PUSH DI
1082 53 PUSH CX
1083 B8 0000 MOV AX,0
1086 26: 80 3D 81 CMP BYTE PTR ES:[DI],81H ; CF. YOMI LENGTH INDICATION
108A 74 1E JE YMLC1
108C 26: 80 3D 80 CMP BYTE PTR ES:[DI],80H ; CF. YOMI LENGTH INDICATION
1090 74 20 JE YMLC2
1092 B9 0002 MOV CX,2
1095 26: F6 05 80 YMLREP: TEST BYTE PTR ES:[DI],80H ; IF LAST YOMI CHARACTER
1099 75 09 JNZ YMLREPE ; THEN JUMP
109B 83 F9 07 CMP CX,7 ; YOMI LENGTH LIMIT CHECK
109E 7F 1A JG YMLERR ; YOMI LENGTH ERROR
10A0 41 INC CX ; COUNT YOMI LENGTH
10A1 47 INC DI ; POINT TO NEXT CHARACTER OF YOMI
10A2 EB F1 JMP SHORT YMLREP
10A4 89 0E 0116 R YMLREPE: MOV KKLENYM,CX ; SET YOMI LENGTH >= 2
10A8 EB 13 JMP SHORT YMLEND
10AA C7 06 0116 R 0001 YMLC1: MOV KKLENYM,1 ; SET YOMI LENGTH IS 1
10B0 EB 0B JMP SHORT YMLEND
10B2 C7 06 0116 R 0000 YMLC2: MOV KKLENYM,0 ; SET YOMI LENGTH IS 0
10B8 EB 03 JMP SHORT YMLEND
10BA B8 0010 YMLERR: MOV AX,KKCEDICT ; EXCEPTION ' DICTIONARY FORMAT ERROR '
10BD 5B YMLEND:
10BE 59 POP BX
10BF 5F POP CX
10C0 C3 POP DI
10C1 RET
YMLENC ENDP
;*****
10C1 E8 1080 R LENGYM PROC
10C4 A1 0116 R CALL YMLENC ;
10C7 48 MOV AX,KKLENYM ;
10C8 C3 DEC AX ; AX = YOMI LENGTH ON DICTIONARY
10C9 RET
LENGYM ENDP
;*****
10C9 83 C7 02 SET2BKJ PROC
10CC FF 06 013E R ADD DI,2 ; POINT TO NEXT CANDIDATE
10D0 C3 INC KKHOSUU ; INCREMENT CANDIDATE NUMBER
10D1 RET
SET2BKJ ENDP
;*****
10D1 53 GETDICT PROC
10D2 50 PUSH BX
10D3 1E PUSH AX
10D4 2B DB PUSH DS
10D6 8E DB SUB BX,BX
10D8 BB 01E8 MOV DS,BX ; DS CONTAINS ZERO
10DB C4 3F MOV BX,DX ; TOP -- INT 7A = 4
10DD 26: 8B 05 LES DI,DS:[BX] ; GET PARAMETER FOR DICTIONARY ( SEG & OFF )
10E0 86 E0 MOV AX,ES:[DI] ; DICTIONARY INDEX LENGTH --- AX
10E2 1F XCHG AH,AL ;
10E3 A3 0112 R POP DS ; RETRIEVE DATA SEGMENT
10E6 89 3E 0140 R MOV KKDICTLN,AX ; DICTIONARY INDEX LENGTH
10EA 58 MOV KKDICOFF,DI ; DICTIONARY TOP OFFSET
10EB 5B POP AX
10EC C3 POP BX
RET

```





```

;M          DI = POINTER IN OIL ( ONLY BL=0 )
;M
;M          OUTPUT: DI = DI + 2 ( ONLY WRITE OIL )
;M
;M          RETURN CODES: (AX)
;M
;M          0 - SUCCESSFUL
;M          1 - INVALID OPERATION
;M
;M          EXTERNAL REFERENCES:
;M
;M          ROUTINES: NONE
;M
;M          TABLES: KANA-KAN COMMON TABLES
;M
;M          REGISTERS: AX - RETURN CODE
;M                   ALL OTHERS UNCHANGED
;M
;M          CHANGE ACTIVITY: VERSION 00.00
;M
;M          *****
;M          ***** KKWOIL *****
;M          ENTRY OF KKWOIL
;M          *****
;M          *****
KKWOIL PROC
;M          PUSH DX ;SAVE REGISTER
;M          PUSH CX
;M          PUSH BX
;M          CMP BL,D00 ;REQUIREMENT IS TO WRITE OIL ?
;M          JE WOI010 ; YES. GOTO
;M          CMP BL,D01 ;REQUIREMENT IS TO CLEAR OIL ?
;M          JE WOI020 ; YES. GOTO
;M          CMP BL,D80 ;REQUIREMENT IS TO INITIALIZE INDICATOR ?
;M          JE WOI040 ; YES. GOTO
;M          JMP SHORT WOIRTN
;M-----
;M          WRITE OIL
;M-----
1140 WOI010: CALL WROI ;WRITE OIL
1150 EB 28 JMP SHORT WOIRTN
;M-----
;M          CLEAR OIL
;M-----
1152 WOI020: CALL CLOIL ;CLEAR OIL
1152 EB 1538 R XOR AL,AL
1155 32 C0 MOV AH,D07
1157 B4 07 INT 16H ;INDICATOR ON
1159 CD 16 MOV DX,APKBDST ;LOAD KBD STATUS FOR APPLICATION
115B 8B 16 00FF R CALL WKBDSST ;RESET KBD STATUS FOR APPLICATION
115F EB 11EB R JMP SHORT WOIRTN
1162 EB 16
;M-----
;M          INITIALIZE INDICATOR
;M-----
1164 WOI040: MOV AH,D02
1164 B4 02 INT 16H ;READ KBD STATUS
1166 CD 16 MOV APKBDST,AX ;SAVE KBD STATUS FOR APPLICATION
1168 A3 00FF R MOV DX,AX ;LOAD KBD STATUS
116B 8B D0 AND DH,0F9H
116D 80 E6 F9 OR DH,HSHFIF ;CHANGE INDICATOR INTO HIRAGANA
1170 80 CE 04 MOV KBDSTAT,DX ;SET KBD STATUS FOR APPLICATION
1173 89 16 0001 R CALL WKBDSST ;WRITE KBD STATUS
1177 EB 11EB R
;M-----
;M          RETURN
;M-----
117A WOIRTN: XOR AX,AX ;SET NOMAL RETURN CODE
117A 33 C0 POP BX ;RESTORE REGISTER
117C 5B POP CX
117D 59 POP DX
117E 5A RET
117F C3 ;RETURN TO CALLER
1180
KKWOIL ENDP
;M          *****
;M          ***** WROI *****
;M          *****
;M          WRITE OPERATOR INFOMATION LINE
;M          *****
;M          *****
WROI PROC
;M          PUSH AX
;M          CALL ERASE CURSOR ; ERASE CURSOR
;M          CMP TVMODE,TV4011 ; TV MODE IS 40*11 ?
;M          JNE WRO010 ; NO. GOTO
;M          CMP DI,D00 ;FIRST CALL ?
;M          JNE WRO010 ; NO. GOTO
;M          CMP KKMODE,SELECTM ;KANA-KAN MODE IS SELECT ?
;M          JE WRO000 ; YES. GOTO
;M          CMP OLDMODE,SELECTM ;OLD KANA-KAN MODE IS SELECT ?
;M          JNE WRO0001 ; NO. GOTO
;M          XOR AL,AL
;M          MOV AH,D07
;M          INT 16H ;INDICATOR ON
;M          MOV DOILP,CLIND11P ;SET DATA POSITION OF INDICATOR
;M          MOV DOILN,CLIND11N ;SET NUMBER OF INDICATOR DATA
;M          CALL CLOIL ;CLEAR OIL
;M          MOV DX,KBDSTAT ;LOAD KANA-KAN KBD STATUS
;M          CALL WKBDSST ;DISPLAY INDICATOR
;M          WRO0001: MOV DOILP,DOILP11K ;SET DATA POSITION OF OIL

```

```

1139
1139 52
113A 51
113B 53
113C 80 FB 00
113F 74 0C
1141 80 FB 01
1144 74 0C
1146 80 FB 80
1149 74 19
114B EB 2D

1140 EB 1180 R
1150 EB 28

1152 EB 1538 R
1155 32 C0
1157 B4 07
1159 CD 16
115B 8B 16 00FF R
115F EB 11EB R
1162 EB 16

1164 B4 02
1166 CD 16
1168 A3 00FF R
116B 8B D0
116D 80 E6 F9
1170 80 CE 04
1173 89 16 0001 R
1177 EB 11EB R

117A 33 C0
117C 5B
117D 59
117E 5A
117F C3
1180

1180 50
1181 EB 12EC R
1184 80 3E 00FA R 03
1189 75 50
118B 83 FF 00
118E 75 4B
1190 80 3E 00F9 R 04
1195 74 2D
1197 80 3E 0194 R 04
119C 75 1A
119E 32 C0
11A0 B4 07
11A2 CD 16
11A4 C6 06 0101 R 00
11A9 C6 06 0102 R 0C
11AE EB 1538 R
11B1 8B 16 0001 R
11B5 EB 11EB R
11B8
11B8 C6 06 0101 R 0C

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1260 75 F5
1262 A1 0105 R
1265 2C 02
1267 A3 0107 R
126A 04 01
126C A3 0109 R
126F E8 10ED R
1272 2B C0
1274
1274 C3
1275

1275
1275 127B R
1277 127B R
1279 1287 R
127B

127B
127B 03 3D
127D 01 3E
127F 01 20
1281 96
1282 01 2A
1284 28 20
1286 FF
1287
1287 03 3D
1289 01 3E
128B 01 20
1288 96
128E 01 2A
1290 FF

1291
1291 06
1292 57
1293 56
1294 52
1295 53
1296 55
1297 A0 00FA R
129A 98
129B 8B E8
129D 4D
129E D1 E5
12A0 2E 8B AE 1275 R
12A5 1E
12A6 07
12A7 D3 00
12A9 2B FF
12AB 2B F6
12AF 32 ED
12AF
12AF 2E 8A 0A
12B2 80 F9 FF
12B5 74 2E
12B7 F6 C1 80
12BA 74 16

12BC
12BC 80 E1 7F
12BF 74 ZE
12C1 56
12C2 8D 36 0009 R
12C6 FC
12C7
12C7 AD
12C8 E8 1139 R
12CB 46
12CC F2 F9
12CE 5E
12CF 46
12D0 EB D3

12D2
12D2 80 E1 7F
12D5 74 D3
12D7 46
12D8
12D8 2E 8A 02
12DB B4 00
12DD E8 1139 R
12E0 E2 F6
12E2 46
12E3 EB CA
12E5
12E5 5D

```

```

JNZ KGR310
MOV AX,DCRSRP
SUB AL,2
MOV DSTRTP,AX ; SET DISPLAY START POSITION
ADD AL,1
MOV DENDP,AX ; SET DISPLAY END POSITION
CALL KKDISP
SUB AX,AX

KGR900:
RET
KKGRP ENDP
;*****
;+ PRIVATE CONSTANT DATA ( FOR KOUTINIT )
;*****
BASE_FOMAT:
DH TV8025_FMT
DW TV8011_FMT
DW TV4011_FMT

TV8025_FMT:
TV8011_FMT:
DB 03,3DH ;" = "
DB 01,3EH ;" > "
DB 01,20H ;" "
DB 11*2+80H ; SPECIAL CODE
DB 01,2AH ;" X "
DB 40,20H ;" "
DB OFFH

TV4011_FMT:
DB 03,3DH ;" = "
DB 01,3EH ;" > "
DB 01,20H ;" "
DB 11*2+80H ; SPECIAL CODE
DB 01,2AH ;" X "
DB OFFH

;*****
; S U B R O U T I N E
;*****
NAME : KOUTINIT

KOUTINIT PROC NEAR
;*****
PUSH ES ; SAVE REGISTERS
PUSH DI
PUSH SI
PUSH DX
PUSH BX
PUSH BP
MOV AL,TVMODE ; GET VIDEO MODE
CBM
MOV BP,AX
DEC BP
SHL BP,1 ; BP X 2
MOV BP,WORD PTR BASE_FOMAT[BP]
PUSH DS
POP ES
MOV BL,0
SUB DI,DI
SUB SI,SI
XOR CH,CH

OINT01:
MOV CL,CS:[BP][SI]
CMP CL,OFFH
JE OINT060
TEST CL,10000000B ; LSB = 1 : ZENKAKU
; JZ OINT040 ; 0 : HANKAKU
;*****
; DISPLAY INPUT BUFFER *****
;*****
OINT020:
AND CL,7FH
JZ OINT010
PUSH SI
LEA SI,KKINBUF ; GET OFFSET OF INPUT BUFFER
CLD

OINT030:
LODSH
CALL KKNOIL ; DISPLAY INPUT BUFFER
INC SI
LOOP OINT030
POP SI
INC SI
JMP SHORT OINT010
;*****
; *****
; DISPLAY O.I.L FORMAT *****
;*****
OINT040:
AND CL,7FH
JZ OINT010
INC SI

OINT050:
MOV AL,CS:[BP][SI]
MOV AH,HANATIR ; GET TO AX HANKAKU CODE
CALL KKNOIL
LOOP OINT050
INC SI
JMP SHORT OINT010

OINT060:
POP BP

```





Appendix A.

```

13D8 BD 0003
13DB
13DB D1 E5
13DD D1 E5
13DF 2E 8B BE 1370 R
13E4 2E 2B 8E 1372 R
13E9 1E
13EA 07
13EB FC
13EC 8A 26 00E1 R
13FO 80 81
13F2 E8 1139 R
13F5 8A 26 00E2 R
13F9 80 79
13FB E8 1139 R
13FE C7 06 0146 R 0000
1404 86 00
1406 B2 31
1408 8B 2E 0148 R
140C D1 E5
140E
140E 83 3E 0144 R 01
1413 74 4E
1415
1415 A1 00E4 R
1418 E8 1139 R
141B 49
141C 8B C2
141E E8 1139 R
1421 49
1422 52
1423 1E
1424 56
1425 C5 36 0140 R
1429 3E 8B 12
142C 86 D6
142E 80 CE 80
1431 80 E6 BF
1434 5E
1435 1F
1436 8A C6
1438 8A 26 00E1 R
143C E8 1139 R
143F 49
1440 8A C2
1442 8A 26 00E2 R
1446 E8 1139 R
1449 5A
144A 49
144B FF 0E 0144 R
144F 42
1450 FF 06 0146 R
1454 45
1455 45
1456 83 3E 0146 R 07
145B 7C B1
145D FF 0E 0146 R
1461 EB 50
1463
1463 F6 C7 01
1466 74 18
1468 F6 C7 02
146B 75 0D
146D FF 06 0144 R
1471 FF 0E 0146 R
1475 80 CF 02
1478 EB 94
147A
147A FF 0E 0144 R
147E EB 33
1480
1480 8B C1
1482 2D 0002
1485 3B 06 018E R
1489 7D 02
148B EB 26
148D
148D FF 0E 0144 R
1491 A1 00E4 R
1494 E8 1139 R
1497 49
1498 8B C2
149A E8 1139 R
149D 49
149E 51
149F 8B 0E 018E R
14A3 3E 014A R
14A6
14A6 AD
14A7 84 00
14A9 E8 1139 R
14AC E2 F8
14AE 59
14AF 2B 0E 018E R
14B3
14B3 41
14B4 41
14B5 A1 00E4 R
14B8
14B8 E8 1139 R
14BB E2 FB
14BD FF 06 0146 R
14C1

KHED030, MOV BP,3
SHL BP,1
SHL BP,1 ; BP X 4
MOV DI,HORD PTR KKBFCAA[BP] ;GET EDITION BUFFER POINT
MOV CX,HORD PTR KKBFCAA[BP][2] ;GET LENG. OF CANDIDATA AREA
PUSH DS
POP ES ; DS <--- ES
CLD
MOV AH,ZENATTR1 ; SET FIRST CODE
MOV AL,81H ; SET FIRST CODE
CALL KKHOIL
MOV AH,ZENATTR2 ; SET SECONDD CODE
MOV AL,79H ; SET SECONDD CODE
CALL KKHOIL
MOV KKOHOCHT,0
MOV DH,HANATTR ; SELECT NUMBER
MOV DL,31H ; SELECT NUMBER
MOV BP,KKOHOOSP
SHL BP,1

KHED040, CMP KKOHOZAN,1
JE KHED050

KHED045, MOV AX,SPACE
CALL KKHOIL ; SET HANKAKU SPACE
DEC CX
MOV AX,DX
CALL KKHOIL ; SET SELECT NUMBER
DEC CX
PUSH DX
PUSH DS
PUSH SI
LDS SI,DHORD PTR KKDICADR ; GET OFFSET & SEGMENT ADDR.
MOV DX,DS:[SI][BP] ; GET KOUHO KANJI CODE
DL,DH
OR DH,10000000B
AND DH,10111111B ; EDIT UPER CODE
POP SI
POP DS
MOV AL,DH
MOV AH,ZENATTR1
CALL KKHOIL ; SET UPER KANJI CODE & ATTRIBUTE
DEC CX
MOV AL,DL
MOV AH,ZENATTR2
CALL KKHOIL ; SET LOWER KANJI CODE & ATTRIBUTE
POP DX
DEC CX
DEC KKOHOZAN
INC DX ; NEXT SELECT NUMBER
INC KKOHOCHT
INC BP ; RENEW KOUHO POINTER
INC BP
CMP KKOHOCHT,7 ; IF SELECT NUMBER >= 7
JL KHED040 ; THEN REPEAT
DEC KKOHOCHT ; ADJUST KOUHO COUNT
JMP SHORT KHED080

KHED050, TEST BH,NUMYOMFG
JZ KHED052
TEST BH,NUMSKIP
JNZ KHED051
INC KKOHOZAN ; FORCED ADJUST "ZAN"
DEC KKOHOCHT ; FORCED ADJUST KOUHO COUNT
OR BH,NUMSKIP
JMP SHORT KHED040

KHED051, DEC KKOHOZAN ; KOUHO ZAN 1 DOWN
JMP SHORT KHED080

KHED052, MOV AX,CX
SUB AX,Z
CMP AX,KKHQLEN
JGE KHED060
JMP SHORT KHED080

KHED060, DEC KKOHOZAN
MOV AX,SPACE
CALL KKHOIL ; SET HANKAKU SPACE
DEC CX
MOV AX,DX
CALL KKHOIL ; SET SELECT NUMBER
DEC CX
PUSH CX
MOV CX,KKHQLEN
MOV SI,OFFSET KKHANQUE

KHED070, LODSW
MOV AH,HANATTR ; HANKAKU ATTRIBUTE SET
CALL KKHOIL ; COPY HANKAKU YOMI DATA
LOOP KHED070
POP CX
SUB CX,KKHQLEN ; CALCULATE COLUMN COUNT

KHED080, INC CX
INC CX
MOV AX,SPACE ; GET HANKAKU SPACE CODE

KHED081, CALL KKHOIL ; SPACE CLEAR
LOOP KHED081
INC KKOHOCHT

KHED090,

```

```

14C1 8A 26 00E1 R      MOV     AH,ZEHATTR1
14C5 B0 81              MOV     AL,81H
14C7 E8 1139 R        CALL    KKHOIL
14CA 8A 26 00E1 R      MOV     AH,ZEHATTR1 ; SET "J" FIRST CODE
14CE B0 7A              MOV     AL,7AH
14D0 E8 1139 R        CALL    KKHOIL
14D3 8A 26 00E1 R      MOV     AH,ZEHATTR1 ; SET "J" SECOND CODE
14D7 B0 8E              MOV     AL,8EH ; SET "ZAN" KANJI CODE & ATTRIBUTE
14D9 E8 1139 R        CALL    KKHOIL ; SET "ZAN" KANJI CODE & ATTRIBUTE
14DC 8A 26 00E2 R      MOV     AH,ZEHATTR2
14E0 B0 63              MOV     AL,63H
14E2 E8 1139 R        CALL    KKHOIL
14E5 8B 16 0144 R      MOV     DX,KKHOZAH
14E9 81 E2 03FF R     AND     DX,03FFH
14ED BD 0014           MOV     BP,10*2
14F0 2B C0             SUB     AX,AX
14F2
14F2 D1 EA             KHE100: SHR     DX,1
14F4 73 0B             JNC    KHE110
14F6 2E: 02 86 1380 R  ADD     AL,BYTE PTR DECAJST[BP]
14FB 27                 DAA
14FC 2E: 12 A6 1381 R  ADC     AH,BYTE PTR DECAJST[1][BP]
1501
1501 4D                 KHE110: DEC     BP
1502 4D                 DEC     BP
1503 75 ED             JNZ    KHE100
1505 8B D0             MOV     DX,AX
1507 B9 0404           MOV     CX,0404H
150A
150A F6 C6 F0           KHE120: TEST    DH,0F0H ; *** ZERO SUPPRESS ***
150D 75 0F             JNZ    KHE130
150F A1 00E4 R         MOV     AX,SPACE ; SET SPACE CODE INSTEAD OF ZERO
1512 E8 1139 R        CALL    KKHOIL
1515 D3 E2             SHL     DX,CL ; GET TO PH-REGISTER
1517 FE CD           DEC     CH
1519 80 FD 01        CHP    CH,1
151C 75 EC             JNZ    KHE120
151E
151E B4 00             KHE130: MOV     AH,HAHATTR ; *** SET NUMBER DATA(S)
1520 8A C6           MOV     AL,DH
1522 24 F0           AND     AL,0F0H
1524 D2 E8           SHR     AL,CL
1526 0C 30           OR      AL,30H ; ADJUST TO DISPLAY CODE
1528 E8 1139 R        CALL    KKHOIL ; SET NUMBER
152B D3 E2           SHL     DX,CL
152D FE CD           DEC     CH
152F 75 ED           JNZ    KHE130
1531 2B C0           SUB     AX,AX
1533 C3
1534
1534 B8 0001          KHE900: MOV     AX,1
1537 C3             RET
1538
KKHOEDT ENDP
;*** CLOIL *****
;
; CLEAR OPERATOR INFORMATION LINE
;
;*****
CLOIL PROC
;
; MOV     AH,81H
; MOV     CX,2000H
; INT     10H
; CALL    ERASE_CURSOR ; ERASE CURSOR
; MOV     DX,KKCUSR ; LOAD KANA-KAN CURSOR POSITION
; MOV     DL,DOILP ; LOAD TOP POSITION OF OIL
; MOV     DH,KKOILP ; LOAD OIL POSITION
; MOV     AL,BLANK ; LOAD BLANK DATA
; MOV     CL,DOILN ; LOAD NUMBER OF OIL
; XOR     CH,CH
CLOIL0: CALL    WDISP ; CLEAR OIL
; INC     DL
; LOOP   CLOIL0
; RET
CLOIL ENDP ;RETURN TO CALLER
;*** WDISP *****
;
; WRITE CHARACTER
;
;*****
WDISP PROC
; PUSH    SI ;SAVE REGISTER
; PUSH    DI
; PUSH    BP
; PUSH    CX
; PUSH    BX
; PUSH    AX
; PUSH    AX
; MOV     AH,82H
; INT     10H ;SET ALTERNATE CURSOR
; POP     AX
; MOV     BL,0FH ;SET COLOR (IN GRAPHIS MODE)
; MOV     CX,D01
; MOV     AH,8AH
; INT     10H ;WRITE DISPLAY
; POP     AX ;RESTORE REGISTER
; POP     BX
; POP     CX
; POP     BP
; POP     DI
; POP     SI
; RET
;RETURN TO CALLER

```

Appendix A.

1573

```

WDISP ENDP
;*****
;M PROGRAM NAME: DTINST
;M DESCRIPTIVE NAME: DATA INSERT PROCESS ROUTINE
;M FUNCTION: THIS ROUTINE INSERTS A CHARACTER FRONT OF THE CURSOR
;M POSITION AND MODIFIES END POS: ON
;M
;M LINKAGE:
;M INPUT: BX-- OFFSET OF DATA POSITION TO INSERT
;M DX-- DATA LENGTH TO INSERT
;M
;M OUTPUT: KANA-KAN COMMON TABLES
;M
;M RETURN CODES: (AX)
;M 0 - SUCCESSFUL
;M 1 - INVALID OPERATION
;M
;M EXTERNAL REFERENCES:
;M ROUTINES: EOPCHECK-- CHECK AND MODIFY END POSITION OF
;M CHARACTERS
;M
;M TABLES: KANA-KAN COMMON TABLES
;M
;M REGISTERS: AX - RETURN CODE
;M ALL OTHERS UNCHANGED
;M
;M CHANGE ACTIVITY: VERSION 00.00
;M
;*****
;
;*****<DTINST>*****
;M
;M WHEN NOT INSERT KEY PRESSED ON INSERT MODE
;M THIS SUBROUTINE IS CALLED
;M
;M BX: OFFSET OF DATA POSITION TO INSERT
;M
;*****<DTINST>*****
DTINST PROC NEAR
    PUSH BX ; SAVE REGISTERS
    PUSH CX
    PUSH DX
    PUSH SI
    PUSH DI

    DTI_010: CALL XMTDATA ; TRANSMIT DATA OF INPUT BUFFER
    DTI_020: MOV SI,DX ; GET OFFSET VALUE OF DATA TO INSERT
    LEA DI,KKINBUF ; GET OFFSET OF INPUT BUFFER
    ADD DI,KKIHCCA ; DI POINTS CURSOR POSITION
    MOV CX,DX ; GET DATA LENGTH
    CLD ; SET DIRECTION FLAG
    REP MOVSB ; INSERT DATA

    DTI_030:
    DTI_040: MOV AX,0H ; SET RETURN CODE(NORMAL)
    POP DI ; RESTORE REGISTERS
    POP SI
    POP DX
    POP CX
    POP BX

    DT_990: RET ; RETURN TO CALLER
DTINST ENDP
;
;*****<XMTDATA>*****
;M
;M TRANSMIT DATA FROM INPUT BUFFER TO ITSELF
;M
;M INPUT DX:DATA LENGTH TO INSERT
;M
;*****<XMTDATA>*****
;
;M XMTDATA PROC NEAR
;M MOV AX,KKHEOP ; GET END POSITION OF CHARACTERS
;M CMP AX,KKHCCA ; CURSOR POS. >= END POS. ?
;M JBE XMT_030 ; NO,...
;M ADD AX,DX ; YES,...
;M CMP AX,KKEOPMAX ; CHECK BOUNDARY
;M JNA XMT_020 ; IF END POINTER <= END OF BUFFER
;M
;M XMT_010: CALL MODEOP ; ELSE
;M ; MODIFY END POSITION
;M
;M XMT_020: MOV CX,KKHEOP ; GET DATA LENGTH OF XMTION
;M SUB CX,KKHCCA ;
;M LEA SI,KKINBUF ; SET POINTER OF MOVEMENT AT FIRST
;M ADD SI,KKHEOP
;M DEC SI
;M MOV DI,SI ; SET POINTER OF MOVEMENT THE POINT
;M ADD DI,DX
;M STD ; SET DIRECTION FLAG
;M REP MOVSB ; XMT DATA
;M CLD ; RESET DIRECTION FLAG

;M XMT_030: ADD KKHEOP,DX ; UPDATE END POSITION OF DATA

```

```

1573
1573 53
1574 51
1575 52
1576 56
1577 57
1578
1578 E8 1593 R
1578
1578 8B F3
157D 8D 3E 0009 R
1581 03 3E 00E6 R
1585 8B CA
1587 FC
1588 F3 A4
158A
158A 8B 0000
158D 5F
158E 5E
158F 5A
1590 59
1591 5D
1592
1592 C3
1593

```

```

1593
1593 A1 00EA R
1594 3B 06 00E6 R
159A 76 24
159C 03 C2
159E 3B 06 00E8 R
15A2 76 03

15A4
15A4 E8 15C5 R
15A7
15A7 8B 0E 00EA R
15AB 2B 0E 00E6 R
15AF 8D 36 0009 R
15B3 03 36 00EA R
15B7 4E
15B8 8B FE
15BA 03 FA
15BC FD
15BD F3 A4
15BF FC
15C0
15C0 01 16 00EA R

```



```

15C4
15C4 C3
15C5

15C5
15C5 8D 36 0009 R
15C9 03 36 00EA R
15CD 83 EE 02
15D0
15D0 83 FA 06
15D3 75 29
15D5
15D5 E8 1612 R
15D8 74 04
15DA B0 06
15DC EB 2E
15DE
15DE A1 00EA R
15E1 3B 06 00E8 R
15E5 74 04
15E7 B0 03
15E9 EB 21
15EB
15EB 83 EE 03
15EE E8 1612 R
15F1 75 04
15F3 B0 06
15F5 EB 15
15F7
15F7 EB 1619 R
15FA B0 09
15FC EB 0E
15FE
15FE E8 1612 R
1601 75 04
1603 B0 03
1605 EB 03
1607
1607 EB 1619 R
160A B0 06
160C
160C 98
160D 29 06 00EA R
1611 C3
1612

```

```

1612
1612 8A 04
1614 22 06 00E1 R
1618 C3
1619

```

```

1619
1619 50
161A 8D 3E 0009 R
161E 03 3E 00E8 R
1622 83 EF 03
1625 A1 00F5 R
1628 FC
1629 AB
162A 32 C0
162C AA
162D 58
162E C3
162F

```

```

XMT_990: RET
XMTDATA ENDP
;*****<MODEOP>*****
;#
;# MODIFY END POINTER
;#
;*****<MODEOP>*****
MODEOP PROC NEAR
LEA SI,KKINBUF ; GET OFFSET OF INPUT BUFFER
ADD SI,KKHEOP
SUB SI,2 ; SI POINTS ATTR OF LAST CHARACTER
MOD_020:
CMP DX,ZENLEN ; CHECK DATA LENGTH OF INSERTION
JNZ MOD_070 ; IF HANKAKU DATA
; ELSE (ZENKAKU)
MOD_030: CALL GETATRI ; GET ATTR OF LAST CHARACTER
JZ MOD_040 ; LAST CHR. = HANKAKU ?
MOV AL,ZENLEN ; NO, LAST CHR. = ZENKAKU
JMP SHORT MOD_990 ; GOTO RETURN
MOD_040: MOV AX,KKHEOP ; YES, LAST CHR. = HANKAKU
CMP AX,KKEOPMAX ; GET END POSITION OF CHARACTERS
JZ MOD_050 ; CHECK BOUNDARY.
MOV AL,INDEX
JMP SHORT MOD_990 ; GOTO RETURN
MOD_050: SUB SI,INDEX ; SI POINTS LAST 2ND CHARACTER ATTR.
CALL GETATRI ; GET ATTR. OF DATA THAT SI POINTS
JNZ MOD_060
MOV AL,ZENLEN
JMP SHORT MOD_990 ; GOTO RETURN
MOD_060: CALL CLEAR3 ; CLEAR LAST 3 BYTE OF INPUT BUFFER
MOV AL,ZENLEN+INDEX ; UPDATE END POINTER
JMP SHORT MOD_990 ; GOTO RETURN
MOD_070: CALL GETATRI ; GETATTR
JNZ MOD_080 ; IF LAST CHARACTER IS HANKAKU
MOV AL,INDEX
JMP SHORT MOD_990
MOD_080: CALL CLEAR3 ; ELSE (ZENKAKU)
MOV AL,ZENLEN ; CLEAR SPACE AREA
; UPDATE END POINTER
MOD_990: CBW
SUB KKHEOP,AX
RET
MODEOP ENDP
;*****<GETATRI>*****
;#
;# SI: OFFSET OF DATA ATTRIBUTE
;#
;*****<GETATRI>*****
GETATRI PROC NEAR
MOV AL,BYTE PTR DS:[SI]
AND AL,ZENATTR1 ; GET ATTRIBUTE OF CHARACTER
; ZENKAKU OR HANKAKU
RET
GETATRI ENDP
;*****<CLEAR3>*****
;#
;# THIS ROUTINE THE CLEAR SPACE AREA (3BYTE)
;#
;# SI: POINTS THE AREA FOR CLEAR CHAR
;#
;*****<CLEAR3>*****
CLEAR3 PROC NEAR
PUSH AX
LEA DI,KKINBUF ; GET OFFSET VALUE OF INPUT BUFFER
ADD DI,KKEOPMAX
SUB DI,INDEX ; SI POINTS THE TOP OF CLEARING AREA
MOV AX,KKCHRS
CWD
STOSW ; SET SPACE CODE
XOR AL,AL
STOSB
POP AX
RET ; RETURN TO CALLER
CLEAR3 ENDP
;*****
;# PROGRAM NAME: DTSHTF
;#
;# DESCRIPTIVE NAME: SHIFT DATA & MODIFY END POSITION
;#
;# FUNCTION: WHEN DELETE KEY OR BACK SPACE KEY IS PRESSED,
;# THIS ROUTINE SHIFT CHARACTERS AFTER THE POSITION
;# POINTED BY AX REGISTER
;#
;# LINKAGE:
;#
;# INPUT: AX -- POINTS DATA TO BE DELETE
;#
;# OUTPUT: DX -- LENGTH OF DATA TO BE DELETED
;#
;# RETURN CODES: (AX)
;#
;# 0 - SUCCESSFUL
;# 1 - SUCCESSFUL
;#
;# EXTERNAL REFERENCES:

```

Appendix A.

```

;
; ROUTINES: EOPCHECK -- CHECK END POSITION AND SET HANKAKU
; SPACE OR PECIAL CHR.
;
; TABLES:
;
; REGISTERS: AX - RETURN CODE
; ALL OTHERS UNCHANGED
;
; CHANGE ACTIVITY: VERSION 00.00
;
;*****
;*****<DTSHFT>*****
;
; THIS ROUTINE SHIFTS DATA OF INPUT BUFFER LEFT
; INPUT: AX -- DATA POSITION TO BE DELETED
;
;*****<DTSHFT>*****
DTSHFT PROC NEAR
;
; PUSH BX ; SAVE REGISTERS
; PUSH CX
; PUSH SI
; PUSH DI
; CMP AX, KKMEOP ; CHECK POINTER
; JAE DTS_020 ; AX >= NED POSITION
; LEA SI, KKINBUF ; GET OFFSET OF INPUT BUFFER
; ADD SI, AX ; SI POINTS DATA TO BE DELETED
; MOV AL, BYTE PTR DS:[SI+1]
; GET ATTR. OF DATA
; KCALC DX, AX ; CHECK ZENKAKU OR HANKAKU
; MOV DI, AX ; SAVE DATA LENGTH
; MOV SI, DI ; DI POINTS DESTINATION POSITION
; ADD SI, DX ; SI POINTS SOURCE POSITION
; LEA CX, KKINBUF ; GET OFFSET OF INPUT BUFFER
; ADD CX, KKMEOP ; GET OFFSET OF DATA TO BE DELETED
; SUB CX, SI ; CX = REPEAT CNT.
; CMP CX, 0 ; CHECK REPEAT CNT.
; JL DTS_020 ; REPEAT CNT. < 0
; JZ DTS_010 ; REPEAT CNT. = 0
; CLD ; SET DIRECTION FLAG
; REP MOVSB ; TRANSFER DATA OF INPUT BUFFER
DTS_010:
; SUB KKMEOP, DX ; SET PARAMETER FOR EOPCHECK
; CALL EOPCHECK ; CHECK EOP
; ADD KKMEOP, DX ; RESET PARAMETER TO RETURN
DTS_020:
; MOV AX, 0 ; SET RETURN CODE ( NORMAL )
; POP DI ; RESTORE REGISTERS
; POP SI
; POP CX
; POP BX
; RET ; RETURN TO CALLER
DTSHFT ENDP
;*****
;
; PROGRAM NAME: EOPCHECK
;
; DESCRIPTIVE NAME: CHECK END POSITION AND SET SPACE OR SPECIAL
;
; FUNCTION: CHECK END POSITION AND SET SPACE OR SPECIAL
; CHARACTER
;
; LINKAGE:
;
; INPUT: DX-- <> 0: LENGTH OF DATA TO BE DELETED
; = 0: INSERT PROCESS
;
; OUTPUT: NONE
;
; RETURN CODES: (AX)
;
; 0 - SUCCESSFUL
; 1 - SUCCESSFUL
;
; EXTERNAL REFERENCES:
;
; ROUTINES: NONE
;
; TABLES:
;
; REGISTERS: AX - RETURN CODE
; ALL OTHERS UNCHANGED
;
; CHANGE ACTIVITY: VERSION 00.00
;
;*****
;*****<EOPCHECK>*****
;
; CHECK END POINTER AND CLEAR FIELD OF SPACE AREA
; INPUT: DX: <> 0: LENGTH OF DELETE DATA
; = 0: INSERT PROCESS
;
;*****<EOPCHECK>*****
EOPCHECK PROC NEAR
;
; PUSH SI ; SAVE REGISTER
; MOV AX, KKMEOP ; GET END POSITION
; CMP AX, KKMEOPMAX ; CHECK BUFFER END
; JAE EOP_990 ; GOTO RETURN
; LEA SI, KKINBUF ; GET OFFSET OF INPUT BUFFER
; ADD SI, AX ; SI POINTS INSERT AREA

```

```

162F
162F 53
1630 51
1631 56
1632 57
1633 3B 06 00EA R
1637 73 31
1639 8D 36 0009 R
163D 03 F0
163F 8A 44 01
1642 E8 1341 R
1645 8B D0
1647 8B FE
1649 03 F2
164B 8D 0E 0009 R
164F 03 0E 00EA R
1653 2B CE
1655 85 F9 00
1658 7C 10
165A 74 03
165C FC
165D F3 A4
165F
165F 29 16 00EA R
1663 E8 1672 R
1666 01 16 00EA R
166A
166A 8B 0000
166D 5F
166E 5E
166F 59
1670 5B
1671 C3
1672

```

```

1672
1672 56
1673 A1 00EA R
1674 3B 06 00E8 R
167A 73 1A
167C 8D 36 0009 R
1680 03 F0

```

1682 8B C2  
 1684 B1 03  
 1686 F6 F1  
 1688 84 C0  
 168A 75 03  
 168C B8 0001  
 168F  
 168F 8B C8  
 1691 E8 169A R  
 1694 E2 FB  
 1696  
 1696 33 C0  
 1698 5E  
 1699 C3  
 169A

```

MOV AX,DX
MOV CL,INDEX ;
DIV CL
TEST AL,AL ; CHECK
JNZ EOP_090
MOV AX,I
EOP_090: MOV CX,AX
EOP_100: CALL SETSPC ; SET HANKAKU SPACE
LOOP EOP_100
EOP_990: XOR AX,AX ; RETURN CODE ( NORMAL )
POP SI ; RESTORE REGISTER
RET ; RETURN TO CALLER
EOPCHECK ENDP
;*****<SETSPC>*****
;
; SET HANKAKU SPACE AFTER END DATA POSITION
; SI: POINTS HEAD POSITION TO BE INSERT
;
;*****<SETSPC>*****
SETSPC PROC NEAR ; SET SPACE CODE
MOV AX,KKCHRSP ; GET SPECIAL CHARACTER CODE
CLD
STOSH AL,AL
XOR AX,AX
POP SI
RET ; RETURN TO CALLER
SETSPC ENDP

```

169A  
 169A A1 00F5 R  
 169D FC  
 169E AB  
 169F 32 C0  
 16A1 AA  
 16A2 C3  
 16A3

```

;*****<SETSPC>*****
;
; PROGRAM NAME ( SUBROUTINE BLOCK )
; : SETSTRCL
; : SETENDCL
; : GETCULM
;
; STATUS:
;
; FUNCTION: CALCULATE COLUMN POSITION ; FROM POINTER OF INPUT BUFF.
; LINKAGE:
; INPUT:
;
; OUTPUT:
;
; RETURN CODES: NON
;
; EXTERNAL REFERENCES
;
; ROUTINES: NONE
;
; TABLES:
;
; REGISTERS: AX - RESULT
; ALL OTHERS UNCHANGED
;
; CHANGE ACTIVITY: VERSION 00.00
;
;*****<SETSTRCL>*****
;
; SET START POSITION FOR DISPLAY ROUTINE
;
;*****<SETSTRCL>*****
SETSTRCL PROC NEAR
MOV AX,KKMRCA ; GET START POSITION
MOV KXINP,AX ; SET START POINT FOR DISPLAY
CALL GETCULM
MOV DSTRTP,AX ; SET START COLUMN FOR DISPLAY
RET
SETSTRCL ENDP
;*****<SETENDCL>*****
;
; SET END POSITION FOR DISPLAY ROUTINE
;
;*****<SETENDCL>*****
SETENDCL PROC NEAR
MOV AX,KKMEOP ; GET END POSITION
CALL GETCULM
MOV DENDP,AX ; SET END POINT (COLUMN) FOR DISPLAY
RET
SETENDCL ENDP
;*****<GETCULM>*****
;
; CALCULATE COLUMN POSITION
;
;*****<GETCULM>*****
GETCULM PROC NEAR
PUSH CX
MOV CL,INDEX
DIV CL
XOR AH,AH ; CLEAR AH
ADD AX,BASE ; GET COLUMN POSITION
POP CX
RET
GETCULM ENDP
;*****<CDCHCK1>*****
;
; CODE REASONABLE CHECK 1
;
; INPUT : BL:CODE
; AL:RANGE TYPE

```

16A3  
 16A3 A1 00E6 R  
 16A6 A3 00F1 R  
 16A9 E8 16BA R  
 16AC A3 0107 R  
 16AF C3  
 16B0

```

;*****<SETSTRCL>*****
;
; SET START POSITION FOR DISPLAY ROUTINE
;
;*****<SETSTRCL>*****
SETSTRCL PROC NEAR
MOV AX,KKMRCA ; GET START POSITION
MOV KXINP,AX ; SET START POINT FOR DISPLAY
CALL GETCULM
MOV DSTRTP,AX ; SET START COLUMN FOR DISPLAY
RET
SETSTRCL ENDP
;*****<SETENDCL>*****
;
; SET END POSITION FOR DISPLAY ROUTINE
;
;*****<SETENDCL>*****
SETENDCL PROC NEAR
MOV AX,KKMEOP ; GET END POSITION
CALL GETCULM
MOV DENDP,AX ; SET END POINT (COLUMN) FOR DISPLAY
RET
SETENDCL ENDP
;*****<GETCULM>*****
;
; CALCULATE COLUMN POSITION
;
;*****<GETCULM>*****
GETCULM PROC NEAR
PUSH CX
MOV CL,INDEX
DIV CL
XOR AH,AH ; CLEAR AH
ADD AX,BASE ; GET COLUMN POSITION
POP CX
RET
GETCULM ENDP
;*****<CDCHCK1>*****
;
; CODE REASONABLE CHECK 1
;
; INPUT : BL:CODE
; AL:RANGE TYPE

```

16B0  
 16B0 A1 00EA R  
 16B3 E8 16BA R  
 16B6 A3 0109 R  
 16B9 C3  
 16BA

```

;*****<SETSTRCL>*****
;
; SET START POSITION FOR DISPLAY ROUTINE
;
;*****<SETSTRCL>*****
SETSTRCL PROC NEAR
MOV AX,KKMRCA ; GET START POSITION
MOV KXINP,AX ; SET START POINT FOR DISPLAY
CALL GETCULM
MOV DSTRTP,AX ; SET START COLUMN FOR DISPLAY
RET
SETSTRCL ENDP
;*****<SETENDCL>*****
;
; SET END POSITION FOR DISPLAY ROUTINE
;
;*****<SETENDCL>*****
SETENDCL PROC NEAR
MOV AX,KKMEOP ; GET END POSITION
CALL GETCULM
MOV DENDP,AX ; SET END POINT (COLUMN) FOR DISPLAY
RET
SETENDCL ENDP
;*****<GETCULM>*****
;
; CALCULATE COLUMN POSITION
;
;*****<GETCULM>*****
GETCULM PROC NEAR
PUSH CX
MOV CL,INDEX
DIV CL
XOR AH,AH ; CLEAR AH
ADD AX,BASE ; GET COLUMN POSITION
POP CX
RET
GETCULM ENDP
;*****<CDCHCK1>*****
;
; CODE REASONABLE CHECK 1
;
; INPUT : BL:CODE
; AL:RANGE TYPE

```

16BA  
 16BA 51  
 16BB B1 03  
 16BD F6 F1  
 16BF 32 E4  
 16C1 03 06 00EE R  
 16C5 59  
 16C6 C3  
 16C7

```

;*****<SETSTRCL>*****
;
; SET START POSITION FOR DISPLAY ROUTINE
;
;*****<SETSTRCL>*****
SETSTRCL PROC NEAR
MOV AX,KKMRCA ; GET START POSITION
MOV KXINP,AX ; SET START POINT FOR DISPLAY
CALL GETCULM
MOV DSTRTP,AX ; SET START COLUMN FOR DISPLAY
RET
SETSTRCL ENDP
;*****<SETENDCL>*****
;
; SET END POSITION FOR DISPLAY ROUTINE
;
;*****<SETENDCL>*****
SETENDCL PROC NEAR
MOV AX,KKMEOP ; GET END POSITION
CALL GETCULM
MOV DENDP,AX ; SET END POINT (COLUMN) FOR DISPLAY
RET
SETENDCL ENDP
;*****<GETCULM>*****
;
; CALCULATE COLUMN POSITION
;
;*****<GETCULM>*****
GETCULM PROC NEAR
PUSH CX
MOV CL,INDEX
DIV CL
XOR AH,AH ; CLEAR AH
ADD AX,BASE ; GET COLUMN POSITION
POP CX
RET
GETCULM ENDP
;*****<CDCHCK1>*****
;
; CODE REASONABLE CHECK 1
;
; INPUT : BL:CODE
; AL:RANGE TYPE

```



Appendix A.

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Appendix A.

```

*****
* MODULE 9 *
*****

```

```

0000
0000 50
0001 88 0040
0004 8E D8
0006 58
0007 C3
0008

```

```

-----
; THIS SUBROUTINE SETS DS TO POINT TO THE BIOS DATA AREA
; INPUT: NONE
; OUTPUT: DS IS SET
-----

```

```

DDS ASSUME CS:CODE,DS:DATA
PROC NEAR
PUSH AX
MOV AX,40H
MOV DS,AX
POP AX
RET
ENDP

```

```

;--- INT 1A
; TIME OF DAY/SOUND SOURCE SELECT
; THIS ROUTINE ALLOWS THE CLOCK TO BE SET/READ.
; AN INTERFACE FOR SETTING THE MULTIPLEXER FOR
; AUDIO SOURCE IS ALSO PROVIDED

```

```

; INPUT
; (AH) = 0 READ THE CURRENT CLOCK SETTING
; RETURNS CX = HIGH PORTION OF COUNT
; DX = LOW PORTION OF COUNT
; AL = 0 IF TIMER HAS NOT PASSED 24 HOURS
; SINCE LAST READ. < 0 IF ON ANOTHER DAY
; (AH) = 1 SET THE CURRENT CLOCK
; CX = HIGH PORTION OF COUNT
; DX = LOW PORTION OF COUNT
; (AH) = 80H SET UP SOUND MULTIPLEXER
; AL =(SOURCE OF SOUND) --> "AUDIO OUT" OR RF MODULATOR
; 00 = 8253 CHANNEL 2
; 01 = CASSETTE INPUT
; 02 = "AUDIO IN" LINE ON I/O CHANNEL
; 03 = COMPLEX SOUND GENERATOR CHIP
; NOTE: COUNTS OCCUR AT THE RATE OF 1193180/65536 COUNTS/SEC
; (OR ABOUT 18.2 PER SECOND -- SEE EQUATES BELOW)

```

```

0008
0008 FB
0009 1E
000A E8 0000 R
000D 80 FC 80
0010 74 2E
0012 0A E4
0014 74 07
0016 FE CC
0018 74 16
001A FB
001B 1F
001C CF
001D FA
001E A0 0070 R
0021 C6 06 0070 R 00
0026 8B 0E 006E R
002A 8B 16 006C R
002E EB EA
0030 FA
0031 89 16 006C R
0035 89 0E 006E R
0039 C6 06 0070 R 00
003E EB DA
0040 51
0041 B1 05
0043 D2 E0
0045 86 C4
0047 E4 61
0049 24 9F
004B 0A C4
004D E6 61
004F 59
0050 EB C8
0052

```

```

-----
ASSUME CS:CODE,DS:DATA
TIME_OF_DAY PROC FAR

```

```

STI ; INTERRUPTS BACK ON
PUSH DS ; SAVE SEGMENT
CALL DDS
CMP AH,80H ; AH=80
JE T4A ; MUX_SET-UP
OR AH,AH ; AH=0
JZ T2 ; READ_TIME
DEC AH ; AH=1
JZ T3 ; SET_TIME
T1: STI ; INTERRUPTS BACK ON
POP DS ; RECOVER SEGMENT
IRET ; RETURN TO CALLER
T2: CLI ; NO TIMER INTERRUPTS WHILE READING
MOV AL,TIMER_OFL ; GET OVERFLOW, AND RESET THE FLAG
MOV TIMER_OFL,0
MOV CX,TIMER_HIGH
MOV DX,TIMER_LOW
JMP T1 ; TOD_RETURN
T3: CLI ; NO INTERRUPTS WHILE WRITING
MOV TIMER_LOW,DX
MOV TIMER_HIGH,CX
MOV TIMER_OFL,0
JMP T1 ; TOD_RETURN
T4A: PUSH CX
MOV CL,5
SAL AL,CL ; SHIFT PARM BITS LEFT 5 POSITIONS
XCHG AL,AH ; SAVE PARM
IN AL,PORT_B ; GET CURRENT PORT SETTINGS
AND AL,10011111B ; ISOLATE MUX BITS
OR AL,AH ; COMBINE PORT BITS/PARM BITS
OUT PORT_B,AL ; SET PORT TO NEW VALUE
POP CX
JMP T1 ; TOD_RETURN
TIME_OF_DAY ENDP

```

```

-----
; THIS ROUTINE WILL READ TIMER1. THE VALUE READ IS RETURNED IN AX.

```

```

READ_TIME PROC NEAR
MOV AL,40H ; LATCH TIMER1
OUT TIM_CTL,AL
PUSH AX ; WAIT FOR 8253 TO INIT ITSELF
POP AX
IN AL,TIMER+1 ; READ LSB
MOV AH,AL ; SAVE IT IN HIGH BYTE
PUSH AX ; WAIT FOR 8253 TO INIT ITSELF
POP AX
IN AL,TIMER+1 ; READ MSB
XCHG AL,AH ; PUT BYTES IN PROPER ORDER
RET
ENDP

```

```

; REAL_VECTOR_SETUP
; THIS ROUTINE WILL INITIALIZE THE INTERRUPT 48 VECTOR TO
; POINT AT THE REAL INTERRUPT ROUTINE.

```

```

0063
0063 50
0064 53
0065 06
0066 33 C0
0068 8E C0

```

```

REAL_VECTOR_SETUP PROC NEAR
PUSH AX ; SAVE THE SCAN CODE
PUSH BX
PUSH ES
XOR AX,AX ; INITIALIZE TO POINT AT VECTOR
; SECTOR(0)
MOV ES,AX

```

```

006A 8B 0120          MOV     BX,40H*4H      ; POINT AT INTERRUPT 40
006D 26: C7 07 0000 E MOV     WORD PTR ES:[BX],OFFSET KEY62_INT ; MOVE IN OFFSET OF
                                ; ROUTINE
0072 43              INC     BX              ; ADD 2 TO BX
0073 43              INC     BX
0074 0E              PUSH    CS              ; GET CODE SEGMENT OF BIOS (SEGMENT
                                ; RELOCATEABLE)
0075 58              POP     AX
0076 26: 89 07        MOV     WORD PTR ES:[BX],AX ; MOVE IN SEGMENT OF ROUTINE
0079 07              POP     ES
007A 5B              POP     BX
007B 5B              POP     AX
007C C3              RET
007D

```

```

REAL_VECTOR_SETUP      ENDP

```

```

;-----
;KB_NOISE
; THIS ROUTINE IS CALLED WHEN GENERAL BEEPS ARE REQUIRED FROM
; THE SYSTEM.
;INPUT
; BX=LENGH OF THE TONE
; CX=CONTAINS THE FREQUENCY
;OUTPUT
; ALL REGISTERS ARE MAINTAINED.
;HINTS
; AS CX GETS LARGER THE TONE PRODUCED GETS LOWER IN PITCH.
;-----

```

```

007D          KB_NOISE      PROC      NEAR
007D FB          STI
007E 50          PUSH   AX
007F 53          PUSH   BX
0080 51          PUSH   CX
0081 E4 61      IN     AL,061H        ; GET CONTROL INFO
0083 50          PUSH   AX              ; SAVE
0084          LOOP01:
0084 24 FC      AND     AL,0FCH        ; TURN OFF TIMER GATE AND SPEAKER
                                ; DATA
0086 E6 61      OUT    061H,AL        ; OUTPUT TO CONTROL
0088 51          PUSH   CX              ; HALF CYCLE TIME FOR TONE
0089 E2 FE      LOOP02: LOOP   LOOP02 ; SPEAKER OFF
008B 0C 02      OR     AL,2           ; TURN ON SPEAKER BIT
008D E6 61      OUT    061H,AL        ; OUTPUT TO CONTROL
008F 59          POP     CX
0090 51          PUSH   CX              ; RETRIEVE FREQUENCY
0091 E2 FE      LOOP03: LOOP   LOOP03 ; ANOTHER HALF CYCLE
0093 4B          DEC     BX              ; TOTAL TIME COUNT
0094 59          POP     CX              ; RETRIEVE FREQ.
0095 75 ED      JNZ   LOOP01          ; DO ANOTHER CYCLE
0097 58          POP     AX              ; RECOVER CONTROL
0098 E6 61      OUT    061H,AL        ; OUTPUT THE CONTROL
009A 59          POP     CX
009B 5B          POP     BX
009C 5B          POP     AX
009D C3          RET
009E

```

```

KB_NOISE      ENDP

```

```

;-----
; EASE OF USE REVECTOR ROUTINE - CALLED THROUGH
; INT 18H WHEN CASSETTE BASIC IS INVOKED (NO DISKETTE
; NO CARTRIDGES)
; KEYBOARD VECTOR IS RESET TO POINT TO "NEW_INT_48"
; PLAN TO TRANSFER KBDIO
; BASIC VECTOR IS SET TO POINT TO E000:0
;-----

```

```

009E          BAS_ENT      PROC      FAR
009E 2B C0      ASSUME DS:ABS0
00A0 8E D8      SUB     AX,AX
00A2 C7 06 0120 R 00F9 R MOV     DS,AX          ;SET ADDRESSING
00A4 A3 0060 R  MOV     KEY62_PTR,OFFSET NEW_INT_48
00A6 C7 06 0062 R E000 MOV     BASIC_PTR,AX   ; SET INT 18=E000:0 JX BASIC
00B1 CD 18      MOV     BASIC_PTR+2,0E000H
00B3          INT     18H          ; GO TO BASIC

```

```

BAS_ENT      ENDP

```

```

;-----
; THIS ROUTINE HANDLES THE TIMER INTERRUPT FROM
; CHANNEL 0 OF THE 8253 TIMER. INPUT FREQUENCY IS 1.19318 MHZ
; AND THE DIVISOR IS 65536, RESULTING IN APPROX. 18.2 INTERRUPTS
; EVERY SECOND.
;-----

```

```

; THE INTERRUPT HANDLER MAINTAINS A COUNT OF INTERRUPTS SINCE POWER
; ON TIME, WHICH MAY BE USED TO ESTABLISH TIME OF DAY.
; INTERRUPTS MISSED WHILE INTS. WERE DISABLED ARE TAKEN CARE OF
; BY THE USE OF TIMER 1 AS A OVERFLOW COUNTER
; THE INTERRUPT HANDLER ALSO DECREMENTS THE MOTOR CONTROL COUNT
; OF THE DISKETTE, AND WHEN IT EXPIRES, WILL TURN OFF THE DISKETTE
; MOTOR, AND RESET THE MOTOR RUNNING FLAGS
; THE INTERRUPT HANDLER WILL ALSO INVOKE A USER ROUTINE THROUGH
; INTERRUPT 1CH AT EVERY TIME TICK. THE USER MUST CODE A ROUTINE
; AND PLACE THE CORRECT ADDRESS IN THE VECTOR TABLE.
;-----

```

```

00B3          ASSUME DS:DATA
00B3 FB          TIMER_INT      PROC      FAR
00B4 1E          STI
00B5 50          PUSH   DS              ; INTERRUPTS BACK ON
00B6 52          PUSH   AX
00B7 52          PUSH   DX              ; SAVE MACHINE STATE
00B8 FF 06 0000 R CALL    DDS
00BA FF 06 006C R INC     TIMER_LOW      ; INCREMENT TIME
00BE 75 04      JNZ    T4              ; TEST_DAY
00C0 FF 06 006E R INC     TIMER_HIGH      ; INCREMENT HIGH WORD OF TIME
00C4          T4:
00C4 83 3E 006E R 18 CMP     TIMER_HIGH,018H ; TEST_DAY
00C9 75 15      JNZ    T5              ; TEST FOR COUNT EQUALLING 24 HOURS
00CB 81 3E 006C R 00B0 CMP     TIMER_LOW,00B0 ; DISKETTE_CTL

```

Appendix A.

```

00D1 75 0D                JNZ     T5                ; DISKETTE_CTL
                                ;-----
                                ; TIMER HAS GONE 24 HOURS
00D3 2B C0                SUB     AX,AX
00D5 A3 004E R            MOV     TIMER_HIGH,AX
00D8 A3 006C R            MOV     TIMER_LOW,AX
00DB C6 06 0070 R 01     MOV     TIMER_OFL,1
00E0                      ;-----
00E0                      ; TEST FOR DISKETTE TIME OUT
00E0                      ; T5:
                                ; LOOP TILL ALL OVERFLOWS TAKEN
                                ; CARE OF
00E0 FE 0E 0040 R        DEC     MOTOR_COUNT
00E4 75 09                JNZ     T6                ; RETURN IF COUNT NOT OUT
00E6 80 26 003F R F0     AND     MOTOR_STATUS,0F0H ; TURN OFF MOTOR RUNNING BITS
00E8 80 80                MOV     AL,FDC_RESET      ; TURN OFF MOTOR, DO NOT RESET FDC
00ED E6 F2                OUT    NEC_CTL,AL        ; TURN OFF THE MOTOR
00EF CD 1C                INT    1CH                ; TRANSFER CONTROL TO A USER
                                ; ROUTINE
00F1 80 20                MOV     AL,EOI
00F3 E6 20                OUT    020H,AL           ; END OF INTERRUPT TO 8259
00F5 5A                    POP     DX
00F6 58                    POP     AX
00F7 1F                    POP     DS                ; RESET MACHINE STATE
00F8 CF                    IRET                    ; RETURN FROM INTERRUPT
00F9                      TIMER_INT    ENDP
;-----
; NEW_INT48
; THIS ROUTINE IS THE INTERRUPT 48 HANDLER WHEN THE MACHINE IS
; FIRST POWERED ON AND CASSETTE BASIC IS GIVEN CONTROL. IT
; HANDLES THE FIRST KEYSTROKES ENTERED FROM THE KEYBOARD AND
; PERFORMS "SPECIAL" ACTIONS AS FOLLOWS:
; IF CTRL-ESC IS THE FIRST SEQUENCE "LOAD CAS1:,"R" IS
; EXECUTED GIVING THE USER THE ABILITY TO BOOT
; FROM CASSETTE
; AFTER THESE KEYSTROKES OR AFTER ANY OTHER KEYSTROKES THE
; INTERRUPT 48 VECTOR IS CHANGED TO POINT AT THE REAL
; INTERRUPT 48 ROUTINE.
;-----
00F9                      NEW_INT_48 PROC FAR
00F9                      CMP     AL,1                ; IS THIS AN ESCAPE KEY?
00FB 74 10                JE      ESC_KEY            ; JUMP IF AL=ESCAPE KEY
00FD 3C 1D                CMP     AL,29             ; ELSE, IS THIS A CONTROL KEY?
00FF 74 06                JE      CTRL_KEY          ; JUMP IF AL=CONTROL KEY
0101 E8 0063 R            CALL    REAL_VECTOR_SETUP ; OTHERWISE, INITIALIZE REAL
                                ; INT 48 VECTOR
0104 CD 48                INT    48H                ; PASS THE SCAN CODE IN AL
0106                      ESC_ONLY:
0106                      IRET                    ; RETURN TO INTERRUPT 48H
0107                      CTRL_KEY:
0107                      OR      KB_FLAG,04H        ; TURN ON CTRL SHIFT IN KB_FLAG
010C CF                    IRET                    ; RETURN TO INTERRUPT
010D                      ESC_KEY:
010D                      TEST    KB_FLAG,04H        ; HAS CONTROL SHIFT OCCURED ?
0112 74 F2                JE      ESC_ONLY          ; NO, ESC ONLY
; CONTROL ESCAPE HAS OCCURED, PUT MESSAGE IN BUFFER FOR CASSETTE
; LOAD
0114 C6 06 0017 R 00     MOV     KB_FLAG,0        ; ZERO OUT CONTROL STATE
0119 1E                    PUSH    DS                ; INITIALIZE ES FOR BIOS DATA
011A 07                    POP     ES                ; SAVE OLD DS
011B 1E                    PUSH    DS                ; POINT DS AT CODE SEGMENT
011C 0E                    PUSH    CS
011D 1F                    POP     DS
011E BE 0138 R            MOV     SI,OFFSET CAS_LOAD ; GET MESSAGE
0121 B9 000E 90           MOV     CX,CAS_LENGTH    ; LENGTH OF CASSETTE MESSAGE
0125 33 C0                XOR     AX,AX
0127 8A 04                MOV     AL,[SI]          ; GET ASCII CHARACTER FROM MESSAGE
0129 CD 79                INT    79H                ; PUT IN KEYBOARD BUFFER
012B 46                    INC     SI
012C E2 F9                LOOP   T_LOOP            ; RETRIEVE BIOS DATA SEGMENT
012E B8 1C0D             MOV     AX,1C0DH
0131 CD 79                INT    79H
0133 1F                    POP     DS
;-----
; *****
; IT IS ASSUMED THAT THE LENGTH OF THE CASSETTE MESSAGE IS
; LESS THAN OR EQUAL TO THE LENGTH OF THE BUFFER. IF THIS IS
; NOT THE CASE THE BUFFER WILL EVENTUALLY CONSUME MEMORY.
;-----
0134 E8 0063 R            CALL    REAL_VECTOR_SETUP
0137 CF                    IRET
;-----
; MESSAGE FOR OUTPUT WHEN CONTROL-ESCAPE IS ENTERED AS FIRST
; KEY SEQUENCE
; CAS_LOAD LABEL BYTE
0138 4C 4F 41 44 20 22     DB 'LOAD "CAS1:","R'
0138 43 41 53 31 3A 22     DB 'LOAD "CAS1:","R'
0138 2C 52
= 000E
0146                      CAS_LENGTH EQU $ - CAS_LOAD
0300                      NEW_INT_48 ENDP
0300                      ORG BEGIN+300H
0300                      ENDS
0300                      END

```



```

*****
*****
*   *
* MODULE 10 *
*   *
*****
*****

```

```

;*****
;
; PROGRAM NAME = RAS.ASM
;
; DESCRIPTIVE NAME = POWER ON SELF TEST
;
; FUNCTION = POWER ON SELF TESTS (POSTS)
;
; NOTES = NONE
;
; DEPENDENCIES = NONE
;
; RESTRICTION = NONE
;
; PROGRAM TYPE = PROCEDURE
;
; PROCESSOR = 8088
;
; MODULE SIZE = VALUE OF LABEL "POST_END"
;
; ATTRIBUTES = SERIALLY RE-USABLE
;
; LINKAGE = RESET_FLAG : 0000 : POST - POWER ON ENTRY
;                1234 : POST - "CTRL"+"ALT"+"DEL" ENTRY
;                3412 : POST - "CTRL"+"ALT"+"INS" ENTRY
;                4321 : DIAG. CARTRIDGE ENTRY
;
; INPUT = NONE
;
; OUTPUT = NONE
;
; EXIT-NORMAL = INT 19 TO BOOT LOADER, OR
;              INT 80 TO DIAG. CARTRIDGE, OR
;              JMP (FAR INDIRECT) TO PCjr CARTRIDGE
;
; EXIT-ERROR = NORMAL MODE:
;              JMP (HEAR) TO E_MSG OR SAME AS EXIT-NORMAL
;              SERVICE MODE:
;              JMP (HEAR) TO E_MSG
;
; EXTERNAL REFERENCES = VIDEO_PARMS : CRTIC PARAMETER TABLE
;                      DISK_BASE  : DISKETTE DRIVE PARAM. TABLE
;                      EXTAB      : EXTENDED SCAN CODE TABLE
;
; EXTERNAL ROUTINES = DDS          : SET DS AS 40H SUBROUTINE
;                      READ_HALF_BIT : CASSETTE SUBROUTINE
;                      SEEK         : SEEK SUBROUTINE
;                      VIDEO_IO     : INT 10H
;                      DISKETTE_IO  : INT 13H
;                      KEYBOARD_IO  : INT 16H
;                      BOOT_STRAP   : INT 19H
;
; DATA AREA = 00000 : 8088 INTERRUPT LOCATIONS
;              00300 : TEMPORARY STACK AREA DURING POST
;              00400 : ROM BIOS DATA AREA
;              00500 : EXTRA DATA AREA
;
; CONTROL BLOCKS = 00400-7 : RS-232C ADAPTER ADDRESSES
;                   00408-F : PRINTER ADAPTER ADDRESSES
;                   00410-1 : INSTALLED HARDWARE
;                   00413-4 : USABLE MEMORY SIZE IN K BYTES
;                   00415-6 : REAL MEMORY SIZE IN K BYTES
;                   0041A-B : KBD BUFFER HEAD POINTER
;                   0041C-D : KBD BUFFER END POINTER
;                   0043E   : DISKETTE SEEK STATUS
;                   00442   : CURRENT PAGE BEING DISPLAYED
;                   0044B   : CASSETE LAST INPUT VALUE
;                   00472-3 : WARM START INDICATOR
;                   00474-7 : DISKETTE SCRACH PADS
;                   00478-B : PRINTER TIMEOUT VALUE
;                   0047C-F : RS-232 TIMEOUT VALUE
;                   00480-3 : KBD BUFFER POINTERS
;                   00484   : INTERRUPT HAPPEND FLAG
;                   00505   : MFG CHECK POINT VALUE
;                   00518   : POST ERROR VALUE
;                   00702   : DISKETTE DRIVES' CONFIGURATION
;
; TABLES = Z0      : SX-08 DATA
;           Z_0     : "
;           Z1      : BRANCH DATA
;           Z2      : "
;           EX_0    : "
;           SYSTR5  : SX-08 DATA
;           PPD     : PRINTER DATA
;
;*****

```

Appendix A.

```

0000          CODE  SEGMENT PUBLIC
                ASSUME CS:CODE,DS:ABS0,ES:NOTHING,SS:STACK

                PUBLIC POST,POST_END
                PUBLIC RESET,D11,DUMMY_RETURN,PRT_HEX,BEEP,VECTOR_TABLE

                EXTRN DDS:NEAR
                EXTRN READ_HALF_BIT:NEAR
                EXTRN SEEK:NEAR
                EXTRN PRINT_SCREEN:FAR                ; INT 05H
                EXTRN TIMER_INT:FAR                  ;      08H
                EXTRN KB_INT:FAR                     ;      09H
                EXTRN DISK_INT:FAR                   ;      0EH
                EXTRN VIDEO_IO:NEAR                  ;      10H
                EXTRN EQUIPMENT:FAR                  ;      11H
                EXTRN MEMORY_SIZE_DETERMINE:FAR      ;      12H
                EXTRN DISKETTE_IO:FAR                 ;      13H
                EXTRN RS232_IO:FAR                   ;      14H
                EXTRN CASSETTE_IO:FAR                 ;      15H
                EXTRN KEYBOARD_IO:FAR                 ;      16H
                EXTRN PRINTER_IO:FAR                 ;      17H
                EXTRN BAS_ENT:FAR                    ;      18H
                EXTRN BOOT_STRAP:NEAR                 ;      19H
                EXTRN TIME_OF_DAY:FAR                 ;      1AH
                EXTRN VIDEO_PARAMS:BYTE               ;      1DH
                EXTRN DISK_BASE:BYTE                  ;      1EH
                EXTRN KBDHMI:FAR                      ;      41H
                EXTRN KEY62_INT:FAR                   ;      48H
                EXTRN EXTAB:BYTE                       ;      49H
                EXTRN KKKFDM:FAR                      ;      78H
                EXTRN BUFFER_QUEUEING:FAR             ;      79H

0000          POST LABEL FAR
= 0000        BEGIN EQU 0
;-----
;          RETURN POINTERS FOR RTNS CALLED BEFORE STACK INITIALIZED ;
;-----
0000 01      Z0    DB      001H          ; START REG ADDR

```

```

0001 60          DB      060H          ; MASK DATA
0002 0A          DB      10           ; PARAMETER LENGTH
0003 00          DB      000H          ; 01 - CARTRIDGE ROM 2 (D0000-)
0004 00          DB      000H          ; 02 - CARTRIDGE ROM 3 (D8000-)
0005 00          DB      000H          ; 03 - CARTRIDGE ROM 4 (E0000-)
0006 00          DB      000H          ; 04 - CARTRIDGE ROM 5 (E8000-)
0007 00          DB      000H          ; 05 - CARTRIDGE ROM 6 (F0000-)
0008 00          DB      000H          ; 06 - CARTRIDGE ROM 7 (F8000-)
0009 00          DB      000H          ; 07 - KANJI ROM
000A 00          DB      000H          ; 08 - PROGRAMMABLE RAM

000B B7          DB      0B7H          ; 09 - VRAM1 (B8000-BFFFF)
000C 00          DB      000H          ; 0A - VRAM2
000D 0086 R     DW      L0_1          ; RETURN ADDR

;----- ENABLE BASE ROM DECODER
000F 00          DB      000H          ; START REG ADDR
0010 23          DB      023H          ; MASK DATA
0011 01          DB      1           ; PARAMETER LENGTH
0012 BC          DB      0BCH          ; 00 - BASE ROM (E0000-FFFF)
0013 0089 R     DW      L0_2          ; RETURN ADDR

;----- INITIALIZE I/O REGISTER DECODERS
0015 80          DB      080H          ; START REG ADDR
0016 00          DB      000H          ; MASK DATA
0017 14          DB      20           ; PARAMETER LENGTH
0018 80          DB      080H          ; 80 - 8259 PIC CS (20H-27H)
0019 80          DB      080H          ; 81 - 8253 PIT CS (40H-47H)
001A 80          DB      080H          ; 82 - 8255 PPI CS (60H-67H)
001B 80          DB      080H          ; 83 - NMI CTRL CS (A0H-A7H)
001C 80          DB      080H          ; 84 - SOUND CHIP CS (C0H-C7H)
001D 9E          DB      09EH          ; 85 - UPD765 FDC CS (F0H-F7H)
001E 80          DB      080H          ; 86 - JOYSTICK READ (200H-207H)
001F 80          DB      080H          ; 87 - JOYSTICK WRITE (200H-207H)
0020 80          DB      080H          ; 88 - PARALLEL PRINTR(378H-37FH)
0021 80          DB      080H          ; 89 - 8250 COMM PORT (2F8H-2FFH)
0022 80          DB      080H          ; 8A - 46305 CRTC (3D0H-3D7H)
0023 00          DB      000H          ; 8B - RESERVED
0024 80          DB      080H          ; 8C - PCJR VGA (3DAH)
0025 80          DB      080H          ; 8D - NATIVE VGA (3DAH)
0026 80          DB      080H          ; 8E - EXT. VGA (3DDH)
0027 80          DB      080H          ; 8F - LIGHT PEN GATE (3DAH,3DEH)
0028 80          DB      080H          ; 90 - CRT/CPU PAGE REG 2 (3D9H)
0029 80          DB      080H          ; 91 - CRT/CPU PAGE REG (3DFH)
002A 00          DB      000H          ; 92 - MODEM CTRL REG (3F8H-3FFH)
002B 80          DB      080H          ; 93 - EXTERNAL BUS CONTROL REG
002C 008C R     DW      L0           ; RETURN ADDR

;----- DISABLE SX-03 DECODER
002E 8E          DB      08EH          ; START REG ADDR
002F 00          DB      000H          ; MASK DATA
0030 01          DB      1           ; PARAMETER LENGTH
0031 00          DB      000H          ; 8E - SX-03 EXT. VIDEO (3DDH)
0032 0177 R     DW      L11          ; RETURN ADDR

;-----
0034 018A R     Z1      DW      L13          ; RETURN ADDR OF ROS CHECKSUM TEST

;----- ALLOCATE PROGRAMMABLE RAM
0036 08          DB      008H          ; START REG ADDR
0037 63          DB      063H          ; MASK DATA
0038 01          DB      1           ; PARAMETER LENGTH
0039 A0          DB      0A0H          ; (00000-1FFFF)
003A 01FD R     DW      MCONF3          ; RETURN ADDR
003C 08          DB      008H          ; START REG ADDR
003D 63          DB      063H          ; MASK DATA
003E 01          DB      1           ; PARAMETER LENGTH
003F A4          DB      0A4H          ; (20000-3FFFF)

0040 01FD R     DW      MCONF3          ; RETURN ADDR
0042 08          DB      008H          ; START REG ADDR
0043 63          DB      063H          ; MASK DATA
0044 01          DB      1           ; PARAMETER LENGTH
0045 A8          DB      0A8H          ; (40000-5FFFF)
0046 01FD R     DW      MCONF3          ; RETURN ADDR
0048 08          DB      008H          ; START REG ADDR
0049 63          DB      063H          ; MASK DATA
004A 01          DB      1           ; PARAMETER LENGTH
004B AC          DB      0ACH          ; (60000-7FFFF)
004C 01FD R     DW      MCONF3          ; RETURN ADDR

;-----
004E 020E R     Z2      DW      L16          ; RETURN ADDR OF 2K RAM TEST

;-----
0050 0DCE R     EX_0    DW      OFFSET E80 ; RETURN ADDR OF E_MSG
0052 0DCE R     DW      OFFSET E80 ; "
0054 0DA7 R     DW      OFFSET TOTLTP0 ; "

;----- MESSAGE AREA FOR POST
;
0056 20 4B 42   F3B      DB      'KB' ; MEMORY SIZE PROMPT
0059 45 52 52 4F 52 ERROR_ERR DB      'ERROR' ; GENERAL ERROR PROMPT
005E 41          MEM_ERR  DB      'A' ; MEMORY ERROR MESSAGE
005F 42          KEY_ERR  DB      'A' ; KEYBOARD ERROR MESSAGE
0060 43          CAS5_ERR DB      'B' ; CASSETTE ERROR MESSAGE
0061 44          COM1_ERR DB      'C' ; SERIAL PORT ERROR MESSAGE (2FXH)
0062 45          COM2_ERR DB      'D' ; SERIAL PORT ERROR MESSAGE (3FXH)
0063 46          ROM_ERR  DB      'E' ; OPTIONAL GENERIC BIOS ROM ERROR
0064 47          CART_ERR DB      'F' ; CARTRIDGE ERROR MESSAGE
0065 48          DISK_ERR DB      'G' ; DISKETTE ERROR MESSAGE
0066 4A          PRT_ERR  DB      'H' ; PARARELL PRINTER ERROR MESSAGE
0067 4B          KFNT_ERR DB      'I' ; KANJI-FONT ROM ERROR MESSAGE
0068 4C          INVC_ERR DB      'L' ; INVALID COMBINATION OF CARTRIDGE

0069           IMASKS LABEL BYTE ; INTERRUPT MASKS FOR 8259
0069 EF          DB      0EFH          ; INTERRUPT CONTROLLER
006A F7          DB      0F7H          ; MODE4
; RS232C INTR MASK

```

Appendix A.

```

=====
; POST ENTRY POINT
;=====
006B RESET LABEL FAR
006B START:
;-----
; SETUP
; DISABLE NMI & MASKABLE INTS, CLEAR MEMORY MAPPING,
; ENABLE BASE ROM, AND SET UP I/O MAPPING
;-----
006B B0 00 MOV AL,0 ; DISABLE NMI
006D E6 A0 OUT NMI_PORT,AL
006F E4 A0 IN AL,NMI_PORT
0071 FA CLI ; DISABLES MASKABLE INTERRUPTS

0072 B0 FF MOV AL,0FFH ; SEND 'FF' TO MFG_TESTER
0074 E6 10 OUT MFG_PORT,AL
0076 B0 00 MOV AL,0 ; CLEAR MFG_PORT 11 & 12
0078 E6 11 OUT MFG_PORT+1,AL
007A E6 12 OUT MFG_PORT+2,AL

007C 8C C8 MOV AX,CS ; SET UP STACK SEGMENT & POINTER
007E 8E D8 MOV SS,AX
0080 8C 0000 R MOV SP,OFFSET Z0
0083 E9 04CA R JMP S8SETJ ; CLEAR MEMORY MAPPING
L0_1: JMP S8SETJ ; ENABLE BASE ROM DECODER
L0_2: JMP S8SETJ ; SET UP I/C REG DECODER
;-----
; SETUP
; DISABLE NMI, MASKABLE INTS, SOUND CHIP, AND VIDEO.
; TURN DRIVE 0 MOTOR OFF.
;-----
008C B0 00 L0: MOV AL,0 ; DISABLE NMI
008E E6 A0 OUT NMI_PORT,AL
0090 E4 A0 IN AL,NMI_PORT
; RESET NMI F/F
; DISABLE ATTENUATION IN SOUND CHIP
0092 B8 207F MOV AX,207FH ; REG ADDRESS IN AH, ATTENUATOR OFF
; IN AL
; 4 ATTENABLE
0095 B9 0004 L1: MOV CX,4
0098 02 C4 ADD AL,AH ; COMBINE REG ADDRESS AND DATA
009A E6 C0 OUT SND_CTL,AL
009C E2 FA LOOP L1 ; USEK*LIB

009E B0 A0 MOV AL,WD_ENABLE+FDC_RESET ; TURN DRIVE 0 MOTOR OFF,
00A0 E6 F2 OUT NEC_CTL,AL ; ENABLE TIMER
00A2 BA 03DA MOV DX,VGA_CTL ; VIDEO GATE ARRAY CONTROL
00A5 EC IN AL,DX ; SYNC VGA TO ACCEPT REG
00A8 B0 04 MOV AL,4 ; SET VGA RESET REG
00AB EE OUT DX,AL ; SELECT IT
00A9 B0 02 MOV AL,2 ; SET SYNC RESET
00AB EE OUT DX,AL ; RESET VIDEO GATE ARRAY
;-----
; TEST 1
; 8088 PROCESSOR TEST
; DESCRIPTION
; VERIFY 8088 FLAGS, REGISTERS, AND CONDITIONAL JUMPS
; MFG ERROR CODE = 0001
;-----
00AC B4 D5 MOV AH,0D5H ; SET SF, CF, ZF, AND AF FLAGS ON
00AE 9E SAHF
00AF 73 4C JNC L4 ; GO TO ERR ROUTINE IF CF NOT SET
00B1 75 4A JNZ L4 ; GO TO ERR ROUTINE IF ZF NOT SET
00B3 78 48 JNP L4 ; GO TO ERR ROUTINE IF PF NOT SET
00B5 79 46 JNS L4 ; GO TO ERR ROUTINE IF SF NOT SET
00B7 9F LAHF ; LOAD FLAG IMAGE TO AH
00B8 B1 05 MOV CL,5 ; LOAD CNT REG WITH SHIFT CNT
00BA D2 EC SHR AH,CL ; SHIFT AF INTO CARRY BIT POS

00BC 73 3F JNC L4 ; GO TO ERR ROUTINE IF AF NOT SET
00BE B0 40 MOV AL,40H ; SET THE OF FLAG ON
00C0 D0 E0 SHL AL,1 ; SETUP FOR TESTING
00C2 71 39 JNO L4 ; GO TO ERR ROUTINE IF OF NOT SET
00C4 32 E4 XOR AH,AH ; SET AH = 0
00C6 9E SAHF ; CLEAR SF, CF, ZF, AND PF
00C7 76 34 JBE L4 ; GO TO ERR ROUTINE IF CF ON
; GO TO ERR ROUTINE IF ZF ON
; GO TO ERR ROUTINE IF SF ON
00C9 78 32 JS L4 ; GO TO ERR ROUTINE IF PF ON
00CB 7A 30 JP L4 ; GO TO ERR ROUTINE IF OF ON
00CD 9F LAHF ; LOAD FLAG IMAGE TO AH
00CE B1 05 MOV CL,5 ; LOAD CNT REG WITH SHIFT CNT
00D0 D2 EC SHR AH,CL ; SHIFT 'AF' INTO CARRY BIT POS
00D2 72 29 JC L4 ; GO TO ERR ROUTINE IF OM
00D4 D0 E4 SHL AH,1 ; CHECK THAT 'OF' IS CLEAR
00D6 70 25 JD L4 ; GO TO ERR ROUTINE IF OM
;----- READ/WRITE THE 8088 GENERAL AND SEGMENTATION REGISTERS
; WITH ALL ONE'S AND ZEROES'S.
;-----
00D8 B8 FFFF MOV AX,0FFFFH ; SETUP ONE'S PATTERN IN AX
00DB F9 STC
00DC 8E D8 L2: MOV DS,AX ; WRITE PATTERN TO ALL REGS
00DE 8C DB MOV BX,DS
00E0 8E C3 MOV ES,BX
00E2 8C C1 MOV CX,ES
00E4 8E D1 MOV SS,CX
00E6 8C D2 MOV DX,SS
00E8 8B E2 MOV SP,DX
00EA 8B EC MOV BP,SP
00EC 8B F5 MOV SI,BP
00EE 8B FE MOV DI,SI
00F0 73 07 JNC L3
00F2 33 C7 XOR AX,DI ; PATTERN MAKE IT THRU ALL REGS
00F4 75 07 JNZ L4 ; NO -GOTO ERR ROUTINE
00F6 F8 CLC

```

```

00F7 EB E3
00F9 0B C7
00FB 74 09

00FD B0 00
00FF E6 11
0101 B0 01
0103 E6 12

0105 F4
0106

L3: JMP L2
    OR AX,DI
    JZ L5 ; ZERO PATTERN MAKE IT THRU?
        ; YES -GOTO NEXT TEST

L4: MOV AL,0 ; MFG ERROR CODE = 0001
    OUT MFG_PORT+1,AL
    MOV AL,1
    OUT MFG_PORT+2,AL

L5: HLT ; HALT

-----
; TEST 2
; 8255 INITIALIZATION AND TEST
; DESCRIPTION
; FIRST INITIALIZE 8255 PROG. PERIPHERAL INTERFACE.
; PORTS A&B ARE LATCHED OUTPUT BUFFERS. C IS INPUT.
; MFG ERROR CODE = 0002
-----

0106 B0 FE
0108 E6 10
010A B0 89
010C E6 63
010E 2B C0
0110 8A C4
0112 E6 60
0114 E4 60
0116 E6 61
0118 E4 61
011A 3A C4
011C 75 06
011E FE C4
0120 75 EE
0122 EB 05
0124 B3 02
0126 E9 0D32 R

L6: MOV AL,0FEH ; SEND 'FE' TO MFG_TESTER
    OUT MFG_PORT,AL
    MOV AL,MODE_8255
    OUT CMD_PORT,AL ; CONFIGURES I/O PORTS
    SUB AX,AX ; TEST PATTERN SEED = 0000
    MOV AL,AH
    OUT PORT_A,AL ; WRITE PATTERN TO PORT A
    IN AL,PORT_A ; READ PATTERN FROM PORT A
    OUT PORT_B,AL ; WRITE PATTERN TO PORT B
    IN AL,PORT_B ; READ OUTPUT PORT
    CMP AL,AH ; DATA AS EXPECTED?
    JNE L7 ; IF NOT, SOMETHING IS WRONG
    INC AH ; MAKE NEW DATA PATTERN
    JNZ L6 ; LOOP TILL 255 PATTERNS DONE
    JMP SHORT L8 ; CONTINUE IF DONE

L7: MOV BL,02H ; SET ERROR FLAG (BH=00 NOW)
    JMP E_MSG ; GO ERROR ROUTINE

L8: MOV DX,PAGREG ;
    MOV AL,1BH ; PAGE 3
    OUT DX,AL ;

012F B0 0D
0131 E6 61

MOV AL,00001101B ; INITIALIZE OUTPUT PORTS
OUT PORT_B,AL

-----
; PART 3
; SET UP 46505 AND VIDEO GATE ARRAY TO GET MEMORY WORKING ;
-----

0133 B0 FD
0135 E6 10
0137 BB 0000 E
013A B9 0010
013D 32 E4
013F 8A C4
0141 BA 03D4
0144 EE
0146 FE C4
0148 42
014A 2E: 8A 07
014B EE
014D 43
014F E2 F0

L9: MOV AL,0FDH ; SEND 'FD' TO MFG_TESTER
    OUT MFG_PORT,AL
    MOV BX,OFFSET VIDEO_PARMS ; POINT TO 46505 PARMS
    MOV CX,16 ; SET PARM LEN
    XOR AH,AH ; AH IS REG 0
    MOV AL,AH ; GET 46505 REG 0
    MOV DX,CRT_CTL ; SET ADDRESS OF 46505
    OUT DX,AL
    INC AH ; NEXT REG VALUE
    INC DX ; POINT TO DATA PORT
    MOV AL,CS:[BX] ; GET TABLE VALUE
    OUT DX,AL ; OUT TO CHIP
    INC BX ; NEXT IN TABLE
    LOOP L9

;----- START VGA WITHOUT VIDEO ENABLED
014F BA 03DA
0152 EC

MOV DX,VGA_CTL ; SET ADDRESS OF VGA
IN AL,DX ; BE SURE ADDR/DATA FLAG IS
; IN THE PROPER STATE
; 0 OF REGISTERS
L10: XOR AH,AH ; AH IS REG COUNTER
    MOV AL,AH ; GET REG 0
    OUT DX,AL ; SELECT IT
    XOR AL,AL ; SET ZERO FOR DATA
    OUT DX,AL
    INC AH ; NEXT REG
    LOOP L10

0153 B1 05
0155 32 E4
0157 8A C4
0159 EE
015A 32 C0
015C EE
015D FE C4
015F E2 F6

MOV DX,VGA_CTL_E ;
IN AL,DX ;
MOV AL,4 ; ENABLE EXT. VIDEO CLOCK
OUT DX,AL ;
OUT DX,AL ;
MOV AL,1 ; DISABLE EXT. VIDEO PROCESSOR
OUT DX,AL ;
OUT DX,AL ;

0161 BA 03DD
0164 EC
0165 B0 04
0167 EE
0168 EE
0169 B0 01
016B EE
016C EE

MOV AX,CS ; SET UP STACK SEGMENT & POINTER
MOV SS,AX
MOV SP,OFFSET Z_0
JMP S8SETJ ; DISABLE SX-03 DECODER

L11:

-----
; TEST 4
; PLANAR BOARD ROS CHECKSUM TEST
; DESCRIPTION
; A CHECKSUM TEST IS DONE FOR EACH ROS MODULE
; ON THE PLANAR BOARD.
; MFG ERROR CODE = 0007 MODULE AT ADDRESS E0000H ERROR
; 0008 MODULE AT ADDRESS E8000H ERROR
; 0003 MODULE AT ADDRESS F0000H ERROR
; 0004 MODULE AT ADDRESS F8000H ERROR
-----

0177 B0 FC
0179 E6 10
017B BA E000
017E 8E DA

L12: MOV AL,0FCH ; SEND 'FC' TO MFG_TESTER
    OUT MFG_PORT,AL
    MOV DX,E0000H ; SET DS TO E000H
    MOV DS,DX

```

Appendix A.

```

0180 33 F4          XOR     SI,SI          ; SET OFFSET
0182 8C 0034 R     MOV     SP,OFFSET Z1   ; SET UP STACK POINTER
0185 B9 8000       MOV     CX,8000H       ; NUMBER OF BYTES TO BE TESTED, 32K
0188 EB 23         JMP     SHORT ROM_CHECKSUM ; 32K ROM OK ?
018A 74 1A         JZ     L14             ; YES -
018C 8B 0003      MOV     BX,0003H       ; SET ERROR CODE (0003)
018F 80 FE F0     CMP     DH,0F0H       ;
0192 74 0F         JE     L_E             ;
0194 43           INC     BX             ; (0004)
0195 80 FE F8     CMP     DH,0F8H       ;
0198 74 09         JE     L_E             ;
019A 43           INC     BX             ;
019B 43           INC     BX             ;
019C 43           INC     BX             ; (0007)
019D 80 FE E0     CMP     DH,0E0H       ;
01A0 74 01         JE     L_E             ;
01A2 43           INC     BX             ; (0008)
01A3 E9 0D32 R     JMP     E_MSG          ; INDICATE ERROR
01A6 80 C4 08     ADD     DH,08H        ; END OF ROM SPACE ?
01A9 75 D3         JNZ     L12            ; NO -
01AB EB 0A         JMP     SHORT L15      ; YES- GOTO NEXT TEST

```

```

-----
; SUBROUTINE
; ARITHMETIC CHECKSUM SUBROUTINE
; ENTRY: DS = DATA SEGMENT OF ROM SPACE TO BE CHECKED
; SI = INDEX OFFSET INTO DS POINTING TO 1ST BYTE
; CX = LENGTH OF SPACE TO BE CHECKED
; EXIT: ZERO FLAG OFF-ERROR, DN= SPACE CHECKED OK
-----

```

```

ROM_CHECKSUM PROC NEAR
RC_0: MOV AL,0
      ADD AL,DS:[SI]
      INC SI
      LOOP RC_0
      OR AL,AL
      RET

```

ROM\_CHECKSUM ENDP

L15:

```

-----
; PART 5
; RAM MAPPING (SAVED IN PORT_A DURING POST)
; BIT 7 : SET IN PART 7
; BIT 6 : SET IN TEST 13 & 15
; BIT 5-4 : RESERVED
; BIT 3 : 64K RAM CARD INSTALLED
; BIT 2 : RESERVED
; BIT 1-0 : NUMBER OF EXP 128K RAM CARD
; 00 NO CARD INSTALLED
; 01 1 CARD INSTALLED (128K - 20000)
; 10 2 CARDS INSTALLED (256K - 40000)
; 11 3 CARDS INSTALLED (384K - 60000)
-----

```

```

01B7 B0 FB          MOV     AL,0FBH        ; SEND 'FB' TO MFG_TESTER
01B9 E6 10         OUT     MFG_PORT,AL   ;
01BB 33 DB         XOR     BX,BX          ; INIT 128K CARD COUNTER
01BD 8E DB         MOV     DS,BX         ; SET FIRST SEGMENT TO BE TESTED
01BF 33 F6         XOR     SI,SI         ;
01C1 E4 62         IN     AL,PORT_C      ; GET CONFIG BITS
01C3 24 08         AND     AL,00001000B ; 64K CARD IN ?
01C5 75 02         JNZ     MCONF0        ;
01C7 B3 08         MOV     BL,00001000B ; SET 64K CARD FLAG BIT
01C9 B8 5A5A      MOV     AX,5A5AH      ; SET WRITE DATA
01CC 89 04         MOV     DS:[SI],AX    ; WRITE DATA TO TOP OF 128K BOUNDRY
01CE EB 00         JMP     $+2           ; DELAY
01D0 EB 00         JMP     $+2           ;
01D2 8B 14         MOV     DX,DS:[SI]    ; READ BACK
01D4 3B C2         CMP     AX,DX         ; 5A5A ?
01D6 74 0E         JE     MCONF1        ; YES-
01D8 F7 D0         NOT    AX             ;
01DA 89 04         MOV     DS:[SI],AX    ; WRITE DATA TO TOP OF 128K BOUNDRY
01DC EB 00         JMP     $+2           ; DELAY
01DE EB 00         JMP     $+2           ;
01E0 8B 14         MOV     DX,DS:[SI]    ; READ BACK
01E2 3B C2         CMP     AX,DX         ; A5A5 ?
01E4 75 10         JNE    MCONF2        ; NO -GOTO MAIN RAM MAPPING
01E6 43           JNE    MCONF2        ; YES-INC 128K CARD COUNT
01E7 83 C4 06     MCONF1: INC    BX      ; INC TABLE POINTER
01EA 8C D8         ADD     SP,6          ;
01EC 80 C4 20     MOV     AX,DS         ; SET SEGMENT OF NEXT 128K
01EF 8E DB         ADD     AH,20H        ;
01F1 80 FC 60     MOV     DS,AX         ;
01F4 72 D3         CMP     AH,60H        ; 512K EXCEEDED ?
01F6 8B C3         JB     MCONF0         ; NO -CHECK NEXT 128K
01F8 E6 60         MCONF2: MOV    AX,BX    ; SAVE # OF 128K CARD IN PORT_A
                          OUT    PORT_A,AL
01FA E9 04CA R     JMP     S8SETJ       ; SET RAM MAPPING
01FD

```

MCONF3:

```

-----
; TEST 6
; BASE 8K READ/WRITE STORAGE TEST
-----

```

TEDFT122

```

; DESCRIPTION
; WRITE/READ/VERIFY DATA PATTERNS AA, 55, FF AND 00 TO
; 1ST 8K OF STORAGE AND THE 2K OF VIDEO RAM (CRT BUFFER).
; VERIFY STORAGE ADDRESSABILITY.
; ON EXIT SET CRT PAGE TO 15. SET TEMPORARY STACK ALSO.
; MFG ERROR CODE = 03XX FOR 128K RAM CARD
; 04XX FOR BASE 64K RAM
; 05XX FOR 64K RAM CARD
; 06XX FOR BOTH 64K RAM

```

```

;
; (XX= ERROR BITS - OR'ED HIGH & LOW BYTES) ;
; 0005 FOR BASE 64K RAM (B8000 ADDR PATH) ;
; 0006 FOR BASE 32K VIDEO RAM (B8000 ADDR PATH);
;-----
01FD B0 FA MOV AL,0FAH ; SEND 'FA' TO MFG_TESTER
01FF E6 10 OUT MFG_PORT,AL ;
0201 BC 004E R MOV SP,OFFSET Z2 ; SET RETURN ADDR
0204 B9 1000 MOV CX,1000H ; SET FOR 4K WORDS (8K BYTES)
0207 33 C0 XOR AX,AX ;
0209 8E C0 MOV ES,AX ; LOAD ES
020B E9 0DEA R JMP PODSTG ; TEST 1ST 4K RAM
020E 74 2B JZ L23 ; TEST OK
0210 B7 03 MOV BH,03H ; ERROR 03....
0212 E4 60 IN AL,PORT_A ; LOAD CONFIG FLAG
0214 A8 03 TEST AL,00000011B ; 128K CARD INSTALLED ?
0216 75 08 JNZ L20 ; YES-
0218 B7 04 MOV BH,04H ; ERROR 04....
021A E4 60 IN AL,PORT_A ;
021C A8 08 TEST AL,00001000B ; 64K CARD INSTALLED ?
021E 75 06 JNZ L21 ; YES-HORRY ABOUT ODD/EVEN
0220 8A D9 MOV BL,CL ; NO -COMBINE ERR BITS
0222 0A DD OR BL,CH ;
0224 EB 12 JMP SHORT L22 ;
0226 80 FC 02 L21: CMP AH,02 ; EVEN BYTE ERROR? ERR 04XX
0229 8A D9 MOV BL,CL ;
022B 74 08 JE L22 ;
022D FE C7 INC BH ; MAKE INTO 05XX ERR
022F 0A DD OR BL,CH ; MOVE AND POSSIBLY COMBINE
;
; ERROR BITS
; ODD BYTE ERROR
0231 80 FC 01 CMP AH,1 ;
0234 74 02 JE L22 ;
0236 FE C7 INC BH ; MUST HAVE BEEN BOTH
; - MAKE INTO 06XX
; JUMP TO ERROR OUTPUT ROUTINE
0238 E9 0D32 R L22: JMP E_MSG ;
;----- SETUP TEMPORARY STACK SEG AND SP
0238 B8 0030 L23: MOV AX,0030H ; GET STACK VALUE
023E 8E D0 MOV SS,AX ; SET THE STACK UP
0240 BC 0100 R MOV SP,OFFSET TOS ; STACK IS READY TO GO
0243 33 C0 XOR AX,AX ; SET UP DATA SEG
0245 8E D8 MOV DS,AX ;
;----- TEST FIRST 2K OF 64K VRAM
0247 E4 60 IN AL,PORT_A ;
0249 A8 03 TEST AL,00000011B ; 128K RAM CARD ?
;
; NO -BYPASS TESTING
024B 74 19 JZ L24 ;
024D B9 4000 MOV CX,4000H ;
0250 A8 01 TEST AL,00000001B ;
0252 74 08 JZ L23_1 ;
0254 B5 20 MOV CH,20H ;
0256 A8 02 TEST AL,00000010B ;
0258 74 02 JZ L23_1 ;
025A B5 60 MOV CH,60H ;
025C 8E C1 MOV ES,CX ;
025E B9 0400 L23_1: MOV CX,0400H ; 2K BYTES
0261 E8 0DEA R CALL PODSTG ;
0264 75 B2 JNZ L17 ;
;----- TEST BOTTOM 2K OF PAGE 3 IN 64K VRAM USING B8000 ADDR PATH
0266 B0 F9 L24: MOV AL,0F9H ; SEND 'F9' TO MFG_TESTER
0268 E6 10 OUT MFG_PORT,AL ;
026A B9 0400 MOV CX,0400H ; 2K BYTES
026D B8 8B80 MOV AX,0BB80H ; POINT TO AREA JUST TESTED WITH
0270 8E C0 MOV ES,AX ; DIRECT ADDRESSING
0272 E8 0DEA R CALL PODSTG ;
0275 74 06 JZ L25 ;
0277 B8 0005 MOV BX,0005H ; ERROR 0005
027A E9 0D32 R JMP E_MSG ;
;----- DISABLE VRAM1 DECODER & ENABLE VRAM2 DECODER
027D E8 04C2 R L25: CALL S8SET ;
0280 09 DB 009H ; START REG ADDR
0281 60 DB 060H ; MASK DATA
0282 02 DB 2 ; PARAMETER LENGTH
0283 00 DB 000H ; DISABLE VRAM1 DECODER
0284 B7 DB 0B7H ; ENABLE VRAM2 DECODER
;
0285 BA 03D9 MOV DX,PAGREG2 ; SET PAGE REG 8
0288 B0 00 MOV AL,0 ;
028A EE OUT DX,AL ;
;----- TEST BOTTOM 2K OF PAGE 8 IN 32K VRAM USING B8000 ADDR PATH
028B B9 0400 L24: MOV CX,0400H ; 2K BYTES
028E B8 8B80 MOV AX,0BB80H ; POINT TO AREA JUST TESTED WITH
0291 8E C0 MOV ES,AX ; DIRECT ADDRESSING
0293 E8 0DEA R CALL PODSTG ;
0296 74 06 JZ L26 ;
0298 B8 0006 MOV BX,0006H ; ERROR 0006
029B E9 0D32 R JMP E_MSG ;
;-----
; PART 7 ;
; ROM CARTRIDGE CONFIGURATION CHECK ;
; DESCRIPTION ;
; THIS ROUTINE CHECKS ROM CARTRIDGE ADDRESS CONFIGURATIONS. ;
; IF ROM CARTRIDGE OVERLAPS SYSTEM ROM AREA, SYSTEM ROM IS ;
; DISABLED AND THEN CARTRIDGE ROM IS ENABLED. ;
; ;
; BASE ROM OVERLAPPED FLAG (SAVED IN PORT A) ;
; BIT 7 : SYSTEM MODE FLAG ;
; 0 NATIVE MODE ;
; 1 PCJR MODE ;
; ;
; BIT 6 : SET IN TEST 13 & 15 ;
; BIT 5-4 : RESERVED ;
; BIT 3 : SET IN PART 5 ;
; BIT 2 : RESERVED ;

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Appendix A.

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;----- BIT 1-0 : SET IN PART 5 -----;
029E B0 F8 L26: MOV AL,0F8H ; SEND 'F8' TO MFG_TES
02A0 E6 10 OUT MFG_PORT,AL ;
;----- DISABLE VRAM2 DECODER -----;
02A2 E8 04C2 R CALL S8SET ;
02A5 0A DB 00AH ; START REG ADDR
02A6 00 DB 000H ; MASK DATA
02A7 01 DB 1 ; PARAMETER LENGTH
02A8 00 DB 000H ;
;----- ALLOCATE CART. ROMS TO ADDR 90000H-BFFFFH TEMPORARILY -----;
02A9 E8 04C2 R CALL S8SET ;
02AC 01 DB 001H ; START REG ADDR
02AD 20 DB 020H ; MASK DATA
02AE 06 DB 6 ; PARAMETER LENGTH
02AF B2 DB 0B2H ; CART. ROM 2 (90000-97FFF)
02B0 B3 DB 0B3H ; CART. ROM 3 (98000-9FFFF)
02B1 B4 DB 0B4H ; CART. ROM 4 (A0000-A7FFF)
02B2 B5 DB 0B5H ; CART. ROM 5 (A8000-AFFFF)
02B3 B6 DB 0B6H ; CART. ROM 6 (B0000-B7FFF)
02B4 B7 DB 0B7H ; CART. ROM 7 (B8000-BFFFF)
;
02B5 BA 01FF MOV DX,S8STATUS ;
02B6 EC IN AL,DX ; READ STATUS
02B9 A8 20 TEST AL,EROM7IN ; F8000-FFFFF OVERLAPPED BY CART.?
02BB 75 1A JNZ RCONF2 ; NO -
02BD B9 B000 MOV CX,0B000H ; GET SYSTEM ID FROM FFFFE
02C0 8E D9 MOV DS,CX ;
02C2 BE FFFE MOV SI,0FFFFEH ;
02C5 8A 24 MOV AH,DS:[SI] ;
02C7 89 FC FD CMP AH,PJSYSID ; PCJR SYSTEM CART.?
02CA 74 03 JE RCONF1 ; YES-
02CC E9 047D R JMP SYSSWP8 ; NO-GO TO SYSTEM SWAP (NATIVE MODE)
02CF 50 RCONF1: PUSH AX ;
02D0 E4 60 IN AL,PORT_A ; SET PCJR MODE FLAG
02D2 0C 80 OR AL,1000000B ;
02D4 E6 60 OUT PORT_A,AL ;
02D6 58 POP AX ;
02D7 A8 40 RCONF2: TEST AL,EROM6IN ; F0000-F7FFF OVERLAPPED BY CART.?
02D9 74 34 JZ RCONF12 ; YES-
;
02DB BA 9000 MOV DX,9000H ; SET TEMPORARY CART. ROM TOP ADDR
02DE 33 F6 XOR SI,SI ; FFSET
02E0 8E DA RCONF3: MOV DS,DX ;
02E2 8B 04 MOV AX,DS:[SI] ; READ ROM CARTRIDGE ID
02E4 50 PUSH AX ; BUS SETTLING
02E5 58 POP AX ;
02E6 3D AA55 CMP AX,0AA55H ; ID FOUND (PCJR MODE) ?
02E9 74 11 JE RCONF7 ; YES-
;
02EB 3D 55AA CMP AX,55AAH ; (NATIVE MODE)
02EE 74 0C JE RCONF7 ; YES-
02F0 81 C2 0080 ADD DX,0080H ; POINT TO NEXT 2K ADDR
02F4 81 FA B000 CMP DX,0B000H ; ADDR F000 ?
02F8 72 E6 JB RCONF3 ; NO -GO CHECK NEXT AREA
02FA EB 37 JMP SHORT RCONF14 ; YES-AREA E0000-FFFFF IS NOT
; OVERLAPPED BY CARTRIDGE
;
02FC 8A 64 02 RCONF7: MOV AH,DS:[SI+2] ; SET CART. ROM LENGTH
02FF 32 C0 XOR AL,AL ;
0301 B1 03 MOV CL,3 ;
0303 D3 E8 SHR AX,CL ;
0305 03 D0 ADD DX,AX ; CALCULATE CART. ROM END ADDR+1
0307 81 FA A000 CMP DX,0A000H ; CART. ROM END ADDR+1 <= E0000 ?
030B 76 D3 JBE RCONF3 ; YES-
030D EB 0F JMP SHORT RCONF13 ; NO -AREA E0000-EFFFF IS
; OVERLAPPED BY CARTRIDGE
;
;----- BASE ROM ACTIVE (F8000-FFFFF) -----;
030F E8 04C2 R RCONF12:CALL S8SET ;
0312 00 DB 000H ; START REG ADDR
0313 60 DB 060H ; MASK DATA
0314 07 DB 7 ; PARAMETER LENGTH
0315 BF DB 0BFH ; DISABLE BASE ROM (E0000-F7FFF)
0316 BA DB 0BAH ; ENABLE CART. ROM 2 (D0000-D7FFF)
0317 BB DB 0BBH ; ENABLE CART. ROM 3 (D8000-DFFFF)
0318 BC DB 0BCH ; ENABLE CART. ROM 4 (E0000-E7FFF)
0319 BD DB 0BDH ; ENABLE CART. ROM 5 (E8000-EFFFF)
031A BE DB 0BEH ; ENABLE CART. ROM 6 (F0000-F7FFF)
031B 00 DB 000H ; DISABLE CART. ROM 7 (F8000-FFFFF)
031C EB 21 JMP SHORT RCONF15 ;
;----- BASE ROM ACTIVE (F0000-FFFFF) -----;
031E E8 04C2 R RCONF13:CALL S8SET ;
0321 00 DB 000H ; START REG ADDR
0322 61 DB 061H ; MASK DATA
0323 01 DB 1 ; PARAMETER LENGTH
0324 BE DB 0BEH ; BASE ROM (E0000-EFFFF) DISABLE
;
0325 E8 04C2 R CALL S8SET ;
0328 01 DB 001H ; START REG ADDR
0329 60 DB 060H ; MASK DATA
032A 06 DB 6 ; PARAMETER LENGTH
032B BA DB 0BAH ; ENABLE CART. ROM 2 (D0000-D7FFF)
032C BB DB 0BBH ; ENABLE CART. ROM 3 (D8000-DFFFF)
032D BC DB 0BCH ; ENABLE CART. ROM 4 (E0000-E7FFF)
032E BD DB 0BDH ; ENABLE CART. ROM 5 (E8000-EFFFF)
032F 00 DB 000H ; DISABLE CART. ROM 6 (F0000-F7FFF)
0330 00 DB 000H ; DISABLE CART. ROM 7 (F8000-FFFFF)
0331 EB 0C JMP SHORT RCONF15 ;
;----- BASE ROM ACTIVE (E0000-FFFFF) -----;
0333 E8 04C2 R RCONF14:CALL S8SET ;
0336 01 DB 001H ; START REG ADDR
0337 60 DB 060H ; MASK DATA
0338 06 DB 6 ; PARAMETER LENGTH
0339 BA DB 0BAH ; ENABLE CART. ROM 2 (D0000-D7FFF)
033A BB DB 0BBH ; ENABLE CART. ROM 3 (D8000-DFFFF)

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033B 00          DB      000H          ; DISABLE CART. ROM 4 (E0000-E7FFF)
033C 00          DB      000H          ; DISABLE CART. ROM 5 (E8000-EFFFF)
033D 00          DB      000H          ; DISABLE CART. ROM 6 (F0000-F7FFF)
033E 00          DB      000H          ; DISABLE CART. ROM 7 (F8000-FFFFF)
;----- ENABLE VRAM2 DECODER
033F E8 04C2 R   RCONF15:CALL S8SET          ;
0342 0A          DB      00AH          ; START REG ADDR
0343 60          DB      060H          ; MASK DATA
0344 01          DB      1           ; PARAMETER LENGTH
0345 B7          DB      0B7H          ; VRAM2 (B8000-BFFFF)

0346 E9 04F5 R   JMP      L26_2          ; CONTINUE NEXT TEST

;-----
; SYSTEM ROM SWAP TO CARTRIDGE ROM
;-----
0349 1E          SYSSWAP:PUSH DS          ; SAVE DS SEG
034A FA          CLI                   ; DISABLE EXTERNAL INTERRUPTS

;-----
; TEST 22
; PCjr CARTRIDGE ROM CHECKSUM TEST
; DESCRIPTION
; CHECK PCjr CART. ROM (F0000-F7FFF) WITH CHECKSUM.
; MFG ERROR CODE = 2AF0 (F0-MSB OF SEGMENT THAT HAS CHECKSUM)
;-----
034B E8 0F7C R   CALL      MFG_UP          ; SEND 'E9' TO MFG_TESTER

034E BA 01FF     MOV      DX,S8STATUS      ;
0351 EC         IN        AL,DX          ;
0352 A8 40     TEST     AL,EROM6IN      ; CART. ROM IN F0000-F7FFF ?
0354 75 2D     JNZ      JROM1          ; NO -BYPASS
0356 B8 F000    MOV      AX,0F000H      ; SET BASE ROM ADDR
0359 8E D8     MOV      DS,AX          ;
035B 33 F6     XOR      SI,SI          ;
035D 8B 04     MOV      AX,DS:[SI]       ;
035F 3D AA55    CMP      AX,0AA55H      ; PCJR CART. ROM ID FOUND ?
0362 74 1F     JE        JROM1          ; YES-BYPASS
0364 B9 8000    MOV      CX,8000H       ; SET ROM LENGTH 32KB
0367 E8 01AD R   CALL     ROS_CHECKSUM    ; ARITHMETIC CHECKSUM GOOD ?
036A 74 17     JZ        JROM1          ; YES-
036C B4 02     MOV      AH,2          ;
036E B7 00     MOV      BH,0          ;
0370 BA 081C    MOV      DX,081CH      ; POSITION CURSOR, ROW 8, COL 28
0373 CD 10     INT      INT_10        ;
0375 B0 F0     MOV      AL,0F0H       ;
0377 E8 0F90 R   CALL     XPC_BYTE       ; DISPLAY MSB OF DATA SEG
037A BB 2AF0    MOV      BX,2AF0H      ; SET ERROR CODE
037D BE 0064 R   MOV      SI,OFFSET CART_ERR ;
0380 E8 0D32 R   CALL     E_MSG          ; GO ERROR ROUTINE
0383          JROM1:
;-----
; ASSUME DS:XXDATA
;
0383 E8 0FE2 R   CALL     DDX            ;
0386 80 3E 0018 R 00  CMP      POST_ERR,00H   ; CHECK FOR "POST_ERR" NON-ZERO
;
0388 1F         POP      DS            ; RESTORE DS
038C 74 0D     JE        JROM2        ; CONTINUE IF NO ERROR
038E B2 02     MOV      DL,2          ; 2 SHORT BEEPS (ERROR)
0390 E8 0FAB R   CALL     ERR_BEEP     ;
0393          ERR_WAITJ:
0393 B4 00     MOV      AH,00        ;
0395 CD 16     INT      INT_16       ; WAIT FOR "ENTER" KEY
0397 3C 0D     CMP      AL,0DH       ;
0399 75 F8     JNE      ERR_WAITJ   ;
;
039B B0 00     MOV      AL,0         ; DISABLE NMI
039D E6 A0     OUT     NMI_PORT,AL   ;
039F E6 60     OUT     PORT_A,AL     ; CLEAR KBD PORT
;----- COPY DATA IN FIRST 128K RAM CARD TO 64K VRAM
; RESET_FLAG,1234H ; SET WARM START INDICATOR FOR
; RETURNING FROM PCJR MODE
03A1 C7 06 0072 R 1234 MOV      RESET_FLAG,1234H ;
;
03A7 8B 1E 0015 R   MOV      BX,TRUE_MEM   ;
03AB 81 FB 0080    CMP      BX,128        ; IS MEMORY SIZE <= 128K ?
03AF 76 27     JBE      SYSSWP2      ; YES-
03B1 83 EB 40     SUB      BX,64         ;
03B4 E4 62     IN      AL,PORT_C     ;
03B6 A8 08     TEST     AL,08H       ;
03B8 75 03     JNZ      SYSSWP1      ;
03BA 83 EB 40     SUB      BX,64         ;
03BD B1 06     SYSSWP1:MOV     CL,6     ;
03BF D3 E3     SHL     BX,CL         ;
03C1 8E C3     MOV     ES,BX         ;
03C3 33 FF     XOR     DI,DI         ;
03C5 8E DF     MOV     DS,DI         ;
03C7 33 F6     XOR     SI,SI         ; COPY 4KB DATA FROM 1ST 128K RAM
03C9 B9 0800    MOV     CX,0800H      ; CARD TO 64K BASE RAM OR 64K RAM CD
03CC F3 A5     REP     MOVSW         ; (DS:SI=>ES:DI)
;----- SET 128K RAM CARD START ADDR FROM 20000H
03CE BA 03DA    MOV     DX,VGA_CTL    ;
03D1 EC         IN      AL,DX         ;
03D2 B0 03     MOV     AL,3         ;
03D4 EE         OUT     DX,AL         ;
03D5 B0 10     MOV     AL,10H       ;
03D7 EE         OUT     DX,AL         ;
;----- ALLOCATE PROGRAMMABLE RAM AT 00000-1FFFF
03D8 BA 01FF    SYSSWP2:MOV     DX,S8STATUS ;
03DB EC         IN      AL,DX         ;
03DC B0 08     MOV     AL,08H       ;
03DE EE         OUT     DX,AL         ;
03DF B0 A0     MOV     AL,0A0H      ;
03E1 EE         OUT     DX,AL         ;

```

Appendix A.

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03E2 80 63      MOV     AL,63H      ;
03E4 EE        OUT     DX,AL      ;
;----- SET UP VIDEO SYSTEM TO PCJR MODE
03E5 BA 03DA    MOV     DX,VGA_CTL  ;

03E8 EC        IN      AL,DX      ;
03E9 80 00     MOV     AL,0        ; DISABLE NATIVE VIDEO PATH
03EB EE        OUT     DX,AL      ;
03EC EE        OUT     DX,AL      ;
03ED 80 01     MOV     AL,1        ; ENABLE VRAM1 & ALL PALETTE REGS
03EF EE        OUT     DX,AL      ;
03F0 80 1F     MOV     AL,1FH     ;
03F2 EE        OUT     DX,AL      ;

;-----
03F3 52        PUSH    DX          ;
03F4 E8 04C2 R CALL    S8SET      ; SET UP VGA
03F7 8C        DB      08CH     ; START REG ADDR
03F8 00        DB      000H     ; MASK DATA
03F9 02        DB      2        ; PARAMETER LENGTH
03FA 80        DB      080H     ; ENABLE PCJR VGA
03FB 00        DB      000H     ; DISABLE NATIVE VGA
03FC E8 04C2 R CALL    S8SET      ; SET UP VRAM DECODER
03FF 89        DB      009H     ; START REG ADDR
0400 60        DB      060H     ; MASK DATA
0401 02        DB      2        ; PARAMETER LENGTH
0402 B7        DB      0B7H     ; ENABLE VRAM1 DECODER (B8000-BFFFF)
0403 00        DB      000H     ; DISABLE VRAM2 DECODER
0404 E8 04C2 R CALL    S8SET      ; SET UP PAGE REG
0407 90        DB      090H     ; START REG ADDR
0408 00        DB      000H     ; MASK DATA
0409 01        DB      1        ; PARAMETER LENGTH
040A 80        DB      000H     ; DISABLE CRT/CPU PAGE REG 2
040B 5A        POP     DX          ;

;-----
040C B9 0007    MOV     CX,7        ; CLEAR VGA REG 0-6 WITH ZERO
040F EC        IN      AL,DX      ;
0410 84 00     MOV     AH,0        ;
0412 8A C4     SYSSWP3:MOV  AL,AH     ;
0414 EE        OUT     DX,AH     ;
0415 80 00     MOV     AL,0        ;
0417 EE        OUT     DX,AL     ;
0418 FE C4     INC     AH          ;
041A E2 F4     LOOP   SYSSWP3     ;

;-----
041C E8 0000 E  ASSUME  DS:DATA      ;
041F E4 62     CALL   DDS          ;
0421 A8 08     IN      AL,PORT_C   ;
0423 75 38     TEST   AL,00001000B ; 64K RAM CARD INSTALLED ?
0425 B8 0080   JNZ   SYSSWP6     ; NO -
0428 33 FF     MOV     BX,128     ; SET INITIAL TRUE MEMORY SIZE
042A B9 2000   XOR    DI,DI       ;
042D 8E C1     SYSSWP4:MOV  ES,CX   ;
042F B8 5A5A   MOV   AX,5A5AH    ;
0432 26: 89 05 MOV   ES:[DI],AX  ;
0435 EB 00     JMP    6+2         ;
0437 EB 00     JMP    8+2         ;
0439 26: 8B 15 MOV   DX,ES:[DI]  ;

043C 33 C2     XOR    AX,DX       ; 128K RAM CARD INSTALLED ?
043E 75 0F     JNZ   SYSSWP5     ; NO -
0440 26: 89 05 MOV   ES:[DI],AX  ; CLEAR MEMORY
0443 81 C3 0080 ADD   BX,128      ;
0447 80 C5 20  ADD   CH,20H     ;
044A 80 FD 80  CMP   CH,80H     ;
044D 72 DE     JB    SYSSWP4     ;
044F 89 1E 0015 R SYSSWP5:MOV  TRUE_MEM,BX ; SAVE RAM SIZE
0453 C7 06 0013 R 0070 MOV  MEMORY_SIZE,112 ;
0459 80 3F     MOV   AL,3FH     ; CPU/CRT PAGE 7
045B EB 14     JMP   SHORT SYSSWP7 ;
045D C7 06 0015 R 0040 SYSSWP6:MOV  TRUE_MEM,64 ; SAVE RAM SIZE
0463 C7 06 0013 R 0030 MOV  MEMORY_SIZE,48 ;
0469 81 26 0010 R 00FB AND   EQUIP_FLAG,0FBH ; PLANAR RAM SIZE 48K
046F 80 1B     MOV   AL,1BH     ; CPU/CRT PAGE 3
0471 BA 03DF   SYSSWP7:MOV  DX,PAGREG ; SET PAGE REG
0474 EE        OUT   DX,AL     ;
0475 A2 008A R  MOV   ; SAVE PAGE REG DATA

0478 C6 06 0062 R 07 MOV  ACTIVE_PAGE,7 ; SAVE CRT PAGE
;----- COPY SWAP HANDLER IN RAM AREA
047D B8 F800    SYSSWP8:MOV  AX,0F800H ; SET BASE ROM
0480 8E D8     MOV   DS,AX      ;
0482 BE 0495 R MOV   SI,OFFSET SYSTRANS ; SET SYS TRANS RTH START ADDR
0485 33 C0     XOR   AX,AX      ;
0487 8E C0     MOV   ES,AX      ;
0489 33 FF     XOR   DI,DI      ; SET COPY AREA TOP ADDR
048B B9 0028   MOV   CX,40      ;
048E F3/ A5    REP   MOVSW      ;

;-----
0490 EA        JUMP  TO SWAP HANDLER TO CHANGE THE SYSTEM
0491 0000     DB    0EAH      ; JUMP FAR
0493 0000     DW    0000H    ;
0493 0000     DW    0000H    ;

;-----
; SUBROUTINE
; SYSTEM SWAP HANDLER
; DESCRIPTION
; 1. IF SYSTEM IS OVERLAPPED BY THE ROM CARTRIDGE, THIS
; PROGRAM IS COPIED TO TEMPORARY RAM AREA.
; 2. THE CONTROL IS PASSED TO THE ROUTINE IN THE RAM AREA.
; 3. THE ROUTINE IN THE RAM AREA DISABLES SYSTEM ROM
; AND ENABLES OVERLAPPED ROM CARTRIDGE.
; 4. THE CONTROL IS PASSED TO THE POWER-ON RESET VECTOR
; IN THE OVERLAPPED ROM CARTRIDGE.
;-----

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0495 BA 01FF      SYSTRANS: MOV  DX,S8STATUS      ;
0498 EC          IN      AL,DX          ; READ AND CLEAR CTRL REG
0499 B9 0015      MOV      CX,3*7          ; SET COUNTER
049C 33 C0        XOR      AX,AX          ;
049E 8E D8        MOV      DS,AX          ;
04A0 BE 0018 90   MOV      SI,SYSTR5-SYSTRANS ; SET TABLE ADDR
04A4 AC          SYSTR1: LODSB          ; LOAD SET DATA
04A5 EE          OUT      DX,AL          ;
04A6 E2 FC        LOOP     SYSTR1          ;
;----- JUMP TO POWER ON RESET VECTOR IN ROM CARTRIDGE
04A8 EA          DB      0EAH          ; JUMP FAR
04A9 FFF0        DW      0FFF0H        ;
04AB F000        DW      0F000H        ;
04AD 00 00 00     SYSTR5: DB      000H,000H,000H ; DISABLE BASE ROM
04B0 01 BA 60     DB      001H,0BAH,060H ; ENABLE CART. ROM 2 (D0000-D7FFF)
04B3 02 BB 60     DB      002H,0BBH,060H ; ENABLE CART. ROM 3 (D8000-DFFFF)
04B6 03 BC 60     DB      003H,0BCH,060H ; ENABLE CART. ROM 4 (E0000-E7FFF)
04B9 04 BD 60     DB      004H,0BDH,060H ; ENABLE CART. ROM 5 (E8000-EFFFF)
04BC 05 BE 60     DB      005H,0BEH,060H ; ENABLE CART. ROM 6 (F0000-F7FFF)
04BF 06 BF 60     DB      006H,0BFH,060H ; ENABLE CART. ROM 7 (F8000-FFFFF)

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;-----
; SUBROUTINE
; SET UP OF PROGRAMMABLE MEMORY & I/O DECODER
; DESCRIPTION
; THIS SUBROUTINE SET UP PROGRAMMABLE MEMORY AND
; I/O DECODER. THE CALL INSTRUCTION IS FOLLOWED BY THE
; PARAMETERS AND ITS LENGTH. THE PARAMETERS INCLUDES CONTROL
; REGISTER (ADDR, CONTROL REG 1 AND 2 ) INFORMATION
; AND ARE SET SEQUENTIALLY.
; THE CALLER SETS FIRST CONTROL REGISTER ADDRESS, MASK DATA
; PARAMETER LENGTH, AND VALIABLE REGISTER 1 VALUE.
;----- CALLING SEQUENCE -----
;
; CALL S8SET          ; CALL SET UP SUBROUTINE
;
; DB  ??H            ; START REG ADDR ( 00H-93H )
; DB  ??H            ; MASK DATA
; DB  ??            ; PARAMETER LENGTH
; DB  ??H            ; FIRST REG 1 VALUE
; DB  ??H            ; SECOND REG 1 VALUE
;
; DB  ??H            ; LAST REG 1 VALUE
;-----

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04C2          S8SET PROC NEAR          ; ENTRY POINT BY "CALL"
04C2 5E          POP      SI          ; SET TABLE POINTER
04C3 1E          PUSH     DS          ; SAVE DS
04C4 33 FF      XOR      DI,DI          ; "CALL" ENTRY FLAG
04C6 8C C8      MOV      AX,CS          ; CS:IP
04C8 EB 07      JMP      SHORT S8SETX    ;
04CA          S8SETJ LABEL NEAR        ; ENTRY POINT BY "JMP"
04CA 8B F4      MOV      SI,SP          ; SET TABLE POINTER
04CC BF FFFF     MOV      DI,0FFFFH      ; "JMP" ENTRY FLAG
04CF 8C D0      MOV      AX,SS          ; SS:SP
04D1 8E D8      S8SETX: MOV     DS,AX          ;
04D3 AD        LODSW          ; GET START REG ADDR (BL) AND
04D4 8B D8      MOV      BX,AX          ; MASK DATA (BH)
04D6 AC        LODSB          ; GET SET DATA LENGTH (CX)
04D7 33 C9      XOR      CX,CX          ;
04D9 8A C8      MOV      CL,AL          ;
04DB BA 01FF     MOV      DX,S8STATUS    ; READ & CLEAR CTRL REG
04DE EC        IN      AL,DX          ;
04DF 8A C3      S801: MOV     AL,BL          ; GET CTRL REG ADDR
04E1 EE        OUT     DX,AL          ; WRITE CTRL REG ADDR
04E2 FE C3      INC     BL          ; SET NEXT CTRL REG ADDR
04E4 AC        LODSB          ; LOAD CTRL REG 2 DATA ( VALIABLE )
04E5 EE        OUT     DX,AL          ; SET REG 1 DATA
04E6 8A C7      MOV     AL,BH          ; SET REG 2 DATA
04E8 EE        OUT     DX,AL          ;
04E9 E2 F4      LOOP     S801          ;
04EB 0B FF      OR      DI,DI          ;
04ED 74 03      JZ      S802          ;
04EF 8B E6      MOV     SP,SI          ;
04F1 C3        RET          ; RETURN TO CALLER ("JUMP")
04F2 1F        S802: POP     DS          ; RESTORE DS
04F3 56        PUSH    SI          ; ADJUST RETURN POINT
04F4 C3        RET          ; RETURN TO CALLER ("CALL")
04F5          S8SET ENDP
04F5          L26_2:

```

1K

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; PART 8
; INTERRUPTS
; DESCRIPTION
; 32 INTERRUPTS ARE INITIALIZED TO POINT TO A DUMMY
; HANDLER. THE BIOS INTERRUPTS ARE LOADED. DIAGNOSTIC
; INTERRUPTS ARE LOADED SYSTEM CONFIGURATION WORD IS PUT
; IN MEMORY. THE DUMMY INTERRUPT HANDLER RESIDES HERE.
;-----

```

```

04F5 E8 0FE2 R   ASSUME DS:XXDATA
04F8 C6 06 0005 R F7 CALL DDX          ; SET DS XXDATA SEG
04FD E8 0F7C R   MOV     MFG_TST,0F7H ; SEND 'F7' TO MFG_TESTER
0500 C7 06 0022 R 0DA7 R C CALL MFG_UP        ; UPDATE MFG CHECKPOINT
                                MOV     MFG_RTN,OFFSET MFG_OUT

```

Appendix A.

```

0506 8C C8          MOV     AX,CS          ; SET DOUBLEWORD POINTER TO MFG.
0508 A3 0024 R     MOV     MFG_RTN+2,AX ; ERROR OUTPUT ROUTINE SO DIAGS.
                                ; DON'T HAVE TO DUPLICATE CODE

                                ASSUME CS:CODE,DS:ABS0
050B 33 C0          XOR     AX,AX          ;
050D 8E D8          MOV     DS,AX          ;
;----- SET UP THE INTERRUPT VECTORS TO TEMP INTERRUPT
050F B9 00FF       MOV     CX,255        ; FILL ALL INTERRUPTS
0512 2B FF         SUB     DI,DI         ; FIRST INTERRUPT LOCATION IS 0000
0514 8E C7         MOV     ES,DI         ; SET ES=0000 ALSO
0516 B8 0599 R     D3:   MOV     AX,OFFSET D11 ; MOVE ADDR OF INTR PROC TO TBL
0519 AB           STOSW
051A 8C C8         MOV     AX,CS         ; GET ADDR OF INTR PROC SEG
051C AB           STOSW
051D E2 F7         LOOP   D3             ; VECTBLO
051F C7 06 0124 R 0000 E C ;----- SET UP BIOS INTERRUPTS
                                MOV     EXST,OFFSET EXTAB ; SET UP EXT. SCAN TABLE
0525 BF 0040 R     MOV     DI,OFFSET VIDEO_INT ; SET UP VIDEO INT
0528 0E           PUSH   CS             ;
0529 1F           POP    DS             ; PLACE CS IN DS
052A BE 15D0 R     MOV     SI,OFFSET VECTOR_TABLE+16
052D B9 0010       MOV     CX,16
0530 A5           MOVSW
D4:   ; MOVE INTERRUPT VECTOR TO LOW
                                ; MEMORY
0531 47           INC     DI             ;
0532 47           INC     DI             ; POINT TO NEXT VECTOR ENTRY
0533 E2 FB         LOOP   D4             ; REPEAT FOR ALL 16 BIOS INTERRUPTS
;----- SET UP INT 78, 79, & 7A
0535 BF 01E0       MOV     DI,78H*4      ; INT 78H
0538 B8 0000 E     MOV     AX,OFFSET KKKFDM ;
053B AB           STOSW
053C 47           INC     DI             ;
053D 47           INC     DI             ;
053E B8 0000 E     MOV     AX,OFFSET BUFFER_QUEING ; INT 79H
0541 AB           STOSW
0542 47           INC     DI             ;
0543 47           INC     DI             ;

0544 B8 3A00       MOV     AX,OFFSET DICT_ADDR ; INT 7AH
0547 AB           STOSW
0548 B8 F000       MOV     AX,0F00H      ;
054B AB           STOSW
;----- SET UP RS232 WRAP TEST INTERRUPTS
054C BF 0210       MOV     DI,84H*4      ;
054F B8 1335 R     MOV     AX,OFFSET WRAP_TEST ;
0552 AB           STOSW
;----- SET UP DEFAULT EQUIPMENT DETERMINATION WORD
; BIT 15,14 = NUMBER OF PRINTERS ATTACHED
; BIT 13 = RESERVED
; BIT 12 = GAME I/O ATTACHED
;
; BIT 11,10,9 = NUMBER OF RS232 CARDS ATTACHED
; BIT 8 = DMA (0=DMA PRESENT, 1=NO DMA ON SYSTEM)
; =====
; BIT 7,6 = NUMBER OF DISKETTE DRIVES
; 00=1, 01=2, 10=3, 11=4 ONLY IF BIT 0 = 1
; BIT 5,4 = INITIAL VIDEO MODE
; 00 - UNUSED
; 01 - NATIVE MODE (20 ZENKAKU X 11)
; 10 - EXTENSION MODE
; 11 - RESERVED
;
; BIT 3,2 = PLANAR RAM SIZE (10=48K,11=64K)
; BIT 1 = RESERVED
; BIT 0 = 1 (IPL DISKETTE INSTALLED)
;-----
0553 8E D9         ASSUME CS:CODE,DS:ABS0
                                MOV     DS,CX          ; SET DS TO 0
                                ; DEFAULT:1 PARALLEL PRINTER,GAMEI/O,
0555 C7 06 0410 R 511C MOV     DATA_WORD[EQUIP_FLAG-RS232_BASE],511CH ;
                                ; NO DMA,NATIVE MODE,64K ON PLANAR
055B C7 06 0408 R 0378 MOV     DATA_WORD[PRINTER_BASE-RS232_BASE],PARAL_PORT ;
                                ; SET PRINTER ADDR

; TEST 9
; INITIALIZE AND TEST THE 8259 INTERRUPT CONTROLLER CHIP
; MFG ERROR CODE = 07XX (XX=00, DATA PATH OR INTERNAL FAILURE,
; XX=ANY OTHER BITS ON=UNEXPECTED INT'S)
;-----
0561 E8 0F7C R     CALL   MFG_UP         ; SEND 'F6' TO MFG_TESTER
0564 B0 13         ASSUME DS:ABS0,CS:CODE
                                MOV     AL,13H          ; ICW1 - RESET EDGE SENSE CIRCUIT,
                                ; SET SINGLE 8259 CHIP & ICW4 READ
0566 E6 20         OUT    INTA00,AL
0568 B0 08         MOV     AL,8          ; ICW2 - SET INTERRUPT TYPE 8 (8-F)
056A E6 21         OUT    INTA01,AL
056C B0 09         MOV     AL,9          ; ICW4 - SET BUFFERED MODE/S�AVE
                                ; AND 8086 MODE
056E E6 21         OUT    INTA01,AL

;-----
; TEST ABILITY TO WRITE/READ THE MASK REGISTER
;-----
0570 B0 00         MOV     AL,0          ; WRITE ZEROES TO IMR
0572 8A D8         MOV     BL,AL         ; PRESET ERROR INDICATOR
0574 E6 21         OUT    INTA01,AL     ; DEVICE INTERRUPTS ENABLED
0576 E4 21         IN     AL,INTA01    ; READ IMR
0578 0A C0         OR     AL,AL          ; IMR = 0?
057A 75 18         JNZ   GERROR         ; NO - GO TO ERROR ROUTINE
057C B0 FF         MOV     AL,0FFH      ; DISABLE DEVICE INTERRUPTS
057E E6 21         OUT    INTA01,AL   ; WRITE ONES TO IMR
0580 E4 21         IN     AL,INTA01   ; READ IMR
0582 04 01         ADD    AL,1          ; ALL IMR BITS ON?
                                ; (ADD SHOULD PRODUCE 0)

```

0584 75 0E

```

JNZ GERROR ; NO - GO TO ERROR ROUTINE
;-----
; CHECK FOR HOT INTERRUPTS
;-----
----- INTERRUPTS ARE MASKED OFF. NO INTERRUPTS SHOULD OCCUR.
;-----
; ENABLE EXTERNAL INTERRUPTS
STI
MOV CX,50H
LOOP $ ; WAIT FOR ANY INTERRUPTS
MOV BL,DATA_AREA[INTR_FLAG-RS232_BASE] ; ANY INT OCCUR?
OR BL,BL
JZ END_TESTG ; NO - GO TO NEXT TEST
GERROR: MOV BH,07H ; SET 07 SECTION OF ERROR MSG
JMP E_MSG

```

0586 FB  
0587 B9 0050  
058A E2 FE  
058C 8A 1E 0484 R  
0590 0A DB  
0592 74 2D  
0594 B7 07  
0596 E9 0D32 R

```

; SUBROUTINE
; TEMPORARY INTERRUPT SERVICE
; DESCRIPTION
; THIS ROUTINE IS ALSO LEFT IN PLACE AFTER THE
; POWER ON DIAGNOSTICS TO SERVICE UNUSED
; INTERRUPT VECTORS. LOCATION 'INTR_FLAG' WILL
; CONTAIN EITHER: 1. LEVEL OF HARDWARE INT. THAT
; CAUSED CODE TO BE EXEC.
;
; 2. 'FF' FOR NON-HARDWARE INTERRUPTS THAT WERE
; EXECUTED ACCIDENTLY.
;-----

```

0599

0599 1E  
059A 50  
059B E8 0000 E  
059E B0 0B  
05A0 E6 20  
05A2 90  
05A3 E4 20  
05A5 8A E0  
05A7 0A C4  
05A9 75 04  
05AB B4 FF  
05AD EB 0A  
05AF E4 21  
05B1 0A C4  
05B3 E6 21  
05B5 B0 20  
05B7 E6 20  
05B9  
05B9 88 26 0084 R  
05BD 58  
05BE 1F  
05BF FB  
05C0  
05C0 CF  
05C1  
  
05C1  
05C1 B0 E0  
05C3 E6 F2  
05C5 B0 A0  
05C7 E6 F2

```

D11 PROC NEAR
ASSUME DS:DATA
PUSH DS
PUSH AX ; SAVE REG AX CONTENTS
CALL DDS
MOV AL,0BH ; READ IN-SERVICE REG
OUT INTA00,AL ; (FIND OUT WHAT LEVEL BEING
; SERVICED)
IN AL,INTA00 ; GET LEVEL
MOV AH,AL ; SAVE IT
OR AL,AH ; 00? (NO HARDWARE ISR ACTIVE)
JNZ HW_INT
MOV AH,OFFH
JMP SHORT SET_INTR_FLAG ; SET FLAG TO FF IF NON-HDWARE
HW_INT: IN AL,INTA01 ; GET MASK VALUE
OR AL,AH ; MASK OFF LVL BEING SERVICED
OUT INTA01,AL
MOV AL,EDI
OUT INTA00,AL
SET_INTR_FLAG:
MOV INTR_FLAG,AH ; SET FLAG
POP AX ; RESTORE REG AX CONTENTS
POP DS
STI ; INTERRUPTS BACK ON
DUMMY_RETURN:
IRET ; NEED IRET FOR VECTOR TABLE
D11 ENDP
;-----
END_TESTG:
;----- FIRE THE DISKETTE WATCHDOG TIMER
MOV AL,WD_ENABLE+WD_STROBE+FDC_RESET
OUT NEC_CTL,AL
MOV AL,WD_ENABLE+FDC_RESET
OUT NEC_CTL,AL
ASSUME CS:CODE,DS:ABS0

```

```

; TEST 10
; 8253 TIMER CHECKOUT
; DESCRIPTION
; VERIFY THAT THE TIMERS (0, 1, AND 2) FUNCTION PROPERLY.
; THIS INCLUDES CHECKING FOR STUCK BITS IN ALL THE TIMERS,
; THAT TIMER 1 RESPONDS TO TIMER 0 OUTPUTS, THAT TIMER 0
; INTERRUPTS WHEN IT SHOULD, AND THAT TIMER 2'S OUTPUT
; WORKS AS IT SHOULD.
; THERE ARE 7 POSSIBLE ERRORS DURING THIS CHECKOUT.
; BL VALUES FOR THE CALL TO E_MSG INCLUDE:
; 0: STUCK BITS IN TIMER 0
; 1: TIMER 1 DOES NOT RESPOND TO TIMER 0 OUTPUT
; 2: TIMER 0 INTERRUPT DOES NOT OCCUR
; 3: STUCK BITS IN TIMER 1
; 4: TIMER 2 OUTPUT INITIAL VALUE IS NOT LOW
; 5: STUCK BITS IN TIMER 2
; 6: TIMER 2 OUTPUT DOES NOT GO HIGH ON TERMINAL COUNT
; 7: TIMER 0 OUTPUT OUT OF RANGE
;-----

```

05C9 E8 0F7C R  
05CC B8 0176  
05CF B8 FFFF  
05D2 E8 06F2 R  
05D5 B8 0036  
  
05D8 E8 06F2 R

```

INITIALIZE TIMER 1 AND TIMER 0 FOR TEST
;-----
CALL MFG_UP ; SEND 'F5' TO MFG_TESTER
MOV AX,0176H ; SET TIMER 1 TO MODE 3 BINARY
MOV BX,0FFFFH ; INITIAL COUNT OF FFFF
CALL INIT_TIMER ; INITIALIZE TIMER 1
MOV AX,0036H ; SET TIMER 0 TO MODE 3 BINARY
; INITIAL COUNT OF FFFF
CALL INIT_TIMER ; INITIALIZE TIMER 0

```

05DB B0 20  
05DD E6 A0

```

SET BIT 5 OF PORT A0 SO TIMER 1 CLOCK WILL BE PULSED BY THE
TIMER 0 OUTPUT RATHER THAN THE SYSTEM CLOCK.
;-----
MOV AL,00100000B
OUT MMI_PORT,AL
;-----

```

05DF B4 00  
05E1 E8 06A9 R  
05E4 73 05

```

CHECK IF ALL BITS GO ON AND OFF IN TIMER 0 (CHECK FOR STUCK
BITS)
;-----
MOV AH,0 ; TIMER 0
CALL BITS_ON_OFF ; LET SUBROUTINE CHECK IT
JNB TIMER1_NZ ; NO STUCK BITS (CARRY FLAG NOT SET)

```

Appendix A.

```

05E6 B3 00          MOV     BL,0           ; STUCK BITS IN TIMER 0
05E8 E9 069F R     JMP     TIMER_ERROR

;-----
; SINCE TIMER 0 HAS COMPLETED AT LEAST ONE COMPLETE CYCLE,
; TIMER 1 SHOULD BE NON-ZERO. CHECK THAT THIS IS THE CASE.
;-----
TIMER1_NZ:
05EB E4 41          IN      AL,TIMER+1     ; READ LSB OF TIMER 1
05ED 8A E0          MOV     AH,AL          ; SAVE LSB

05EF E4 41          IN      AL,TIMER+1     ; READ MSB OF TIMER 1
05F1 3D FFFF       CMP     AX,0FFFFH     ; STILL FFFF?
05F4 75 05          JNE    TIMER0_INTR    ; NO - TIMER 1 HAS BEEN BUMPED
05F6 B3 01          MOV     BL,1          ; TIMER 1 WAS NOT BUMPED BY TIMER 0
05F8 E9 069F R     JMP     TIMER_ERROR

;-----
; CHECK FOR TIMER 0 INTERRUPT
;-----
TIMER0_INTR:
05FB FB            STI     ; ENABLE MASKABLE EXT INTERRUPTS
05FC E4 21          IN      AL,INTA01     ;
05FE 24 FE          AND     AL,0FEH       ; MASK ALL INTRs EXCEPT LVL 0
0600 20 06 0484 R  AND     DATA_AREA[INTR_FLAG-RS232_BASE],AL ; CLR INT RCVD
0604 E6 21          OUT    INTA01,AL      ; WRITE THE 8259 INTR
0606 33 C9          XOR     CX,CX          ; SET LOOP COUNT
0608 F6 06 0484 R 01 TEST    DATA_AREA[INTR_FLAG-RS232_BASE],1 ; THRO INT OCCUR?
060D 75 07          JNZ    RESET_INTRS    ; YES - CONTINUE
060F E2 F7          LOOP   WAIT_INTR_LOOP ; WAIT FOR INTR FOR SPECIFIED TIME
0611 B3 02          MOV     BL,2          ; TIMER 0 INTR DIDN'T OCCUR
0613 E9 069F R     JMP     TIMER_ERROR

;-----
; HOUSEKEEPING FOR TIMER 0 INTERRUPTS
;-----
RESET_INTRS:
0616 FA            CLI

;-----
; CHECK COUNT DOWN SPEED OF TIMER 0
;-----
0617 B0 00          MOV     AL,0           ;
0619 E6 43          OUT    TIM_CTL,AL     ; LATCH TIMER 0
061B 50            PUSH    AX             ; PAUSE
061C 58            POP     AX             ;
061D E4 40          IN      AL,TIMER+0    ; READ TIMER 0 LSB
061F 8A D0          MOV     DL,AL          ;
0621 E4 40          IN      AL,TIMER+0    ; " MSB
0623 8A F0          MOV     DH,AL          ;

0625 B9 05E2       MOV     CX,05E2H       ; WAIT 1 MS (CH=5, CL=226)
0628 D3 E8          SHR     AX,CL           ;
062A FE CD          DEC     CH             ;
062C 75 FA          JNZ    TIMER0_W        ;

062E B0 00          MOV     AL,0           ;
0630 E6 43          OUT    TIM_CTL,AL     ; LATCH TIMER 0
0632 50            PUSH    AX             ; PAUSE
0633 58            POP     AX             ;
0634 E4 40          IN      AL,TIMER+0    ; READ TIMER 0 LSB
0636 8A C8          MOV     CL,AL          ;
0638 E4 40          IN      AL,TIMER+0    ; " MSB
063A 8A E8          MOV     CH,AL          ;
063C B3 07          MOV     BL,7          ; TIMER 0 OUTPUT OUT OF RANGE

063E 2B D1          SUB     DX,CX           ; (0955H)
0640 80 FE 0A       CMP     DH,0AH        ; WITHIN TOLERANCE ?
0643 77 5A          JA     TIMER_ERROR     ; NO -
0645 80 FE 08       CMP     DH,08H        ;
0648 72 55          JB     TIMER_ERROR     ;

;-----
; RESET D5 OF PORT A0 SO THAT THE TIMER 1 CLOCK WILL BE
; PULSED BY THE SYSTEM CLOCK.
;-----
064A B0 00          MOV     AL,0           ; MAKE AL = 00
064C E6 A0          OUT    NMI_PORT,AL    ;

;-----
; CHECK FOR STUCK BITS IN TIMER 1
;-----
064E B4 01          MOV     AH,1           ; TIMER 1
0650 E8 06A9 R     CALL    BITS_ON_OFF    ;
0653 73 04          JNB    TIMER2_INIT     ; NO STUCK BITS
0655 B3 03          MOV     BL,3           ; STUCK BITS IN TIMER 1
0657 EB 46          JMP     SHORT_TIMER_ERROR

;-----
; INITIALIZE TIMER 2
;-----
TIMER2_INIT:
0659 B8 02B6         MOV     AX,02B6H       ; SET TIMER 2 TO MODE 3 BINARY
065C BB FFFF         MOV     BX,0FFFFH     ; INITIAL COUNT
065F E8 06F2 R     CALL    INIT_TIMER

;-----
; SET P80 OF PORT B OF 8255 (TIMER 2 GATE)
;-----
0662 E4 61          IN      AL,PORT_B      ; CURRENT STATUS
0664 0C 01          OR     AL,00000010B    ; SET BIT 0 - LEAVE OTHERS ALONE
0666 E6 61          OUT    PORT_B,AL

;-----
; CHECK FOR STUCK BITS IN TIMER 2
;-----
0668 B4 02          MOV     AH,2           ; TIMER 2
066A E8 06A9 R     CALL    BITS_ON_OFF    ;
066D 73 04          JNB    REINIT_T2      ; NO STUCK BITS

```

```

066F B3 05          MOV     BL,5           ; STUCK BITS IN TIMER 2
0671 EB 2C          JMP     SHORT TIMER_ERROR
;-----
; RE_INITIALIZE TIMER 2 WITH MODE 0 AND A SHORT COUNT
;-----
0673              REINIT_T2:
;-----
0673 E4 61          ;----- DROP GATE TO TIMER 2
0675 24 FE          IN     AL,PORT_B       ; CURRENT STATUS
0677 E6 61          AND     AL,1111110B    ; RESET BIT 0 - LEAVE OTHERS ALONE
0679 B8 02B0        OUT     PORT_B,AL      ;
067C BB 000A        MOV     AX,02B0H      ; SET TIMER 2 TO MODE 0 BINARY
067F E8 06F2 R      MOV     BX,000AH      ; INITIAL COUNT OF 10
                    CALL    INIT_TIMER
;-----
; CHECK PC5 OF PORT_C OF 8255 TO SEE IF THE OUTPUT OF TIMER 2
; IS LOW
;-----
0682 E4 62          IN     AL,PORT_C       ; CURRENT STATUS
0684 24 20          AND     AL,00100000B  ; MASK OFF OTHER BITS
0686 74 04          JZ     CK2_ON         ; IT'S LOW
0688 B3 04          MOV     BL,4          ; PC5 OF PORT_C WAS HIGH WHEN IT
068A EB 13          JMP     SHORT TIMER_ERROR ; SHOULD HAVE BEEN LOW
;-----
; TURN GATE BACK ON
068C E4 61          CK2_ON: IN     AL,PORT_B       ; CURRENT STATUS
068E 0C 01          OR     AL,00000001B  ; SET BIT 0 - LEAVE OTHERS ALONE
0690 E6 61          OUT     PORT_B,AL
;-----
; CHECK PC5 OF PORT_C TO SEE IF THE OUTPUT OF TIMER 2 GOES
; HIGH
;-----
0692 B9 000A        CK2_LO: MOV     CX,000AH      ; WAIT FOR OUTPUT GO HIGH, SHOULD
0695 E2 FE          LOOP    CK2_LO        ; BE LONGER THAN INITIAL COUNT
0697 E4 62          IN     AL,PORT_C       ; CURRENT STATUS
0699 24 20          AND     AL,00100000B  ; MASK OFF ALL OTHER BITS
069B 75 65          JNZ    POD13_END      ; IT'S HIGH - WE'RE DONE!
069D B3 06          MOV     BL,6          ; TIMER 2 OUTPUT DID NOT GO HIGH
;-----
; 8253 TIMER ERROR OCCURRED. SET BH WITH MAJOR ERROR
; INDICATOR AND CALL E_MSG TO INFORM THE SYSTEM OF THE ERROR.
; (BL ALREADY CONTAINS THE MINOR ERROR INDICATOR TO TELL
; WHICH PART OF THE TEST FAILED.)
;-----
069F              TIMER_ERROR:
069F B7 08          MOV     BH,8           ; TIMER ERROR INDICATOR
06A1 E8 0D32 R      CALL    E_MSG
06A4 EB 5C          JMP     SHORT POD13_END
;-----
; SUBROUTINE
; BITS ON/OFF
; DESCRIPTION
; USED FOR DETERMINING IF A PARTICULAR TIMER'S BITS GO ON
; AND OFF AS THEY SHOULD. THIS ROUTINE ASSUMES THAT THE
; TIMER IS USING BOTH THE LSB AND THE MSB.
; CALLING PARAMETER
; (AH) = TIMER NUMBER (0, 1, OR 2)
; RETURNS
; (CF) = 1 IF FAILED
; (CF) = 0 IF PASSED
; REGISTERS AX, BX, CX, DX, DI, AND SI ARE ALTERED.
;-----
06A6              LATCHES LABEL BYTE
06A6 00            DB     00H           ; LATCH MASK FOR TIMER 0
06A7 48            DB     40H           ; LATCH MASK FOR TIMER 1
06A8 80            DB     80H           ; LATCH MASK FOR TIMER 2
;-----
06A9              BITS_ON_OFF PROC NEAR
06A9 33 DB         XOR     BX,BX           ; INITIALIZE BX REGISTER
06AB 33 F6         XOR     SI,SI           ; 1ST PASS - SI = 0
;-----
06AD 8A 0040        MOV     DX,TIMER       ; BASE PORT ADDRESS FOR TIMERS
06B0 92 D4         ADD     DI,AX           ;
06B2 8F 06A6 R     MOV     DI,OFFSET LATCHES ; SELECT LATCH MASK
06B5 32 C0         XOR     AL,AL           ; CLEAR AL
06B7 86 C4         XCHG   AL,AX           ; AX -> AL
06B9 83 F8         ADD     DI,AX           ; TIMER LATCH MASK INDEX
;-----
; 1ST PASS - CHECKS FOR ALL BITS TO COME ON
; 2ND PASS - CHECKS FOR ALL BITS TO GO OFF
06BB              OUTER_LOOP:
06BB B9 000B        MOV     CX,8           ; OUTER LOOP COUNTER
06BE              INNER_LOOP:
06BE 51            PUSH    CX              ; SAVE OUTER LOOP COUNTER
06BF 33 C9         XOR     CX,CX           ; INNER LOOP COUNTER
06C1              TST_BITS:
06C1 2E: 8A 05     MOV     AL,CS:[DI]     ; TIMER LATCH MASK
06C4 E4 43         OUT     TIM_CTL,AL     ; LATCH TIMER
06C6 50          PUSH    AX              ; PAUSE
06C7 58          POP     AX
06C8 EC          IN     AL,DX           ; READ TIMER LSB
06C9 0B F6         OR     SI,SI           ;
06CB 75 0C         JNE    SECOND         ; SECOND PASS
06CD 0C 01         OR     AL,01H         ; TURN LS BIT ON
06CF 0A D8         OR     BL,AL          ; TURN 'ON' BITS ON
06D1 EC          IN     AL,DX           ; READ TIMER MSB
06D2 0A F8         OR     BH,AL          ; TURN 'ON' BITS ON
06D4 83 FB FF     CMP     BX,OFFFH       ; ARE ALL TIMER BITS ON?
06D7 EB 07         JMP     SHORT TST_CMP  ; DON'T CHANGE FLAGS
06D9              SECOND:
06D9 22 D8         AND     BL,AL          ; CHECK FOR ALL BITS OFF
06DB EC          IN     AL,DX           ; READ MSB
06DC 22 F8         AND     BH,AL          ; TURN OFF BITS
06DE 0B DB         OR     BX,BX          ; ALL OFF?
06E0              TST_CMP:

```

Appendix A.

```

06E0 74 07      JE      CHK_END      ; YES - SEE IF DONE
06E2 E2 DD      LOOP     TST_BITS    ; KEEP TRYING
06E4 59         POP      CX          ; RESTORE OUTER LOOP COUNTER
06E5 E2 D7      LOOP     INNER_LOOP   ; TRY AGAIN
06E7 F9         STC      STC          ; ALL TRIES EXHAUSTED - FAILED TEST
06E8 C3         RET      RET          ;
CHK_END:
06E9 59         POP      CX          ; POP FORMER OUTER LOOP COUNTER
06EA 46         INC      SI          ;
06EB 83 FE 02   CMP      SI,2         ; CHECK FOR ALL BITS TO GO OFF
06EE 75 CB      JNE     OUTER_LOOP   ; TIMER BITS ARE WORKING PROPERLY
06F0 F8         CLC      CLC          ;
06F1 C3         RET      RET          ;
06F2           ENDP     ENDP
-----
; SUBROUTINE
; INITIALIZE TIMER
; DESCRIPTION
; ASSUMES BOTH THE LSB AND MSB OF THE TIMER WILL BE USED.
; CALLING PARAMETERS
;
; (AH) = TIMER #
; (AL) = BIT PATTERN OF INITIALIZATION WORD
; (BX) = INITIAL COUNT
; (BH) = MSB COUNT
; (BL) = LSB COUNT
; ALTERS REGISTERS DX AND AL.
-----
06F2           SUBROUTINE SUBROUTINE
06F2 E6 43      OUT      TIM_CTL,AL    ; OUTPUT INITIAL CONTROL WORD
06F4 BA 0040    MOV      DX,TIMER     ; BASE PORT ADDR FOR TIMERS
06F7 02 D4      ADD      DL,AH        ; ADD IN THE TIMER #
06F9 8A C3      MOV      AL,BL        ; LOAD LSB
06FB EE        OUT      DX,AL       ;
06FC 52        PUSH     DX          ; PAUSE
06FD 5A        POP      DX          ;
06FE 8A C7      MOV      AL,BH        ; LOAD MSB
0700 EE        OUT      DX,AL       ;
0701 C3         RET      RET          ;
0702           ENDP     ENDP
-----
0702           SUBROUTINE SUBROUTINE
0702           POD13_END:
-----
; TEST 11
; CRT ATTACHMENT TEST
; DESCRIPTION
; 1. INIT CRT TO 40 X 25 - COLOR
; 2. CHECK FOR VERTICAL AND VIDEO ENABLES, AND CHECK
;    TIMING OF SAME
; 3. CHECK VERTICAL INTERRUPT
; 4. CHECK RED, BLUE, GREEN, AND INTENSIFY DOTS
; MFG ERROR CODE = 09XX (XX-SEE COMMENTETS IN CODE)
-----
= A04C
= C418
= 00C8
0702 E8 0F7C R  CALL     MFG_UP      ; SEND 'F4' TO MFG_TESTER
0705 FA        CLI      CLI          ;
0706 B0 70      MOV      AL,01110000B ; SET TIMER 1 TO MODE 0
0708 E6 43      OUT      TIM_CTL,AL
070A B9 8000    MOV      CX,8000H     ;
070D E2 FE      LOOP     $          ; WAIT FOR MODE SET TO "TAKE"
070F B0 00      MOV      AL,0         ;
0711 E6 41      OUT      TIMER+1,AL ; SEND FIRST BYTE TO TIMER
0713 BA 03DA    MOV      DX,VGA_CTL   ; SET ADDRESSING TO VIDEO ARRAY
0716 2B C9      SUB      CX,CX        ;
;----- LOOK FOR VERTICAL
Q2:
0718 EC        IN      AL,DX      ; GET STATUS
0719 A8 08      TEST     AL,00001000B ; VERTICAL THERE YET?
071B 75 06      JNE     Q3          ; CONTINUE IF IT IS
071D E2 F9      LOOP     Q2         ; KEEP LOOKING TILL COUNT EXHAUSTED
071F B3 00      MOV      BL,0        ;
0721 EB 4C      JMP      SHORT Q115   ; NO VERTICAL = ERROR 0900
;----- GOT VERTICAL - START TIMER
Q3:
0723 32 C0      XOR      AL,AL        ;
0725 E6 41      OUT      TIMER+1,AL  ; SEND 2ND BYTE TO TIMER TO START
0727 2B DB      SUB      BX,BX       ; INIT. ENABLE COUNTER
;----- WAIT FOR VERTICAL TO GO AWAY
Q4:
0729 33 C9      XOR      CX,CX        ;
072B EC        IN      AL,DX      ; GET STATUS
072C A8 08      TEST     AL,00001000B ; VERTICAL STILL THERE?
072E 74 06      JZ      Q5          ; CONTINUE IF IT'S GONE
0730 E2 F9      LOOP     Q4         ; KEEP LOOKING TILL COUNT EXHAUSTED
0732 B3 01      MOV      BL,01H     ;
0734 EB 39      JMP      SHORT Q115   ; VERTICAL STUCK ON = ERROR 0901
;----- NOW START LOOKING FOR ENABLE TRANSITIONS
Q5:
0736 2B C9      SUB      CX,CX        ;
0738 EC        IN      AL,DX      ; GET STATUS
0739 A8 01      TEST     AL,00000001B ; ENABLE ON YET?
073B 75 0A      JNE     Q7          ; GO ON IF IT IS
;
073D A8 08      TEST     AL,00001000B ; VERTICAL ON AGAIN?
073F 75 22      JNE     Q11         ; CONTINUE IF IT IS
0741 E2 F5      LOOP     Q6         ; KEEP LOOKING IF NOT
0743 B3 02      MOV      BL,02H     ;
0745 EB 28      JMP      SHORT Q115   ; ENABLE STUCK OFF = ERROR 0902
;----- MAKE SURE VERTICAL WENT OFF WITH ENABLE GOING ON
Q7:
0747 A8 08      TEST     AL,00001000B ; VERTICAL OFF?
0749 74 04      JZ      Q8          ; GO ON IF IT IS
074B B3 03      MOV      BL,03H     ;

```



```

074D EB 20
074F 2B C9
0751 EC
0752 A8 01
0754 74 06
0756 E2 F9
0758 B3 04
075A EB 13
075C 43
075D 74 04
075F A8 08
0761 74 D3
0763 B0 40
0765 E6 43
0767 81 FB 00C8
0768 74 05
076D B3 05
076F E9 07FF R
0772 E4 41
0774 8A E0
0776 90
0777 E4 41
0779 86 E0
077B FB
077C 90
077D 3D A04C
0780 73 04
0782 B3 06
0784 EB 79
0786 3D C418
0789 76 04
078B B3 07
078D EB 70
078F 2B C9
0791 E4 21
0793 24 DF
0795 E6 21
0797 20 06 0484 R
0798 FB
079C F6 06 0484 R 20
07A1 75 06
07A3 E2 F7
07A5 B3 08
07A7 EB 56
07A9 E4 21
07AB 0C 20
07AD E6 21
07AF 52
0780 33 C0
07B2 CD 10
07B4 B8 0507
07B7 CD 10
07B9 B8 095F
07BC BB 077F
07BF B9 0028
07C2 CD 10
07C4 E8 04C2 R
07C7 8D
07C8 00
07C9 01
07CA 00
07CB E8 04C2 R
07CE 8C
07CF 00
07D0 01
07D1 80
07D2 5A
07D3 33 C0
07D5 2B C9
07D7 EE
07D8 EC
07D9 A8 10
07DB 75 08
07DD E2 F9
07DF B3 10
07E1 0A DC
07E3 EB 1A
07E5 2B C9
07E7 EC
07E8 A8 10

;----- JMP SHORT Q115 ; VERTICAL STUCK ON = ERROR 0903
;----- NOW WAIT FOR ENABLE TO GO OFF
Q8: SUB CX,CX
Q9: IN AL,DX ; GET STATUS
TEST AL,00000001B ; ENABLE OFF YET?
JE Q10 ; PROCEED IF IT IS
LOOP Q9 ; KEEP LOOKING IF NOT YET LOW
MOV BL,04H ;
JMP SHORT Q115 ; ENABLE STUCK ON = ERROR 0904
;----- ENABLE HAS TOGGLED, BUMP COUNTER AND TEST FOR NEXT VERTICAL
Q10: INC BX ; BUMP ENABLE COUNTER
JZ Q11 ; IF COUNTER WRAPS, ERROR
TEST AL,00001000B ; DID ENABLE GO LOW BECAUSE OF
; VERTICAL?
; IF NOT, LOOK FOR ANOTHER ENABLE
; TOGGLE
;----- HAVE HAD COMPLETE VERTICAL-VERTICAL CYCLE, NOW TEST RESULTS
Q11: MOV AL,40H ; LATCH TIMER1
OUT TIM_CTL,AL ;
CMP BX,EPF ; NUMBER OF ENABLES BETWEEN
; VERTICALS O.K.?
;
;
Q115: MOV BL,05H ;
Q12: JMP Q22 ; WRONG # ENABLES = ERROR 0905
IN AL,TIMER+1 ; GET TIMER VALUE LOW
MOV AH,AL ; SAVE IT
NOP ;
IN AL,TIMER+1 ; GET TIMER HIGH
XCHG AH,AL ;
STI ; INTERRUPTS BACK ON
NOP ;
CMP AX,MAVT ;
JAE Q13 ;
MOV BL,06H ;
JMP SHORT Q22 ; VERTICALS TOO FAR APART
; = ERROR 0906
;
;
Q13: CMP AX,MIVT ;
JBE Q14 ;
MOV BL,07H ;
JMP SHORT Q22 ; VERTICALS TOO CLOSE TOGETHER
; = ERROR 0907
;
;----- TIMINGS SEEM O.K., NOW CHECK VERTICAL INTERRUPT (LEVEL 5)
Q14: SUB CX,CX ; SET TIMEOUT REG
IN AL,INTA01
AND AL,11011111B ; UNMASK INT. LEVEL 5
OUT INTA01,AL
AND DATA_AREA[INTR_FLAG-RS232_BASE],AL ; ENABLE INTS.
STI ;
Q15: TEST DATA_AREA[INTR_FLAG-RS232_BASE],00100000B ; SEE IF
; INT 5 HAPPENED YET
JNZ Q16 ; GO ON IF IT DID
LOOP Q15 ; KEEP LOOKING IF IT DIDN'T
MOV BL,08H ;
JMP SHORT Q22 ; NO VERTICAL INTERRUPT
; = ERROR 0908
;
Q16: IN AL,INTA01 ; DISABLE INTERRUPTS FOR LEVEL 5
OR AL,00100000B
OUT INTA01,AL
;
;----- SEE IF RED, GREEN, BLUE AND INTENSIFY DOTS WORK
; FIRST, SET A LINE OF REVERSE VIDEO, INTENSIFIED BLANKS
; INTO VIDEO BUFFER
PUSH DX ;
XOR AX,AX ; ENABLE DISPLAY & CLEAR SCREEN
INT INT_10 ; (MODE: 40 X 25 - COLOR)
MOV AX,0507H ; SET TO VIDEO PAGE 7
INT INT_10 ;
MOV AX,095FH ; WRITE CHARS, UNDERSCORE
MOV BX,077FH ; PAGE 7, REVERSE VIDEO, HIGH INT
MOV CX,40 ; 40 CHARACTERS
INT INT_10 ;
CALL S8SET ; DISABLE NATIVE VGA
DB 08DH ; START REG ADDR
DB 000H ; MASK DATA
DB 1 ; PARAMETER LENGTH
CALL S8SET ; ENABLE PCJR VGA
DB 08CH ; START REG ADDR
DB 000H ; MASK DATA
DB 1 ; PARAMETER LENGTH
POP DX ;
;
Q17: XOR AX,AX ; START WITH BLUE DOTS
SUB CX,CX ;
OUT DX,AL ; SET VIDEO ARRAY ADDRESS FOR DOTS
;----- SEE IF DOT COMES ON
Q18: IN AL,DX ; GET STATUS
TEST AL,00010000B ; DOT THERE?
JNZ Q19 ; GO LOOK FOR DOT TO TURN OFF
LOOP Q18 ; CONTINUE TESTING FOR DOT ON
;
MOV BL,10H ;
OR BL,AH ; OR IN DOT BEING TESTED
JMP SHORT Q22 ; DOT NOT COMING ON = ERROR 091X
; ( X=0, BLUE; X=1, GREEN;
; X=2, RED; X=3, INTENSITY)
;----- SEE IF DOT GOES OFF
Q19: SUB CX,CX
Q20: IN AL,DX ; GET STATUS
TEST AL,00010000B ; IS DOT STILL ON?

```

Appendix A.

```

07EA 74 08          JE      Q21          ; GO ON IF DOT OFF
07EC E2 F9          LOOP     Q20          ; ELSE, KEEP WAITING FOR DOT
                                       ; TO GO OFF
07EE B3 20          MOV     BL,20H          ;
07F0 0A DC          OR      BL,AH          ; OR IN DOT BEING TESTED
07F2 EB 0B          JMP     SHORT Q22       ; DOT STUCK ON = ERROR 092X
                                       ; (X=0, BLUE; X=1, GREEN;
                                       ; X=2, RED; X=3, INTENSITY)
;----- ADJUST TO POINT TO NEXT DOT
07F4 FE C4          Q21:  INC     AH          ;
07F6 80 FC 04      CMP     AH,4          ; ALL 4 DOTS DONE?
07F9 74 09          JE      Q23          ; GO END
07FB 8A C4          MOV     AL,AH          ;
07FD EB D6          JMP     Q17          ; GO LOOK FOR ANOTHER DOT
07FF B7 09          Q22:  MOV     BH,09H       ; SET MSB OF ERROR CODE
0801 E9 0D32 R      JMP     E_MSG         ;
;-----
0804 B0 76          Q23:  MOV     AL,01110110B ; RE-INIT TIMER 1
0806 E6 43          OUT     TIM_CTL,AL    ;
0808 B0 00          MOV     AL,00H       ;
080A E6 41          OUT     TIMER+1,AL   ;
080C EB 00          JMP     $+2          ;
080E E6 41          OUT     TIMER+1,AL   ;
;-----
; PART 12
; SET UP KEYBOARD PARAMETERS
;-----
0810 E8 0F7C R      ASSUME DS:ABS0
0813 33 C0          CALL    MFG_UP        ; SEND 'F3' TO MFG_TESTER
0815 8E D8          XOR     AX,AX
0817 C7 06 0008 R 0000 E C MOV     NMI_PTR,OFFSET KBDNMI ; SET INTERRUPT VECTOR
081D C7 06 0120 R 0FEA R C MOV     KEY62_PTR,OFFSET KEY_SCAN_SAVE ; SET VECTOR FOR
                                       ; POD INT HANDLER
0823 0E            PUSH   CS
0824 58            POP    AX
0825 A3 0122 R      MOV     KEY62_PTR+2,AX
                                       ASSUME DS:DATA
0828 E8 0000 E      CALL    DDS          ; SET DATA SEGMENT
082B BE 0304 R      MOV     SI,OFFSET KB_BUFFER_J ; SET KEYBOARD PARMS
082E 89 36 001A R   MOV     BUFFER_HEAD,SI
0832 89 36 001C R   MOV     BUFFER_TAIL,SI
0836 89 36 0080 R   MOV     BUFFER_START,SI
083A 83 C6 32      ADD     SI,50         ; SET DEFAULT BUFFER OF 50 BYTES
083D 89 36 0082 R   MOV     BUFFER_END,SI
0841 E4 A0          IN      AL,NMI_PORT  ; CLEAR NMI F/F
0843 B0 80          MOV     AL,80H       ; ENABLE NMI
0845 E6 A0          OUT     NMI_PORT,AL  ;
;----- IF A KEY IS STUCK, THE BUFFER SHOULD FILL WITH THAT KEY'S
; CODE. THIS WILL BE CHECKED LATER.
;-----
; TEST 13
; 32K VRAM (VRAM2) TEST
; MFG ERROR CODE = 1EXX BASE 32K VRAM ERROR
; 1FXX EXP 32K VRAM ERROR
;-----
0847 E8 0F7C R      CALL    MFG_UP        ; SEND 'F2' TO MFG TESTER
;----- ENABLE NATIVE AND PCJR VGA FOR BLANKING SCREEN
084A E8 04C2 R      CALL    S8SET        ;
084D 8C            DB      08CH        ; START REG ADDR
084E 00            DB      000H        ; MASK DATA
084F 02            DB      2          ; PARAMETER LENGTH
0850 80            DB      080H        ; ENABLE PCJR VGA
0851 80            DB      080H        ; ENABLE NATIVE VGA
0852 BA 03DA      MOV     DX,VGA_CTL   ; DISABLE VIDEO
0855 EC            IN      AL,DX
0856 33 C0          XOR     AX,AX
0858 EE            OUT     DX,AL
0859 EE            OUT     DX,AL
;----- BASE 32K VRAM AND EXP 32K VRAM TEST (EXCEPT REGEN BUFF)
085A B8 B800      MOV     AX,08800H    ; SET START ADDR (B8000)
085D 8E C0          MOV     ES,AX
085F B9 1E00      MOV     CX,1E00H     ; SET TEST MEM SIZE 15K
0862 BB 0010      MOV     BX,0010H
0865 BA 01FF      MOV     DX,S8STATUS
0868 EC            IN      AL,DX
0869 A8 80          TEST   AL,VRAM2IN
086B 75 02          JNZ   VRM1
086D B3 20          MOV     BL,20H
086F 53            PUSH   BX
VRM1:  TEST   BH,00001000B ; 1ST 16KB ?
0870 F6 C7 08      JNZ   VRM2          ; NO -
0873 75 06          IN      AL,PORT_A
0875 E4 60          OR     AL,01000000B ; SET RETENTION TEST REQ'ED FLAG
0877 0C 40          OUT   PORT_A,AL
0879 E6 60          OUT   PODSTG,ALL    ; CALL MEM TEST
VRM2:  CALL   PODSTG
087B E8 0DEA R      POP    BX
087E 5B            JZ     VRM3
087F 74 06          ;
0881 E8 1007 R      CALL   PUT_LOGO    ; PUT LOGO ON SCREEN
0884 E9 097F R      JMP    Q29          ; ERROR
0887 E4 60          VRM3:  IN      AL,PORT_A
0889 24 BF          AND   AL,10111111B ; RESET RETENTION TEST REQ'ED FLAG
088B E6 60          OUT   PORT_A,AL
088D B9 2000      MOV     CX,2000H    ; SET TEST MEM SIZE 16K
0890 BA 03D9      MOV     DX,PAGREG2
0893 80 C7 0B      ADD   BH,00001000B ; UPDATE CPU/CRT PAGE
0896 8A C7          MOV     AL,BH
0898 EE            OUT   DX,AL        ; SET PAGE REG 2

```



Appendix A.

```

; AFTER THIS, MEMORY WILL BE EITHER TESTED
; OR CLEARED, DEPENDING ON THE CONTENTS OF
; "RESET_FLAG".
; MFG ERROR CODES = 0AXX PLANAR BOARD ERROR
;                   0BXX 64K RAM CARD ERROR
;                   0CXX ERRORS IN BOTH ODD & EVEN BYTES
;                   IN A 128K SYS
;                   1YXX 128K RAM CARD ERROR
;                   Y=SEGMENT HAVING TROUBLE
;                   1EXX BASE 32K VRAM ERROR
;                   1FXX 32K VRAM CARD ERROR
;                   XX= ERROR BITS
-----
          ASSUME DS:DATA
091D EB 0800 E CALL DD5
0920 EB 0F7C R CALL MFG_UP ; SEND 'F0' TO MFG_TESTER

          IN AL,PORT_A ; RESTORE RAM MAP INFO MATION
0923 E4 60 MOV BX,64
0925 BB 0040 TEST AL,00001000B ; 64K RAM CARD?
0928 A8 08 JZ Q25_1 ; NO -
092A 74 03 ADD BX,64
092C 83 C3 40 Q25_1: TEST AL,00000001B ; 1ST 128K RAM CARD IN?
092F A8 01 JZ Q25_2 ; YES-
0931 74 04 ADD BX,128
0933 81 C3 0080 Q25_2: TEST AL,00000010B ; 2ND 128K RAM CARD IN?
0937 A8 02 JZ Q25_3 ; YES-
0939 74 04 ADD BX,256
093B 81 C3 0100 Q25_3: MOV [MEMORY_SIZE],BX ; SET CONTINUOUSE MEMORY SIZE
093F 89 1E 0013 R MOV [TRUE_MEM],BX ; SET TOTAL MEMORY WORD
0943 89 1E 0015 R ;----- SIZE HAS BEEN DETERMINED, NOW TEST OR CLEAR ALL OF MEMORY
          MOV AX,B ; DISPLAY GOOD MEMORY SIZE
0947 88 0008 CALL MSIZE
094A E8 09D5 R XOR AX,AX
094D 33 C0 MOV DX,0200H ; SET START ADDR (&K BYTE)
094F BA 0200 MOV ES,DX
0952 8E C2 MOV CX,7000H ; TEST 56K BYTES
0954 B9 7000 XOR DX,DX
0957 33 D2 Q26: PUSH DX
0959 52 PUSH BX
095A 53 PUSH AX
095B 50 IN AL,PORT_A
095C E4 60 OR AL,01000000B ; SET RETENTION TEST REQ'ED FLAG
095E 0C 40

          OUT PORT_A,AL ; TEST OR FILL MEM
0960 E6 60 CALL PODSTG
0962 E8 0DEA R POP AX
0965 58 POP BX
0966 5B POP DX
0967 5A POP DX
0968 75 27 JNZ Q31 ; ERROR
096A 05 0040 Q27: ADD AX,64 ; DISPLAY GOOD MEMORY SIZE
096D E8 09D5 R CALL MSIZE
0970 80 C6 10 ADD DH,10H
0973 8E C2 MOV ES,DX
0975 B9 8000 MOV CX,8000H ; TEST SIZE 64K
0978 3B C3 CMP AX,BX ; END OF MEMORY ?
097A 75 DD JNE Q26 ; NO -
097C E9 0A11 R JMP Q43 ; GOTO NEXT TEST
;----- ON ENTRY TO MEMORY ERROR ROUTINE, CX HAS ERROR BITS
; AH HAS ODD/EVEN INFO, OTHER USEFUL INFO ON THE STACK
097F 80 FB 10 Q29: CMP BL,10H ; BASE 32K VRAM ?
0982 75 04 JNE Q30 ; NO -
0984 B7 1E Q29_1: MOV BH,1EH ; ERROR CODE - BASE 32K VRAM
0986 EB 2F JMP SHORT Q33
0988 80 FF 10 Q30: CMP BH,10H ; BASE 32K VRAM ?
098B 72 F7 JB Q29_1 ; YES-
098D B7 1F MOV BH,1FH ; ERROR CODE - 32K VRAM CARD
098F EB 26 JMP SHORT Q33

0991 E4 60 Q31: IN AL,PORT_A
0993 A8 03 TEST AL,00000011B ; 128K RAM CARD IN ?
0995 74 1A JZ Q32 ; NO -
0997 50 PUSH AX ; SAVE
0998 51 PUSH CX
0999 B1 0D MOV CL,13 ; FIND END ADDR OF 128K RAM CARDS
099B D3 E0 SHL AX,CL
099D 3B D0 CMP DX,AX ; 128K RAM CARD AREA ?
099F 59 POP CX ; RESTORE
09A0 58 POP AX
09A1 73 0E JAE Q32
09A3 8A D9 MOV BL,CL ; YES-FORM ERROR BITS ("XX")
09A5 0A DD OR BL,CH
09A7 B1 04 MOV CL,4 ; ROTATE MOST SIGNIFIGANT
; NIBBLE OF SEGMENT
; TO LOW NIBBLE OF DH
09A9 D2 EE SHR DH,CL
09AB B7 10 MOV BH,10H
09AD 0A FE OR BH,DH ; FORM "1Y" VALUE
09AF EB 1E JMP SHORT Q35
09B1 B7 0A Q32: MOV BH,0AH ; ERROR 0A....
09B3 A8 08 TEST AL,00001000B ; TEST FOR 64K RAM CARD PRESENT
09B5 75 06 JNZ Q34 ; WORRY ABOUT ODD/EVEN IF IT IS
09B7 8A D9 Q33: MOV BL,CL
09B9 0A DD OR BL,CH ; COMBINE ERROR BITS IF IT ISN'T
09BB EB 12 JMP SHORT Q35
09BD 80 FC 02 Q34: CMP AH,02 ; EVEN BYTE ERROR? ERR 0AXX
09C0 8A D9 MOV BL,CL
09C2 74 0B JE Q35

09C4 FE C7 INC BH ; MAKE INTO 0BXX ERR
09C6 0A DD OR BL,CH ; MOVE AND COMBINE ERROR BITS
09C8 80 FC 01 CMP AH,1 ; ODD BYTE ERROR
09CB 74 02 JE Q35

```

09CD FE C7  
 09CF BE 005E R  
 09D2 E8 0D32 R

```

INC BH ; MUST HAVE BEEN BOTH
; - MAKE INTO 0CXX
Q35: MOV SI,OFFSET MEM_ERR ;
CALL E_MSG ; LET ERROR ROUTINE FIGURE OUT
; WHAT TO DO

```

```

-----
; SUBROUTINE
; MEMORY SIZE PRINT ON CRT
; DESCRIPTION
; PRINT TESTED MEMORY OK MSG ON THE CRT
; CALL PARMS: AX = K OF GOOD MEMORY (IN HEX)
-----

```

09D5  
 09D5 53  
 09D6 51  
 09D7 52  
 09D8 50  
 09D9 B4 02  
 09DB BA 0921  
 09DE B7 00  
 09E0 CD 10  
 09E2 58  
 09E3 50  
 09E4 BB 000A  
 09E7 B9 0003  
 09EA 33 D2  
 09EC F7 F3  
 09EE 80 CA 30  
 09F1 52  
 09F2 E2 F6  
 09F4 B9 0003  
 09F7 58  
 09F8 E8 0FA1 R  
 09FB E2 FA  
 09FD B9 0003  
 0A00 BE 0056 R  
 0A03 2E: 8A 04  
 0A06 46  
 0A07 E8 0FA1 R  
 0A0A E2 F7  
 0A0C 58  
 0A0D 5A  
 0A0E 59  
 0A0F 5B  
 0A10 C3  
 0A11

```

MSIZE PROC NEAR
PUSH BX
PUSH CX
PUSH DX
PUSH AX ; SAVE WORK REGS
MOV AH,2 ; SET CURSOR TOWARD THE END OF
MOV DX,0921H ; (ROW 9, COL. 33)
MOV BH,0 ; PAGE 0
INT INT_10 ;
POP AX ;
PUSH AX ;
MOV BX,10 ; SET UP FOR DECIMAL CONVERT
MOV CX,3 ; OF 3 NIBBLES
Q36: XOR DX,DX ;
DIV BX ; DIVIDE BY 10
OR DL,30H ; MAKE INTO ASCII
PUSH DX ; SAVE
LOOP Q36 ;
MOV CX,3 ;
Q37: POP AX ; RECOVER A NUMBER
CALL PRT_HEX
LOOP Q37
MOV CX,3
Q38: MOV SI,OFFSET F3B ; PRINT " KB"
INC SI
CALL PRT_HEX
LOOP Q38
POP AX
POP DX
POP CX
POP BX
Q35E: RET
MSIZE ENDP

```

0A11

```

-----
Q43:
; TEST 16
; KEYBOARD TEST
; DESCRIPTION
; NMI HAS BEEN ENABLED FOR QUITE A FEW
; SECONDS NOW. CHECK THAT NO SCAN CODES
; HAVE SHOWN UP IN THE BUFFER. (STUCK
; KEY) IF THEY HAVE, DISPLAY THEM AND
; POST ERROR.
; MFG ERROR CODE = 2000 STRAY NMI INTERRUPTS OR KBD RCV ERRORS
; 21XX CARD FAILURE
; XX=01, KB DATA STUCK HIGH
; XX=02, KB DATA STUCK LOW
; XX=03, NO NMI INTERRUPT
-----

```

= 0070

0A11 E8 0F7C R  
 0A14 E8 0000 E  
 0A17 BA 0201  
 0A1A EC  
 0A1B 24 F0  
 0A1D 74 06  
 0A1F E4 62  
 0A21 24 80  
 0A23 75 03  
 0A25 EB 7D 90  
 0A28 E4 61  
 0A2A 24 FC  
 0A2C E6 61  
 0A2E 80 B6  
 0A30 E6 43  
 0A32 80 40  
 0A34 E6 A0  
 0A36 80 20  
 0A38 BA 0042  
 0A3B EE  
 0A3C 2B C0  
 0A3E 8B C8  
 0A40 EE  
 0A41 E4 61  
 0A43 0C 01  
 0A45 E6 61  
 0A47 E4 62  
 0A49 24 40  
 0A4B 75 06  
 0A4D E2 F8  
 0A4F B3 02  
 0A51 EB 49  
 0A53 06

```

INT_70 EQU 70H
ASSUME DS:DATA
CALL MFG_UP ; SEND 'EF' TO MFG_TESTER
CALL DDS ; ESTABLISH ADDRESSING
;----- CHECK LINK CARD, IF PRESENT
MOV DX,JOY_PORT ;
IN AL,DX ; BURN-IN MODE ?
AND AL,0F0H ;
JZ F6 ; YES-BYPASS CHECK
IN AL,PORT_C ; KEYBOARD CABLE ATTACHED ?
AND AL,10000000B ;
JNZ F7 ; NO -
JMP F6_X ; YES-BYPASS CHECK
F6:
IN AL,PORT_B ;
AND AL,1111100B ; DROP SPEAKER DATA
OUT PORT_B,AL ;
MOV AL,0B6H ; MODE SET TIMER 2
OUT TIM_CTL,AL ;
MOV AL,040H ; DISABLE NMI
OUT NMI_PORT,AL ;
MOV AL,32 ; LSB TO TIMER 2
; (APPROX. 40kHz VALUE)
F7:
MOV DX,TIMER+2 ;
OUT DX,AL ;
SUB AX,AX ;
MOV CX,AX ;
OUT DX,AL ; MSB TO TIMER 2 (START TIMER)
IN AL,PORT_B ;
OR AL,1 ;
OUT PORT_B,AL ; ENABLE TIMER 2
IN AL,PORT_C ; SEE IF KEYBOARD DATA ACTIVE
AND AL,01000000B ;
JNZ F7_1 ; EXIT LOOP IF DATA SHOWED UP
LOOP F7_0 ;
MOV BL,02H ; SET NO KEYBOARD DATA ERROR
SHORT F6_1 ; (2102)
F7_1: PUSH ES ; SAVE ES

```

Appendix A.

```

0A54 2B C0          SUB     AX,AX          ; SET UP SEGMENT REG
0A56 8E C0          MOV     ES,AX          ; *
0A58 26: C7 06 0008 R 0599 C  MOV     ES:[NMI_PTR],OFFSET D11 ; SET UP NEW NMI VECTOR
0A5F A2 0084 R      MOV     INTR_FLAG,AL   ; RESET INTR FLAG
0A62 E4 61          IN      AL,PORT_B     ; DISABLE INTERNAL BEEPER TO
0A64 0C 30          OR      AL,00110000B  ; PREVENT ERROR BEEP
0A66 E6 61          OUT     PORT_B,AL
0A68 80 C0          MOV     AL,0C0H       ;
0A6A E6 A0          OUT     NMI_PORT,AL  ; ENABLE NMI
0A6C B9 0100        MOV     CX,0100H      ;
0A6F E2 FE          LOOP   $              ; WAIT A BIT
0A71 E4 61          IN      AL,PORT_B     ; RE-ENABLE BEEPER
0A73 24 CF          AND     AL,11001111B
0A75 E6 61          OUT     PORT_B,AL
0A77 A0 0084 R      MOV     AL,INTR_FLAG  ; GET INTR FLAG
0A7A 0A C0          OR      AL,AL         ; WILL BE NON-ZERO IF NMI HAPPENED
0A7C 83 03          MOV     BL,03H        ; SET POSSIBLE ERROR CODE
0A7E 26: C7 06 0008 R 0000 C  MOV     ES:[NMI_PTR],OFFSET KBDNMI ; RESET NMI VECTOR
0A85 07             POP     ES             ; RESTORE ES
0A86 74 14          JZ      F6_1          ; JUMP IF NO NMI (2103)
0A88 B0 00          MOV     AL,00H        ; DISABLE FEEDBACK CKT
0A8A E6 A0          OUT     NMI_PORT,AL  ;
0A8C E4 61          IN      AL,PORT_B     ;
0A8E 24 FE          AND     AL,11111110B  ; DROP GATE TO TIMER 2
0A90 E6 61          OUT     PORT_B,AL
0A92 E4 62          IN      AL,PORT_C     ; SEE IF KEYBOARD DATA ACTIVE
0A94 24 40          AND     AL,01000000B
0A96 74 0C          JZ      F6_X          ; EXIT LOOP IF DATA WENT LOW
0A98 E2 F8          LOOP   F6_2          ;
0A9A B3 01          MOV     BL,01H        ; SET KEYBOARD DATA STUCK HIGH ERR
                                ; (2101)
0A9C B7 21          MOV     BH,21H        ; POST ERROR "21XX"
0A9E BE 005F R      MOV     SI,OFFSET KEY_ERR ; GET MSG ADDR
0AA1 E8 0D32 R      CALL    E_MSG         ; PRINT MSG ON SCREEN

0AA4 B0 00          MOV     AL,00H        ; DISABLE FEEDBACK CKT
0AA6 E6 A0          OUT     NMI_PORT,AL  ;

0AAB B8 FF20        MOV     AX,0FF20H     ; INITIALIZE KANA-KANJI FUNCTION
0AAB CD 70          INT     INT_70        ;

0AAD B8 0703        MOV     AX,0703H     ; DISABLE INDICATOR ROW DISPLAY
0AB0 CD 16          INT     INT_16        ;

;-----
; TEST 17
; CASSETTE INTERFACE TEST
; DESCRIPTION
; TURN CASSETTE MOTOR OFF. WRITE A BIT OUT TO THE
; CASSETTE DATA BUS. VERIFY THAT CASSETTE DATA
; READ IS WITHIN A VALID RANGE.
; MFG ERROR CODE = 2300 (DATA PATH ERROR)
; 23FF (RELAY FAILED TO PICK)
;-----
= 0A9A
= 08AD
MAX_PERIOD EQU 0A9AH ;NOM.+10X
MIN_PERIOD EQU 08ADH ;NOM -10X
;----- TURN THE CASSETTE MOTOR OFF
CALL MFG_UP ; SEND 'EE' TO MFG_TESTER
IN AL,PORT_B
OR AL,00001001B ; SET TIMER 2 SPK OUT, AND CASSETTE
OUT PORT_B,AL ; OUT BITS ON, CASSETTE MOT OFF
;----- WRITE A BIT
IN AL,INTA01
OR AL,01H ; DISABLE TIMER INTERRUPTS
OUT INTA01,AL
MOV AL,0B6H ; SEL TIM 2, LSB, MSB, MD 3
OUT TIMER+3,AL ; WRITE 8253 CMD/MODE REG
MOV AX,1234 ; SET TIMER 2 CNT FOR 1000 USEC
OUT TIMER+2,AL ; WRITE TIMER 2 COUNTER REG
MOV AL,AH ; WRITE MSB
OUT TIMER+2,AL
SUB CX,CX ; CLEAR COUNTER FOR LONG DELAY
LOOP $ ; WAIT FOR COUNTER TO INIT
;----- READ CASSETTE INPUT
IN AL,PORT_C ; READ VALUE OF CASS IN BIT
AND AL,10H ; ISOLATE FROM OTHER BITS
MOV LAST_VAL,AL
CALL READ_HALF_BIT ; TO SET UP CONDITIONS FOR CHECK
CALL READ_HALF_BIT
JCXZ F8 ; CAS_ERR
PUSH BX ; SAVE HALF BIT TIME VALUE
CALL READ_HALF_BIT
POP AX ; GET TOTAL TIME
JCXZ F8 ; CAS_ERR
ADD AX,BX
CMP AX,MAX_PERIOD
JAE F8 ; CAS_ERR
CMP AX,MIN_PERIOD
JB F8 ;
MOV DX,JOY_PORT
IN AL,DX
AND AL,0F0H ; DETERMINE MODE
CMP AL,00010000B ; MFG?
JE F9
CMP AL,01000000B ; SERVICE?
JNE T16_END ; GO TO NEXT TEST IF NOT

;----- CHECK THAT CASSETTE RELAY IS PICKING (CAN'T DO TEST IN NORMAL
; MODE BECAUSE OF POSSIBILITY OF WRITING ON CASSETTE IF "RECORD"
; BUTTON IS DEPRESSED.)
F9: IN AL,PORT_B
MOV DL,AL ; SAVE PORT B CONTENTS
AND AL,11100101B ; SET CASSETTE MOTOR ON
OUT PORT_B,AL

```

```

080A 33 C9          XOR     CX,CX
080C E2 FE          LOOP    $           ; WAIT FOR RELAY TO SETTLE
080E E8 0000 E      CALL   READ_HALF_BIT
0811 E8 0000 E      CALL   READ_HALF_BIT
0814 8A C2          MOV     AL,DL       ; DROP RELAY
0816 E6 61          OUT    PORT_B,AL
0818 E3 0E          JCXZ   T16_END     ; READ_HALF_BIT SHOULD TIME OUT IN
                                ; THIS SITUATION
                                ; ERROR 23FF
081A BB 23FF        MOV     BX,23FFH
081D EB 03          JMP     SHORT F81
081F
F8:
081F BB 2300        MOV     BX,2300H   ; CAS_ERR
                                ; ERR_CODE 2300H
0822 BE 0060 R      MOV     SI,OFFSET CASS_ERR ; CASSETTE WRAP FAILED
0825 E8 0D32 R      CALL   E_MSG      ; GO PRINT ERROR MSG
0828 E4 21          T16_END: IN AL,INTA01 ;
082A 24 FE          AND    AL,0FEH    ; ENABLE TIMER INTS
082C E6 21          OUT    INTA01,AL
082E E4 A0          IN     AL,NMI_PORT ; CLEAR NMI FLIP/FLOP
0830 B0 80          MOV     AL,80H    ; ENABLE NMI INTERRUPTS
0832 E6 A0          OUT    NMI_PORT,AL

```

```

;-----
; TEST 18
; SERIAL PORT (2FX) TEST
; DESCRIPTION:
; CHECKS IF THE SERIAL CARD IS ATTACHED. IF NOT, EXITS.
; VERIFIES THAT THE SERIAL CARD UART FUNCTIONS PROPERLY.
; ERROR CODES RETURNED BY 'UART' RANGE FROM 1 TO 1FH AND
; ARE REPORTED VIA REGISTER DL. SEE LISTING OF 'UART'
; FOR POSSIBLE ERRORS.
; MFG. ERROR CODES = 23XX FOR SERIAL PORT 2FX
;-----

```

```

ASSUME CS:CODE,DS:DATA
;-----

```

```

; TEST SERIAL CARD INS8250 UART (2FX)
;-----

```

```

0834 E8 0F7C R      CALL   MFG_UP      ; SEND 'ED' TO MFG TESTER
0837 E8 0000 E      CALL   DDS        ; POINT TO DATA AREA
083A E4 62          IN     AL,PORT_C  ; TEST FOR SERIAL CARD PRESENT
083C 24 02          AND    AL,0000010B ; ONLY CONCERNED WITH BIT 1
083E 75 17          JNZ   TM         ; IT'S NOT THERE
0840 BA 02F8        MOV     DX,RS232_2_PORT ; ADDRESS OF SERIAL CARD
0843 89 16 0000 R   MOV     RS232_BASE,DX ; SAVE RS232 CARD ADDR
0847 80 0E 0011 R 02 OR     BYTE PTR EQUIP_FLAG+1,02H ; SET EQUIPMENT FLAG
084C E8 111D R      CALL   UART       ; ASYNCH. COMM. ADAPTER TEST
084F 73 06          JNC   TM         ; PASSED
0851 BE 0061 R      MOV     SI,OFFSET COM1_ERR ; CODE FOR DISPLAY
0854 E8 0D32 R      CALL   E_MSG      ; REPORT ERROR
0857
TM:

```

```

;-----
; TEST 19
; PARALLEL PORT TEST
; MFG ERROR CODE = 28XX DATA PORT WRAP ERROR
; 29XX CONTROL PORT WRAP ERROR
; (XX ERROR BITS)
;-----

```

```

ASSUME CS:CODE,DS:DATA ;

```

```

0857 E8 0F7C R      CALL   MFG_UP      ; SEND 'EC' TO MFG TESTER
085A E8 0000 E      CALL   DDS        ; POINT TO DATA AREA

```

```

;----- CHECK DATA PORT

```

```

085D 0E          PUSH   CS         ; SET DS
085E 07          POP    ES
085F BF 0BAC R   MOV     DI,OFFSET PPD ; SET TEST PATARN ADDR (DATA)
0862 BA 037A    MOV     DX,PARAL_PORT+2 ; SET CONTROL PORT
0865 B0 08          MOV     AL,08H    ; SET SELECT IN SIGNAL
0867 EE          OUT    DX,AL
0868 B2 78          MOV     DL,LOW_PARAL_PORT ; SET DATA PORT
086A B7 28          MOV     BH,28H   ; SET ERROR CODE 28XX
086C B9 0004    MOV     CX,4     ; SET LOOP COUNTER
086F 26: 8A 05    PP1:   MOV     AL,ES:[DI] ; LOAD TEST PATRAN
0872 47          INC    DI
0873 8A E0          MOV     AH,AL
0875 EE          OUT    DX,AL    ; OUT DATA
0876 50          PUSH  AX        ; DELAY
0877 58          POP   AX
0878 EC          IN     AL,DX    ; INPUT DATA
0879 32 E0          XOR    AH,AL    ; COMPARE DATA
087B 75 37          JNZ   PERR     ; OK ?
087D E2 F0          LOOP  PP1

```

```

;----- CHECK CONTROL PORT

```

```

087F BA 0201    MOV     DX,JOY_PORT ; SERVICE MODE LOOP ?
0882 EC          IN     AL,DX
0883 24 F0          AND    AL,0F0H
0885 3C 20          CMP    AL,00100000B ;
0887 74 08          JE     PP2       ; YES-
0889 81 3E 0072 R 3412 CMP    RESET_FLAG,3412H ; WARM START WITH "CTRL+ALT+INS"?
088F 75 2B          JNE   PP4       ; NO -GOTO NEXT TEST
0891 BA 037A    PP2:   MOV     DX,PARAL_PORT+2 ; SET CONTROL PORT WRITE
0894 B7 29          MOV     BH,29H   ; SET ERROR CODE 29XX
0896 B1 04          MOV     CL,4     ; SET LOOP COUNTER
0898 26: 8A 05    PP3:   MOV     AL,ES:[DI] ; LOAD TEST PATRAN
089B 47          INC    DI
089C 8A E0          MOV     AH,AL
089E EE          OUT    DX,AL    ; DATA SAVE
089F 50          PUSH  AX        ; OUT DATA
08A0 58          POP   AX        ; DELAY
08A1 EC          IN     AL,DX    ; INPUT DATA
08A2 24 1F          AND    AL,1FH   ; DROP BIT 7-5
08A4 32 E0          XOR    AH,AL    ; COMPARE DATA

```





```

0C41 24 40          AND    AL,01000000B    ; HAS WATCHDOG GONE OFF?
0C43 75 04          JNZ    F11             ; PROCEED IF IT HAS
0C45 83 03          MOV    BL,03H         ; SET ERROR CODE
0C47 EB 11          JMP    SHORT F13
0C49 B0 80          MOV    AL,FDC_RESET  ;
0C4B E6 F2          OUT    NEC_CTL,AL     ; DISABLE WATCHDOG TIMER
0C4D B4 00          MOV    AH,0           ; RESET NEC FDC
0C4F 8A D4          MOV    DL,AH          ; SET FOR DRIVE 0
0C51 CD 13          INT    INT_13         ; VERIFY STATUS AFTER RESET
0C53 F6 C4 FF       TEST   AH,0FFH        ; STATUS OK?
0C55 B3 01          MOV    BL,01H        ; SET ERROR CODE
0C58 74 08          JZ     F14            ; YES-
0C5A B7 26          MOV    BH,26H        ; DSK_ERR:(26XX)
0C5C BE 065 R       MOV    SI,OFFSET DISK_ERR ; GET ADDR OF MSG
0C5F E8 0D32 R      CALL   E_MSG          ; GO PRINT ERROR MSG
;-----
0C62 B8 0181       MOV    AX,0000000110000001B ; AH-DRIVE CONFIG. FLAG
;                                     ; AL-TURN MOTOR ON
0C65 B2 00          MOV    DL,0           ; SELECT DRIVE 0
0C67 E6 F2          OUT    NEC_CTL,AL     ; WRITE FDC CONTROL REG
0C69 50             PUSH   AX             ;
0C6A 52             PUSH   DX             ;
;
0C6B 2B C9          SUB    CX,CX          ; WAIT FOR 0.5 SECOND
0C6D E2 FE          LOOP  $              ;
0C6F E2 FE          LOOP  $              ;
0C71 B5 01          MOV    CH,1           ; SELECT TRACK 1
0C73 88 16 003E R  MOV    SEEK_STATUS,DL ;
0C77 E8 0000 E     CALL  SEEK           ; RECALIBRATE DISKETTE
0C7A 5A             POP    DX             ;
0C7B 58             POP    AX             ;
0C7C 72 13          JC    F14_2          ;
0C7E 08 26 0302 R DR     BYTE PTR JEQUIP_FLAG,AH ; SET DISKETTE CONFIG.
0C82 B1 06          MOV    CL,6           ; SET 0 OF DISKETTE
0C84 8A EA          MOV    CH,DL          ;
0C86 D2 E5          SHL   CH,CL          ;
0C88 80 26 0010 R 3F AND   BYTE PTR EQUIP_FLAG,3FH ;
0C8D 08 2E 0010 R DR     BYTE PTR EQUIP_FLAG,CH ;
0C91 D1 E0          F14_2: SHL   AX,1     ;
0C93 80 E4 FE       AND   AH,11111110B   ;
0C96 0C 80          OR    AL,FDC_RESET   ;
0C98 42             INC   DX             ;
0C99 80 FA 04       CMP   DL,4           ;
0C9C 72 C9          JB    F14_1          ;
0C9E F6 06 0302 R 0F TEST  BYTE PTR JEQUIP_FLAG,0FH ; DISKETTE DRIVE ATTACHED ?
0CA3 74 05          JZ    F14_3          ; NO -
0CA5 80 0E 0010 R 01 OR     BYTE PTR EQUIP_FLAG,01H ; SET IPL DISKETTE INDICATOR
;-----
0CAA B0 80          F14_3: MOV    AL,FDC_RESET   ; TURN DRIVE 0 MOTOR OFF
0CAC E6 F2          OUT    NEC_CTL,AL     ;
0CAE C6 06 0084 R 00 MOV    INTR_FLAG,00H  ; SET STRAY INTERRUPT FLAG = 00
0CB3 BF 0078 R      MOV    DI,OFFSET PRINT_TIM_OUT ;SET DEFAULT PRT TIMEOUT
0CB6 1E             PUSH  DS             ;
0CB7 07             POP   DS             ;
0CB8 B8 1414       MOV    AX,1414H      ; DEFAULT=20
0CBB AB             STOSW ;
0CBC AB             STOSW ;
0CBD B8 0101       MOV    AX,0101H     ;RS232 DEFAULT=01
0CC0 AB             STOSW ;
0CC1 AB             STOSW ;
;
0CC2 E4 21          IN     AL,INTA01     ;
0CC4 24 FE          AND   AL,0FEH       ; ENABLE TIMER INT. (LVL 0)
0CC6 E6 21          OUT    INTA01,AL     ;
;
0CC8 58             POP    AX             ; RESTORE & SAVE CONF. FLAG
0CC9 50             PUSH   AX             ;
0CCA A8 80          TEST  AL,10000000B   ; PCJR MODE ?
0CCC 75 20          JNZ   F17            ; YES-BYPASS BEEP
;
0CCE 1E             PUSH  DS             ;
0CCF E8 0FE2 R     CALL  DDX            ;
0CD2 80 3E 0018 R 00 CMP    POST_ERR,00H  ; CHECK FOR "POST_ERR" NON-ZERO
;
0CD7 1F             POP   DS             ; RESTORE DS
0CD8 74 0F          JE    F16            ; CONTINUE IF NO ERROR
;
0CDA B2 02          MOV    DL,2           ;
0CDC E8 0FAB R     CALL  ERR_BEEP      ; 2 SHORT BEEPS (ERROR)
0CDF             ERR_WAIT:
0CDF B4 00          MOV    AH,00         ;
0CE1 CD 16          INT    INT_16        ; WAIT FOR "ENTER" KEY
0CE3 3C 0D          CMP    AL,0DH        ;
0CE5 75 F8          JNE   ERR_WAIT      ;
0CE7 EB 05          JMP    SHORT F17     ;
;-----
0CE9 B2 01          F16:  MOV    DL,1           ; 1 SHORT BEEP (NO ERRORS)
0CEB E8 0FAB R     CALL  ERR_BEEP      ;
;-----
0CEE 58             F17:  POP    AX             ; RESTORE CONTENTS OF CONF. FLAG
0CEF 86 E0          XCHG  AH,AL         ; (SAVED AT LABEL F7B)
;-----
0CF1 B0 00          MOV    AL,0           ; CLEAR KBD PORT
0CF3 E6 60          OUT    PORT_A,AL     ;
;
0CF5 BA 0201       MOV    DX,JOY_PORT   ; GET MFG./ SERVICE MODE INFO
0CF8 EC             IN     AL,DX         ;
0CF9 24 F0          AND   AL,0F0H        ; IS HIGH ORDER NIBBLE = 0?
0CFB 75 03          JNZ   F19            ; (BURN-IN MODE)
0CFD E9 006B R     JMP    START         ; YES-GOTO BEGINNING OF POST
0D00 3C 20          F18:  CMP    AL,00100000B ; SERVICE MODE LOOP?
0D02 74 F9          F19:  JE     F18           ; YES-BRANCH TO START
0D04 81 3E 0072 R 4321 CMP    RESET_FLAG,4321H ; DIAG. CONTROL PROGRAM START?

```

Appendix A.

```

0D0A 74 19          JE      F21          ; YES-
0D0C 3C 10          CMP     AL,00010000B ; MFG DCP RUN REQUEST?
0D0E 74 15          JE      F21          ; YES-

;----- GOTO PCJR SYSTEM CARTRIDGE
0D10 F6 C4 80      TEST   AH,10000000B ; PCJR MODE ?
0D13 74 03          JZ     F20          ; NO
0D15 E9 0349 R     JMP     SYSSWAP     ; YES-GO SYSTEM SWAP RTN.

;----- GOTO BOOT LOADER
0D18 B8 0019      F20:  MOV   AX,0019H   ; CLEAR SCREEN (GRAPHIC MODE)
0D1B CD 10        INT    INT_10      ; (MODE: 320 X 200 - COLOR)

0D1D C7 06 0072 R 1234  MOV   RESET_FLAG,1234H ; SET WARM START INDICATOR IN CASE
0D23 CD 19        INT    INT_19      ; OF CARTRIDGE RESET
; GOTO THE BOOT LOADER

;----- GOTO DIAGNOSTIC TESTS OR MFG TESTS
0D25 FA          F21:  CLI     DS:AB50    ; DISABLE EXTERNAL INTERRUPTS
0D26 2B C0        SUB     AX,AX
0D28 8E D8        MOV     DS,AX      ; RESET TIMER INT.
0D2A C7 06 0020 R 0000 E C  MOV   WORD PTR INT_PTR,OFFSET TIMER_INT ;
0D30 CD 80        INT    INT_80      ; ENTER DCP THROUGH INT. 80H

;-----
; SUBROUTINE
; GENERAL ERROR HANDLER FOR THE POST
; ENTRY REQUIREMENTS:
; SI = OFFSET(ADDRESS) OF MESSAGE BUFFER
; BX= ERROR CODE FOR MANUFACTURING OR SERVICE MODE
; REGISTERS ARE NOT PRESERVED
; LOCATION "POST_ERR" IS SET NON-ZERO IF AN ERROR OCCURS IN
; CUSTOMER MODE
; SERVICE/MANUFACTURING FLAGS AS FOLLOWS: (HIGH NIBBLE OF PORT 201)
; 0000 = MANUFACTURING MODE (BURN-IN)
; 0001 = MANUFACTURING MODE (SYSTEM TEST)
; 0010 = SERVICE MODE (LOOP POST)
; 0100 = SERVICE MODE (SYSTEM TEST)
;-----
E_MSG  PROC      NEAR
0D32 8A C7        MOV     AL,BH      ; SEND DATA TO ADDR 11 & 12
0D34 E6 11        OUT    MFG_PORT+1,AL ; SEND HIGH BYTE
0D36 8A C3        MOV     AL,BL      ;
0D38 E6 12        OUT    MFG_PORT+2,AL ; SEND LOW BYTE

0D3A 8A 0201      MOV     DX,JOY_PORT ;
0D3D EC          IN     AL,DX       ; GET MODE BITS
0D3E 24 F0        AND    AL,0F0H    ; ISOLATE BITS OF INTEREST
0D40 74 65        JZ     MFG_OUT    ; MANUFACTURING MODE (BURN-IN)
0D42 3C 10        CMP    AL,00010000B ;
0D44 74 61        JE     MFG_OUT    ; MFG. MODE (SYSTEM TEST)
0D46 8A F0        MOV     DH,AL     ; SAVE MODE
0D48 80 FF 0A     CMP    BH,0AH     ; PRE-CRT ATTACHMENT TEST ERROR ?
0D4B 72 72        JB     BEEPS     ; YES-DO BEEP OUTPUT
0D4D 53          PUSH  BX         ; SAVE ERROR AND MODE FLAGS
0D4E 56          PUSH  SI
0D4F 52          PUSH  DX
0D50 84 02        MOV    AH,2      ; SET CURSOR
0D52 BA 0801      MOV    DX,0801H  ; ROW 8, COL. 1
0D55 B7 00        MOV    BH,0      ; PAGE 0
0D57 CD 10        INT    INT_10    ;
0D59 BE 0059 R    MOV    SI,OFFSET ERROR_ERR
0D5C B9 0005      MOV    CX,5      ; PRINT WORD "ERROR"
0D5F 2E: 8A 04     EM_0: MOV    AL,CS:[SI]
0D62 46          INC    SI
0D63 E8 0FA1 R    CALL  PRT_HEX
0D66 E2 F7        LOOP  EM_0

;----- LOOK FOR A BLANK SPACE TO POSSIBLY PUT CUSTOMER LEVEL ERRORS
; IN CASE OF MULTI ERROR)
0D68 B2 07        MOV    DL,7      ; SET CURSOR
0D6A B4 02        MOV    AH,2      ; ROW 8, COL 7 (OR ABOVE, IF
0D6C B7 00        MOV    BH,0      ; MULTIPLE ERRORS)
0D6E CD 10        INT    INT_10    ;
0D70 B4 08        MOV    AH,8      ; READ CHARACTER THIS POSITION
0D72 CD 10        INT    INT_10    ;
0D74 3C 47        CMP    AL,'G'    ; ERROR "G" ?

0D76 74 06        JE     EM_1_0    ; YES-
0D78 42          INC    DX        ; POINT TO NEXT POSITION
0D79 42          INC    DX
0D7A 3C 20        CMP    AL,' '    ; BLANK?
0D7C 75 EC        JNE   EM_1      ; GO CHECK NEXT POSITION, IF NOT
0D7E 5A          POP    DX        ; RECOVER ERROR POINTERS
0D7F 5E          POP    SI
0D80 5B          POP    BX
0D81 80 FE 20     CMP    DH,00100000B ; SERVICE MODE?
0D84 74 12        JE     SVC_OUT   ;
0D86 80 FE 40     CMP    DH,01000000B ;
0D89 74 0D        JE     SVC_OUT   ;
0D8B 2E: 8A 04     MOV    AL,CS:[SI] ; GET ERROR CHARACTER
0D8E E8 0FA1 R    CALL  PRT_HEX    ; DISPLAY IT
0D91 80 FF 20     CMP    BH,20H    ; ERROR BELOW 20? (MEM TROUBLE?)
0D94 73 1F        JAE   EM_2      ; NO
0D96 EB 0A        JMP   SHORT BP2  ; YES-HALT SYSTEM IF SO.

0D98 8A C7        SVC_OUT:MOV  AL,BH ; PRINT MSB
0D9A E8 0F90 R    CALL  XPC_BYTE  ; DISPLAY IT
0D9D 8A C3        MOV    AL,BL    ; PRINT LSB
0D9F E8 0F90 R    CALL  XPC_BYTE

0DA2 B2 02        BP2:  MOV    DL,2   ; 2 SHORT BEEPS
0DA4 E8 0FAB R    CALL  ERR_BEEP ;
0DA7

MFG_OUT:

```

```

ODA7 FA
ODA8 E4 61
ODAA 24 FC
ODAC E6 61
ODAE 2A C0
ODB0 E6 F2
ODB2 E6 A0
ODB4 F4

```

```

ODB5 1E
ODB6 EB 0FE2 R
ODB9 88 3E 0018 R
ODBD 1F

```

```
ODDE C3
```

```

ODBF FA
ODC0 8C C8
ODC2 8E D0

```

```

ODC4 B2 02
ODC6 BC 0050 R
ODC9 B3 01
ODCB E9 0FC0 R
ODCE 33 C9
ODD0 E2 FE

```

```

ODD2 FE CA
ODD4 75 F3
ODD6 80 FF 05
ODD9 75 CC
ODDB 80 FE 20
ODEE 74 05
ODE0 80 FE 40
ODE3 75 C2
ODE5 B3 01
ODE7 E9 0FC0 R
ODEA

```

```

TOTLTP0:CLI
IN AL,PORT_B ; DISABLE INTS.
AND AL,0FCH ; DISABLE SOUND
OUT PORT_B,AL
SUB AL,AL ; STOP DISKETTE MOTOR
OUT NEC_CTL,AL
OUT NMI_PORT,AL ; DISABLE NMI
HLT ; HALT

```

```

EM_2: ASSUME DS:XXDATA ;
PUSH DS
CALL DDX ;
MOV POST_ERR,BH ; SET ERROR FLAG NON-ZERO
POP DS
ASSUME DS:NOTHING
RET ; RETURN TO CALLER

```

```

BEEPS: CLI
MOV AX,C5 ; SET CODE SEG= STACK SEG
MOV SS,AX ; (STACK IS LOST, BUT THINGS ARE
; OVER, ANYWAY)
MOV DL,2 ; 2 BEEPS
MOV SP,OFFSET EX_0 ; SET DUMMY RETURN
EB: MOV BL,1 ; SHORT BEEP
JMP BEEP ;

```

```

EBO: XOR CX,CX ; WAIT (BEEP OFF)
LOOP $ ;

```

```

DEC DL ; DONE YET?
JNZ EB ; LOOP IF NOT
CMP BH,05H ; 64K CARD ERROR?
JNE TOTLTP0 ; END IF NOT
CMP DH,00100000B ; SERVICE MODE?
JE EB1
CMP DH,01000000B ;
JNE TOTLTP0 ; END IF NOT
EB1: MOV BL,1 ; ONE MORE BEEP FOR 64K ERROR IF IN
JMP BEEP ; SERVICE MODE (JUMP TO TOTLTP0)

```

```
E_MSG ENDP
```

```

-----
; SUBROUTINE
; THIS ROUTINE PERFORMS A READ/WRITE TEST ON A BLOCK OF
; STORAGE (MAX. SIZE = 64KB). IF "WARM START", FILL
; BLOCK WITH 0000 AND RETURN.
; DATA PATTERNS USED:
; 0->FF ON ONE BYTE TO TEST DATA BUS
; AAAA,5555,00FF,FF00 FOR ALL WORDS
; FILL WITH 0000 BEFORE EXIT
; ON ENTRY:
; ES = ADDRESS OF STORAGE TO BE TESTED
; DS = ADDRESS OF STORAGE TO BE TESTED
; CX = WORD COUNT OF STORAGE BLOCK TO BE TESTED
; (MAX. = 8000H (32K WORDS))
; ON EXIT:
; ZERO FLAG = OFF IF STORAGE ERROR
; IF ZERO FLAG = OFF, THEN CX = XOR'ED BIT PATTERN
; OF THE EXPECTED DATA PATTERN VS. THE ACTUAL DATA
; READ. (I.E., A BIT "0" IN AL IS THE BIT IN ERROR)
; AH=03 IF BOTH BYTES OF WORD HAVE ERRORS
; AH=02 IF LOW (EVEN) BYTE HAS ERROR
; AH=01 IF HI (ODD) BYTE HAS ERROR
; AX,BX,CX,DX,DI,SI,BP ARE ALL DESTROYED.
-----

```

```

ODEA FC
ODEB 2B FF
ODED 2B C0

```

```

ODEF 8E D8
ODF1 8B 1E 0472 R
ODF5 81 FB 1234
ODF9 8C C2
ODFB 8E DA
ODFD 75 0B
ODFF F3/ AB
OE01 8E D8
OE03 89 1E 0472 R
OE07 8E DA
OE09 C3

```

```

OE0A 81 FB 4321
OE0E 74 EF
OE10 81 FB 3412
OE14 74 E9

```

```

OE16 8B 05
OE18 8A 05
OE1A 32 C4
OE1C 74 05
OE1E 84 00

```

```

OE20 E9 0EAF R
OE23 FE C4
OE25 8A C4
OE27 75 ED

```

```

OE29 8B E9
OE2B 8B AAAA
OE2E 8B D8
OE30 BA 5555
OE33 F3/ AB

```

```

PODSTG PROC NEAR
ASSUME DS:ABS0
CLD ; SET DIRECTION TO INCREMENT
SUB DI,DI ; SET DI=0000 REL. TO START OF SEG
SUB AX,AX ; INITIAL DATA PATTERN FOR 00-FF
; TEST
MOV DS,AX ; SET DS TO ABS0
MOV BX,DATA_WORD[RESET_FLAG-RS232_BASE] ; WARM START?
CMP BX,1234H
MOV DX,ES
MOV DS,DX ; RESTORE DS
JNE P1
P1: REP STOSW ; SIMPLE FILL WITH 0 ON WARM-START
MOV DS,AX
MOV DATA_WORD[RESET_FLAG-RS232_BASE],BX
MOV DS,DX ; RESTORE DS
RET ; AND EXIT
;-----
P1: CMP BX,4321H ; DIAG. RESTART?
JE ; DO FILL WITH ZEROS
CMP BX,3412H ; WARM START WITH RS232C EXT WRAP?
JE ; DO FILL WITH ZEROS
;-----
P2: MOV [DI],AL ; WRITE TEST DATA
MOV AL,[DI] ; GET IT BACK
XOR AL,AH ; COMPARE TO EXPECTED
JZ PY
MOV AH,0 ;
;-----
PY: JMP P8 ; ERROR EXIT IF MISCOMPARE
INC AH ; FORM NEW DATA PATTERN
MOV AL,AH ;
JNZ P2 ; LOOP TILL ALL 256 DATA PATTERNS
; DONE
MOV BP,CX ; SAVE WORD COUNT
MOV AX,0AAAAH ; LOAD DATA PATTERN
MOV BX,AX ;
MOV DX,05555H ; LOAD OTHER DATA PATTERN
REP STOSW ; FILL WORDS FROM LOW TO HIGH
; WITH AAAA

```

Appendix A.

```

DEC DI ; POINT TO LAST WORD WRITTEN
DEC DI
STD ; SET DIRECTION FLAG TO GO DOWN
MOV SI,DI ; SET INDEX REGS. EQUAL
MOV CX,BP ; RECOVER WORD COUNT
; GO FROM HIGH TO LOW
P3: LODSW ; GET WORD FROM MEMORY
XOR AX,BX ; EQUAL WHAT S/B THERE?
JNZ P8 ; GO ERROR EXIT IF NOT
MOV AX,DX ; GET 35 DATA PATTERN
STOSW ; STORE IT IN LOCATION JUST READ
LOOP P3 ; LOOP TILL ALL BYTES DONE
MOV CX,BP ; RECOVER WORD COUNT
CLD ; BACK TO INCREMENT
INC SI ; ADJUST PTRS
INC SI
MOV DI,SI
MOV BX,DX ; S/B DATA PATTERN TO BX
;-----
MOV DX,JOY_PORT ; NORMAL MODE ?
IN AL,DX
MOV DX,BX
AND AL,0F0H
CMP AL,0F0H ; YES-BYPASS 00FF & FF00 PATTERN TEST
JE P4
;-----
MOV DX,00FFH ; DATA FOR CHECKERBOARD PATTERN
PX: LODSW ; GET WORD FROM MEMORY
XOR AX,BX ; EQUAL WHAT S/B THERE?
JNZ P8 ; GO ERROR EXIT IF NOT
MOV AX,DX ; GET OTHER PATTERN
STOSW ; STORE IT IN LOCATION JUST READ
LOOP PX ; LOOP TILL ALL BYTES DONE
MOV CX,BP ; RECOVER WORD COUNT
STD ; DECREMENT
DEC SI ; ADJUST PTRS
DEC SI
MOV DI,SI
MOV BX,DX ; S/B DATA PATTERN TO BX
NOT DX ; MAKE PATTERN FF00
OR DL,DL ; FIRST PASS?
JZ PX ; INCREMENT
CLD
ADD SI,4
NOT DX
MOV DI,SI
MOV CX,BP
P4: ; LOW TO HIGH
LODSW ; GET A WORD
XOR AX,DX ; SHOULD COMPARE TO DX
JNZ P8 ; GO ERROR IF NOT
STOSW ; WRITE 0000 BACK TO LOCATION
; JUST READ
LOOP P4 ; LOOP TILL DONE
STD ; BACK TO DECREMENT
DEC SI ; ADJUST POINTER DOWN TO LAST WORD
DEC SI ; WRITTEN
;----- CHECK IF IN SERVICE/MFG MODES, IF SO, PERFORM REFRESH CHECK
MOV DX,JOY_PORT
IN AL,DX ; GET OPTION BITS
AND AL,0F0H
CMP AL,0F0H ; ALL BITS HIGH=NORMAL MODE
JE P6
IN AL,PORT_A ; RETENTION TEST REQ'ED ?
TEST AL,01000000B ; NO -
JZ P6 ; NO -
;----- WAIT ABOUT 12 SECONDS WITHOUT ACCESSING MEMORY
; IF REFRESH IS NOT WORKING PROPERLY, THIS SHOULD
; BE ENOUGH TIME FOR SOME DATA TO GO SOUR.
MOV AL,49 ; SET OUTER LOOP COUNT
P5: LOOP $
DEC AL
JNZ P5
P6: MOV CX,BP ; RECOVER WORD COUNT
P7: LODSW ; GET WORD
OR AX,AX ; = TO 0000
JNZ P8 ; ERROR IF NOT
LOOP P7 ; LOOP TILL DONE
JMP SHORT P11 ; THEN EXIT
P8: MOV CX,AX ; SAVE BITS IN ERROR
XOR AH,AH
OR CH,CH ; HIGH BYTE ERROR?
JZ P9
INC AH ; SET HIGH BYTE ERROR
P9: OR CL,CL ; LOW BYTE ERROR?
JZ P10
ADD AH,2
P10: OR AH,AH ; SET ZERO FLAG=0 (ERROR INDICATION)
P11: CLD ; SET DIR FLAG BACK TO INCREMENT
RET ; RETURN TO CALLER
PODSTG ENDP
;-----
; SUBROUTINE
; OPTIONAL ROM CHECK
; DESCRIPTION
; THIS ROUTINE CHECKS OPTIONAL ROM MODULES (CHECKSUM
; FOR MODULES FROM C0000->CFFFF, CRC CHECK FOR CARTRIDGES
; FROM D0000->F7FFF).
; IF CHECK IS OK, CALLS INIT/TEST CODE IN MODULE.
; MFG ERROR CODE = 25XX (XX=M5B OF SEGMENT IN ERROR)
;-----

```

```

0EC4      75 19
0EC6      8B C5
0EC8      A8 80
0ECA      74 08
0ECA      81 F9 AA55
0ECE      75 08
0ED0      EB 19
0ED2      81 F9 55AA
0ED6      74 13
0ED8      81 FA D000
0EDC      72 0D

0EDE      8A DE
0EE0      B7 25
0EE2      BE 0068 R
0EE5      BA F800
0EE8      52
0EE9      EB 45

0EEB      2B F6

0EED      2A C0
0EEF      8A 67 02
0EF2      D1 E0
0EF4      50
0EFS      81 FA D000
0EF9      9C
0EFA      B1 04
0EFC      D3 E8
0EFE      03 D0
0F00      9D

0F01      59
0F02      52
0F03      72 07

0F05      E8 0F53 R
0F08      74 2B
0F0A      EB 05
0F0C      E8 01AD R
0F0F      74 24
0F11      B4 02

0F13      B7 00
0F15      BA 081C
0F18      CD 10
0F1A      8C DA
0F1C      8A C6
0F1E      E8 0F90 R
0F21      8A DE
0F23      B7 25
0F25      80 FE D0
0F28      BE 0064 R
0F2B      73 03
0F2D      BE 0063 R
0F30
0F30      E8 0D32 R
0F33      EB 1C

0F35
0F35      B8 ---- R
0F38      8E C0
0F3A      8B C5
0F3C      A8 80
0F3E      75 11
0F40      26: C7 06 0014 R 0003 C
0F47      26: 8C 1E 0016 R
0F4C      26: FF 1E 0014 R
0F51
0F51      5A
0F52      C3
0F53

ROM_CHECK PROC NEAR
MOV AX,BP ;
TEST AL,10000000B ; PCJR MODE ?
JZ ROMC1 ; NO -
CMP CX,0AA55H ; ROM ID = AA55H(WORD) ?
JNE ROMC2 ; NO -
JMP SHORT ROMC3 ; YES-
ROMC1: CMP CX,55AAH ; ROM ID = 55AAH(WORD) ?
JE ROMC3 ; YES-
ROMC2: CMP DX,0D000H ; OPTIONAL ROM AREA ?
JB ROMC3 ; YES-

MOV BL,DH ;
MOV BH,25H ;
MOV SI,OFFSET INVC_ERR ; INVALID CART. ERROR
MOV DX,0F800H ;
PUSH DX ;
JMP SHORT ROM_CHECK_0 ; GOTO ERROR HANDLER

;-----
ROMC3: SUB SI,SI ; SET SI TO POINT TO BEGINNING
; (REL. TO DS)
SUB AL,AL ; ZERO OUT AL
MOV AH,[BX+2] ; GET LENGTH INDICATOR
SHL AX,1 ; FORM COUNT
AX ; SAVE COUNT
CMP DX,0D000H ; SEE IF POINTER IS BELOW D000
PUSHF ; SAVE RESULTS
MOV CL,4 ; ADJUST
SHR AX,CL ;
ADD DX,AX ; SET POINTER TO NEXT MODULE
POPF ; RECOVER FLAGS FROM POINTER RANGE
; CHECK
POP CX ; RECOVER COUNT IN CX REGISTER
PUSH DX ; SAVE POINTER
JB ROM_1 ; DO ARITHMETIC CHECKSUM IF BELOW
; D0000
CALL CRC_CHECK ; DO CRC CHECK
JZ ROM_CHECK_1 ; PROCEED IF OK
JMP SHORT ROM_2 ; ELSE POST ERROR
ROM_1: CALL ROS_CHECKSUM ; DO ARITHMETIC CHECKSUM
JZ ROM_CHECK_1 ; PROCEED IF OK
ROM_2: MOV AH,2

MOV BH,0 ; PAGE 0
DX,081CH ; POSITION CURSOR, ROW 8, COL 28
INT INT_10 ;
MOV DX,DS ; RECOVER DATA SEG
MOV AL,DH ;
CALL XPC_BYTE ; DISPLAY MSB OF DATA SEG
MOV BL,DH ; FORM XX VALUE OF ERROR CODE
MOV BH,25H ; FORM 25 PORTION
CMP DH,0D00H ; IN CARTRIDGE SPACE?
MOV SI,OFFSET CART_ERR
MOV ROM_CHECK_0
MOV SI,OFFSET ROM_ERR

ROM_CHECK_0: CALL E_MSG ; GO ERROR ROUTINE
JMP SHORT ROM_CHECK_END ; AND EXIT

ROM_CHECK_1: MOV AX,XXDATA ; SET ES TO POINT TO XXDATA AREA
MOV ES,AX ;
MOV AX,BP ; PCJR MODE ?
TEST AL,10000000B ; YES-
JNZ ROM_CHECK_END ; YES-
MOV ES:IO_ROM_INIT,0003H ; LOAD OFFSET
MOV ES:IO_ROM_SEG,DS ; LOAD SEGMENT
CALL DHWORD PTR ES:IO_ROM_INIT ; CALL INIT./TEST ROUTINE

ROM_CHECK_END: POP DX ; RECOVER POINTER
RET ; RETURN TO CALLER

ROM_CHECK ENDP

;-----
; SUBROUTINE
; CRC CHECK/GENERATION
; DESCRIPTION
; CRC CHECK/GENERATION ROUTINE ROUTINE
; TO CHECK A ROM MODULE USING THE POLYNOMIAL:
; X16 + X12 + X5 + 1
; CALLING PARAMETERS:
; DS = DATA SEGMENT OF ROM SPACE TO BE CHECKED
; SI = INDEX OFFSET INTO DS POINTING TO 1ST BYTE
; CX = LENGTH OF SPACE TO BE CHECKED (INCLUDING CRC BYTES)
; ON EXIT:
; ZERO FLAG = SET = CRC CHECKED OK
; AH = 00
; AL = ??
; BX = 0000
; CL = 04
; DX = 0000 IF CRC CHECKED OK, ELSE, ACCUMULATED CRC
; SI = (SI(ENTRY)+BX(ENTRY))
;-----
CRC_CHECK PROC NEAR
ASSUME DS:NOTHING
MOV BX,CX ; SAVE COUNT
MOV DX,0FFFFH ; INIT. ENCODE REGISTER
CLD ; SET DIR FLAG TO INCREMENT

XOR AH,AH ; INIT. WORK REG HIGH
MOV CL,4 ; SET ROTATE COUNT
CRC_1: LODSB ; GET A BYTE
XOR DH,AL ; FORM Aj + Cj + 1
MOV AL,DH ;
ROL AX,CL ; SHIFT WORK REG BACK 4

```

Appendix A.

```

0F64 33 D0      XOR    DX,AX      ; ADD INTO RESULT REG
0F66 D1 C0      ROL    AX,1       ; SHIFT WORK REG BACK 1
0F68 86 F2      XCHG  DH,DL      ; SWAP PARTIAL SUM INTO RESULT REG
0F6A 33 D0      XOR    DX,AX      ; ADD WORK REG INTO RESULTS
0F6C D3 C8      ROR    AX,CL      ; SHIFT WORK REG OVER 4
0F6E 24 E0      AND   AL,11100000B ; CLEAR OFF (EFGH)
0F70 33 D0      XOR    DX,AX      ; ADD (ABCD) INTO RESULTS
0F72 D1 C8      ROR    AX,1       ; SHIFT WORK REG ON OVER (AH=0 FOR
; NEXT PASS)
0F74 32 F0      XOR    DH,AL      ; ADD (ABCD INTO RESULTS LOW)
0F76 4B         DEC    BX         ; DECREMENT COUNT
0F77 75 E4      JNZ   CRC_1      ; LOOP TILL COUNT = 0000
0F79 0B D2      OR    DX,DX      ; DX S/B = 0000 IF O.K.
0F7B C3         RET             ; RETURN TO CALLER
0F7C         CRC_CHECK   ENDP
;-----
; SUBROUTINE
; MFG CHECKPOINT HANDLER
; DESCRIPTION
; MFG CHECKPOINT DATA IS LOADED FROM SAVE AREA, DATA IS
; DECREMENTED BY ONE, AND THEN SAVED.
;-----
0F7C         MFG_UP   PROC    NEAR
0F7C 50         PUSH   AX
0F7D 1E         PUSH   DS
0F7E B8 ---- R  ASSUME  DS:XXDATA
0F81 8E D8     MOV    AX,XXDATA
0F83 A0 0005 R  MOV    DS,AX
0F86 E6 10     MOV    AL,MFG_TST ; GET MFG CHECKPOINT
0F88 FE C8     OUT   MFG_PORT,AL ; OUTPUT IT TO TESTER
0F8A A2 0005 R  DEC    AL         ; DROP IT BY 1 FOR THE NEXT TEST
0F8D 1F         MOV    MFG_TST,AL
0F8E 58         ASSUME DS:ABS0
0F8F C3         POP    DS
0F90         POP    AX
0F90         MFG_UP   ENDP
; ASSUME CS:CODE,DS:DATA
;-----
; SUBROUTINE
; CONVERT AND PRINT ASCII CODE
; NOTE: AL MUST CONTAIN NUMBER TO BE CONVERTED.
; AX AND BX DESTROYED.
;-----
0F90         XPC_BYTE  PROC    NEAR
0F90 50         PUSH   AX ; SAVE FOR LOW NIBBLE DISPLAY
0F91 B1 04     MOV    CL,4 ; SHIFT COUNT
0F93 D2 E8     SHR    AL,CL ; NIBBLE SWAP
0F95 E8 0F9B R CALL  XLAT_PR ; DO THE HIGH NIBBLE DISPLAY
0F98 58         POP    AX ; RECOVER THE NIBBLE
0F99 24 0F     AND   AL,0FH ; ISOLATE TO LOW NIBBLE
0F9B         XLAT_PR  PROC    NEAR ; FALL INTO LOW NIBBLE CONVERSION
0F9B 04 90     ADD   AL,090H ; CONVERT 00-0F TO ASCII CHARACTER
0F9D 27         DAA   ; ADD FIRST CONVERSION FACTOR
; ADJUST FOR NUMERIC AND ALPHA
; RANGE
0F9E 14 40     ADC   AL,040H ; ADD CONVERSION AND ADJUST LOW
; NIBBLE
; ADJUST HIGH NIBBLE TO ASCII RANGE
0FA0 27         DAA   ; RANGE
0FA1         PRT_HEX  PROC    NEAR
0FA1 53         PUSH   BX
0FA2 B4 0E     MOV    AH,14 ; DISPLAY CHARACTER IN AL
0FA4 BB 000F   MOV    BX,15 ; (WHITE)
0FA7 CD 10     INT   INT_10 ; CALL VIDEO_IO
0FA9 5B         POP    BX
0FAA C3         RET
0FAB         PRT_HEX  ENDP
0FAB         XLAT_PR  ENDP
0FAB         XPC_BYTE  ENDP
;-----
; SUBROUTINE
; BEEP ON ERROR
; DESCRIPTION
; THIS ROUTINE ISSUES SHORT TONES TO INDICATE FAILURES THAT
; 1: OCCUR BEFORE THE CRT IS STARTED,
; 2: TO CALL THE OPERATORS ATTENTION TO AN ERROR
; AT THE END OF POST, OR
; 3: TO SIGNAL THE SUCCESSFUL COMPLETION OF POST
; ENTRY PARAMETERS:
; DL = NUMBER OF APPROX. 1/2 SEC TONES TO SOUND
;-----
0FAB         ERR_BEEP  PROC    NEAR
0FAB 9C         PUSHF ; SAVE FLAGS
0FAC 53         PUSH   BX
0FAE FA         CLI    ; DISABLE SYSTEM INTERRUPTS
0FAE B3 01     G3:   MOV    BL,1 ; SHORT_BEEP:
0FB0 E8 0FC0 R CALL  BEEP ; COUNTER FOR A SHORT BEEP
0FB3 E2 FE     LOOP $ ; DO THE SOUND
0FB5 FE CA     DEC    DL ; DELAY BETWEEN BEEPS
0FB7 75 F5     JNZ   G3 ; DONE WITH SHORTS
0FB9 E2 FE     LOOP $ ; DO SOME MORE
0FBB E2 FE     LOOP $ ; LONG DELAY BEFORE RETURN
0FBD 5B         POP    BX ; RESTORE ORIG CONTENTS OF BX
0FBE 9D         POPF  ; RESTORE FLAGS TO ORIG SETTINGS
0FBF C3         RET    ; RETURN TO CALLER
0FC0         ERR_BEEP  ENDP
;-----
; SUBROUTINE
; SOUND BEEP
;-----

```

```

0FC0
0FC0 B0 B6
0FC2 E6 43
0FC4 B8 0533
0FC7 E6 42
0FC9 8A C4
0FCB E6 42
0FCD E4 61
0FCF 8A E0
0FD1 0C 03
0FD3 E6 61
0FD5 2B C9
0FD7 E2 FE
0FD9 FE CB
0FDB 75 FA
0FDD 8A C4
0FDF E6 61
0FE1 C3

```

0FE2

```

0FE2 50
0FE3 B8 ---- R
0FE6 8E D8
0FE8 58
0FE9 C3
0FEA

```

0FEA

```

0FEA E8 0000 E
0FED BE 0304 R
0FF0 88 04
0FF2 8B C4
0FF4 80 E4 E0

0FF7 74 0D
0FF9 32 C0
0FFB E6 A0
0FFD B8 2000
1000 BE 005F R
1003 E8 0D32 R
1006 CF
1007

```

= 0002

= 008C

= 006A

= 00D6

= 10FB

= 9874

= 0001

= 000F

= 00FF

= 00FF

= 0002

= 000C

= 000E

1007

1007 50

1008 53

1009 51

100A B8 0019

100D CD 10

100F B4 01

1011 89 2000

1014 CD 10

1016 33 DB

1018 B8 1000

101B CD 10

101D 43

101E 80 FB 10

1021 72 F5

```

BEEP PROC NEAR
MOV AL,10110110B ; SEL TIM 2,LSB,MSB,BINARY
OUT TIMER+3,AL ; WRITE THE TIMER MODE REG
MOV AX,533H ; DIVISOR FOR 1000 HZ
OUT TIMER+2,AL ; WRITE TIMER 2 CNT - LSB
MOV AL,AH
OUT TIMER+2,AL ; WRITE TIMER 2 CNT - MSB
IN AL,PORT_B ; GET CURRENT SETTING OF PORT
MOV AH,AL ; SAVE THAT SETTING
OR AL,03 ; TURN SPEAKER ON
OUT PORT_B,AL
SUB CX,CX ; SET CNT TO WAIT 500 MS
G7: LOOP 8 ; DELAY BEFORE TURNING OFF
DEC BL ; DELAY CNT EXPIRED?
JNZ G7 ; NO - CONTINUE BEEPING SPK
MOV AL,AH ; RECOVER VALUE OF PORT
OUT PORT_B,AL ; RETURN TO CALLER
RET

```

BEEP ENDP

```

;-----
; SUBROUTINE
; SET DS TO POINT TO XXDATA SEGMENT
;-----

```

```

DDX PROC NEAR
PUSH AX
MOV AX,XXDATA
MOV DS,AX
POP AX
RET
DDX ENDP

```

```

;-----
; SUBROUTINE
; SAVE KBD SCAN CODE
; DESCRIPTION
; TO SAVE ANY SCAN CODE RECEIVED BY THE NMI ROUTINE
; (PASSED IN AL) DURING POST IN THE KEYBOARD BUFFER
; CALLED THROUGH INT. 48H
; MFG ERROR CODE = 2000H
;-----

```

```

KEY_SCAN_SAVE PROC FAR
ASSUME DS:DATA
CALL DDS ; POINT DS TO DATA AREA
MOV SI,OFFSET KB_BUFFER_J ; POINT TO FIRST LOC. IN BUFR
MOV [SI],AL ; SAVE SCAN CODE
MOV AX,SP ; CHECK FOR STACK UNDERFLOW
AND AH,11100000B ; (THESE BITS WILL BE 111 IF UNDERFLOW HAPPEND)

JZ KS_1
XOR AL,AL
OUT NMI_PORT,AL ; SHUT OFF NMI
MOV BX,2000H ; ERROR CODE 2000H
MOV SI,OFFSET KEY_ERR ; POST MESSAGE
CALL E_MSG ; AND HALT SYSTEM
IRET ; RETURN TO CALLER
KEY_SCAN_SAVE ENDP

```

SUBTTL LOGO --- LOGO DISPLAY SUBROUTINE

```

;-----
; SUBROUTINE
; PUT LOGO ON SCREEN
; DESCRIPTION
; THIS PROC DISPLAYS IBM LOGO, A MESSAGE, AND A COLOR BAR
; ON THE SCREEN
;-----

```

```

LINENO EQU 2 ; LINE NUMBER OF ONE ROW
BACKROW EQU 140 ; UPPER LIMIT ROW OF BLUE BACK SCREEN
LOGOSROW EQU 106 ; LOGO START ROW POSITION
LOGOSCOLD EQU 214 ; LOGO START COLUMN POSITION
START_ADDR EQU LOGOSROW*160/4+LOGOSCOLD/2 ; REGEN OFFSET ADDR
WHITE_BOX1 EQU 9874H ; 2 BYTE CODE OF WHITE BOX
BLUE EQU 1 ; COLOR CODE OF BLUE
WHITE EQU 15 ; COLOR CODE OF WHITE
EOL EQU 0FFH ; END OF LINE
EOF EQU 0FFH ; END OF FILE
VSETCSR EQU 02H ; REQUEST SET CURSOR POSITION
VWRITDOT EQU 0CH ; REQUEST WRITE DOT
VWRITTY EQU 0EH ; REQUEST WRITE TTY

```

```

PUT_LOGO PROC NEAR
PUSH AX ; SAVE AX, BX & CX
PUSH BX
PUSH CX
MOV AX,0019H ; CLEAR SCREEN (GRAPHIC MODE)
INT INT_10 ; (MODE: 320 X 200 - COLOR)
MOV AH,1 ; ERASE CURSOR
MOV CX,2000H
INT INT_10
XOR BX,BX ; DISABLE SCREEN
LOGO: MOV AX,1000H
INT INT_10
INC BX
CMP BL,16
JB LOGO

```

## Appendix A.

```

;----- PAINT BACKGROUND SCREEN
1023 BE B800      MOV SI,REGEN_START ; SET REGEN BUFFER ADDRESS
1024 8E C6        MOV ES,SI           ; TO ES
;
1028 88 1111      MOV AX,BLUE*1000H OR BLUE*100H OR BLUE*10H OR BLUE
;
1028 33 FF        XOR DI,DI           ; START ROW/COLUMN IS 0/0
102D 89 0AF0      MOV CX,BACKROW*320/2/2/4; 2 PEL/BYTE,2 WORD/BYTE,4 SCAN
;
LOG01: MOV DH,4       ; SCAN COUNT IN REGEN
      PUSH DI        ;
      PUSH CX        ;
      REP STOSW      ; WRITE BLUE CX WORD
      POP CX         ;
      POP DI         ;
      ADD DI,2000H   ;
      DEC DH         ; ALL DONE ?
      JNZ LOG01      ;
;
;----- WRITE COLOR BAR
1040 32 FF        XOR BH,BH          ;
1042 BA 0900      MOV DX,0900H       ; SET ROW/COLUMN POSITION TO 9-0
1045 B4 02        MOV AH,VSETCSR     ; SET CURSOR POSITION
1047 CD 10        INT INT_10        ;
1049 B3 01        MOV BL,BLUE         ; SET START COLOR AS BLUE
104B B4 0E        MOV AH,VWRITTY   ;
104D B0 98        MOV AL,HIGH WHITE_BOX1 ;
104F CD 10        INT INT_10        ;
1051 B4 0E        MOV AH,VWRITTY   ;
1053 B0 74        MOV AL,LOW WHITE_BOX1 ;
1055 CD 10        INT INT_10        ;
1057 FE C3        INC BL           ; SET NEXT COLOR
1059 80 FB 10     CMP BL,16         ; ALL COLOR WRITEN ?
105C 72 ED        JB LOG03        ; NO
105E FE C6        INC DH           ; NEXT ROW
1060 80 FE 0A     CMP DH,0AH       ;
1063 76 E0        JBE LOG02        ;
;
;----- WRITE IBM PATTERN
1065 BB 10C8 R    MOV BX,OFFSET IBM ; POINT DATA AREA
1068 BA 006A      MOV DX,LOGOSROW  ; GET START ROW OF LOGO
;
LOG04: MOV CX,LOGOSCOL ; GET START COLUMN OF LOGO
LOG05: MOV AL,CS:[BX] ; GET DATA FOR WHITE PART COLUMN NBR
      CMP AL,0        ; IF LENGTH IS 0
      JE LOG08        ; THEN SKIP
LOG06: PUSH DX        ; SAVE CURRENT ROW POSITION
      MOV AH,LINEND  ; SET LINE NUMBER
LOG07: PUSH AX        ; SAVE FOR COUNT
      MOV AH,VWRDOT  ; WRITE DOT
      MOV AL,WHITE   ; DOT COLOR IS BLUE
      INT INT_10     ;
;
107F 42          INC DX           ; NEXT ROW
1080 58          POP AX           ;
1081 FE CC      DEC AH           ;
1083 75 F3      JNZ LOG07        ; REPEAT FOR ALL LINE IN ROW
;
1085 5A          POP DX           ; RETORE CURRENT ROW POSITION
;
1086 41          INC CX           ; NEXT COLUMN
1087 FE C8      DEC AL           ; ALL DOT WRITE ?
1089 75 EA      JNZ LOG06        ; NO
;
LOG08: INC BX           ;
      MOV AL,CS:[BX] ; GET DATA FOR BLUE PART COLUMN NBR
      CMP AL,EOL     ; END OF COLUMN ?
      JNE LOG09      ; NO, CONTINUE
;
1093 43          INC BX           ; GET NEXT DATA
1094 2E: 80 3F FF CMP BYTE PTR CS:[BX],EOF ; ALL END ?
1098 74 0B      JE LOG010       ; YES, EXIT
;
109A 83 C2 04    ADD DX, LINEND*2  ; SET NEXT ROW, TIMES 2
109D EB CC      JMP SHORT LOG04  ; BECAUSE SKIP BLUE LOW
; REPEAT FROM START
;
LOG09: CBW         ; CONVERT TO WORD
      ADD CX,AX    ; ADD TO SKIP BLUE PART
      INC BX      ; NEXT DATA
      JMP SHORT LOG05 ;
;
;----- ROUND 'B'
LOG010: PUSH DS     ; SAVE DS
      PUSH ES     ;
      POP DS      ; SET REGEN ADDR TO DS FOR QUICK ADDR
      MOV AL,BLUE*10H OR BLUE ; SET COLOR
;
10AA A2         DB 0A2H        ; MOV [50C4],AL
10AB 50C4      DW 50C4H        ;
;
10AD A2         DB 0A2H        ; MOV [5166],AL
10AE 5166      DW 5166H        ;
;
10B0 A2         DB 0A2H        ; MOV [5345],AL
10B1 5345      DW 5345H        ;
;
10B3 A2         DB 0A2H        ; MOV [72A5],AL
10B4 72A5      DW 72A5H        ;
;
10B6 A2         DB 0A2H        ; MOV [7486],AL
10B7 7486      DW 7486H        ;

```



```

10B9 A2          ; DB 9A2H          ; MOV [7524],AL
10BA 7524       ; DW 7524H         ;
10BC 1F         ; POP DS           ; RESTORE DS
;-----
10BD 88 0B00    MOV AX,0B00H      ; ENABLE SCREEN
10C0 87 01      MOV BH,1          ;
10C2 CD 10      INT INT_10        ;
;
10C4 59         POP CX           ; RESTORE AX, BX & CX
10C5 5B         POP BX           ;
10C6 58         POP AX           ;
;
10C7 C3         RET                ; RETURN TO CALLER

```

```

;----- DATA STRUCTURE
; DATA HAVE NUMBER OF PIXEL, FITST BYTE HAS LENGTH OF WHITE
; PART, SECOND BYTE HAS LENGTH OF BLUE PART
10C8
IBM LABEL BYTE
;-----
10C8 14 05 1B 07 0E 0D    DB      W B W B W B W B W B W B W B W B EOL
      0E FF              20, 5, 27, 7, 14,13, 14,
10D0 14 05 1E 04 10 09    DB      20, 5, 30, 4, 16, 9, 16, EOL
      10 FF
10D8 00 06 08 10 08 08    DB      0, 6, 8,16, 8, 8, 9, 9, 13, 5,13, EOL
      09 09 0D 05 0D FF
10E4 00 06 08 10 17 0B    DB      0, 6, 8,16, 23, 11, 15, 1,15, EOL
      0F 01 0F FF
10EE 00 06 08 10 17 0B    DB      0, 6, 8,16, 23, 11, 8, 1,13, 1, 8, EOL
      08 01 0D 01 08 FF
10FA 00 06 08 10 08 08    DB      0, 6, 8,16, 8, 8, 9, 9, 8, 3, 9, 3, 8, EOL
      09 09 08 03 09 03
      08 FF
1108 14 05 1E 04 0D 05    DB      20, 5, 30, 4, 13, 5, 5, 5,13, EOL
      05 05 0D FF
1112 14 05 1B 07 0D 07    DB      20, 5, 27, 7, 13, 7, 1, 7,13, EOL
      01 07 0D FF
111C FF
111D

```

```

PUT_LOGO ENDP
;-----
; SUBROUTINE
; ASYNCHRONOUS COMMUNICATIONS ADAPTER POWER ON DIAGNOSTIC TEST
; DESCRIPTION:
; THIS SUBROUTINE PERFORMS A THOROUGH CHECK OUT OF AN INS8250
; LSI CHIP
; THE TEST INCLUDES:
; 1) INITIALIZATION OF THE CHIP TO ASSUME ITS MASTER RESET STATE.
; 2) READING REGISTERS FOR KNOWN PERMANENT ZERO BITS.
; 3) TESTING THE INS8250 INTERRUPT SYSTEM AND THAT THE 8250
; INTERRUPTS TRIGGER AN 8259 (INTERRUPT CONTROLLER) INTERRUPT.
; 4) PERFORMING THE LOOP BACK TEST:
; A) TESTING WHAT WAS WRITTEN/READ AND THAT THE TRANSMITTER
; HOLDING REG EMPTY BIT AND THE RECEIVER INTERRUPT WORK
; PROPERLY.
; B) TESTING IF CERTAIN BITS OF THE DATA SET CONTROL REGISTER
; ARE 'LOOPED BACK' TO THOSE IN THE DATA SET STATUS
; REGISTER.
; C) TESTING THAT THE TRANSMITTER IS IDLE WHEN TRANSMISSION
; TEST IS FINISHED.
; THIS SUBROUTINE EXPECTS TO HAVE THE FOLLOWING PARAMETER PASSED:
; (DX)= ADDRESS OF THE INS8250 CARD TO TEST.
; NOTE: THE ASSUMPTION HAS BEEN MADE THAT THE MODEM ADAPTER IS
; ----- LOCATED AT 03F8H; THE SERIAL PRINTER AT 02F8H.
; IT RETURNS:
; (CF) = 1 IF ANY PORTION OF THE TEST FAILED
;       = 0 IF TEST PASSED
; (BX) = FAILURE KEY FOR ERROR MESSAGE (ONLY VALID IF TEST FAILED)
; (BH) = 23H SERIAL PRINTER ADAPTER TEST FAILURE
;       = 24H MODEM ADAPTER TEST FAILURE
; (BL) = 02H PERMANENT ZERO BITS IN INTERRUPT ENABLE REGISTER
;       WERE INCORRECT
;       03H PERMANENT ZERO BITS IN INTERRUPT IDENTIFICATION
;       REGISTER WERE INCORRECT
;       04H PERMANENT ZERO BITS IN DATA SET CONTROL REGISTER
;       WERE INCORRECT
;       05H PERMANENT ZERO BITS IN THE LINE STATUS REGISTER
;       WERE INCORRECT
;       06H RECEIVED DATA AVAILABLE INTERRUPT TEST FAILED
;       (THE INTERRUPT WAS NOT GENERATED)
;       07H RESERVED FOR REPORTING THE TRANSMITTER HOLDING
;       REGISTER EMPTY INTERRUPT TEST FAILED
;       (NOT USED AT THIS TIME BECAUSE OF THE DIFFERENCES
;       BETWEEN THE 8250'S WHICH WILL BE USED)
;       08H-08H RECEIVER LINE STATUS INTERRUPT TEST FAILED
;       (THE INTERRUPT WAS NOT GENERATED)
;       08H - OVERRUN ERROR
;       09H - PARITY ERROR
;       0AH - FRAMING ERROR
;       0BH - BREAK INTERRUPT ERROR
;       0CH-0FH MODEM STATUS INTERRUPT TEST FAILED
;       (THE INTERRUPT WAS NOT GENERATED)
;
;       0CH - DELTA CLEAR TO SEND ERROR
;       0DH - DELTA DATA SET READY ERROR
;       0EH - TRAILING EDGE RING INDICATOR ERROR
;       0FH - DELTA RECEIVE LINE SIGNAL DETECT ERROR
;       AN 8250 INTERRUPT OCCURRED AS EXPECTED, BUT NO
;       8259 (INTR CONTROLLER) INTERRUPT WAS GENERATED
;       DURING THE TRANSMISSION TEST, THE TRANSMITTER
;       HOLDING REGISTER WAS NOT EMPTY WHEN IT SHOULD
;       HAVE BEEN.

```

Appendix A.

```

12H DURING THE TRANSMISSION TEST, THE RECEIVED DATA
AVAILABLE INTERRUPT DIDN'T OCCUR.
13H TRANSMISSION ERROR - THE CHARACTER RECEIVED
DURING LOOP MODE WAS NOT THE SAME AS THE ONE
TRANSMITTED
14H DURING TRANSMISSION TEST, THE 4 DATA SET CONTROL
OUTPUTS WERE NOT THE SAME AS THE 4 DATA SET
CONTROL INPUTS.
15H THE TRANSMITTER WAS NOT IDLE AFTER THE TRANS-
MISSION TEST COMPLETED.
16H RECEIVED DATA AVAILABLE INTERRUPT FAILED TO CLEAR
17H TRANSMITTER HOLDING REG EMPTY INTR FAILED TO CLEAR
18H-1BH RECEIVER LINE STATUS INTERRUPT FAILED TO CLEAR
1CH-1FH MODEM STATUS INTERRUPT FAILED TO CLEAR
ON EXIT:
- THE MODEM OR SERIAL PRINTER'S 8259 INTERRUPT (WHICHEVER
DEVICE WAS TESTED) IS DISABLED.
- THE 8250 IS IN THE MASTER RESET STATE.
ONLY THE DS REGISTER IS PRESERVED - ALL OTHERS ARE ALTERED.
-----
= 0084
WRAP EQU 84H ; LOOP BACK TRANSMISSION TEST
; INTERRUPT VECTOR ADDRESS
UART ASSUME CS:CODE,DS:DATA
PROC NEAR
PUSH DS ; CURRENT ENABLED INTERRUPTS
IN AL,INTA01 ; SAVE FOR EXIT
PUSH AX ; DISABLE TIMER INTR DURING THIS
OR AL,00000001B ; TEST
;
OUT INTA01,AL ; SAVE CALLER'S FLAGS (SAVE INTR
PUSHF ; FLAG)
; SAVE BASE ADDRESS OF ADAPTER CARD
PUSH DX ; SET UP 'DATA' AS DATA SEGMENT
CALL DDS ; ADDRESS
-----
; INITIALIZE PORTS FOR MASTER RESET STATES AND TEST PERMANENT
; ZERO DATA BITS FOR CERTAIN PORTS.
-----
112A E8 1280 R CALL I8250
112D 73 03 JNC AT1 ; ALL OK
112F E9 1251 R JMP AT14 ; A PORT'S ZERO BITS WERE NOT ZERO!
-----
; INS8250 INTERRUPT SYSTEM TEST
;
; ONLY THE INTERRUPT BEING TESTED WILL BE ENABLED.
-----
1132 8F 0069 R AT1: SET DI AND SI FOR CALLS TO 'SUI'
1135 33 F6 MOV DI,OFFSET IMASKS ; BASE ADDRESS OF INTERRUPT MASKS
1137 80 FE 02 XOR SI,SI ; MODEM INDEX
113A 75 02 CMP DH,2 ; OR SERIAL?
113C 46 JNE AT2 ; NO - IT'S MODEM
113D 47 INC SI ; IT'S SERIAL PRINTER
; SERIAL PRINTER 8259 MASK ADDRESS
; RECEIVED DATA AVAILABLE INTERRUPT TEST
AT2: CALL SUI ; SET UP FOR INTERRUPTS
INC BL ; ERROR REPORTER (INIT. IN I8250)
INC DX ; POINT TO INTERRUPT ENABLE
; REGISTER
; ENABLE RECEIVED DATA AVAILABLE
; INTR
1144 80 01 MOV AL,1
;
1146 EE OUT DX,AL
1147 53 PUSH BX ; SAVE ERROR REPORTER
1148 83 C2 04 ADD DX,4 ; POINT TO LINE STATUS REGISTER
114B 84 01 MOV AH,1 ; SET RECEIVER DATA READY BIT
114D 8B 0400 MOV BX,0400H ; INTR TO CHECK, INTR IDENTIFIER
1150 B9 0003 MOV CX,3 ; INTERRUPT ID REG 'INDEX'
1153 E8 12B4 R CALL ICT ; PERFORM TEST FOR INTERRUPT
1156 5B POP BX ; RESTORE ERROR INDICATOR
1157 3C FF CMP AL,0FFH ; INTERRUPT ERROR OCCUR?
1159 74 36 JE AT4 ; YES
115B E8 12E7 R CALL C5059 ; GENERATE 8259 INTERRUPT?
115E 72 34 JC AT5 ; NO
1160 4A DEC DX
1161 4A DEC DX ; RESET INTR BY READING RECR BUFR
1162 EC IN AL,DX ; DON'T CARE ABOUT THE CONTENTS!
1163 42 INC DX
1164 42 INC DX ; INTR ID REG
1165 E8 12FA R CALL W8250C ; WAIT FOR INTR TO CLEAR
1168 73 03 JNC AT3 ; OK
116A E9 124E R JMP AT13 ; DIDN'T CLEAR
-----
; TRANSMITTER HOLDING REGISTER EMPTY INTERRUPT TEST
; THIS TEST HAS BEEN MODIFIED BECAUSE THE DIFFERENT 8250'S
; THAT MAY BE USED IN PRODUCING THIS PRODUCT DO NOT FUNCTION
; THE SAME DURING THE STANDARD TEST OF THIS INTERRUPT
; (STANDARD BEING THE SAME METHOD FOR TESTING THE OTHER
; POSSIBLE 8250 INTERRUPTS). IT IS STILL VALID FOR TESTING
; IF AN 8259 INTERRUPT IS GENERATED IN RESPONSE TO THE 8250
; INTERRUPT AND THAT THE 8250 INTERRUPT CLEARS AS IT SHOULD.
;
; IF THE TRANSMITTER HOLDING REGISTER EMPTY INTERRUPT IS NOT
; GENERATED WHEN THAT INTERRUPT IS ENABLED, IT IS NOT TREATED
; AS AN ERROR. HOWEVER, IF THE INTERRUPT IS GENERATED, IT
; MUST GENERATE AN 8259 INTERRUPT AND CLEAR PROPERLY TO PASS
; THIS TEST.
-----
116D E8 12D6 R AT3: CALL SUI ; SET UP FOR INTERRUPTS
1170 FE C3 INC BL ; BUMP ERROR REPORTER
1172 4A DEC DX ; POINT TO INTERRUPT ENABLE
; REGISTER

```

```

1173 B0 02
1175 EE
1176 EB 00
1178 42
1179 2B C9
117B EC
117C 3C 02
117E 74 04
1180 E2 F9
1182 EB 12
1184
1184 E8 12E7 R
1187 72 0B
1189 E8 12FA R
118C 73 08
118E E9 124E R
1191 E9 1215 R
1194 EB 7D
MOV AL,2 ; ENABLE XMITTER HOLDING REG EMPTY
; INTR
OUT DX,AL
JMP $+2 ; I/O DELAY
INC DX ; INTR IDENTIFICATION REG
SUB CX,CX
AT31: IN AL,DX ; READ IT
CMP AL,2 ; XMITTER HOLDING REG EMPTY INTR?
JE AT32 ; YES
LOOP AT31 ; THE INTR DIDN'T OCCUR - TRY NEXT
JMP SHORT AT6 ; TEST
; THE INTR DID OCCUR
AT32: CALL C5059 ; GENERATE 8259 INTERRUPT?
JC AT5 ; NO
CALL W8250C ; WAIT FOR THE INTERRUPT TO CLEAR
; (IT SHOULD ALREADY BE CLEAR
; BECAUSE 'ICT' READ THE INTR ID
; REG)
JNC AT6 ; IT CLEARED
JMP AT13 ; ERROR
AT4: JMP AT11 ; AVOID OUT OF RANGE JUMPS
AT5: JMP SHORT AT10

```

```

-----
; RECEIVER LINE STATUS INTERRUPT TEST
; THERE ARE 4 BITS WHICH COULD GENERATE THIS INTERRUPT.
; EACH ONE IS TESTED INDIVIDUALLY.
; WHEN: AH TESTING
; -----
; 2 OVERRUN
; 4 PARITY
; 8 FRAMING
; 10H BREAK INTR
-----

```

```

1196 4A
1197 B0 04
1199 EE
119A 83 C2 04
119D B9 0003
11A0 BD 0004
11A3 B4 02
11A5 E8 12D6 R
11A8 FE C3
11AA 53
11AB 8B 0601
11AE E8 12B4 R
11B1 5B
11B2 24 1E
11B4 3A C4
11B6 75 5D
11B8 E8 12E7 R
11BB 72 56
11BD 83 EA 03
11C0 E8 12FA R
11C3 73 03
11C5 E9 124E R
11C8 4D
11C9 74 07
11CB D0 E4
11CD 83 C2 03
11D0 EB D3
AT6: DEC DX ; POINT TO INTERRUPT ENABLE
; REGISTER
; ENABLE RECEIVER LINE STATUS INTR
MOV AL,4
OUT DX,AL
ADD DX,4 ; POINT TO LINE STATUS REGISTER
MOV CX,3 ; INTR ID REG 'INDEX'
MOV BP,4 ; LOOP COUNTER
MOV AH,2 ; INITIAL BIT TO BE TESTED
AT7: CALL SUI ; SET UP FOR INTERRUPTS
INC BL ; BUMP ERROR REPORTER
PUSH BX ; SAVE IT
MOV BX,0601H ; INTR TO CHECK, INTR IDENTIFIER
CALL ICT ; PERFORM TEST FOR INTERRUPT
POP BX
AND AL,0001110B ; MASK OUT BITS THAT DON'T MATTER
CMP AL,AH ; TEST BIT ON?
JNE AT11 ; NO
CALL C5059 ; GENERATE 8259 INTERRUPT?
JC AT10 ; NO
SUB DX,3 ; INTR ID REG
CALL W8250C ; WAIT FOR THE INTR TO CLEAR
JNC AT7_0 ; OK
JMP AT13 ; IT DIDN'T
AT7_0: DEC BP ; ALL FOUR BITS TESTED?
JE AT8 ; YES - GO ON TO NEXT TEST
SHL AH,1 ; GET READY FOR NEXT BIT
ADD DX,3 ; LINE STATUS REGISTER
JMP AT7 ; TEST NEXT BIT

```

```

-----
; MODEM STATUS INTERRUPT TEST
; THERE ARE 4 BITS WHICH COULD GENERATE THIS INTERRUPT.
; THEY ARE TESTED INDIVIDUALLY.
; WHEN: AH TESTING
; -----
; 1 DELTA CLEAR TO SEND
; 2 DELTA DATA SET READY
; 4 TRAILING EDGE RING INDICATOR
; 8 DELTA RECEIVE LINE SIGNAL DETECT
-----

```

```

11D2 83 C2 04
11D5 EC
11D6 EB 00
11D8 83 EA 05
11DB 80 08
11DD EE
11DE 83 C2 05
11E1 B9 0004
11E4 BD 0004
11E7 B4 01
11E9 E8 12D6 R
11EC FE C3
11EE 53
11EF 8B 0001
11F2 E8 12B4 R
11F5 5B
11F6 24 0F
11F8 3A C4
11FA 75 19
11FC E8 12E7 R
11FF 72 12
1201 83 EA 04
1204 E8 12FA R
1207 72 45
1209 4D
120A 74 0B
120C D0 E4
AT8: ADD DX,4 ; MODEM STATUS REGISTER
IN AL,DX ; CLEAR DELTA BITS THAT MAY BE ON
; BECAUSE OF DIFFERENCES AMONG
; 8250'S.
JMP $+2 ; I/O DELAY
SUB DX,3 ; INTERRUPT ENABLE REGISTER
MOV AL,8 ; ENABLE MODEM STATUS INTERRUPT
OUT DX,AL
ADD DX,5 ; POINT TO MODEM STATUS REGISTER
MOV CX,4 ; INTR ID REG 'INDEX'
MOV BP,4 ; LOOP COUNTER
MOV AH,1 ; INITIAL BIT TO BE TESTED
AT9: CALL SUI ; SET UP FOR INTERRUPTS
INC BL ; BUMP ERROR INDICATOR
PUSH BX ; SAVE IT
MOV BX,0001H ; INTR TO CHECK, INTR IDENTIFIER
CALL ICT ; PERFORM TEST FOR INTERRUPT
POP BX
AND AL,0000111B ; MASK OUT BITS THAT DON'T MATTER
CMP AL,AH ; TEST BIT ON?
JNE AT11 ; NO
CALL C5059 ; GENERATE 8259 INTERRUPT?
JC AT10 ; NO
SUB DX,4 ; INTR ID REG
CALL W8250C ; WAIT FOR INTERRUPT TO CLEAR
JC AT13 ; IT DIDN'T
DEC BP ; ALL FOUR BITS TESTED - GO ON
JE AT12 ; GET READY FOR NEXT BIT
SHL AH,1

```

Appendix A.

```

120E 83 C2 04      ADD    DX,4          ; MODEM STATUS REGISTER
1211 EB D6        JMP    AT9           ; TEST NEXT BIT
;-----
; POSSIBLE 8259 INTERRUPT CONTROLLER PROBLEM
;-----
1213 B3 10      AT10: MOV    BL,10H      ; SET ERROR REPORTER
1215 EB 3A      AT11: JMP    SHORT AT14
;-----
; SET 9600 BAUD RATE AND DEFINE DATA WORD AS HAVING 8
; BITS/WORD, 2 STOP BITS, AND ODD PARITY.
;-----
1217 42        AT12: INC    DX          ; LINE CONTROL REGISTER
1218 EB 130A R   CALL   $8250
;-----
; SET DATA SET CONTROL WORD TO BE IN LOOP MODE
;-----
1218 83 C2 04      ADD    DX,4          ; CURRENT STATE
121E EC        IN     AL,DX       ; I/O DELAY
121F EB 00      JMP    $+2          ; SET BIT 4 OF DATA SET CONTROL REG
1221 0C 10      OR     AL,00010000B
1223 EE        OUT   DX,AL
1224 EB 00      JMP    $+2          ; I/O DELAY
1226 42        INC    DX
1227 42        INC    DX          ; MODEM STATUS REG
1228 EC        IN     AL,DX       ; CLEAR POSSIBLE MODEM STATUS
; INTERRUPT WHICH COULD BE CAUSED
; BY THE OUTPUT BITS BEING LOOPED
; TO THE INPUT BITS
; I/O DELAY
1229 EB 00      JMP    $+2          ; RECEIVER BUFFER
122B 83 EA 06   SUB    DX,6          ; DUMMY READ TO CLEAR DATA READY
122E EC        IN     AL,DX       ; BIT IF IT WENT HIGH ON WRITE TO
; MCR
;-----
; PERFORM THE LOOP BACK TEST
;-----
122F 42        INC    DX          ; INTR ENBL REG
1230 52        PUSH   DX
1231 BA 0201    MOV    DX,JOY_PORT
1234 EC        IN     AL,DX
1235 24 FD      AND    AL,0F0H
1237 3C 20      CMP    AL,00100000B ; SERVICE MODE LOOP?
1239 5A        POP    DX
123A 74 0A      JE     AT13_0       ; YES-
123C A1 0072 R  MOV    AX,RESET_FLAG
123F 3D 3412    CMP    AX,3412H    ; WARM START WITH "CTRL+ALT+INS"?
1242 B0 00      MOV    AL,0        ; SET FOR INTERNAL WRAP TEST
1244 75 02      JNE   AT13_1       ; YES-
1246 B0 FF      MOV    AL,0FFH     ; SET FOR EXTERNAL WRAP TEST
1248 CD 84      AT13_0: INT    WRAP ; DO LOOP BACK TRANSMISSION TEST
124A B1 00      AT13_1: MOV    CL,0   ; ASSUME NO ERRORS
124C 73 05      JNC   AT15         ; WRAP TEST PASSED
124E 80 C3 10   AT13: ADD    BL,10H ; ERROR INDICATOR
;-----
; AN ERROR WAS ENCOUNTERED SOMEWHERE DURING THE TEST
;-----
1251 B1 01      AT14: MOV    CL,1   ; SET FAIL INDICATOR
;-----
; HOUSEKEEPING: RE-INITIALIZE THE 8250 PORTS (THE LOOP BIT
; WILL BE RESET), DISABLE THIS DEVICE INTERRUPT, SET UP
; REGISTER BH IF AN ERROR OCCURRED, AND SET OR RESET THE
; CARRY FLAG.
;-----
1253 5A        AT15: POP    DX          ; GET BASE ADDRESS OF 8250 ADAPTER
1254 53        PUSH   BX          ; SAVE ERROR CODE
1255 EB 1280 R   CALL   I8250       ; RE-INITIALIZE 8250 PORTS
1258 5B        POP    BX
1259 2E: 8A 25   MOV    AH,CS:[DI]  ; GET DEVICE INTERRUPT MASK
125C 20 26 0084 R AND    INTR_FLAG,AH ; CLEAR DEVICE'S INTERRUPT FLAG BIT
1260 80 F4 FF   XOR    AH,0FFH     ; FLIP BITS
1263 E4 21     IN     AL,INTA01   ; GET CURRENT INTERRUPT PORT
1265 0A C4     OR     AL,AH       ; DISABLE THIS DEVICE INTERRUPT
1267 E6 21     OUT   INTA01,AL   ; RE-ESTABLISH CALLER'S INTERRUPT
1269 9D        POPF   ; FLAG
; ANY ERRORS?
126A 0A C9     OR     CL,CL       ; NO
126C 74 0C     JE     AT17        ; ASSUME MODEM ERROR
126E B7 24     MOV    BH,24H     ; OR IS IT SERIAL?
1270 80 FE 02   CMP    DH,2       ; IT'S MODEM
1273 75 02     JNE   AT16        ; IT'S SERIAL PRINTER
1275 B7 23     MOV    BH,23H     ; SET CARRY FLAG TO INDICATE ERROR
1277 F9        AT16: STC
1278 EB 01     JMP    SHORT AT18
127A F8        AT17: CLC          ; RESET CARRY FLAG - NO ERRORS
127B 58        AT18: POP    AX          ; RESTORE ENTRY ENABLED INTERRUPTS
127C E6 21     OUT   INTA01,AL   ; DEVICE INTR5 RE-ESTABLISHED
127E 1F        POP    DS          ; RESTORE REGISTER
127F C3        RET
1280          UART    ENDP
;-----
; SUBROUTINE
; INITIALIZE INS8250 PORTS TO THE MASTER RESET STATUS.
; THIS ROUTINE ALSO TESTS THE PORTS' PERMANENT ZERO BITS.
; EXPECTS TO BE PASSED:
; (DX) = ADDRESS OF THE 8250 TRANSMIT/RECEIVE BUFFER
; UPON RETURN:
; (CF) = 1 IF ONE OF THE PORTS' PERMANENT ZERO BITS WAS NOT
; ZERO (ERR)
; (DX) = PORT ADDRESS THAT FAILED TEST
; (AL) = MEANINGLESS
; (BL) = 2 INTR ENBL REG BITS NOT 0

```



Appendix A.

```

12BD 2B C9
12BF EC
12C0 A8 01
12C2 74 02
12C4 E2 F9
12C6 59
12C7 3A C7
12C9 75 08
12CB 0A DB
12CD 74 06
12CF 03 D1

12D1 EC
12D2 C3

12D3 80 FF
12D5 C3
12D6

12D6 50
12D7 FB

12D8 2E: 8A 25
12D8 20 26 0084 R

12DF E4 21
12E1 22 C4
12E3 E6 21

12E5 58
12E6 C3
12E7

12E7 51
12E8 2B C9
12EA 2E: 8A 05
12ED 34 FF

12EF 84 06 0084 R
12F3 75 03
12F5 E2 F8
12F7 F9

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12F8 59
12F9 C3
12FA

12FA 51
12FB 2B C9
12FD EC
12FE 3C 01
1300 74 05
1302 E2 F9
1304 F9
1305 EB 01
1307 F8
1308 59
1309 C3

AT21: SUB CX,CX ; WAIT FOR 8250 INTERRUPT TO OCCUR
      IN AL,DX ; READ INTR ID REG
      TEST AL,1 ; INTERRUPT PENDING?
      JE AT22 ; YES - RETURN W/ INTERRUPT ID IN AL
      LOOP AT21 ; NO - TRY AGAIN
AT22: POP CX ; AL = 1 IF NO INTERRUPT OCCURRED
      CMP AL,BH ; INTERRUPT WE'RE LOOKING FOR?
      JNE AT23 ; NO
      OR BL,BL ; DONE WITH TEST FOR THIS INTERRUPT
      JE AT24 ; RETURN W/ CONTENTS OF INTR ID REG
      ADD DX,CX ; READ STATUS REGISTER TO CLEAR THE

      IN AL,DX ; INTERRUPT (WHEN BL=1)
      RET ; RETURN CONTENTS OF STATUS REG

AT23: MOV AL,0FFH ; SET ERROR INDICATOR
AT24: RET
      ICT ENDP

;-----
; SUBROUTINE
; SET UP CONDITIONS FOR 8250 TESTING
; DESCRIPTION
; TO SET UP CONDITIONS FOR THE TESTING OF 8250 AND
; 8259 INTERRUPTS. ENABLES MASKABLE EXTERNAL INTERRUPTS,
; CLEARS THE 8259 INTR RECEIVED FLAG BIT, AND ENABLES THE
; DEVICE'S 8259 INTR (WHICHEVER IS BEING TESTED).
; IT EXPECTS TO BE PASSED:
; (DS) = ADDRESS OF SEGMENT WHERE INTR_FLAG IS DEFINED
; (DI) = OFFSET OF THE INTERRUPT BIT MASK
; UPON RETURN:
; INTR_FLAG BIT FOR THE DEVICE = 0
; NO REGISTERS ARE ALTERED.
;-----
SUI PROC NEAR
     PUSH AX
     STI ; ENABLE MASKABLE EXTERNAL
        ; INTERRUPTS
     MOV AH,CS:[DI] ; GET INTERRUPT BIT MASK
     AND INTR_FLAG,AH ; CLEAR 8259 INTERRUPT REC'D FLAG
        ; BIT
     IN AL,INTA01 ; CURRENT INTERRUPTS
     AND AL,AH ; ENABLE THIS INTERRUPT, TOO
     OUT INTA01,AL ; WRITE TO 8259 (INTERRUPT
        ; CONTROLLER)
     POP AX
SUI RET
     ENDP

;-----
; SUBROUTINE
; CHECKS IF A 8259 INTERRUPT IS GENERATED BY THE
; 8250 INTERRUPT.
; EXPECTS TO BE PASSED:
; (DI) = OFFSET OF INTERRUPT BIT MASK
; (DS) = ADDRESS OF SEGMENT WHERE INTR_FLAG IS DEFINED.
; RETURNS:
; (CF) = 1 IF NO INTERRUPT IS GENERATED
; 0 IF THE INTERRUPT OCCURRED
; (AL) = COMPLEMENT OF THE INTERRUPT MASK
; NO OTHER REGISTERS ARE ALTERED.
;-----
C5059 PROC NEAR
      PUSH CX
      SUB CX,CX ; SET PROGRAM LOOP COUNT
      MOV AL,CS:[DI] ; GET INTERRUPT MASK
      XOR AL,0FFH ; COMPLEMENT MASK SO ONLY THE INTR
        ; TEST BIT IS ON
      TEST INTR_FLAG,AL ; 8259 INTERRUPT OCCUR?
      JNE AT27 ; YES - CONTINUE
      LOOP AT25 ; WAIT SOME MORE
      STC ; TIME'S UP - FAILED
C5059 RET

AT25: TEST INTR_FLAG,AL
      JNE AT27
      LOOP AT25
      STC

MSCBI010.INC

AT27: POP CX
      RET
C5059 ENDP

;-----
; SUBROUTINE
; TO WAIT FOR ALL ENABLED 8250 INTERRUPTS TO CLEAR (SO
; NO INTRs WILL BE PENDING). EACH INTERRUPT COULD TAKE UP TO
; 1 MILLISECOND TO CLEAR. THE INTERRUPT IDENTIFICATION
; REGISTER WILL BE CHECKED UNTIL THE INTERRUPT(S) IS CLEARED
; OR A TIMEOUT OCCURS.
; EXPECTS TO BE PASSED:
; (DX) = ADDRESS OF THE INTERRUPT ID REGISTER
; RETURNS:
; (AL) = CONTENTS OF THE INTR ID REGISTER
; (CF) = 1 IF INTERRUPTS ARE STILL PENDING
; 0 IF NO INTERRUPTS ARE PENDING (ALL CLEAR)
; NO OTHER REGISTERS ARE ALTERED.
;-----
W8250C PROC NEAR
      PUSH CX
      SUB CX,CX
AT28: IN AL,DX ; READ INTR ID REG
      CMP AL,1 ; INTERRUPTS STILL PENDING?
      JE AT29 ; NO - GOOD FINISH
      LOOP AT28 ; KEEP TRYING
      STC ; TIME'S UP - ERROR
      JMP SHORT AT30
AT29: CLC
AT30: POP CX
      RET

```

130A

W8250C ENDP

```

;-----
; SUBROUTINE
; TO SET AN IHS8250 CHIP'S BAUD RATE TO 9600 BPS AND
; DEFINE IT'S DATA WORD AS HAVING 8 BITS/WORD, 2 STOP BITS,
; AND ODD PARITY.
; EXPECTS TO BE PASSED:
; (DX) = LINE CONTROL REGISTER
; UPON RETURN:
; (DX) = TRANSMIT/RECEIVE BUFFER ADDRESS
; ALTERS REGISTER AL. ALL OTHERS REMAIN INTACT.
;-----

```

130A  
130A B0 80  
130C EE  
130D EB 00  
130F 83 EA 03  
1312 B0 0C  
1314 EE  
1315 EB 00  
1317 42  
1318 B0 00  
131A EE  
  
131B EB 00  
131D 42  
131E 42  
131F B0 0F  
  
1321 EE  
1322 EB 00  
1324 83 EA 03  
1327 EC  
  
1328 C3  
1329

```

S8250 PROC NEAR
MOV AL,80H ; SET DLAB = 1
OUT DX,AL
JMP $+2 ; I/O DELAY
SUB DX,3 ; LSB OF DIVISOR LATCH
MOV AL,12 ; DIVISOR = 12 PRODUCES 9600 BPS
OUT DX,AL ; SET LSB
JMP $+2 ; I/O DELAY
INC DX ; MSB OF DIVISOR LATCH
MOV AL,0 ; HIGH ORDER OF DIVISORS
OUT DX,AL ; SET MSB

JMP $+2 ; I/O DELAY
INC DX
INC DX ; LINE CONTROL REGISTER
MOV AL,00001111B ; 8 BITS/WORD, 2 STOP BITS, ODD
; PARITY

OUT DX,AL ; I/O DELAY
JMP $+2 ; RECEIVER BUFFER
SUB DX,3 ; IN CASE WRITING TO PORT LCR
IN AL,DX ; CAUSED DATA READY TO GO HIGH!

RET
S8250 ENDP

```

```

;-----
; SUBROUTINE
; TO READ AN 8250 REGISTER. MAY ALSO BUMP ERROR
; REPORTER (BL) AND/OR REG DX (PORT ADDRESS) DEPENDING ON
; WHICH ENTRY POINT IS CHOSEN.
; THIS SUBROUTINE WAS WRITTEN TO AVOID MULTIPLE USE OF I/O
; TIME DELAYS FOR THE 8250. IT WAS THE MOST EFFICIENT WAY TO
; INCLUDE THE DELAYS.
; UPON RETURN
; REG AL WILL CONTAIN THE CONTENTS OF PORT(DX)
;-----

```

1329  
1329 32 C0  
132B EE  
132C FE C3  
132E 42  
132F EC  
1330 C3  
1331

```

RR1 PROC NEAR
XOR AL,AL
OUT DX,AL ; DISABLE ALL INTERRUPTS
INC BL ; BUMP ERROR REPORTER
RR2: INC DX ; INCR PORT ADDR
RR3: IN AL,DX ; READ REGISTER

RET
RR1 ENDP

```

```

;-----
; THESE ARE THE VECTORS WHICH ARE MOVED INTO
; THE 8086 INTERRUPT AREA DURING POWER ON.
; ONLY THE OFFSETS ARE DISPLAYED HERE, CODE
; SEGMENT WILL BE ADDED FOR ALL OF THEM, EXCEPT
; WHERE NOTED.
;-----

```

15C0  
15C0  
15C0 0000 E  
15C2 0000 E  
15C4 0599 R  
15C6 0599 R  
15C8 0599 R  
15CA 0599 R  
15CC 0000 E  
15CE 0599 R  
15D0 0000 E  
15D2 0000 E  
15D4 0000 E  
15D6 0000 E  
15D8 0000 E  
15DA 0000 E  
15DC 0000 E  
15DE 0000 E  
15E0 0000  
15E2 0000 E  
15E4 0000 E  
15E6 05C0 R  
15E8 05C0 R  
15EA 0000 E  
15EC 0000 E  
15EE 05C0 R

```

ASSUME CS:CODE
ORG BEGIN+15C0H
VECTOR_TABLE LABEL WORD ; VECTOR TABLE FOR MOVE TO INTERRUPTS
DW OFFSET TIMER_INT ; INTERRUPT 08H
DW OFFSET KB_INT ; INTERRUPT 09H
DW OFFSET D11 ; INTERRUPT 0AH
DW OFFSET D11 ; INTERRUPT 0BH
DW OFFSET D11 ; INTERRUPT 0CH
DW OFFSET D11 ; INTERRUPT 0DH
DW OFFSET DISK_INT ; INTERRUPT 0EH
DW OFFSET D11 ; INTERRUPT 0FH
DW OFFSET VIDEO_IO ; INTERRUPT 10H
DW OFFSET EQUIPMENT ; INTERRUPT 11H
DW OFFSET MEMORY_SIZE_DETERMINE ; INTERRUPT 12H
DW OFFSET DISKETTE_IO ; INTERRUPT 13H
DW OFFSET RS232_IO ; INTERRUPT 14H
DW CASSETTE_IO ; INTERRUPT 15H
DW OFFSET KEYBOARD_IO ; INTERRUPT 16H
DW OFFSET PRINTER_IO ; INTERRUPT 17H
DW 00000H ; INTERRUPT 18H
DW OFFSET BOOT_STRAP ; INTERRUPT 19H
DW TIME_OF_DAY ; INTERRUPT 1AH
DW DUMMY_RETURN ; INTERRUPT 1BH - KEYBD BREAK ADDR
DW DUMMY_RETURN ; INTERRUPT 1CH - TIMER BREAK ADDR
DW VIDEO_PARMS ; INTERRUPT 1DH
DW OFFSET DISK_BASE ; INTERRUPT 1EH
DW DUMMY_RETURN ; INTERRUPT 1FH

```

```

;-----
; POWER ON RESET VECTOR
;-----

```

```

;-----
; POWER ON RESET
;-----

```

15F0 EA  
15F1 006B R  
15F3 F800  
  
15F5 30 39 2F 31 30 2F  
38 34  
  
15FD FF

```

DB 0EAH ; JUMP FAR
DW 0F800H ; CODE SEGMENT
DB '09/10/84' ; RELEASE MARKER
DB 0FFH ; FILLER

```

Appendix A.

```
15FE ED
15FF
15FF

; SYSTEM IDENTIFIER
; CHECKSUM
;

DB 0EDH
DB 0FFH
POST_END LABEL FAR
CODE ENDS
END
```



## Appendix B Logic Diagrams

Appendix B. Logic Diagrams

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# Appendix C

## Character Code Table/Character Font

Code	Character
00	
01	
02	
03	
04	
05	
06	
07	
08	
09	
0A	
0B	
0C	
0D	
0E	
0F	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
1A	
1B	
1C	
1D	
1E	
1F	
20	
21	
22	
23	
24	
25	
26	
27	
28	
29	
2A	
2B	
2C	
2D	
2E	
2F	
30	
31	
32	
33	
34	
35	
36	
37	
38	
39	
3A	
3B	
3C	
3D	
3E	
3F	
40	
41	
42	
43	
44	
45	
46	
47	
48	
49	
4A	
4B	
4C	
4D	
4E	
4F	
50	
51	
52	
53	
54	
55	
56	
57	
58	
59	
5A	
5B	
5C	
5D	
5E	
5F	
60	
61	
62	
63	
64	
65	
66	
67	
68	
69	
6A	
6B	
6C	
6D	
6E	
6F	
70	
71	
72	
73	
74	
75	
76	
77	
78	
79	
7A	
7B	
7C	
7D	
7E	
7F	
80	
81	
82	
83	
84	
85	
86	
87	
88	
89	
8A	
8B	
8C	
8D	
8E	
8F	
90	
91	
92	
93	
94	
95	
96	
97	
98	
99	
9A	
9B	
9C	
9D	
9E	
9F	
A0	
A1	
A2	
A3	
A4	
A5	
A6	
A7	
A8	
A9	
AA	
AB	
AC	
AD	
AE	
AF	
B0	
B1	
B2	
B3	
B4	
B5	
B6	
B7	
B8	
B9	
BA	
BB	
BC	
BD	
BE	
BF	
C0	
C1	
C2	
C3	
C4	
C5	
C6	
C7	
C8	
C9	
CA	
CB	
CC	
CD	
CE	
CF	
D0	
D1	
D2	
D3	
D4	
D5	
D6	
D7	
D8	
D9	
DA	
DB	
DC	
DD	
DE	
DF	
E0	
E1	
E2	
E3	
E4	
E5	
E6	
E7	
E8	
E9	
EA	
EB	
EC	
ED	
EE	
EF	
F0	
F1	
F2	
F3	
F4	
F5	
F6	
F7	
F8	
F9	
FA	
FB	
FC	
FD	
FE	
FF	

■ Display Character Code(AN of CG1)

		bit 4~7																
		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	
bits 3	0	NUL	▶	SP	0	@	P	'	p	Ç	É	á	☐	☐	☐	☐	∞	≡
	1	☺	◀	!	1	A	Q	a	q	ü	æ	í	☐	☐	☐	☐	β	±
	2	☹	↕	"	2	B	R	b	r	é	Æ	ó	☐	☐	☐	☐	Γ	≥
	3	♥	!!	#	3	C	S	c	s	â	ô	ú	☐	☐	☐	☐	π	≤
	4	♦	¶	\$	4	D	T	d	t	ä	ö	ñ	☐	☐	☐	☐	Σ	f
	5	♣	§	%	5	E	U	e	u	à	ò	Ñ	☐	☐	☐	☐	σ	∫
	6	♠	▬	&	6	F	V	f	v	å	û	ä	☐	☐	☐	☐	μ	÷
	7	•	↕	'	7	G	W	g	w	ç	ù	o	☐	☐	☐	☐	τ	≈
	8	•	↑	(	8	H	X	h	x	ê	ÿ	¿	☐	☐	☐	☐	Φ	°
	9	○	↓	)	9	I	Y	i	y	ë	Ö	┌	☐	☐	☐	☐	⊖	•
	A	⊙	→	*	:	J	Z	j	z	è	Ü	┐	☐	☐	☐	☐	Ω	•
	B	♂	←	+	;	K	[	k	{	ï	ç	½	☐	☐	☐	☐	δ	√
	C	♀	└	,	<	L	\	l		î	ℒ	¼	☐	☐	☐	☐	∞	n
	D	♪	↔	—	=	M	]	m	}	ì	¥	ì	☐	☐	☐	☐	φ	2
	E	♪	▲	.	>	N	^	n	~	Ä	Ŕ	«	☐	☐	☐	☐	€	■
	F	☼	▼	/	?	O	_	o	Δ	Å	f	»	☐	☐	☐	☐	∩	

■ Display Character Code (ANK of CG2)

bit 4~7

		bit 4~7															
		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
bit 0~3	0	NUL		SP	0	@	P	`	p			▒	ー	タ	ミ		
	1			!	1	A	Q	a	q			。	ア	チ	ム		
	2			"	2	B	R	b	r			「	イ	ツ	メ		
	3			#	3	C	S	c	s			」	ウ	テ	モ		
	4			\$	4	D	T	d	t			、	エ	ト	ヤ		
	5			%	5	E	U	e	u			・	オ	ナ	ユ		
	6			&	6	F	V	f	v			ヲ	カ	ニ	ヨ		
	7			'	7	G	W	g	w			ア	キ	ヌ	ラ		
	8			(	8	H	X	h	x			イ	ク	ネ	リ		
	9			)	9	I	Y	i	y			ウ	ケ	ノ	ル		
	A			*	:	J	Z	j	z			エ	コ	ハ	レ		
	B		←	+	;	K	[	k	]			オ	サ	ヒ	ロ		
	C		↑	·	<	L	¥	ℓ				ヤ	シ	フ	ワ		
	D			-	=	M	]	m	{			ユ	ス	ヘ	ン		
	E		→	·	>	N	^	n	~			ヨ	セ	ホ	。		
	F		←	/	?	O	-	o				ツ	ソ	マ	。		

1st byte of the 2 byte code char.      2nd byte of the 2 byte code char.

■ Thermal Printer (IBM5513) Character Code

		bit 4~7												
		0	1	2	3	4	5	6	7	8	9	A	E	F
bit 0~3	0	NUL	␣	SP	0	ā	P	°	P	Q	É	á	⊗	≡
	1		␣	!	1	Ā	Q	á	q	Ü	æ	í	β	±
	2		DC2	"	2	B	R	b	r	é	Æ	ó	Γ	Σ
	3	␣	!!	#	3	C	S	c	s	ā	ō	ú	Π	≤
	4	␣	DC4	\$	4	D	T	d	t	ä	ö	ñ	Σ	ƒ
	5	␣	á	%	5	E	U	e	u	ä	ö	ñ	σ	∫
	6	␣	-	&	6	F	V	f	v	á	ü	æ	∫	+
	7	+	±	'	7	G	W	g	w	š	ü	ø	τ	∞
	8	␣	CAN	(	8	H	X	h	x	ē	ÿ	¿	Ξ	°
	9	HT	↓	)	9	I	Y	i	Y	ē	ö	Γ	Θ	•
	A	LF	→	⌘	:	J	Z	j	z	è	ü	¬	Ω	·
	B	VT	ESC	+	;	K	[	k	[	ì	4	½	∫	∫
	C	FF	L	,	<	L	\	l	l	í	£	¼	∞	∞
	D	CR	↵	-	=	M	]	m	]	î	¥	∫	∞	2
	E	SO	␣	.	>	N	^	n	~	â	℞	⊗	€	■
	F	SI	␣	/	?	O	_	o	DEL	â	f	⊗	∩	SP

Remark) Applied in English Mode



# Thermal Transfer Printer (IBM5512) Character Code

The IBM 5512 has three kinds of character sets:

1. PC character set  
corresponds to AN of character generator 1.
2. Nihongo DOS character set  
corresponds to ANK of character generator 2. "7F", however, is ☒ and "A0" is blank.
3. Kana character set (See the following chart)

bit 4~7

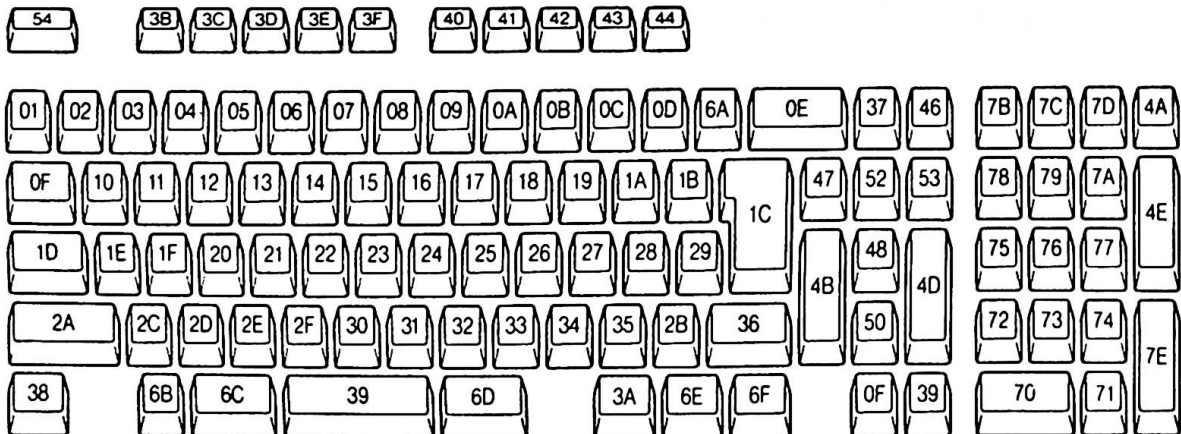
		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	
bit 0~3	0	NUL	▶	SP	0	@	P	'	p			á	ー	タ	ミ		☒	
	1	☺	◀	!	1	A	Q	a	q			。	ア	チ	ム			円
	2	☹	↕	"	2	B	R	b	r			「	イ	ツ	メ			年
	3	♥	!!	#	3	C	S	c	s			」	ウ	テ	モ			月
	4	♦	¶	\$	4	D	T	d	t			,	エ	ト	ヤ			日
	5	♣	§	%	5	E	U	e	u			.	オ	ナ	ユ			時
	6	♠	▬	&	6	F	V	f	v			ヲ	カ	ニ	ヨ			分
	7	•	↕	'	7	G	W	g	w			ア	キ	ヌ	ラ			秒
	8	•	↑	(	8	H	X	h	x			イ	ク	ネ	リ	♠		干
	9	○	↓	)	9	I	Y	i	y			ウ	ケ	ノ	ル	♥		市
	A	●	→	*	:	J	Z	j	z			エ	コ	ハ	レ	♦		区
	B	♂	←	+	;	K	[	k	{			オ	サ	ヒ	ロ	♣		町
	C	♀	└	,	<	L	\	l				ヤ	シ	フ	ワ	●		村
	D	♪	↔	—	=	M	]	m	}			ユ	ス	ヘ	ン	○		人
	E	♪	▲	.	>	N	^	n	~			ヨ	セ	ホ	.			☒
	F	☼	▼	/	?	O	_	o	△			ツ	ソ	マ	.			

■ Scan Code

The following chart shows scan codes for Native and English modes.  
( ) is for English mode.

Scan Code	Key	Scan Code	Key	Scan Code	Key	Scan Code	Key	Scan Code	Key	Scan Code	Key
01	ESC	12	E	23	H	34	.	46	Scroll	6E(**)	カタカナ
02	1	13	R	24	J	35	/		Lock	6F(**)	ひらがな
03	2	14	T	25	K	36	⏪ 右	47	⏩	70 (52)	0
04	3	15	Y	26	L	37	*	48	↑	71 (53)	.
05	4	16	U	27	;	38	前面	4A	-	72 (4F)	1
06	5	17	I	28	'	39	スペース	4B	←	73 (50)	2
07	6	18	O	29	,	3A	英数	4D	→	74 (51)	3
08	7	19	P	2A	⏪ 左	3B	PF1	4E	+	75 (4B)	4
09	8	1A	[	2B	\	3C	PF2	50	↓	76 (4C)	5
0A	9	1B	]	2C	Z	3D	PF3	52	挿入	77 (4D)	6
0B	0	1C	↩	2D	X	3E	PF4	53	削除	78 (47)	7
0C	-	1D	Ctrl	2E	C	3F	PF5	54	Fn	79 (48)	8
0D	=	1E	A	2F	V	40	PF6	55	Invalid	7A (49)	9
0E	後退	1F	S	30	B	41	PF7	6A(**)	¥	7B (37)	*
0F	↔	20	D	31	N	42	PF8	6B(**)	漢字	7C (35)	/
10	Q	21	F	32	M	43	PF9	6C (39)	無変換	7D (33)	,
11	W	22	G	33	,	44	PF10	6D (39)	変換	7E (1C)	↩

\*\*.....「Not used」



■ Keyboard Hankaku (half-size) Character Code (1 of 2)

Alphanumeric				Katakana				CTRL	Alt
Lower Case		Upper Case		Lower Case		Upper Case			
Esc	1B	Esc	1B	Esc	1B	Esc	1B	(Cassette)	- 1
1	31	!	21	ヌ	C7		-1	- 1	Null+78
2	32	@	40	フ	CC		-1	NUL(00)	Null+79
3	33	#	23	ア	B1	ア	A7	- 1	Null+7A
4	34	\$	24	ウ	B3	ウ	A9	- 1	Null+7B
5	35	%	25	エ	B4	エ	AA	- 1	Null+7C
6	36	^	5E	オ	B5	オ	AB	RSO(1E)	Null+7D
7	37	&	26	ヤ	D4	ヤ	AC	- 1	Null+7E
8	38	*	2A	ユ	D5	ユ	AD	- 1	Null+7F
9	39	(	28	ヨ	D6	ヨ	AE	- 1	Null+80
0	30	)	29	ワ	DC	ヲ	A6	- 1	Null+81
-	2D	_	5F	ホ	CE		-1	US(1F)	Null+82
=	3D	+	2F	へ	CD		-1	- 1	Null+83
後退*	08	後退*	08	後退*	08	後退*	08	DEL(7F)	- 1
→	09	← Null+0F		→	09	← Null+0F		- 1	- 1
q	71	Q	51	タ	C0		-1	DC1(11)	Null+10
w	77	W	57	テ	C3		-1	ETB(17)	Null+11
e	65	E	45	イ	B2	イ	A8	ENQ(05)	Null+12
r	72	R	52	ス	BD		-1	DC2(12)	Null+13
t	74	T	54	カ	B6		-1	DC4(14)	Null+14
y	79	Y	59	ン	DD		-1	EM(19)	Null+15
u	75	U	55	ナ	C5		-1	NAK(15)	Null+16
i	69	I	49	ニ	C6		-1	HT(09)	Null+17
o	6F	O	4F	ラ	D7		-1	SI(0F)	Null+18
p	70	P	50	セ	BE		-1	DLE(10)	Null+19
[	5B	{	7B	"	DE		-1	Esc(1B)	- 1
]	5D	}	7D	。	DF	「	A2	GS(1D)	- 1
改行**	0D	改行**	0D	改行**	0D	改行**	0D	LF(0A)	- 1

Remark: 「- 1」 is 「Not Used」.

\*..... Backspace

\*\*... Carriage Return

## ■ Keyboard Hankaku (half-size) Character Code (2 of 2)

Alphanumeric		Katakana		CTRL	Alt
Lower Case	Upper Case	Lower Case	Upper Case		
Ctrl -1	-1	-1	-1	-1	-1
a 61	A 41	チ C1	-1	SOH(01)	Null+1E
s 73	S 53	ト C4	-1	DC3(13)	Null+1F
d 64	D 44	シ BC	-1	EOT(04)	Null+20
f 66	F 46	ハ CA	-1	ACK(06)	Null+21
g 67	G 47	キ B7	-1	BELL(07)	Null+22
h 68	H 48	ク B8	-1	BS(08)	Null+23
j 6A	J 4A	マ CF	-1	LF(0A)	Null+24
k 6B	K 4B	ノ C9	-1	VT(0B)	Null+25
l 6C	L 4C	リ D8	-1	FF(0C)	Null+26
; 3B	: 3A	レ DA	-1	-1	-1
' 27	" 22	ケ B9	-1	-1	-1
` 60	(~) -1	ム D1	J A3	-1	-1
Left Shift-1	-1	-1	-1	-1	-1
(\ ) -1	7C	ロ DB	-1	-1	-1
z 7A	Z 5A	ツ C2	ヅ AF	SUB(1A)	Null+2C
x 78	X 58	サ BB	-1	CAN(18)	Null+2D
c 63	C 43	ソ BF	-1	EXT(03)	Null+2E
v 76	V 56	ヒ CB	-1	SYN(16)	Null+2F
b 62	B 42	コ BA	-1	STX(02)	Null+30
n 6E	N 4E	ミ D0	-1	SO(02)	Null+31
m 6D	M 4D	モ D3	-1	CR(0D)	Null+32
, 2C	< 3C	ネ C8	・ A4	-1	-1
. 2E	> 3E	ル D9	。 A1	-1	-1
/ 2F	? 3F	メ D2	、 A5	-1	-1
Right Shift-1	-1	-1	-1	-1	-1
* 2A	Prt -1	* 2A	Prt -1	Echo -1	-1
	Screen		Screen		
¥ 5C	- 7E	- B0	-1	-1	-1

Remark: 「-1」 is 「Not Used」.

: (\ ) and (~) are valid only in English mode.

■ Keyboard Zenkaku (full-size) Character Code (1 of 2)

Scan Code	Alphanumeric		Katakana		Hiragana	
	Lower Case	Upper Case	Lower Case	Upper Case	Lower Case	Upper Case
02	8250	8149	836B	-1	82CA	-1
03	8251	8197	8374	-1	82D3	-1
04	8252	8194	8341	8340	82A0	829F
05	8253	8190	8345	8344	82A4	82A3
06	8254	8193	8347	8346	82A6	82A5
07	8255	814F	8349	8248	82A8	82A7
08	8256	8195	8384	8383	82E2	82E1
09	8257	8196	8386	8385	82E4	82E3
0A	8258	8169	8388	8387	82E6	82E5
0B	824F	816A	838F	8392	82ED	82F0
0C	817C	8151	837A	8192	82D9	8192
0D	8181	817B	8377	8158	82D6	8158
10	8291	8270	835E	-1	82BD	-1
11	8297	8276	8365	-1	82C4	-1
12	8285	8264	8343	8342	82A2	82A1
13	8292	8271	8358	-1	82B7	-1
14	8284	8273	834A	-1	82A9	-1
15	8299	8278	8393	-1	82F1	-1
16	8295	8274	8369	-1	82C8	-1
17	8289	8268	836A	-1	82C9	-1
18	828F	826E	8389	-1	82E7	-1
19	8290	826F	835A	8177	82B9	8177
1A	816D	816F	814A	-1	814A	-1
1B	816E	8170	814B	8175	814B	8175
1E	8281	8260	8360	-1	82BF	-1
1F	8293	8272	8367	-1	82C6	-1
20	8284	8263	8356	-1	82B5	-1
21	8286	8265	836E	-1	82CD	-1
22	8287	8266	834C	-1	82AB	-1
23	8288	8267	834E	-1	82AD	-1

Remark : 「-1」 is 「Not Used」.

## ■ Keyboard Zenkaku (full-size) Character Code (2 of 2)

Scan Code	Alphanumeric		Katakana		Hiragana	
	Lower Case	Upper Case	Lower Case	Upper Case	Lower Case	Upper Case
24	828A	8269	837D	-1	82DC	-1
25	828B	826A	836D	-1	82CC	-1
26	828C	826B	838A	-1	82E8	-1
27	8147	8146	838C	8178	82EA	8178
28	814C	818D	8350	8396	82AF	8396
29	814D	8160	8380	8176	82DE	8176
2B	815F	8162	838D	-1	82EB	-1
2C	829A	8279	8363	8362	82C2	F2C1
2D	8298	8277	8354	-1	82B3	-1
2E	8283	8262	835C	-1	82BB	-1
2F	8296	8275	8371	-1	82D0	-1
30	8282	8261	8352	-1	82B1	-1
31	828E	826D	837E	-1	82DD	-1
32	828D	826C	8382	-1	82E0	-1
33	8143	8271	836C	8141	82CB	8141
34	8144	8172	828B	8142	82E9	8142
35	815E	8148	8381	8145	82DF	8145
6A	818F	8150	815B	-1	815B	-1











Remark : 「-1」 is 「Not Used」.

■ Keyboard Character Code

Lower Case		Upper Case		CTRL		Alt	
Alt	- 1		- 1		- 1		- 1
Space	20	Space	20	Space	20	Space	20
AN	- 1		- 1		- 1		- 1
PF1	Null+3B	PF11	Null+54	PF21	Null+5E	PF31	Null+68
PF2	Null+3C	PF12	Null+55	PF22	Null+5F	PF32	Null+69
PF3	Null+3D	PF13	Null+56	PF23	Null+60	PF33	Null+6A
PF4	Null+3E	PF14	Null+57	PF24	Null+61	PF34	Null+6B
PF5	Null+3F	PF15	Null+58	PF25	Null+62	PF35	Null+6C
PF6	Null+40	PF16	Null+59	PF26	Null+63	PF36	Null+6D
PF7	Null+41	PF17	Null+5A	PF27	Null+64	PF37	Null+6E
PF8	Null+42	PF18	Null+5B	PF28	Null+65	PF38	Null+6F
PF9	Null+43	PF19	Null+5C	PF29	Null+66	PF39	Null+70
PF10	Null+44	PF20	Null+5D	PF30	Null+67	PF40	Null+71
Scroll	- 1		- 1	Break	- 1		- 1
Lock							
Home	Null+47	Home	Null+47		Null+77		- 1
Cur up	Null+48	Cur up	Null+48		- 1		- 1
	- 2D		- 2D		- 1		- 1
Cur left	Null+4B	Cur left	Null+4B		Null+73		- 1
Cur right	Null+4D	Cur right	Null+4D		Null+74		- 1
	+ 2B		+ 2B		- 1		- 1
Cur down	Null+50	Cur down	Null+50		- 1		- 1
挿入	Null+52	挿入	Null+52		- 1		- 1
削除	Null+53	(Num Pad '.')			- 1		- 1
Fn	- 1		- 1		- 1		- 1
漢字	- 1	漢字	- 1		- 1		- 1
無変換	20	無変換	20		- 1		- 1
変換	20	変換	20		- 1		- 1
カタカナ	- 1	カタカナ	- 1		- 1	半角	- 1
ひらがな	- 1	ひらがな	- 1		- 1	全角	- 1

Remark: 「- 1」 is 「Not Used」.

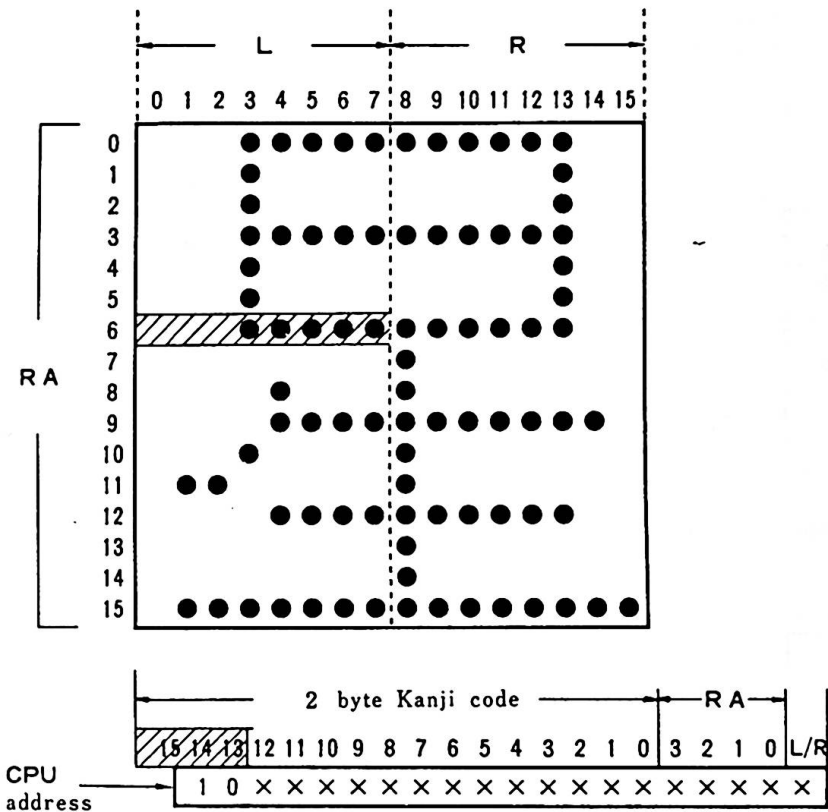
■ Combined key functions

Key combination	Function sample
<b>Ctrl</b> + Scroll Lock	Break
<b>Ctrl</b> + 印刷	Echo
 + 印刷	Print Screen
Fn+Q	Pause
<b>前面</b> + Fn+N	Numeric Lock
Fn+ 	Page Up
Fn+ 	Page Down
 or Fn+ 	Home Position
Fn+ 	End
<b>Ctrl</b> + 	Screen Clear (BASIC)
Fn+ 	Execution
Fn+H	Interrupt
<b>前面</b> + <b>Ctrl</b> + 	Screen right
<b>前面</b> + <b>Ctrl</b> + 	Screen left
<b>前面</b> + <b>Ctrl</b> + 挿入	System Reset
<b>前面</b> + <b>Ctrl</b> + 削除	System Reset
<b>Ctrl</b> + ESC	Cassette auto-load
Fn+T	Termination

Remark) Functions may differ from application to application.  
This is a sample.

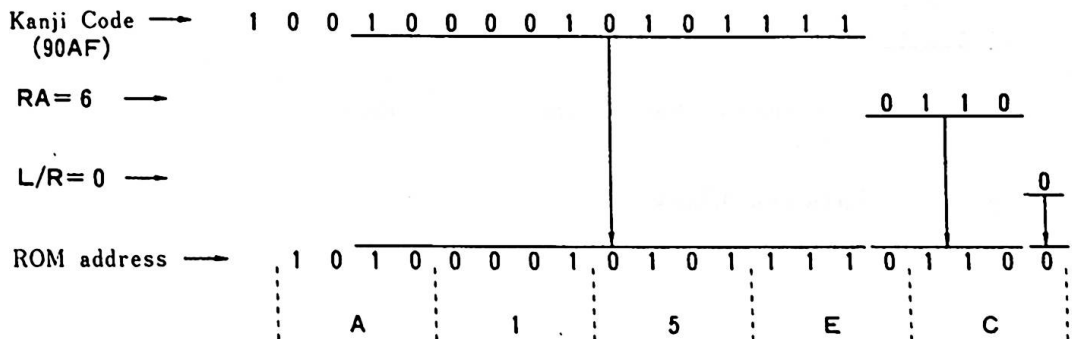


■ Kanji ROM address calculation



Calculation Sample

The Kanji ROM address for the shaded dot pattern can be calculated as follows :



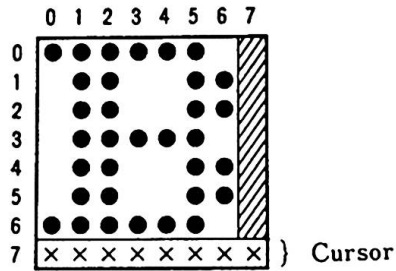
■ Character Font

CG1 Alphanumeric · Special Character 8×8

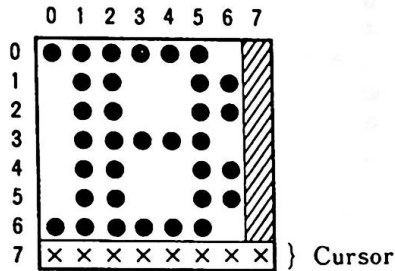
CG2 Alphanumeric · Special Char. and Katakana 8×8

Alphanumeric · Special Char. Hankaku Char. of Katakana 8×16

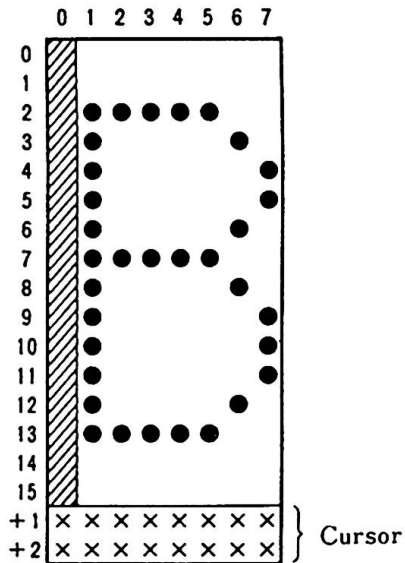
Hiragana and Kanji 16×16



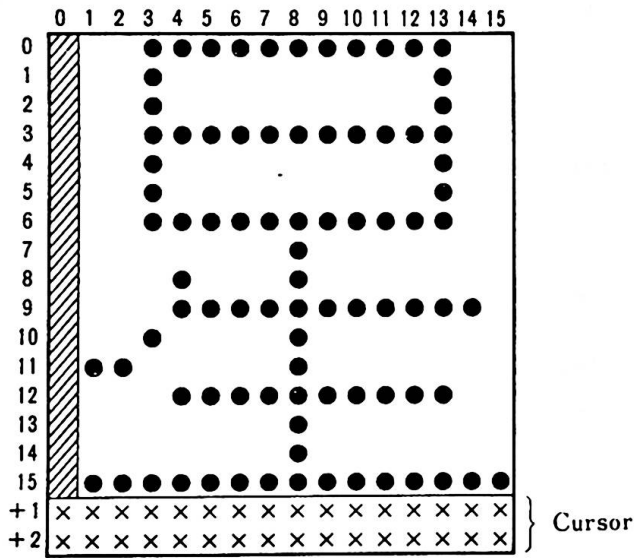
CG1 Sample : B  
7×7 (8×8)  
Alphanumeric · Special Char.



CG2 Sample : B  
7×7 (8×8)  
Alphanumeric · Special Char. · Katakana



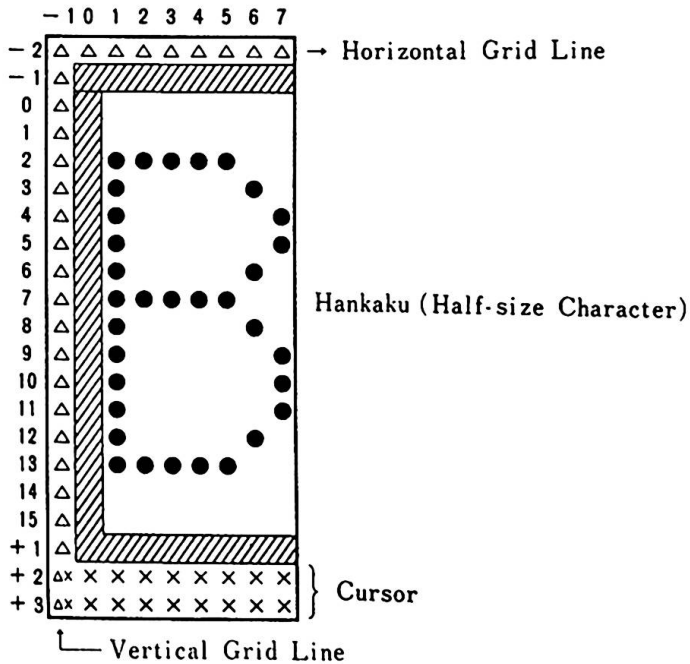
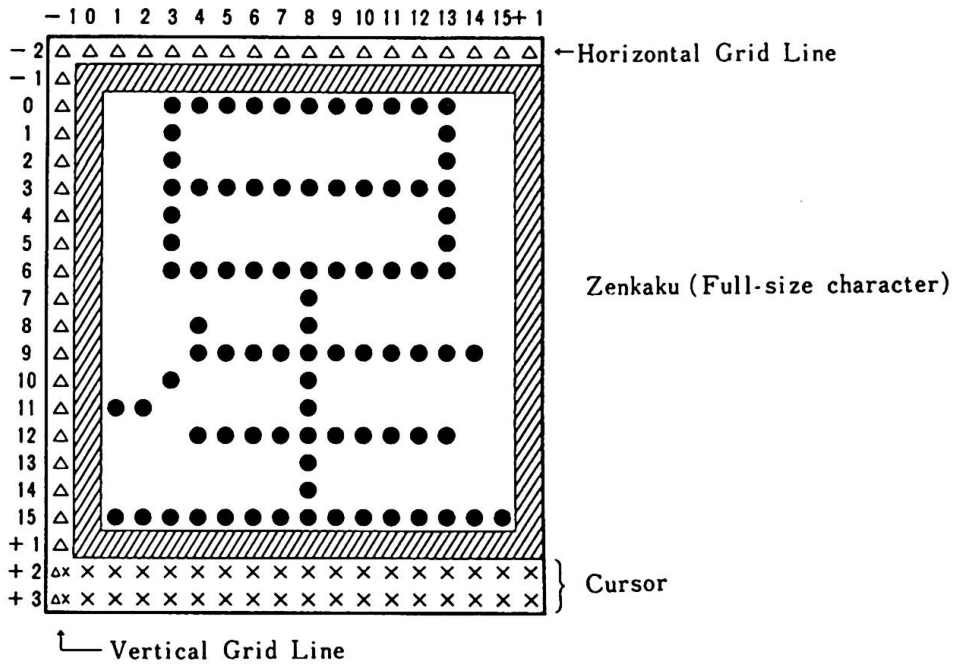
CG2 7×16 (8×16)  
Hankaku : Alphanumeric · Special Char.  
Katakana



CG2 15×16 (16×16)  
Kanji, Hiragana

Remark (▨) : 「always blank」  
XX : cursor dot

■ Character Display Format of Extension Video mode



Remarks) ▨ : 「always blank」 portion  
 XX : Cursor dot  
 Δ : Grid Line dot



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