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TITLE DCMS3, *REV A*
*   BBHCLA TEST
*   PART NO.
*   DCMX3      60134626-001
*   DCMS3      60134627-001
*   DCML3      60134628-001
*
*   DESCRIPTION
*   -----
*   THIS T & V PROGRAM VERIFIES PROPER OPERATION OF THE LEVEL-6 MLCP
*   BROADBAND HDLC COMMUNICATION LINE ADAPTER (BHCLA). IT PROVIDES
*   A FIRST LEVEL OF DIAGNOSIS WHEN FAILURES ARE DETECTED, AND MAKES
*   FACILITIES AVAILABLE TO SUPPORT EXTENSIVE PROBLEM INVESTIGATIONS.
*
*   THE SUBSYSTEM OPTIONS SUPPORTED BY THIS PROGRAM ARE:
*
*   MLC 9101  MLCP CONTROLLER
*   DCM 9112  BH4DLD PAC - BROADBAND HDLC, CURRENT MODE.
*   DCM 9113  BH4DLE PAC - BROADBAND HDLC, BALANCED VOLTAGE.
*
*   REVISION HISTORY
*   -----
*   001   JUNE 1978   DCMS3/DCML3   ORIGINAL RELEASE
*
*   THIS DOCUMENT AND THE INFORMATION CONTAINED THEREIN IS CONFIDENTIAL AND
*   PROPRIETARY TO AND THE EXCLUSIVE PROPERTY OF HONEYWELL INFORMATION SYSTEMS
*   INC. IT IS MADE AVAILABLE ONLY TO HONEYWELL AUTHORIZED RECIPIENTS FOR
*   THEIR USE SOLELY IN THE MAINTENANCE AND OPERATION OF HONEYWELL PRODUCTS.
*   THIS DOCUMENT AND INFORMATION MUST BE MAINTAINED IN STRICTEST CONFIDENCE;
*   IT MUST NOT BE REPRODUCED IN WHOLE OR IN PART; AND IT SHALL NOT BE DIS-
*   CLOSED TO ANY OTHER PARTY WITHOUT THE PRIOR WRITTEN CONSENT OF HONEYWELL.
*   *****
*   PROGRAM PREPARATION:
*   -----
*   THE ROOT SOURCE OF THIS PROGRAM, AFTER THE ADDITION OF THE APPROPRIATE
*   TITLE AND END STATEMENTS, WAS PROCESSED BY THE HOST RESIDENT ASSEMBLER
*   TO CREATE EITHER SHORT OR LONG ADDRESS FORM (SAF OR LAF) OBJECT TEXT
*   AND LISTING. THE OBJECT TEXT WAS FURTHER PROCESSED BY THE HOST
*   RESIDENT LINKER USING THE APPROPRIATE CONSOLE ZVSLIB LIBRARY TO CREATE
*   A PUNCH SEGMENT CONTAINING AN EXECUTABLE MODULE. THE ASSEMBLY LISTING
*   WAS AUGMENTED WITH CROSS REFERENCE DATA, PLUS THE LOAD MAP FROM THE
*   LINKER TO CREATE A LIST SEGMENT.
*
*   NAME      DCMX3      DCMS3      DCML3
*   DOCUMENT  60134626-001  60134627-001  60134628-001
*
*   DISTRIBUTION
*   -----
*   THE ELEMENTARY ITEMS SUBMITTED TO THE T & V PROGRAM DISTRIBUTION CENTER
*   WERE THE EXECUTABLE LINKED IMAGES, ON DISKETTE, OF DCMS3 AND DCML3, AND
*   MAGNETIC TAPE IMAGES OF THE AUGMENTED LISTINGS.
*
*   REPRODUCTIONS OF THE EXECUTABLE LINKED IMAGES MAY BE AS DUPLICATE CARD
*   DECKS OR AS A MEMBER OF A MULTIPLE MEMBER FILE. IN THE MOST FREQUENT
*   CASE, IT WILL BE FOUND AS MEMBER "SP" (SAF) OR "LP" (LAF) WITHIN FILE
*   "PROGFILE" OF A DISKETTE VOLUME ENTITLED "DIAGS".
*
*   DISTRIBUTION OF THE LISTINGS, WHICH SHOULD BE AVAILABLE IF ANY COMPLEX
*   MAINTENANCE OR REPAIR IS TO BE PERFORMED, IS NORMALLY AS A PRINTED COPY.
*
*   ROUTINE DEMONSTRATION
*   -----
*   A MINIMUM SATISFACTORY TEST FOR NORMAL OPERATION MAY BE OBTAINED BY
*   RUNNING ONE PASS OF "A" MODE AND ONE PASS OF "C" MODE AT 56
*   K BAUD OR LESS FOR EACH BBHCLA. OPERATION AT 72 KB RUNS ONLY A
*   SUBSET OF THE PROGRAM. ONLY SINGLE FRAME TESTS WITH CHAR. SIZE
*   OF 8 BITS ARE RUN AT GREATER THAN 56 K BAUD.
*
*   STORAGE
*   -----
*   THIS PROGRAM REQUIRES AT LEAST 16 K WORDS OF MAIN MEMORY.
*
*   OPERATION
*   -----
*   LOAD AND START (OR RESTART) THE PROGRAM. THE PROGRAM IDENTIFICATION WILL
*   BE DISPLAYED ON THE CONSOLE. THE INITIAL START WILL ALSO DISPLAY:
*
*   THE ZVSLIB REVISION NUMBER
*   THE ADDRESS FORM (SAF OR LAF)
*   I/O EQUIPMENT DETECTED IN THE SYSTEM
*   MEMORY SIZE
*
*   THIS DISPLAY MUST BE VERIFIED BY THE OPERATOR. THIS DISPLAY IS OMITTED
*   ON RESTARTS.
*
*   THE CONSOLE SEARCH RULES ARE: FIND THE CONSOLE WITH THE LOWEST CHANNEL
*   NUMBER CONNECTED THRU AN MDC CONTROLLER. IF THERE IS NO CONSOLE ON AN
*   MDC, THEN SEARCH FOR A TERMINAL WITH THE HIGHEST CHANNEL NUMBER ASSIGNED
*   TO AN ACIA ADAPTER ON AN MLC CONTROLLER. IF NO ASYNC ADAPTER IS FOUND,
*   THEN GO TO THE FULL CONTROL PANEL.
*
*   THERE ARE THREE CONSOLE CHANNEL OPTIONS DETERMINED BY THE VALUE OF LO-
*   CATION "ZVSTTY".
*
*   IF ZVSTTY EQUALS (0000), SEARCH FOR A CONSOLE.
*   IF ZVSTTY EQUALS (FFFF), ASSUME THERE IS NO CONSOLE.
*   IF ZVSTTY EQUALS NEITHER (0000), NOR (FFFF), THEN IT IS THE CONSOLE CHAN-
*   NEL NUMBER. NOTE: DEFAULT IS TO SEARCH FOR A CONSOLE.
*
*   ALL CONSOLE I/O IS EVEN PARITY. IF CONSOLE IS ON MLC, IT MUST BE ASYNC
*   AND THE BAUD RATE SET AT 1200 TO MATCH THE PROGRAM SUPPLIED RATE. IF IT
*   IS NECESSARY TO CHANGE THE PROGRAM BAUD RATE, THEN THE NEW BAUD RATE
*   CODE SHOULD BE PUT INTO LOCATION "ZVSBUD" IN HEX. THE TERMINAL BAUD RATE
*   MUST BE SET TO MATCH THIS NEW BAUD RATE. THE CORRECT HEX VALUE MAY BE
*   OBTAINED FROM THE FOLLOWING TABLE.
*
*   -----*
*   BAUD RATE TABLE
*   -----*
    
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* THE PROGRAM THEN DISPLAYS (1ST PASS ONLY);
 * MLCP FW REV AA
 * AND THEN
 * BHCLA LINE 0 FW REV BB
 * DATA SET STATUS = QQQQ
 * AND/OR
 * BHCLA LINE 4 FW REV CC
 * DATA SET STATUS = PPPP
 * WHERE AA, BB, AND CC ARE FIRMWARE REV NUMBERS AND PPPP AND QQQQ
 * ARE DATA SET STATUS VALUES READ AFTER AN MLCP INITIALIZE.
 * THE MLCP REV NUMBER IS STORED IN LOC "MLC-FR".
 * BHCLA FIRMWARE REV NUMBERS ARE STORED IN LOCATIONS
 * HCFW-1 AND HCFW-2 FOR LINE ADAPTERS 1 AND 2
 * RESPECTFULLY. THESE ARE FOR EXAMINATION, IF
 * DESIRED BY USERS WITH NO CONSOLE.
 * THE PROGRAM WILL RUN A FEW SECONDS PER BHCLA IF THERE ARE NO
 * HARDWARE FAULTS. IT WILL THEN DISPLAY;
 * LINE 0 SPEED = AAAA BITS/SEC
 * AND/OR
 * LINE 4 SPEED = BBBB BITS/SEC
 * THESE VALUES DEVIDED BY DEC "10" ARE STORED IN LOCATIONS
 * HCLS-1 AND HCLS-2 FOR USES WITHOUT A CONSOLE. A HEXIDECIMAL
 * TO DECIMAL CONVERSION MUST BE MADE WHEN INTERPRETING THESE
 * MEMORY LOCATIONS.
 * THE TESTED SPEED CORRESPONDS TO THE SETTING OF THE TEST
 * CLOCK ON THE MLCP. THIS CAN BE VARIED BY MEANS OF A HEX
 * ROTARY SWITCH ON THE MLCP.
 * PASS TIME FOR THE PROGRAM IS APPROXOMATELY 2 MIN. PER BBHCLA
 * FOR "A" MODE OR "C" MODE.
 * AT THE END OF THE PASS TIME THE PROGRAM WILL DISPLAY;
 * PASS
 * AND CONTINUE ON TO THE NEXT PASS. FOR SYSTEMS WITHOUT A CONSOLE
 * THE PROGRAM WILL HALT WITH E0 = HEX 100 AT THE END OF EACH PASS.
 * IF THERE IS NO CONSOLE PRESENT REFER TO THE MANUAL "SERIES 60
 * LEVEL 6 T + V OPERATOR'S MANUAL", DUC AW94 FOR INSTRUCTIONS
 * ON ENTERING DATA AND INTERPRETING PROGRAM MESSAGES.
 * *****
 * CONNECTOR LOOP INFORMATION
 * THE LOOP CONNECTOR MUST BE AS FOLLOWS. PINS REFERENCED
 * ARE ON THE OUTPUT CONNECTOR OF THE LINE ADAPTER.
 * 301/303 CURRENT MODE ADAPTER - BH4DLU
 * PIN SIGNAL TIES TO PIN SIGNAL
 * 22 RTS LR2 BIT 1 26 DSR LR5 BIT 0
 * 22 RTS LR2 BIT 1 27 CD LR5 BIT 1
 * 22 RTS LR2 BIT 1 25 CTS LR5 BIT 1
 * 20 DTR LR2 BIT 0 24 RING LR5 BIT 3
 * 6 TR DATA 14 RC DATA
 * 4 TEST CLK A 2 TR CLK
 * 12 TEST CLK B 10 RCV CLK
 * V.35 BALANCED LINE ADAPTER (BH4DLE)
 * 27 RQS LR2 BIT 1 23 CD LR5 BIT 2
 * 27 RQS LR2 BIT 1 21 CTS LR5 BIT 1
 * 26 DTR LR2 BIT 0 22 DSR LR5 BIT 0
 * 26 DTR LR2 BIT 0 20 RING LR5 BIT 3
 * 19 XMIT DATA (-) 14 RCV DATA (-)
 * 18 XMIT DATA (+) 16 RCV DATA (+)
 * 6 TEST CLK (+) 2 TR CLK (+)
 * 6 TEST CLK (+) 10 RCV CLK (+)
 * 8 TEST CLK 4 TR CLK (-)
 * 8 TEST CLK 12 RCV CLK (-)
 * TO OPERATE IN EXTERNAL LOOPBACK:
 * 1. DIRECT CONNECT MUST BE SET
 * 2. LR2 BITS 2,3, AND 5 MUST BE RESET
 * ERROR REPORTS
 * ERRORS WILL CAUSE THE PROGRAM TO HALT. AN ERROR MESSAGE WILL
 * BE DISPLAYED IF A CONSOLE IS PRESENT.
 * ERROR DISPLAYS ARE AS FOLLOWS:

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ERR MBXX AT YYYY

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AA BB CC DD EE FF GG HH
R7 R6 R5 R4 R3 R2 R1 M

```

OR

ERR DBXX AT YYYY LINE ZZ *

```

AA BB CC DD EE FF GG HH
R7 R6 R5 R4 R3 R2 R1 M

```

WHERE

```

MB = MOTHER BOARD
DB = DAUGHTER BOARD
XX = TEST LABEL. SEE JUMP TABLE AT LOCATION "HDTSA"
    FOR A LIST OF TESTS PERFORMED.
    EACH TEST NAME IS SUFFIXED BY -XX.
YYYY = ERROR LOCATION IN LISTING. HAS COMMENT
      GIVING FAILING FUNCTION.
ZZ = LINE NUMBER

```

FOR SPECIALIST USAGE:

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R1-R7, M ARE CONTENTS OF REGISTERS.
AA - HH ARE CHANNEL PROGRAM OPERATION RESULTS.

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IN ALL CASES:

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R3 = CHANNEL NUMBER

```

IN GENERAL

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R6 = SHOULD BE DATA
R5 = ACTUAL DATA
R7 = WORD NUMBER IN BLOCK TRANSFER

```

MLCP CHANNEL PROGRAM INFORMATION IS AS FOLLOWS

```

AA = CCP P VALUE, RCV
BB = CCP P VALUE, XMIT
CC = LR5 RCV/ LR5 XMIT
DD = ERROR CODE/LAST LR2 OUTPUT
EE = RCV LR7/CCP FLAG
FF = RCV CHAR COUNT (NEG)/XMIT CHAR COUNT (NEG)
GG = RCV FRAME (NEG)/XMIT FRAME (NEG)
HH = RCV CONFIG/ SIZE LAST XMIT BYTE

```

IF THE LEFT 8 BITS OF "DD" ARE NON ZERO THE MLCP
MLCP CHANNEL PROGRAM DETECTED AN ERROR. THE VALUE
SHOULD BE INTERPRETED FROM THE FOLLOWING TABLE:

```

1 LR7 STATUS WRONG AFTER INITIAL ILS RUPT
2 LR5 ADAPTER READY BIT NOT SET FOR RCV AFTER INITIAL ILS RUPT
3 BART DOESN'T BRANCH AFTER INITIAL ILS RUPT
4 LR5 ADAPTER RDY BIT SET WHEN SHOULD BE 0.
5 MISSED CLOSE FLAG, RANGE COMPLETED
6 NO ILS RUPT AFTER LAST FRAME.
7 MISSING ILS RUPT BETWEEN FRAMES
8 ADAPTER READY NOT SET AFTER FIRST CRI OF 2ND, 3D, OR 4TH FRAME.
9 ADAPTER READY SET WHEN SHOULD BE CLEARED
A LR7 ERROR IN LB 0,1,2 FOR RCV AT EOF.
B PENDING RECEIVE REQUEST AFTER LAST MESSAGE
C PENDING TRANSMIT REQUEST AFTER TRANSMIT SHUT OFF
D TWO SEQUENTIAL INPUT LR7 INSTRUCTIONS GAVE DIFFERENT RESULTS.
E DELAY XMIT BIT SET DIDN'T INHIBIT DATA TRANSMISSION.
10 READ CRC STATUS SET WHEN NOT IN DIAGNOSTIC MODE.

```

A PROGRAM HALT WITH P DISPLAYING A VALUE LESS THAN
HEX 100 INDICATES THAT A TRAP OCCURED. P DISPLAYS ONE
BEYOND THE ACTUAL TRAP VECTOR WHICH WAS TAKEN. THE
TRAP SAVE AREA IS FROM LOC. 2 - 9. REFER TO MODEL
34/36 T + V OPERATING INSTRUCTION MANUAL, DOC
71010460-200, SECTION 1.1 .

FOR SPECIALIST USAGE THERE IS A TABLE OF CHANNEL PROGRAM (CCP)
PARAMETERS WHICH IS FILLED AT THE TIME OF AN ERROR. (OUTTAB)

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*****
*****

```

RESTRICTIONS;

THIS PROGRAM REQUIRES A SYSTEM CONSOLE IF IT IS RUN ON A
SYSTEM WHICH DOES NOT HAVE THE FULL CONTROL PANEL.

BHCLA FIRMWARE IS AT REV 1 AT THE INITIAL RELEASE OF
THIS PROGRAM.

```

*****

```

SOURCE = DCMS3A3

STANDARD REGISTER ASSIGNMENTS

```

$B5 V$LIB ENTRY
$B2 ENTRY TO MAJOR TEST
$B4,$B1 ENTRY TO SUB-ROUTINES
E $B7E ENTRY TO ERROR ROUTINE
E $R3 CHANNEL

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*E $R5E      'IS' DATA WORD
*E $R6E      'SHOULD BE' DATA WORD
*E $R7E      TABLE COUNT
*
*****
* STATUS BITS SET BY CCP IN MLCP STATUS
*
* BIT 02 DATA SERVICE ERROR (X'2000')
* BIT 06 ABORT RECEIVED (X'0200')
* BIT 09 CRC ERROR (X'0040)
* BIT 11 EOF RECEIVED (X'0010')
*
* FOR RECEIVE THESE BITS ARE
* MAPPED FROM BITS 2,1,3, AND 0 OF LR7
*
*****
* TEST STRUCTURE (FOR SPECIALIST USE ONLY)
*
* CHANNEL PROGRAM INFORMATION
* DEFINITION OF CONTROL FLAG PASSED TO CCP
*
* BIT 7      INSERT 100 MS IN XMIT CCP AFTER CHAR "N" IS TRANSMITTED.
* BIT 6      SEND "ABORT" AFTER CHAR "N" IS TRANSMITTED.
* BIT 5      TURN ON RCV AFTER FRAME "X" IS TRANSMITTED.
* BIT 4      READ IN CRC INFORMATION
* BIT 3      INSERT 100 MS IN RCV CCP AFTER CHAR "N" IS INPUT.
* BIT 2      INSERT DELAY BETWEEN FRAMES
* BIT 1      ISSUE RCV RESYNC (OUT LR3)
* BIT 0      ACCUMULATE CHAR. COUNT. IF = 0, CCP'S USE MINIMUM DATA LOOP
*
* "X" AND "N" ARE FRAME AND CHAR NUMBERS PASSED TO THE CCP'S BEFORE
* EXECUTION TO SPECIFY AT WHAT TIME THE ACTION SPECIFIED BY THE ABOVE
* FLAG IS TO BE TAKEN. THE FIRST CHARACTER
* AND FRAME ARE NUMBERED "1".
*
* THE PERTINANT LCT PARAMETERS ARE:
*
* LOC      DESCRIPTION
*
* 3        SIZE OF LAST BYTE
* 4        SIZE OF RESIDUE EXPECTED
* E        LR5 STORAGE (RCV)
* 14       LR2 STORAGE (RCV + XMIT)
* 17       LR6, RCV
* 18       CHAR COUNT, RCV, (-).
* 19       CONTROL FLAG FRAME NO., XMIT
* 1A       LR7, RCV
* 1B       FRAME COUNT, RCV (-)
* 1C       RCV TEMPORARY STORAGE
* 1D       CONTROL FLAG CHAR NO., RCV
* 1E       XMIT FW REV
* 1F       RCV FW REV
* 2E       LR5 STORAGE, XMIT
* 37       LR7, XMIT
* 38       CHAR COUNT, XMIT, (-)
* 39       TEMPORARY STORAGE, XMIT
* 3A       ERROR CODE
* 3B       FRAME COUNT, XMIT, (-)
* 3C       CONTROL FLAG CHAR NO., XMIT
* 3D       WORD SIZE MASK
* 3E       CONTROL FLAG (SEE ABOVE DESCRIPTION)
* 3F       BIT 0 = INTER FRAME FILL STATE (0 = ABORTS)
*          BIT 1 = DELAYED TRANSMIT OF FIFO
*          BIT 2 = INITIAL FILL STATE (0 = ABORTS)
*          BITS 3 - 7 = CONTROL FLAG FRAME COUNT, RCV
*
*****
* CALLING SEQUENCE
*
* A COMMON CALLING SEQUENCE IS USED FOR DIFFERENT TESTS THROUGHOUT
* THE PROGRAM. IT IS:
*
* LNJ $B2,XXX CALL TO SPECIFIC TEST
* DC A        RCV CONFIG (LR6)
* DC B        XMIT CONFIG (LR7)
* DC C        BIT 0 = INTER FRAME FILL MODE (0 = ABORT)
*           BIT 1 = DELAY XMIT BIT
*           BIT 2 = INITIAL FILL MODE (0 = ABORT)
*           BITS 5 - 7 = BYTE SIZE OF LAST BYTE
*           (1 - 7 = 1 - 7 BITS, 0 = 8 BITS)
* DC D        INITIAL LR2 VALUE
* DC E        CCP CONTROL FLAG. (SEE PRIOR DESCRIPTION)
* DC F        BITS 0-7 = RCV FRAME FOR WHICH CONTROL FLAG IS VALID
*           THIS IS PASSED AS A NEGATIVE QUANTITY.
*           BITS 8 - 15 = CHAR NUMBER IN FRAME FOR WHICH
*           CONTROL FLAG IS VALID.
*           THIS IS ALSO PASSED AS A NEGATIVE QUANTITY.
* DC G        AS ABOVE BUT FOR XMIT
* DC H        1ST TRANSMIT BUFFER ADDRESS
* DC I        RANGE
* DC J        CONTROL WORD
*
*
* DC X        LAST TRANSMIT DATA BUFFER ADDRESS
* DC Y        RANGE
* DC Z        CONTROL WORD ( CONTAINS LAST BLOCK BIT)

```


000752	01A6	5048		SOR	\$R5,8	ALIGN EXTENDED ID
000753	01A7	D470	2100	OR	\$R5,=Z'2100'	
000754	01A9	DF00	116D	STR	\$R5,<DEVID	
000755				*		
000756	01AB	D970	21F6	CMR	\$R5,=Z'21F6'	
000757	01AD	09A9		BNE	>JP3	BR = NOT BH4DLD
000758	01AE	9570	03C0	AND	\$R1,=X'3C0'	
000759	01B0	1047		SOR	\$R1,7	GET LINE NUMBER
000760	01B1	ID04		CMV	\$R1,=4	CHECK FOR LEGAL LINE VALUE
000761	01B2	0353		BG	>JP2	BRANCH IF NO GOOD
000762				*		
000763	01B3	9F00	1177	STR	\$R1,<TPR	
000764	01B5	1041		SOR	\$R1,1	GET LINE ADAPTER NUMBER
000765	01B6	DF10	11B9	STR	\$R5,<ATLT,\$R1	STORE ID
000766				*		
000767	01B8	9800	1177	LDR	\$R1,<TPR	
000768	01BA	9F00	1C00	STR	\$R1,<RTB	
000769				CALL	ZV\$TC,IDMSG	LINE
	01BC	FBC0	0003			
	01BE	D380	0000	X		
	01C0	0F80				
	01C1	11F7				
000770				CALL	ZV\$TD,RTB	NUMBER
	01C2	FBC0	0003			
	01C4	D380	0000	X		
	01C6	0F80				
	01C7	1C00				
000771				CALL	ZV\$T,EQ	=
	01C8	FBC0	0003			
	01CA	D380	0000	X		
	01CC	0F80				
	01CD	11FB				
000772				CALL	ZV\$TH,DEVID	DEVICE ID
	01CE	FBC0	0003			
	01D0	D380	0000	X		
	01D2	0F80				
	01D3	116D				
000773				*		
000774	01D4	0F80	016E	B	<SALT	
000775				*		
000776	01D6	D970	21F7	JP3	CMR \$R5,=Z'21F7'	
000777	01D8	0980	0185	BNE	<JP2	B = NOT BH4DLE
000778	01DA	7C00		LDV	\$R7,=0	
000779	01DB	0F80	01AE	B	<CON4	
000780				*		
000781				**	MODIFY LIST OF 10 CONTROL WORDS	
000782				*		
000783				*		
000784				*		
000785	01DD	2CF4		CON3	LDV \$R2,=-12	NUMBER OF I/O TO BE CHANGED
000786				CALL	ZV\$IZ	INITIALIZE TRAPS AGAIN
	01DE	FBC0	0001			
	01E0	D380	0000	X		
000787	01E2	9800	11AE			
000788	01E4	DB80	11AE			
000789	01E6	C870	003F	ALL	LDR \$R1,<TMPSTR	GET FIRST ADDRESS INPUT
000790	01E8	9570	FC00	LAB	\$B5,<CON11+12	PUT ADDRESS OF CONTROL TABLE IN \$B5
000791	01EA	C525		LDR	\$R4,=X'3F'	LOAD MASK TO CLEAR CHANNEL
000792	01EB	9454		AND	\$R1,=Z'FC00'	CLEAR SUBCH. & FUNCTION
000793	01EC	9F25		AND	\$R4,\$B5,\$R2	CLEAR CHANNEL
000794	01ED	27F9		OR	\$R1,\$R4	PUT CHANNEL NUMBER IN
000795				STR	\$R1,\$B5,\$R2	STORE IT BACK IN CONTROL TABLE
000796				BINC	\$R2,>ALL	
000797				*		
000798	01EE	8700	117E	GSTR	CL <QFLG	CLEAR "QUICK FLAG"
000799	01F0	8700	117F	CL	<PFLAG	CLEAR PRINT FLAG
000800	01F2	8700	1184	CL	<XLLOOP	CLEAR EXT LOOP FLAG
000801	01F4	8700	1185	CL	<CBL00P	CLEAR CONNECTOR LOOP FLAG
000802				RUTBG	CALL ZV\$GC,MESG2	ASK FOR MODE
	01F6	FBC0	0003			
	01F8	D380	0000	X		
	01FA	0F80				
	01FB	11DE				
000803				CALL	ZV\$IA,MASK,TPR	
	01FC	FBC0	0003			
	01FE	D380	0000	X		
	0200	0F80				
	0201	1176				
	0202	1177				
000804	0203	9800	1177	LDR	\$R1,<TPR	GET INPUT
000805	0205	1048		SOR	\$R1,8	
000806	0206	9970	0043	CMR	\$R1,=X'43'	C
000807	0208	0F00	01F6	NOP	<RUTBG	
000808	020A	0980	0210	BNE	<RUT1	
000809	020C	8A80	1185	INC	<CBL00P	SET CONNECTOR LOOP FLAG
000810	020E	0F80	023E	B	<RUT4	
000811				*		
000812	0210	9970	0041	RUT1	CMR \$R1,=X'41'	A
000813	0212	0900	023E	BE	<RUT4	
000814	0214	9970	0045	CMR	\$R1,=X'45'	E
000815	0216	0980	021C	BNE	<RUTEND	
000816	0218	8A80	1184	INC	<XL00P	SET EXTERNAL LOOP FLAG
000817	021A	0F80	023E	B	<RUT4	
000818				*		
000819	021C	0F00	0210	RUTEND	NOP <RUT1	
000820	021E	9970	0051	CMR	\$R1,=X'51'	(Q)UICK PASS
000821	0220	0980	0226	BNE	<RUTBGA	
000822	0222	8A80	117E	INC	<QFLG	
000823	0224	0F80	01F6	B	<RUTBG	
000824	0226	9970	0050	RUTBGA	CMR \$R1,=X'50'	P
000825	0228	0980	022E	BNE	<RUTBGB	
000826	022A	8A80	117F	INC	<PFLAG	SET TO PRINT TEST LABELS
000827	022C	0F81	FFC9	B	RUTBG	
000828	022E	9970	0049	RUTBGB	CMR \$R1,=X'49'	I
000829	0230	0900	0105	BE	<RESTRT	
000830				*		
000831				CALL	ZV\$TC,NOGO	INVALID
	0232	FBC0	0003			
	0234	D380	0000	X		
	0236	0F80				
	0237	023A				
000832	0238	0F81	FFBD			
000833				*	b RUTBG	


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000834 023A 494E 5641 4C49      NOGO TEXT 'INVALIDS'
000835 023D 4424
000836 023E 9870 2A2A      *
000837 0240 9F00 11EA      RUT4 LDR $R1,=A***
000838          0242 FBC0 0003          STR $R1,<ERMG+1
          0244 D380 0000          CALL ZV$TC,MESG3      PRINT MLCP
          0246 OF80
          0247 0250
000839 0248 8753          CL =SR3
000840 0249 C380 0ED0          LNJ $B4,<FLN      SET CHANNEL NUMBER TO 0
000841 024B 0000          HLT $B4,<FLN      FIND ACTIVE LINE NUMBER
000842 024C E380 0C9A          LNJ $B6,<PREV     IMPOSSIBLE, INDICATES NO BBHDL
000843 024E OF80 0253          B <STLOOP        PRINT FIRMWARE REV NUMBER
000844 0250 4D4C 4350 2020      MESG3 TEXT 'MLCP'
000845
000846
000847
000848
000849
000850 0253 9800 025D      *
000851 0255 9F00 00FF      * JUMP TABLE FOR TESTS
000852 0257 8700 1179      *
000853 0259 8700 1199      *
000854 025B 8700 1198      *
000855
000856 025D OF76          *
000857 025E E380 02DE      NOP >STLOOP
000858 0260 OF7E          HD TSA LNJ $B6,<TON-AA      TURN ON TESTS
000859 0261 E380 02F1          LNJ $B6,<LPTS-A      SIMPLE DATA LOOP
000860 0263 OF7B          LNJ $B6,<LPTS-A
000861 0264 E380 07EE          LNJ $B6,<SPED-W      DO SPEED TEST.
000862 0266 OF78          LNJ $B6,<LPTS-A
000863 0267 E380 0384          LNJ $B6,<CBLP-E      CONNECTOR LOOP STATUS TEST
000864 0269 OF75          LNJ $B6,<LPTS-A
000865 026A 9800 1192          LDR $R1,<SPEED      GET SPEED/10
000866 026C 9970 1770          CMR $R1,=6000
000867 026E 0201 0006          BL LOSPD          B = LOW SPEED
000868 0270 1C01          LDV $R1,=1
000869 0271 9F00 1161          STR $R1,<SPFLG      SET FLAG FOR 72 KB
000870 0273 OF80 02B7          B <HSPD
000871
000872 0275 8700 1161      *
000873 0277 E380 0312      LOPSD CL <SPFLG
000874 0279 OF65          LNJ $B6,<LPTS-B      5 BIT CHAR
000875 027A E380 032D          LNJ $B6,<LPTS-A
000876 027C OF04          LNJ $B6,<LPT-BA      70 CHAR, 5 BIT MODE
000877 027D E380 034E          LNJ $B6,<LPTS-C      6 BIT CHAR.
000878 027F OF5F          LNJ $B6,<LPTS-D      7 BIT CHAR
000879 0280 E380 0369          LNJ $B6,<LPTS-D
000880 0282 OF5C          LNJ $B6,<TRAN-F      TRANSPARENCY TEST 1
000881 0283 E380 0408          LNJ $B6,<TRAN-F
000882 0285 OF59          LNJ $B6,<SUP-G      TEST SUPERVISORY CONTROL BIT
000883 0286 E380 042E          LNJ $B6,<SUP-G
000884 0288 OF56          LNJ $B6,<SUP-G
000885 0289 E380 044B          LNJ $B6,<PRT-GA      ICB, CFX, AFX TESTS
000886          *          INCLUDES TESTS GA TO GR.
000887 028B OF53          *
000888          *
000889 028C E380 04B6          LNJ $B6,<MULT-H      2 FRAMES BACK TO BACK
000890 028E OF50          LNJ $B6,<MULT-H
000891 028F E380 04D8          LNJ $B6,<MULT-I      2 FRAMES, FLAGS BETWEEN
000892 0291 OF04          LNJ $B6,<MULT-I
000893 0292 E380 04FB          LNJ $B6,<MULT-IA     2 FRAMES, DELAYED TRANSMISSION MODE
000894 0294 OF04          LNJ $B6,<MULT-IA
000895 0295 E380 051E          LNJ $B6,<TERM-J      SINGLE FRAME TERMINATION TESTS
000896          *          INCLUDES TESTS JA TO JK
000897 0297 OF01 0001          *
000898 0299 E380 0593          LNJ $+2
000899 029B OF04          LNJ $B6,<MULT-L      2 FRAMES, ABORTS BETWEEN
000900 029C E380 05B6          LNJ $+4
000901 029E OF04          LNJ $B6,<UND1-K      UNDERUN, ABORT TEST 1
000902 029F E380 05D1          LNJ $+4
000903 02A1 OF04          LNJ $B6,<RCV-EA      RECEIVE END TEST 1
000904 02A2 E380 05F0          LNJ $+4
000905 02A4 OF04          LNJ $B6,<RCV-EB      RECEIVE END TEST 2
000906 02A5 E380 060F          LNJ $+4
000907 02A7 OF2A          LNJ $B6,<RCV-EC      RECEIVE END TEST 3
000908 02A8 E380 062E          LNJ $B6,<RCV-ED      RECEIVE END TEST 4
000909 02AA OF27          LNJ $B6,<RCV-ED
000910 02AB E380 064D          LNJ $B6,<RCV-ED
000911          *          DO TWO FRAME TESTS
000912 02AD OF04          *          INCLUDES TESTS MA - M7
000913 02AE E380 077F          LNJ $+4
000914 02B0 OF21          LNJ $B6,<RCV-EN      RECEIVE END TEST
000915 02B1 E380 07A4          LNJ $B6,<RCV-EN
000916 02B3 OF1E          LNJ $B6,<RCV-EO      RECEIVE END TEST
000917 02B4 E380 07C9          LNJ $B6,<RCV-EO
000918 02B6 OF1B          LNJ $B6,<RCV-EP      RECEPVE END TEST
000919 02B7 E380 0817          LNJ $B6,<RCV-EP
000920 02B9 OF04          HSPD LNJ $B6,<RAN-KD      RANDOM DATA TEST
000921 02BA 8980 1161          LNJ $+4
000922 02BC 0300 02C0          CMZ <SPFLG
000923          *          B = 72 KB
000924 02BE E380 0856          LNJ $B6,<PCRC-X      PARTIAL BYTE, CRC TEST
000925 02C0 OF04          LNJ $+4
000926 02C1 8A80 1179          BYPSS INC <LOOP
000927 02C3 8980 117E          CMZ <OFLG          BUMP LOOPCOUNT
000928 02C5 0980 01EE          BNE <OFLG          CHEC QUICK FLAG
000929 02C7 OF0A          LNJ $+4            B = QUICK PASS
000930 02C8 8980 0000          CMZ <ZV$TTY
000931 02CA 0981 0006          BNE <ZV$TTY
000932 02CC 1C00          LDV $R1,=0
000933 02CD 9F00 00FF          STR $R1,<STOP
000934 02CF OF80 00FF          B <STOP
000935          PSPT CALL ZV$TC,PASMSG      INSERT EUP HALT
          02D1 FBC0 0003
          02D3 D380 0000
          02D5 OF80
          02D6 02DB
000936 02D7 8A80 1182          INC <PASSC          BUMP PASS COUNT
000937 02D9 OF80 00FF          B <STOP

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000938 02DB 5041 5353 2024
000939
000940
000941
000942
000943 02DE 9870 4141
000944 02E0 9F00 11EA
000945
000946 02E2 8700 116A
000947 02E4 A380 0AF2
000948 02E6 000C
000949 02E7 000C
000950 02E8 000C
000951 02E9 0045
000952 02EA 0080
000953 02EB 0000
000954 02EC 0000
000955 02ED 1800
000956 02EE 0005
000957 02EF 0060
000958
000959 02F0 8386
000960
000961
000962
000963
000964
000965 02F1 9870 2041
000966 02F3 9F00 11EA
000967
000968 02F5 FBC0 0003
000969 02F7 D380 0000
000970 02F9 0F80
000971 02FA 1800
000972 02FB 117D
000973 02FC 118F
000974 02FD 8700 1174
000975 02FE AB80 0307
000976 0301 AF80 0ECF
000977 0303 C380 0D80
000978
000979 0305 A380 08CD
000980 0307 000C
000981 0308 000C
000982 0309 000C
000983 030A 0047
000984 030B 0000
000985 030C 0000
000986 030D 0000
000987 030E 1800
000988 030F 0010
000989 0310 0060
000990
000991 0311 8386
000992
000993
000994 0312 9870 2042
000995 0314 9F00 11EA
000996 0316 B380 0D3D
000997
000998 0318 AB80 0322
000999 031A AF80 0ECF
001000 031C 8700 1174
001001 031E C380 0D80
001002
001003 0320 A380 08CD
001004 0322 0000
001005 0323 0000
001006 0324 0000
001007 0325 0047
001008 0326 0000
001009 0327 0000
001010 0328 0000
001011 0329 1800
001012 032A 0020
001013 032B 0060
001014
001015 032C 8386
001016
001017
001018 032D 9870 4241
001019 032F 9F00 11EA
001020
001021 0331 FBC0 0003
001022 0333 D380 0000
001023 0335 0F80
001024 0336 1800
001025 0337 117D
001026 0338 1190
001027 0339 AB80 0343
001028 033B AF80 0ECF
001029 033D 8700 1174
001030 033F C380 0D80
001031
001032 0341 A380 08CD
001033 0343 0000
001034 0344 0000
001035 0345 0047
001036 0346 0000
001037 0347 0000
001038 0348 0000
001039 0349 0000
001040 034A 1800
001041 034B 0046
001042 034C 0060
001043
001044 034D 8386
001045

```

```

PASMSG TEXT 'PASS $'
-----
*
* INITIAL TURN ON TESTS
*
TON-AA LDR $R1,=A'AA'
STR $R1,<ERMG+1 REPORT TEST SECTION
*
CL <CHSZ SET FOR 8 BIT BYTES
LNJ $B2,<TONTST INITIAL TURN ON TEST
DC X'C' RCV CONFIG, 8 BITS, LR 6
DC X'C' XMIT CONFIG, 8 BITS, LR 7
DC X'C' FILL MUDE, ABORT IDLE
DC X'45' LR2, TEST, XMIT ON
DC X'80' ACTION FLAG
DC 0 RCV ACTION FRAME, CHAR
DC 0 XMIT ACT FRAME, CHAR
DC <SDB DATA ADDRESS
DC 5 RANGE
DC X'60' LAST BLOCK, VALID
*
JMP $B6 EXIT
*****
*
* - SIMPLE DATA TEST
*
LPTS-A LDR $R1,=A' A'
STR $R1,<ERMG+1 REPORT TEST
CALL ZV$F,SDB,PAT,C8 FILL 8 WORDS OF 0303
*
CL <HEAD CLEAR HEADER FLAG
LAB $B2,<PAR1
STB $B2,<PARPTR STORE PARAMETER POINTER
LNJ $B4,<GHEAD GENERATE HEADER
*
PAR1 LNJ $B2,<LPTS LOOP TEST
DC =X'0' LR6, RCV CFG, 8 BITS
DC X'C' LR7, XMIT, 8 BITS
DC X'C' FILL STATE, ABORT IDLE
DC =X'47' LR2 CONTROL, RCV, XMIT, TEST
DC X'0' ACTION FLAG
DC 0 RCV ACTION FRAME, CHAR
DC 0 XMIT ACT FRAME, CHAR
DC <SDB DATA
DC 16 RANGE
DC X'60' LAST BLOCK, VALID
*
JMP $B6
-----
*
* LOOP TEST B - 5 BIT CHARACTERS, ASCENDING DATA
*
LPTS-B LDR $R1,=A' B'
STR $R1,<ERMG+1 REPORT TEST
LNJ $B3,<FACDATA FILL ASCENDING DATA
*
LAB $B2,<PAR2
STB $B2,<PARPTR
CL <HEAD CLEAR HEADER FLAG
LNJ $B4,<GHEAD GENERATE HEADER
*
PAR2 LNJ $B2,<LPTS
DC 0 5 BITS, LR6, RCV
DC X'0' 5 BITS, XMIT, LR7
DC 0 FILL MUDE = ABORT
DC =X'47' LR2 - RCV, XMIT, TEST
DC X'0' ACTION FLAG
DC 0 RCV ACTION FRAME, CHAR
DC 0 XMIT ACT FRAME, CHAR
DC <SDB DATA
DC X'20' RANGE
DC X'60' LAST,V
*
JMP $B6
-----
*
* TEST USING 5 BIT CHAR AND RANGE > FIFO LENGTH
*
LPT-BA LDR $R1,=A'BA'
STR $R1,<ERMG+1 REPORT TEST
CALL ZV$F,SDB,PAT,C70 FILL 70 WORDS OF 0303
*
LAB $B2,<PAR3
STB $B2,<PARPTR
CL <HEAD CLEAR HEADER FLAG
LNJ $B4,<GHEAD GENERATE HEADER
*
PAR3 LNJ $B2,<LPTS LOOP TEST
DC =X'0' LR6, RCV CFG, 5 BITS
DC 0 LR7, XMIT, 5 BITS
DC 0 FILL STATE, ABORT IDLE
DC =X'47' LR2 CONTROL, RCV, XMIT, TEST
DC X'0' ACTION FLAG
DC 0 RCV ACTION FRAME, CHAR
DC 0 XMIT ACT FRAME, CHAR
DC <SDB DATA
DC 70 RANGE
DC X'60' LAST BLOCK, VALID
*
JMP $B6
-----

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001039
001040
001041
001042 034E 9870 2043
001043 0350 9F00 11EA
001044 0352 B380 0D3D
001045
001046 0354 AB80 035E
001047 0356 AF80 0ECF
001048 0358 8700 1174
001049 035A C380 0D80
001050
001051 035C A380 08CD
001052 035E 0004
001053 035F 0004
001054 0360 0004
001055 0361 0047
001056 0362 0000
001057 0363 0000
001058 0364 0000
001059 0365 1800
001060 0366 0040
001061 0367 0060
001062
001063 0368 8386
001064
001065
001066
001067
001068 0369 9870 2044
001069 036B 9F00 11EA
001070 036D B380 0D3D
001071
001072 036F AB80 0379
001073 0371 AF80 0ECF
001074 0373 8700 1174
001075 0375 C380 0D80
001076
001077 0377 A380 08CD
001078 0379 0008
001079 037A 0008
001080 037B 0008
001081 037C 0047
001082 037D 0000
001083 037E 0000
001084 037F 0000
001085 0380 1800
001086 0381 0080
001087 0382 0060
001088
001089 0383 8386
001090
001091
001092
001093
001094
001095
001096
001097
001098
001099
001100
001101
001102
001103
001104
001105
001106
001107
001108
001109 0384 9870 2045
001110 0386 9F00 11EA
001111 0388 8F00 2063
001112 038A 0002
001113
001114 038B 9800 1185
001115 038D 1980 0393
001116 038F 8F80 2063
001117 0391 0002
001118 0392 8386
001119
001120 0393 C380 2000
001121 0395 8753
001122 0396 C380 0ED0
001123 0398 0FF7
001124 0399 C380 0EEC
001125
001126 039B 3880 039E
001127 039D 8AD3
001128 039E 9380 1095
001129 03A0 139A
001130 03A1 004E
001131 03A2 0200
001132 03A3 0000
001133
001134 03A4 BF00 1177
001135
001136
001137
001138 03A6 3042
001139 03A7 9830 1189
001140 03A9 9F00 116D
001141 03AB 8800 1177
001142 03AD 9970 21F7
001143 03AF 0905
001144
001145 03B0 B380 10EB
001146 03B2 03F8
001147 03B3 0FB4
001148 03B4 B380 10EB
001149 03B6 03FD

* LOOP TEST C - 6 BIT CHAR, ASCENDING DATA
* LPTS=C LDR $R1,=A' C'
STR $R1,<ERMG+1
LNJ $B3,<FACDTA FILL ASCENDING DATA
*
LAB $B2,<PAR4
STB $B2,<PARPTR
CL <HEAD CLEAR HEADER FLAG
LNJ $B4,<GHEAD GENERATE HEADER
*
PAR4 LNJ $B2,<LPTS 6, BITS, RCV LR 6
DC 4 6 BITS, LRT, XMIT
DC X'4' FILL MODE, ABORT
DC 4 LR2, RCV, XMIT, TEST
DC =X'47' ACTION FLAG
DC X'0' RCV ACTION FRAME, CHAR
DC 0 XMIT ACT FRAME, CHAR
DC <SDB DATA
DC =X'40' RANGE
DC X'60' LAST,V
*
JMP $B6
-----
* LOOP TEST D - 7 BIT CHARACTERS, ASCENDING DATA
* LPTS=D LDR $R1,=A' D'
STR $R1,<ERMG+1 REPORT
LNJ $B3,<FACDTA FILL ASCENDING DATA
*
LAB $B2,<PAR5
STB $B2,<PARPTR
CL <HEAD CLEAR HEADER FLAG
LNJ $B4,<GHEAD GENERATE HEADER
*
PAR5 LNJ $B2,<LPTS LOOP TEST
DC 8 7 BITS, LR6, RCV
DC X'8' 7 BITS, LRT, XMIT
DC 8 ABORT IDLE, LAST = 7 BITS
DC X'47' LR2, RCV, XMIT, TEST
DC X'0' ACTION FLAG
DC 0 RCV ACTION FRAME, CHAR
DC <SDB XMIT ACT FRAME, CHAR
DC X'80' DATA
DC X'60' LAST,V
*
JMP $B6
-----
* CONNECTOR LOOP STATUS TEST
* SEE THE PROGRAM HEADING FOR A DESCRIPTION OF LOOP-BACK
* CONNECTORS FOR BH4DL0 AND BH4DLE.
*
* IN THE TEST A CHANNEL PROGRAM IS SET UP WHICH OUTPUTS THE
* FOLLOWING 4 VALUES TO LR2.
* X'00',X'80',X'40',X'3C'
*
* THE FOLLOWING VALUES ARE INPUT FROM LK5.
*
* BH4DL0 00,10,E0,0
* BH4DLE 00,90,60,0
*
CBLP-E LDR $R1,=A' E'
STR $R1,<ERMG+1 REPORT TEST
SAVE <SAVMAJ,=Z'0002' SAVE B6
*
LDR $R1,<CBLOOP GET CONNECTOR LOOP FLAG
BNEZ $R1,<CBLP1 BRANCH IF THERE IS A CONNECTOR LOOP
RSTR <SAVMAJ,=Z'0002'
*
JMP $B6 EXIT TEST, NO CONNECTOR LOOP
*
CBLP1 LNJ $B4,<PLB PRINT TEST LABEL
CL =R3 SET FOR CHANNEL 0
CBLP2 LNJ $B4,<FLN FIND ACTIVE LINE
B >CBLP8 NO MORE LINES
LNJ $B4,<GENITZ DO GENERAL INITIALIZE
* SEND OUT CHANNEL PROGRAM
*
BODD $R3,<CBLP7 BRANCH IF XMIT CHANNEL
INC =R3 MAKE XMIT CHAN
*
CBLP7 LNJ $B1,<SDATA CHANNEL PROGRAM
DC <CCP4 RANGE
DC (CCP5-CCP4)*2 RAM ADDRESS
DC X'200' EVEN CPU ADDRESS
*
STR $R3,<TPR
* SEND OUT DATA FOR CHANNEL PROGRAM IF BH4DLE
*
LDR $R3,2 GET LA NUMBER
LDR $R1,<ATLT,$R3 GET ID
STR $R1,<DEVID
LDR $R3,<TPR
CMR $R1,=Z'21F7'
BE >CHLPY B = BH4DLE
*
LNJ $B3,<SETLCT
DC <LCT9 LCT FOR BH4DL0
B >CHLPY
CHLPY LNJ $B3,<SETLCT
DC <LCT10 LCT FOR BH4DLE

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001150
001151
001152
001153
001154 03B7 B380 10EB
001155 03B9 0402
001156
001157 03BA 8751
001158 03BB C380 0F0B
001159 03BD 0F82
001160 03BE 4000
001161 03BF C380 1067
001162
001163
001164
001165 03C1 9B80 03F0
001166 03C3 A800 116D
001167 03C5 A970 21F6
001168 03C7 0903
001169 03C8 9B80 03F4
001170
001171 03CA C380 0F9E
001172 03CC D570 F700
001173 03CE E811
001174 03CF D956
001175 03D0 0900 03D4
001176 03D2 E3C0 0BDB
001177
001178
001179
001180 03D4 C800 11AB
001181 03D6 BF00 1177
001182 03D8 3B00 03DB
001183 03DA 88D3
001184 03DB C380 0EE8
001185 03DD 8055
001186 03DE 0054
001187 03DF 0703
001188 03E0 E380 0FBA
001189
001189 03E2 B800 1177
001190 03E4 8756
001191 03E5 5900 03E9
001192 03E7 E3C0 0BC6
001193
001194 03E9 8AD1
001195 03EA 1D04
001196 03EB 0201 FFCF
001197 03ED 3E04
001198 03EE 0F80 0396
001199
001200
001201 03F0 0000
001202 03F1 1000
001203 03F2 E000
001204 03F3 0000
001205
001206
001207
001208 03F4 0000
001209 03F5 9000
001210 03F6 6000
001211 03F7 0000
001212
001213
001214 03F8 103E
001215 03F9 E03F
001216 03FA 0105
001217 03FB 0125
001218 03FC 0000
001219
001220
001221
001222 03FD 903E
001223 03FE 603F
001224 03FF 0105
001225 0400 0125
001226 0401 0000
001227
001228
001229
001230
001231 0402 0206
001232 0403 0007
001233 0404 0226
001234 0405 0027
001235 0406 1037
001236 0407 0000
001237
001238
001239
001240
001241
001242
001243
001244
001245 0408 9870 2046
001246 040A 9F00 11EA
001247
001248
001249
001250
001251 040C 9880 1800
001252 040E 9870 FFFF
001253 0410 9F71
001254 0411 9870 FCFE
001255 0413 2CC0
001256
001257 0414 9F71
001258 0415 1011
001259 0416 27FE
001260 0417 AB80 0421
001261 0419 AF80 OECE
    
```

```

*
* SEND OUT CHANNEL PROGRAM START
*
CHLPX  LNJ  $B3,<SETLCT
        DC  <LCT6
*
CBLP9  CL   =$R1
CBLP5  LNJ  $B4,<CHCT
        B   >$+2
        DC  Z'4000'
        LNJ  $B4,<DLAYLG
*
* READ DATA SET STATUS
*
LAB    $B1,<DS-SB
LDR    $R2,<DEVID
CMR    $R2,Z'21F6'
BE     >CUK
LAB    $B1,<DS-SB1
*
CUR    LNJ  $B4,<DSSTA
        AND $R5,Z'F700'
        LDR $R6,$B1,$R1
        CMR $R5,$R6
        BE  <CBLP-3
        LNJ $B6,ERRDB
*
* READ LCT STAT - CHAN PROG SETS BIT 5 = 1 IF IT FAILED
*
CBLP-3 LDR  $R4,<CONTIO
        STR $R3,<TPR
        BEVN $R3,CBLP6
        DEC  =$R3
CBLP6  LNJ  $B4,<CGSCH
        IO  =$R5,=$R4
*
BIOT   >$+2+$AF
LNJ    $B6,<ERRMB
*
LDR    $R3,<TPR
CL     =$R6
BEZ    $R5,<CBLP4
LNJ    $B6,ERRDB
*
CBLP4  INC  =$R1
        CMV $R1,=4
        BL  CBLP5
        ADV $R3,=4
        B  <CBLP2
*
* TABLE OF VALUES TO BE RETURNED (BH4DL)
DS-SB  DC  Z'0000'
        DC  Z'1000'
        DC  Z'E000'
        DC  Z'0'
*
* BH4DLE
DS-SB1 DC  Z'0000'
        DC  Z'9000'
        DC  Z'6000'
        DC  Z'0000'
*
LCT9   DC  Z'103E'
        DC  Z'E03F'
        DC  Z'0105'
        DC  Z'0125'
        DC  0
*
* LCT FOR BH4DLE
LCT10  DC  Z'903E'
        DC  Z'603F'
        DC  Z'0105'
        DC  Z'0125'
        DC  0
*
* LCT TABLE
LCT6   DC  Z'0206'
        DC  Z'0007'
        DC  Z'0226'
        DC  Z'0027'
        DC  Z'1037'
        DC  0
*
*-----*
*
* TRANSPARENCY TEST, FORCE "0 INSERTION" TO OCCUR ON ALL BYTE
* BOUNDRIES WITHIN MESSAGE
*
TRAN-F LDR  $R1,=A' F'
        STR  $R1,<ERMG+1
*
**
** FORM DATA, FIRST TWO CHAR. ARE HEX 'FF'. THEN FOLLOWS A
** ROTATING PATTERN WITH 0'S FLOATING THROUGH A FIELD OF ONES.
*
LAB    $B1,<SDB
LDR    $R1,Z'FFFF'
STR    $R1,+$B1
LDR    $R1,Z'FC7E'
LDV    $R2,=-X'40'
*
TRAN1  STR  $R1,+$B1
        SCL $R1,1
        BINC $R2,>TRAN1
LAB    $B2,<PAR6
        STB $B2,<PARPTR
    
```

DO CHANNEL CONTROL

START IO
225 MS DELAY

GET ID

BRANCH = BH4DL

STRIP STATUS IO PERTINENT BITS
PICK UP SHOULD BE

BRANCH IF GOOD
DATA SET STATUS BAD

INPUT LCT STATUS
STORE CHANNEL NUMBER
BRANCH IF RECEIVE CHANNEL

FORM FC AND ADDRESS
INPUT LCT STATUS TO R5

INSTRUCTION WAS NAK'ED

GET BACK TEST CHANNEL

ERROR, CHANNEL PROGRAM GOT
WRONG DATA SET STATUS

DO FOR NEXT VALUE
BUMP CHANNEL NUMBER
TRY NEXT CHANNEL

PAUSE DISABLE, RECEIVE
DITTO, XMIT

PAUSE DISABLE,RCV
PAUSE DISABLE, XMIT

RCV P, MSB
RCV P, LSB
XMIT P, MSB
XMIT P, LSB
BYTE TO INPUT
END OF LIST

```

001262 041B 8700 1174          CL  <HEAD
001263 041D C380 0D80          LNJ  $B4,<GHEAD      CLEAR HEADER FLAG
                                *                               GENERATE HEADER
001264
001265
001266 041F A380 08CD          *
                                *
001267 0421 000C          PAR6 LNJ  $B2,<LPTS      LOOP TEST
                                DC   X'0C'      LR 6, RCV, 8 BITS
                                DC   X'0C'      LR7, XMIT
                                DC   X'0C'      FILL MODE, ABORT
                                DC   X'47'      LR2 CONTROL, RCV, XMIT, TEST
                                DC   X'0'       ACTION FLAG
                                DC   0        RCV ACTION FRAME, CHAR
                                DC   0        XMIT ACT FRAME, CHAR
                                DC   <SDB     DATA ADDRESS
                                DC   X'82'    RANGE
                                DC   X'60'    LAST, VALID
001271
001272 0422 0047          *
001273 0426 0000          *
001274 0427 1800          *
001275 0428 0082          *
001276 0429 0060          *
001277
001278 042B 0F00 0414          *
001279 042D 8386          *
                                *
001280
001281
001282
001283
001284
001285
001286
001287
001288
001289
001290 042E 9870 2047          *
001291 0430 9F00 11EA          SUP-G LDR  $R1,=A' G'      REPORT
001292 0432 9870 8000          STR  $R1,<ERMG+1        SET BIT FOR SUPERVISORY FRAME
001293 0434 9F00 1174          LDR  $R1,=Z'8000'
001294 0436 AB80 0440          STR  $R1,<HEAD
001295 0438 AF80 0ECF          LAB  $B2,<PAR7
001296 043A B380 0D3D          STB  $B2,<PARPTR
                                LNJ  $B3,<FACDTA      FILL ASCENDING DATA
                                *                               GENERATE HEADER
001297
001298 043C C380 0D80          *
001299
001300 043E A380 08CD          *
001301 0440 0008          PAR7 LNJ  $B2,<LPTS      LOOP TEST
001302 0441 0008          DC   X'08'      7 BITS, LR6, RCV
001303 0442 000C          DC   X'08'      7 BITS, LR7, XMIT
001304 0443 0047          DC   X'0C'      ABORT IDLE, 0 BIT RESIDUE
001305 0444 0000          DC   X'47'      LR2, RCV, XMIT, TEST
001306 0445 0000          DC   X'0'       ACTION FLAG
001307 0446 0000          DC   0        RCV ACTION FRAME, CHAR
001308 0447 1800          DC   0        XMIT ACT FRAME, CHAR
001309 0448 005A          DC   <SDB     DATA
001310 0449 0060          DC   90       RANGE
001311
001312 044A 8386          *
001313
001314
001315
001316
001317
001318
001319
001320
001321
001322 044B 8753          *
001323 044C 8752          *
                                *
001324
001325 044D 9B80 0487          PRT-GA CL  = $R3        SET FOR CHANNEL 0
001326 044F 92ED          PRLNLP CL  = $R2
                                *
001327
001328 0450 9F00 047A          PRT-LP LAB  $B1,<PRO-A    GET ADDRESS OF TABLE
001329 0452 9570 000E          LLH  $R1,$B1,+$R2      GET LR7 FOR XMIT
001330 0454 9F00 047B          *
001331 0456 1041          PRT-1 STR  $R1,<GLR7        STRIP TO BYTE SIZE
001332 0457 AB80 04AD          AND  $R1,=Z'000E'
001333 0459 C812          STR  $R1,<GLR8
001334 045A CF51          SUR  $R1,1
001335 045B 1008          LAB  $B2,<TCBBSZ        GET TCB CONVERSION ADD
001336 045C 9F00 1174          LDR  $R4,$B2,$R1
001337 045E 92ED          STR  $R4,=$R1
001338 045F 9F00 0479          SOL  $R1,8            TCB SIZE (IF CONFIGURED)
001339 0461 92ED          STR  $R1,<HEAD
001340 0462 9400 1174          LLH  $R1,$B1,+$R2      GET LR6 FOR RCV
001341 0464 9F00 1174          STR  $R1,<GLR6
001342 0466 92ED          LLH  $R1,$B1,+$R2      GET LCP, AFX INFO
001343 0467 1982          OR   $R1,<HEAD
001344 0468 8386          STR  $R1,<HEAD
001345
001346 0469 9470 4700          BNEZ $R1,>PRT-2        GET TEST LABEL
001347 046B 9F00 11EA          JMP  $B6            DONE WITH TEST
001348 046D AF00 077E          *
001349
001350
001351
001352
001353
001354 046F B380 0D3D          PRT-2 OR   $R1,=Z'4700'  PUT IN G
001355 0471 AB80 0479          STR  $R1,<ERMG+1        STORE TEST LABEL
001356 0473 AF80 0ECF          STR  $R2,<DEX          STORE INDEX
001357 0475 C380 0D80          *
001358
001359 0477 A380 08CD          *
001360 0479 0000          *
001361 047A 0000          *
001362 047B 0000          *
001363 047C 0047          *
001364 047D 0000          *
001365 047E 0000          *
001366 047F 0000          *
001367 0480 1800          *
001368 0481 0100          *
001369 0482 0060          *
001370
001371 0483 AB80 077E          *
001372 0485 0F80 044D          *
                                *
001373
001374

```

```

001375
001376
001377
001378
001379
001380
001381
001382
001383
001384
001385
001386
001387
001388 0487 8888
001389 0488 0041
001390 0489 2C2C
001391 048A 0042
001392 048B 2828
001393 048C 0043
001394 048D 2020
001395 048E 0044
001396 048F 2424
001397 0490 0045
001398 0491 4848
001399 0492 2046
001400 0493 4444
001401 0494 3047
001402 0495 4040
001403 0496 4048
001404 0497 4C4C
001405 0498 5049
001406 0499 0909
001407 049A 014A
001408 049B 0505
001409 049C 024B
001410 049D 0101
001411 049E 044C
001412 049F 0D0D
001413 04A0 054D
001414 04A1 E9E9
001415 04A2 544E
001416 04A3 E5E5
001417 04A4 454F
001418 04A5 EDED
001419 04A6 1150
001420 04A7 E9E9
001421 04A8 2351
001422 04A9 E9E9
001423 04AA 5552
001424 04AB 0000
001425
001426
001427
001428 04AD 0005
001429 04AE 0001
001430 04AF 0002
001431 04B0 0007
001432 04B1 0007
001433 04B2 0003
001434 04B3 0000
001435 04B4 0004
001436 04B5 0000
001437
001438
001439
001440 04B6 9870 2048
001441 04B8 9F00 11EA
001442 04BA 9870 1100
001443 04BC 9F00 117B
001444
001445
001446
001447 04BE B380 0D3D
001448 04C0 8700 1174
001449 04C2 AB80 04CA
001450 04C4 AF80 0ECF
001451 04C6 C380 0D80
001452
001453
001454 04C8 A380 0A4A
001455 04CA 0008
001456 04CB 0008
001457 04CC 0008
001458 04CD 0047
001459 04CE 0000
001460 04CF 0000
001461 04D0 0000
001462
001463 04D1 1800
001464 04D2 0060
001465 04D3 0040
001466
001467 04D4 1830
001468 04D5 0020
001469 04D6 0060
001470
001471 04D7 8386
001472
001473
001474
001475
001476
001477
001478
001479
001480
001481 04D8 9870 2049
001482 04DA 9F00 11EA
001483 04DC 9870 1100
001484 04DE 9F00 117B
001485
001486
001487

```

```

* TABLE OF TESTS FOR PROTOCOL TESTS
*
* WORD 1
*
*   BITS 0 - 7   LR7, XMIT
*   BITS 8 - 15  LR6, RCV
*
* WORD 2
*
*   BITS 0 - 3   ADDRESS FIELD LENGTH
*   BITS 4 - 7   LCF LENGTH
*   BITS 8 - 15  ASCII TEST IDENTIFIER
*
PRO-A DC Z'8888' CFX, 7 BITS
      DC Z'0041' TEST GA
PRO-B DC Z'2C2C' TCB, 8 BITS
      DC Z'0042' TEST GB
PRO-C DC Z'2828' TCB, 7 BITS
      DC Z'0043' TEST GC
PRO-D DC Z'2020' TCB, 5 BITS
      DC Z'0044' TEST GD
PRO-E DC Z'2424' TCB, 6 BITS
      DC Z'0045' TEST GE
PRO-F DC Z'4848' ADDRESS FIELD EXTENTION 7 BITS
      DC Z'2046' ADD = 2 BYTES
PRO-G DC Z'4444' AFX, 6 BITS
      DC Z'3047' 3 BYTE ADDRESS FIELD
PRO-H DC Z'4040' AFX, 5 BITS
      DC Z'4048' 4 BYT4
PRO-I DC Z'4C4C' AFX, 8 BITS
      DC Z'5049' 5 BYTES
PRO-J DC Z'0909' LOGICAL CONTROL FIELD TEST, 7 BITS
      DC Z'014A' 1 BYTE LCF
PRO-K DC Z'0505' LCF TEST, 6 BITS
      DC Z'024B' 2 BYTE LCF
PRO-L DC Z'0101' LCF, 5 BITS
      DC Z'044C' 4 BYTES
PRO-M DC Z'0D0D' LCF TEST, 8 BITS
      DC Z'054D' 5 BYTES
PRO-N DC Z'E9E9' ALL FIELDS, 7 BITS
      DC Z'544E' 5 BYTE LCF, 4 BYTE AFX
PRO-O DC Z'E5E5' ALL FIELDS, 6 BITS
      DC Z'454F' 4 BYTE LCF, 5 BYTE AFX
PRO-P DC Z'EDED' 8 BITS
      DC Z'1150' 1 LCF, 1 AFX
PRO-Q DC Z'E9E9'
      DC Z'2351' 2 LCF, 3 AFX
PRO-R DC Z'E9E9' TCB ON XMIT, NOT RECEIVE
      DC Z'5552'
      DC Z'0' END OF LIST
RESV Z'0

```

* CONVERSION TABLE FOR BYTE SIZE CONFIG TO TCB CODE

```

*
* TCBBSZ DC 5
*        DC 1
*        DC 6
*        DC 7
*        DC 3
*        DC 0
*        DC 4
*        DC 0
*
* 0 IS CODE FOR 8

```

* TRANSFER AND RECEIVE TWO MESSAGES BACK TO BACK

```

*
MULT-H LDR $R1,=A'H'
      STR $R1,<ERMG+1' REPORT TEST SECTION
      LDR $R1,=Z'1100' EXPECTED STATUS FLAG
      STR $R1,<EXST

```

* FORM DATA (ASCENDING)

```

*
LNJ $B3,<FACDTA FILL SEND BUFFER WITH ASCENDING DATA
CL <HEAD CLEAR HEADER FLAG
LAB $B2,<PARB
STB $B2,<PARPTR
LNJ $B4,<GHEAD GENERATE HEADER

```

* PARB

```

LNJ $B2,<RET RETURN END TEST
DC 8 LR6, RCV
DC 8 LR7, XMIT
DC 8 FILL WITH ABORTS
DC X'47' LR2,RCV, XMIT,TEST
DC X'0' ACTION FLAG
DC 0 RCV ACTION FRAME, CHAR
DC 0 XMIT ACT FRAME, CHAR

```

* <SDB

```

DC <SDB XMIT BUFFER 1
DC X'60' RANGE
DC X'40' VALID BIT

```

* <SDB+X'30'

```

DC <SDB+X'30' DATA ADD 2
DC X'20' RANGE
DC X'60' LAST, VALID, NO DELAY

```

* TRANSFER, RECEIVE 2 FRAMES, FLAGS BETWEEN

```

*
MULT-I LDR $R1,=A'I'
      STR $R1,<ERMG+1' REPORT TEST SECTION
      LDR $R1,=Z'1100' EXPECTED STATUS FLAG
      STR $R1,<EXST

```

* FORM DATA (ASCENDING)

```

001488
001489 04E0 B380 0D3D
001490 04E2 8700 1174
001491 04E4 AB80 04EC
001492 04E6 AF80 0ECF
001493 04E8 C380 0D80
001494
001495
001496 04EA A380 0A4A
001497 04EC 0004
001498 04ED 0004
001499 04EE 0084
001500 04EF 0047
001501 04F0 0020
001502 04F1 0000
001503 04F2 0000
001504 04F3 1800
001505 04F4 0010
001506 04F5 0040
001507
001508 04F6 1808
001509 04F7 0010
001510 04F8 0060
001511
001512 04F9 0F5F
001513
001514 04FA 8386
001515
001516
001517
001518
001519
001520
001521 04FB 9870 4941
001522 04FD 9F00 11EA
001523 04FF 9870 1100
001524 0501 9F00 117B
001525
001526
001527
001528
001529 0503 B380 0D3D
001530 0505 8700 1174
001531 0507 AB80 050F
001532 0509 AF80 0ECF
001533 050B C380 0D80
001534
001535
001536 050D A380 0A4A
001537 050F 0004
001538 0510 0004
001539 0511 00C4
001540 0512 0047
001541 0513 0020
001542 0514 0000
001543 0515 0000
001544 0516 1800
001545 0517 0010
001546 0518 0040
001547
001548 0519 1808
001549 051A 0010
001550 051B 0060
001551
001552 051C 0F5F
001553
001554 051D 8386
001555
001556
001557
001558
001559
001560
001561
001562
001563
001564
001565
001566
001567
001568 051E 8752
001569
001570 051F 9880 056C
001571 0521 92ED
001572 0522 9F00 055E
001573 0524 9570 000E
001574 0526 9F00 055F
001575 0528 1041
001576
001577 0529 C810 04AD
001578 052B CF51
001579
001580 052C 1008
001581 052D 9F00 1174
001582 052F 92ED
001583 0530 9F00 055D
001584 0532 92ED
001585 0533 9400 1174
001586 0535 9F00 1174
001587 0537 92ED
001588 0538 1900 056B
001589 053A 9470 4A00
001590 053C 9F00 11EA
001591
001592 053E 92ED
001593 053F 9470 0080
001594 0541 9F00 0561
001595 0543 92ED
001596 0544 AF00 077E
001597 0546 9F52
001598 0547 9470 FF00
001599 0549 9F00 0563
001600 054B 8252

*
LNJ $B3,<FACDTA
CL <HEAD
LAB $B2,<PAR9
STB $B2,<PARPTR
LNJ $B4,<GHEAD
CLEAR HEADER FLAG
GENERATE HEADER

*
*
PAR9 LNJ $B2,<RET
DC 4
DC 4
DC X'84'
DC X'47'
DC X'20'
DC 0
DC 0
DC <SDB
DC X'10'
DC X'40'
RECEIVE END TEST
LR6,RCV
LR7,XMIT
FILL WITH FLAGS
LR2 CONTROL,RCV,XMIT,TEST
ACTION FLAG,DELAY BETWEEN FRAMES
RCV ACTION FRAME,CHAR
XMIT ACT FRAME,CHAR
SEND BUFFER
RANGE
VALID

*
DC <SDB+X'8'
DC X'10'
DC X'60'
ADD,2ND CCB
RANGE
LAST,VALID.

*
NOP >MULT-1

*
JMP $B6
EXIT TEST

-----
*
*
* TRANSFER, RECEIVE 2 FRAMES, FLAGS BETWEEN, DELAYED XMIT OF XMIT FIFO
*
*
MUL-IA LDR $R1,=A'IA'
STR $R1,<ERMG+1
LDR $R1,=Z'1100'
STR $R1,<EXST
REPORT TEST SECTION
EXPECTED STATUS FLAG

*
*
* FORM DATA (ASCENDING)
*
*
LNJ $B3,<FACDTA
CL <HEAD
LAB $B2,<PAR9IA
STB $B2,<PARPTR
LNJ $B4,<GHEAD
CLEAR HEADER FLAG
GENERATE HEADER

*
*
PAR9IA LNJ $B2,<RET
DC 4
DC 4
DC X'C4'
DC X'47'
DC X'20'
DC 0
DC 0
DC <SDB
DC X'10'
DC X'40'
RECEIVE END TEST
LR6,RCV
LR7,XMIT
FILL WITH FLAGS, DELAYED XMIT
LR2 CONTROL,RCV,XMIT,TEST
ACTION FLAG,DELAY BETWEEN FRAMES
RCV ACTION FRAME,CHAR
XMIT ACT FRAME,CHAR
SEND BUFFER
RANGE
VALID

*
DC <SDB+X'8'
DC X'10'
DC X'60'
ADD,2ND CCB
RANGE
LAST,VALID.

*
NOP >MUL-IA

*
JMP $B6
EXIT TEST

-----
*
*
* SINGLE FRAME TERMINATION TESTS
*
*
* IN THIS TEST FRAMES ARE ABORTED IN VARIOUS STATES WITHIN
* THE ADDRESS, CONTROL, LCF, AND TEXT FIELDS.
*
* IF AN ERROR IS REPORTED WITH A LABEL "JX" WHERE X IS ANY
* CHAR, REFER TO THE ENTRY IN TABLE "TRMTBL" WHICH ENDS WITH
* -X TO FIND THE TEST CONDITIONS.
*
*
TERM-J CL =SR2
INDEX
*
TRMMLP LAB $B1,<TRMTBL
LLH $R1,$B1,+SR2
STR $R1,<TRML7
AND $R1,=Z'000E'
STR $R1,<TRML8
SOR $R1,1
* CONVERT FROM MLCB BYTE SIZE CODE TO TCB CODE
LDR $R4,<TCBBSZ.$R1
STR $R4,=$R1
TABLE OF TESTS
GET XMIT LR7
STRIP IO BYTE SIZE
STORE FOR LAST BYTE

*
SOL $R1,8
STR $R1,<HEAD
LLH $R1,$B1,+SR2
STR $R1,<TRML6
LLH $R1,$B1,+SR2
OR $R1,<HEAD
STR $R1,<HEAD
LLH $R1,$B1,+SR2
BEZ $R1,<END-J
OR $R1,=Z'4A00'
STR $R1,<ERMG+1
GET TCB INFO
GET RCV LR6
GET LCF, AFX INFO
GET TEST LABEL
FORM TEST J-

*
LLH $R1,$B1,+SR2
OR $R1,=X'80'
STR $R1,<TRMFLG
LLH $R1,$B1,+SR2
STR $R2,<DEX
STR $R1,=$R2
OR $R1,=Z'FF00'
STR $R1,<TRMAC
NEG $R2
FLAG
PUT IN BIT FOR CCP TO COUNT CHAR
XMIT CHAR COUNT
STORE INDEX
FRAME
    
```

```

001601 054C 8AD2          INC      =S'R2
001602 054D A570 00FF    AND      S'R2,=Z'00FF'
001603 *
001604 054F 0F04          NOP      >S+4
001605 0550 0F04          NOP      >S+4
001606 0551 AF00 0565    STR      S'R2,<TRMRNG
001607 *
001608 * GENERATE DATA, ASCENDING
001609 *
001610 0553 B380 0D3D      LNJ      $B3,<FACDTA
001611 *
001612 0555 AB80 055D      LAB      $B2,<TRML6
001613 0557 AF80 0ECF      STB      $B2,<PARPTR
001614 0559 C380 0D80      LNJ      $B4,<GHEAD
001615 *
001616 055B A380 0970      LNJ      $B2,<ABUND
001617 055D 0000      TRML6   DC      0
001618 055E 0000      TRML7   DC      0
001619 055F 0000      TRML8   DC      0
001620 0560 0047      TRMFLG  DC      X'47'
001621 0561 0000      TRMAC   DC      0
001622 0562 0000      TRMRNG  DC      <SDB
001623 0563 0000      TRMRNG  DC      X'200'
001624 0564 1800      TRMRNG  DC      X'60'
001625 0565 0200
001626 0566 0060
001627 *
001628 0567 AB00 077E      LDR      S'R2,<DEX
001629 0569 AF80 051F      B        <TRMLP
001630 *
001631 056B 8386      END-J   JMP      $B6
001632 *
001633 * TABLE FOR TERMINATION TESTS
001634 *
001635 * WORD 1
001636 *   BITS 0-7   LR7, XMIT
001637 *   BITS 8-15 LR6, RCV
001638 *
001639 * WORD 2
001640 *   BITS 0 - 3   ADDRESS FIELD LENGTH
001641 *   BITS 4 - 7   LCF LENGTH
001642 *   BITS 7 - 15 ASCII TEST LABEL
001643 *
001644 * WORD 3
001645 *   BITS 0 - 7   ACTION FLAG
001646 *   BITS 8 - 15 ACTION CHAR
001647 *
001648 * ALL TESTS ARE IN 8 BIT MODE
001649 *
001650 * ABORT DURING ADDRESS STATE
001651 *
001652 * TRMTBL DC Z'0C0C'      8 BIT CHAR
001653 *         DC Z'0041'      TEST JA
001654 *         DC Z'02FD'      ABORT, 3 CHAR
001655 *
001656 * ABORT DURING ADDRESS FIELD EXTENTION
001657 *
001658 * TRM-B DC Z'0C0C'      AFX, 8 BITS
001659 *         DC Z'1042'      TEST JB
001660 *         DC Z'02FC'      ABORT, 4 CHAR
001661 *
001662 * ABORT DURING ADDRESS FIELD EXTENTION, > 32 BITS
001663 *
001664 * TRM-C DC Z'4C4C'      AFX, 8 BITS
001665 *         DC Z'3043'      3 BYTE AF, TEST JC
001666 *         DC Z'02FB'      ABORT, 5 CHAR
001667 *
001668 * ABORT DURING CONTROL FIELD STATE
001669 *
001670 * TRM-D DC Z'0C0C'      8 BITS, CFX
001671 *         DC =Z'0044'      TEST JD
001672 *         DC =Z'02FC'      ABORT, 4 CHAR
001673 *
001674 * ABORT DURING CONTROL FIELD STATE, > 32 BITS
001675 *
001676 * TRM-E DC Z'4C4C'      CF, AFX
001677 *         DC =Z'3045'      TEST JE
001678 *         DC Z'02FA'      ABORT, 6 CHAR
001679 *
001680 * ABORT DURING EXTENDED CONTROL FIELD STATE
001681 *
001682 * TRM-F DC Z'8C8C'      8 BITS, CFX
001683 *         DC Z'0046'      TEST JF
001684 *         DC =Z'02FB'      ABORT, 5 CHAR
001685 *
001686 * ABORT DURING TCB STATE
001687 *
001688 * TRM-G DC Z'2C2C'      TCB
001689 *         DC Z'0047'      TEST JG
001690 *         DC Z'02FB'      ABORT, 5 CHAR
001691 *
001692 * ABORT DURING LCF STATE
001693 *
001694 * TRM-H DC Z'0D0D'      LCF
001695 *         DC Z'0448'      4 BYTE LCF TEST JH
001696 *         DC Z'02FB'      ABORT, 5 CHAR
001697 *
001698 * ABORT DURING TEXT STATE
001699 *
001700 * TRM-J DC Z'0C0C'      8 BITS
001701 *         DC Z'004A'      TEST JJ
001702 *         DC Z'02FB'      ABORT, 5 CHAR
001703 *         RESV I2,0      END
001704 *
001705 * -----
001706 *
001707 *
001708 *
001709 * TRANSFER, RECEIVE 2 FRAMES, ABORTS BETWEEN
001710 *
001711 *
001712 *
001713 0593 9870 204C      MULT-L  LDR      S'R1,=A' L'

```



```

001714 0595 9F00 11EA STR $R1,<ERMG+1 REPORT TEST SECTION
001715 0597 9870 1100 LDR $R1,=Z'1000' TURN ON RCV AFTER 1 XMIT CHAR
001716 0599 9F00 117B STR $R1,<EXST
*
* * FORM DATA (ASCENDING)
*
001721 059B B380 0D3D LNJ $B3,<FACDTA
001722 059D 8700 1174 CL <HEAD CLEAR HEADER FLAG
001723 059F AB80 05A7 LAB $B2,<PAR10
001724 05A1 AF80 0ECF STB $B2,<PARPTR
001725 05A3 C380 0D80 LNJ $B4,<GHEAD GENERATE HEADER
*
*
001727 05A5 A380 0A4A PAR10 LNJ $B2,<RET RECEIVE END TEST
001728 05A7 0004 DC 4 LR6, RCV
001729 05A8 0004 DC 4 LR7, XMIT
001730 05A9 0004 DC 4 FILL WITH ABORTS
001731 05AA 0047 DC =X'47' LR2 CONTROL, RCV, XMIT, TEST
001732 05AB 0020 DC X'20' ACTION FLAG, DELAY BETWEEN FRAMES
001733 05AC 0000 DC 0 RCV ACT FRAME, CHAR
001734 05AD 0000 DC =X'0' XMIT ACT FRAME, CHAR
*
*
001737 05AE 1800 DC <SDB SEND BUFFER
001738 05AF 0010 LC X'10' RANGE
001739 05B0 0040 DC X'40' VALID
*
*
001741 05B1 1808 DC <SDB+X'8' ADD, 2ND CCB
001742 05B2 0010 DC X'10' RANGE
001743 05B3 0060 DC X'60' LAST, VALID
*
*
001745 05B4 0F04 NOP >$+4
*
*
001747 05B5 8386 JMP $B6 EXIT TEST
*
* -----
*
* UNDERUN TEST 1 - DELAY 100 MS AFTER TRANSMITTING 5 CHAR
*
001754 05B6 9870 2046 UNDI-K LDR $R1,=A' K'
001755 05B8 9F00 11EA STR $R1,<ERMG+1 REPORT TEST SECTION
*
* * FORM ASCENDING DATA
*
001756 05BA B380 0D3D LNJ $B3,<FACDTA
001757 05BC 8700 1174 CL <HEAD CLEAR HEADER FLAG
001758 05BE AB80 05C6 LAB $B2,<PAR11
001759 05C0 AF80 0ECF STB $B2,<PARPTR
001760 05C2 C380 0D80 LNJ $B4,<GHEAD GENERATE HEADER
*
*
001764 05C4 A380 0970 PAR11 LNJ $B2,<ABUND ABORT, UNDERUN TEST
001765 05C6 0008 DC =X'8' RCV CONFIG, 7 BITS
001766 05C7 0008 DC =X'8' XMIT CONFIG, 7 BITS
001767 05C8 0008 DC =8 FILL MODE, ABORTS
001768 05C9 0047 DC =X'47' LR2, RCV, XMIT, TEST ON
001769 05CA 0081 DC X'81' ACTION FLAG DELAY INSERT ON XMIT
001770 05CB 0001 DC 0 RC ACT FR, CHR
001771 05CC FFFB DC =Z'FFFB' DELAY FR 1, CHR 5
001772 05CD 1800 DC <SDB SEND BUFFER
001773 05CE 0007 DC 7 RANGE
001774 05CF 0060 DC X'60' LAST, VALID
*
*
001775 05D0 8386 JMP $B6
*
* -----
*
* SINGLE FRAME TESTS
*
* THE FOLLOWING TESTS (EA - ED) ARE DESIGNED TO TEST
* ARE DESIGNED TO TEST DIFFERENT END CONDITIONS OF
* SINGLE FRAME MESSAGES FILLING AND OVERFLOWING THE
* RCV FIFO BUFFER. TO DO THIS THE RECEIVER IS NOT
* TURNED ON UNTIL AFTER THE FIRST XMIT FRAME IS
* TRANSMIT.
*
* -----
*
001789 05D1 9870 4541 RCV-EA LDR $R1,=A'EA' REPORT TEST
001790 05D3 9F00 11EA STR $R1,<ERMG+1 EXPECTED STATUS
001791 05D5 9870 1000 LDR $R1,=Z'1000'
001792 05D7 9F00 117B STR $R1,<EXST
*
*
001794 05D9 B380 0D3D LNJ $B3,<FACDTA FILL ASCENDING DATA
001795 05DB 8700 1174 CL <HEAD CLEAR HEADER FLAG
001796 05DD AB80 05E5 LAB $B2,<PAR12
001797 05DF AF80 0ECF STB $B2,<PARPTR
001798 05E1 C380 0D80 LNJ $B4,<GHEAD GENERATE HEADER
*
*
001801 05E3 A380 0A4A PAR12 LNJ $B2,<RET RECEIVE END TEST
001802 05E5 000C DC =X'C' RCV CONFIGURATION, 8 BITS
001803 05E6 000C DC =X'C' XMIT CONFIG, 8 BITS
001804 05E7 008C DC X'8C' FILL WITH FLAGS
001805 05E8 0045 DC =X'45' LR2 CONTROL, TEST, XMIT
001806 05E9 0084 DC X'84' FLAG, DELAYED TURN ON OF RCV
001807 05EA 0000 DC 0
001808 05EB FF00 DC Z'FF00' XMIT CONTROL FRAME, CHR
*
*
001809 05EC 1800 DC <SDB SEND BUFFER
001810 05ED 0040 DC 64 RANGE IN BYTES
001811 05EE 0060 DC X'60' VALID
*
* -----
*
001816 05F0 9870 4542 RCV-EB LDR $R1,=A'EB' REPORT TEST
001817 05F2 9F00 11EA STR $R1,<ERMG+1 EXPECTED STATUS
001818 05F4 9870 1000 LDR $R1,=Z'1000'
001819 05F6 9F00 117B STR $R1,<EXST
*
*
001821 05F8 B380 0D3D LNJ $B3,<FACDTA FILL ASCENDING DATA
001822 05FA 8700 1174 CL <HEAD CLEAR HEADER FLAG
001823 05FC AB80 0604 LAB $B2,<PAR13
001824 05FE AF80 0ECF STB $B2,<PARPTR
001825 0600 C380 0D80 LNJ $B4,<GHEAD GENERATE HEADER
*

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```

001827
001828 0602 A380 0A4A
001829 0604 000C
001830 0605 000C
001831 0606 008C
001832 0607 0045
001833 0608 0084
001834 0609 0000
001835 060A FF00
001836
001837 060B 1800
001838 060C 0041
001839 060D 0060
001840
001841 060E 8386
001842
001843 060F 9870 4543
001844 0611 9F00 11EA
001845 0613 9870 1000
001846 0615 9F00 117B
001847
001848 0617 B380 0D3D
001849 0619 8700 1174
001850 061B AB80 0623
001851 061D AF80 0ECF
001852 061F C380 0D80
001853
001854
001855 0621 A380 0A4A
001856 0623 000C
001857 0624 000C
001858 0625 008C
001859 0626 0045
001860 0627 0084
001861 0628 0000
001862 0629 FF00
001863
001864 062A 1800
001865 062B 0042
001866 062C 0060
001867
001868 062D 8386
001869
001870 062E 9870 4544
001871 0630 9F00 11EA
001872 0632 9870 2000
001873 0634 9F00 117B
001874
001875 0636 B380 0D3D
001876 0638 8700 1174
001877 063A AB80 0642
001878 063C AF80 0ECF
001879 063E C380 0D80
001880
001881
001882 0640 A380 0A4A
001883 0642 000C
001884 0643 000C
001885 0644 000C
001886 0645 0045
001887 0646 0084
001888 0647 0000
001889 0648 FF00
001890
001891 0649 1800
001892 064A 0043
001893 064B 0060
001894
001895 064C 8386
001896
001897
001898
001899
001900
001901
001902
001903
001904
001905
001906
001907
001908
001909
001910
001911
001912 064D 8751
001913 064E 9880 069A
001914
001915 0650 A85D 0699
001916 0651 2900
001917 0653 AF00 11EA
001918 0655 A85D
001919 0656 AF00 117B
001920 0658 A85D
001921 0659 AF00 068C
001922 065B A85D
001923 065C AF00 0690
001924 065E A85D
001925 065F AF00 0693
001926 0661 A85D
001927 0662 AF00 068E
001928 0664 9F00 077E
001929
001930
001931
001932
001933
001934
001935 0666 1C0C
001936 0667 9F00 0689
001937 0669 8280 068C
001938 066B 0002
001938 066C 0510

*
PAR13  LNJ  $B2,<RET          RECEIVE END TEST
        DC  =X'1C'         RCV CONFIGURATION, 8 BITS
        DC  =X'1C'         XMIT CONFIG, 8 BITS
        DC  X'8C'         FILL WITH FLAGS
        DC  =X'45'        LR2 CONTROL TEST, XMIT
        DC  X'84'         FLAG, DELAYED TURN ON OF RCV
        DC  0
        DC  Z'FF00'      XMIT CONTROL FRAME, CHR
*
        DC  <SDB         SEND BUFFER
        DC  65           RANGE IN BYTES
        DC  X'60'        VALID
*
        JMP  $B6
-----
RCV-EC  LDR  $R1,=A'EC'    REPORT TEST
        STR  $R1,<ERMG+1  EXPECTED STATUS
        LDR  $R1,=Z'1000'
        STR  $R1,<EXST
*
        LNJ  $B3,<FACDTA  FILL ASCENDING DATA
        CL  <HEAD        CLEAR HEADER FLAG
        LAB  $B2,<PAR14
        STB  $B2,<PARPTR
        LNJ  $B4,<GHEAD  GENERATE HEADER
*
*
PAR14  LNJ  $B2,<RET          RECEIVE END TEST
        DC  =X'1C'         RCV CONFIGURATION, 8 BITS
        DC  =X'1C'         XMIT CONFIG, 8 BITS
        DC  X'8C'         FILL WITH FLAGS
        DC  =X'45'        LR2 CONTROL TEST, XMIT
        DC  X'84'         FLAG, DELAYED TURN ON OF RCV
        DC  0
        DC  Z'FF00'      XMIT CONTROL FRAME, CHR
*
        DC  <SDB         SEND BUFFER
        DC  66           RANGE IN BYTES
        DC  X'60'        VALID
*
        JMP  $B6
-----
RCV-ED  LDR  $R1,=A'ED'    REPORT TEST
        STR  $R1,<ERMG+1  EXPECTED STATUS FLAG
        LDR  $R1,=Z'2000'
        STR  $R1,<EXST
*
        LNJ  $B3,<FACDTA  FILL ASCENDING DATA
        CL  <HEAD        CLEAR HEADER FLAG
        LAB  $B2,<PAR15
        STB  $B2,<PARPTR
        LNJ  $B4,<GHEAD  GENERATE HEADER
*
*
PAR15  LNJ  $B2,<RET          RECEIVE END TEST
        DC  =X'1C'         RCV CONFIGURATION, 8 BITS
        DC  =X'1C'         XMIT CONFIG, 8 BITS
        DC  =X'1C'         FILL WITH ABORTS
        DC  =X'45'        LR2 CONTROL TEST, XMIT
        DC  X'84'         FLAG, DELAYED TURN ON OF RCV
        DC  0
        DC  Z'FF00'      RCV CONTROL FRAME, CHAR
*
*
        DC  <SDB         XMIT FRAME CONTROL INFO
        DC  67           SEND BUFFER
        DC  X'60'        RANGE IN BYTES
        DC  X'60'        VALID
*
*
*-----*
*
* TWO FRAME TESTS
*
* THE FOLLOWING TESTS TRANSMITS 2 FRAMES BEFORE TURNING ON RCV.
* THEY ARE DESIGNED TO TEST VARIOUS END CONDITIONS. REFER
* TO TABLE "PAR" AND FIND THE LABEL GIVEN IN THE ERROR
* PRINTOUT TO FIND SPECIFIC TEST CONDITIONS.
*
* REFER TO THE DESCRIPTION OF THE CCP CONTROL FLAG IN THE
* HEADER FOR ACTION TAKEN BY EACH TEST.
*
* RCV IS NOT TURNED ON UNTIL AFTER BOTH FRAMES ARE TRANSMITTED
* FOR MOST OF THESE TESTS.
*
TFR-M  CL  =SRI
TLUP   LAB  $B1,<PAR
*
        LDR  $R2,$B1,+SRI  GET LABEL
        BEZ  $R2,<ETFR     B = DONE
        STR  $R2,<ERMG+1
        LDR  $R2,$B1,+SRI  GET EXPECTED STATUS FLAG
        STR  $R2,<EXST
        LDR  $R2,$B1,+SRI  GET CCP CONTROL FLAG
        STR  $R2,<TFLG
        LDR  $R2,$B1,+SRI  GET RANGE OF FIRST
        STR  $R2,<TRNG1
        LDR  $R2,$B1,+SRI  GET RANGE 2
        STR  $R2,<TRNG2
        LDR  $R2,$B1,+SRI  GET XMIT CONTROL FRAME, CHAR (NEG)
        STR  $R2,<XCON
        STR  $R1,<DEX      STORE INDEX
*
*
* IF ONE BYTE + CRC FOLLOWED BY FLAG, CHANGE TO 3 BYTES
* + NO CRC
*
        LDV  $R1,=X'1C'
        STR  $R1,<PAR22    SET FOR 8 BIT INITIAL
        LB  <TFLG,=Z'0002' GET ABORT BIT
*
        BBT  >MTS1       B = ABORT

```

001939	066D	A800	0693	LDR	\$R2,<TRNG2	RANGE 2
001940	066F	2001		CMV	\$R2,=1	
001941	0670	098C		BNE	>MTS1	
001942				*		
001943	0671	A870	FEFD	LDR	\$R2,=Z'FEFD'	
001944	0673	AF00	068E	STR	\$R2,<XCON	
001945	0675	2C03		LDV	\$R2,=3	
001946	0676	AF00	0693	STR	\$R2,<TRNG2	
001947	0678	9870	002E	LDR	\$R1,=X'2E'	
001948	067A	9F00	0689	STR	\$R1,<PAR22	SET FOR NO CRC
001949				*		
001950	067C	B380	0D3D	MTS1	LNJ	\$B3,<FACDTA
001951	067E	8700	1174	CL	<HEAD	FILL ASCENDING DATA
001952	0680	AB80	0688	LAB	\$B2,<PAR16	CLEAR HEADER FLAG
001953	0682	AF80	0ECF	STB	\$B2,<PARPTR	
001954	0684	C380	0D80	LNJ	\$B4,<GHEAD	GENERATE HEADER
001955				*		
001956				*		
001957	0686	A380	0A4A	LNJ	\$B2,<RET	RECEIVE END TEST
001958	0688	000C		PAR16	DC	8 BYTE, RCV
001959	0689	000C		PAR22	DC	8 BYTE, XMIT
001960	068A	008C		DC	X'8C'	FLAG IDLE BETWEEN
001961	068B	0045		DC	X'45'	XMIT, TEST (LK 2)
001962	068C	0000		TFLG	DC	CONTROL FLAG
001963	068D	0000		DC	0	RCV CONTROL, FRAME, CHAR
001964	068E	FE00		XCON	DC	XMIT CONTROL FRAME, CHAR
001965				*		
001966	068F	1800		DC	<SDB	SEND BUFFER 1
001967	0690	0000		TRNG1	DC	0
001968	0691	0040		DC	X'40'	CONTROL
001969				*		
001970	0692	1840		DC	<SDB+X'40'	SEND BUFFER 2
001971	0693	0000		TRNG2	DC	0
001972	0694	0060		DC	X'60'	RANGE 2
001973				*		CONTROL
001974	0695	9800	077E	LDR	\$R1,<DEX	
001975	0697	0F80	064E	B	<TLUP	DO NEXT 2 FRAME TEST
001976				*		
001977	0699	8386		ETFR	JMP	\$B6
001978				*		END OF TEST
001979				*		
001980				*		TABLE FOR TWO FRAME TESTS
001981	069A	6D61		PAR	DC	=A'MA'
001982	069B	1100		DC	X'1100'	LABEL
001983	069C	0084		DC	X'84'	EXPECTED STATUS FLAG
001984	069D	003F		DC	63	CCP CONTROL FLAG
001985	069E	0002		DC	X'2'	RANGE, 1ST FRAME
001986	069F	FEFE		DC	Z'FEFE'	RANGE, 2ND FRAME
001987				*		XMIT CONTROL, FRAME 2, CHAR 2
001988	06A0	6D62		DC	=A'MB'	LABEL
001989	06A1	1300		DC	X'1300'	EXPECTED STATUS FLAG
001990	06A2	0084		DC	X'84'	CCP CONTROL FLAG
001991	06A3	003F		DC	63	RANGE, 1ST FRAME
001992	06A4	0001		DC	1	RANGE OF SECOND
001993	06A5	FEFE		DC	Z'FEFE'	XMIT CONTROL, FRAME 2, CHAR 2
001994				*		
001995	06A6	6D63		DC	=A'MC'	LABEL
001996	06A7	1100		DC	X'1100'	EXPECTED STATUS FLAG
001997	06A8	0084		DC	X'84'	CCP CONTROL FLAG
001998	06A9	003F		DC	63	RANGE, 1ST FRAME
001999	06AA	0003		DC	3	RANGE, 2ND FRAME
002000	06AB	FEFD		DC	Z'FEFD'	XMIT CONTROL, FRAME 2, CHAR 3
002001				*		
002002	06AC	6D64		DC	=A'MD'	LABEL
002003	06AD	1200		DC	X'1200'	EXPECTED STATUS FLAG
002004	06AE	0084		DC	X'84'	CCP CONTROL FLAG
002005	06AF	003F		DC	63	RANGE, 1ST FRAME
002006	06B0	0004		DC	4	RANGE, 2ND FRAME
002007	06B1	FEFC		DC	Z'FEFC'	XMIT CONTROL, FRAME 2, CHAR 4
002008				*		
002009	06B2	6D65		DC	=A'ME'	LABEL
002010	06B3	1300		DC	X'1300'	EXPECTED STATUS FLAG
002011	06B4	0086		DC	X'86'	CCP CONTROL FLAG
002012	06B5	003F		DC	63	RANGE, 1ST FRAME
002013	06B6	0003		DC	3	RANGE, 2ND FRAME
002014	06B7	FEFE		DC	Z'FEFE'	XMIT CONTROL, FRAME 2, CHAR 2
002015				*		
002016	06B8	6D66		DC	=A'MF'	LABEL
002017	06B9	1300		DC	X'1300'	EXPECTED STATUS FLAG
002018	06BA	0086		DC	X'86'	CCP CONTROL FLAG
002019	06BB	003F		DC	63	RANGE, 1ST FRAME
002020	06BC	0004		DC	4	RANGE, 2ND FRAME
002021	06BD	FEFD		DC	Z'FEFD'	XMIT CONTROL, FRAME 2, CHAR 3
002022				*		
002023	06BE	6D67		DC	=A'MG'	LABEL
002024	06BF	1600		DC	X'1600'	EXPECTED STATUS FLAG
002025	06C0	0086		DC	X'86'	CCP CONTROL FLAG
002026	06C1	003F		DC	63	RANGE, 1ST FRAME
002027	06C2	0006		DC	6	RANGE, 2ND FRAME
002028	06C3	FEFB		DC	Z'FEFB'	XMIT CONTROL, FRAME 2, CHAR 5
002029				*		
002030				*		
002031	06C4	6D2A		DC	=A'MH'	LABEL
002032	06C5	1700		DC	X'1700'	EXPECTED STATUS FLAG
002033	06C6	0086		DC	X'86'	CCP FLAG
002034	06C7	003F		DC	63	RANGE 1
002035	06C8	0007		DC	7	RANGE 2
002036	06C9	FEFA		DC	Z'FEFA'	XMIT CONTROL FRAME 2, CHAR 6
002037				*		
002038	06CA	6D68		DC	=A'MH'	LABEL
002039	06CB	1100		DC	X'1100'	EXPECTED STATUS FLAG
002040	06CC	0084		DC	X'84'	CCP CONTROL FLAG
002041	06CD	0040		DC	64	RANGE, 1ST FRAME
002042	06CE	0002		DC	2	RANGE, 2ND FRAME
002043	06CF	FEFE		DC	Z'FEFE'	XMIT CONTROL, FRAME 2, CHAR 2
002044				*		
002045	06D0	6D69		DC	=A'MI'	LABEL
002046	06D1	1300		DC	X'1300'	EXPECTED STATUS FLAG
002047	06D2	0084		DC	X'84'	CCP CONTROL FLAG
002048	06D3	0040		DC	64	RANGE, 1ST FRAME
002049	06D4	0001		DC	1	RANGE, 2ND FRAME
002050	06D5	FEFF		DC	Z'FEFF'	XMIT CONTROL, FRAME 2, CHAR 1
002051				*		

002052	06D6	6D6A	DC	=A'MJ'	LABEL
002053	06D7	1200	DC	X'1200'	EXPECTED STATUS FLAG
002054	06D8	0084	DC	X'84'	CCP CONTROL FLAG
002055	06D9	0040	DC	64	RANGE, 1ST FRAME
002056	06DA	0003	DC	3	RANGE, 2ND FRAME
002057	06DB	FEFD	DC	Z'FEFD'	XMIT CONTROL, FRAME 2, CHAR 3
002058			*		
002059	06DC	6D6B	DC	=A'MK'	LABEL
002060	06DD	1200	DC	X'1200'	EXPECTED STATUS FLAG
002061	06DE	0084	DC	X'84'	CCP CONTROL FLAG
002062	06DF	0040	DC	64	RANGE, 1ST FRAME
002063	06E0	0004	DC	4	RANGE, 2ND FRAME
002064	06E1	FEFC	DC	Z'FEFC'	XMIT CONTROL, FRAME 2, CHAR 4
002065			*		
002066	06E2	6D6C	DC	=A'ML'	LABEL
002067	06E3	1300	DC	X'1300'	EXPECTED STATUS FLAG
002068	06E4	0086	DC	X'86'	CCP CONTROL FLAG
002069	06E5	0040	DC	64	RANGE, 1ST FRAME
002070	06E6	0003	DC	3	RANGE, 2ND FRAME
002071	06E7	FEFE	DC	Z'FEFE'	XMIT CONTROL, FRAME 2, CHAR 2
002072			*		
002073	06E8	6D6D	DC	=A'MM'	LABEL
002074	06E9	1300	DC	X'1300'	EXPECTED STATUS FLAG
002075	06EA	0086	DC	X'86'	CCP CONTROL FLAG
002076	06EB	0040	DC	64	RANGE, 1ST FRAME
002077	06EC	0004	DC	4	RANGE, 2ND FRAME
002078	06ED	FEFD	DC	Z'FEFD'	XMIT CONTROL, FRAME 2, CHAR 3
002079			*		
002080	06EE	6D6E	DC	=A'MN'	LABEL
002081	06EF	1300	DC	X'1300'	EXPECTED STATUS FLAG
002082	06F0	0086	DC	X'86'	CCP CONTROL FLAG
002083	06F1	0040	DC	64	RANGE, 1ST FRAME
002084	06F2	0005	DC	5	RANGE, 2ND FRAME
002085	06F3	FEFC	DC	Z'FEFC'	XMIT CONTROL, FRAME 2, CHAR 4
002086			*		
002087	06F4	6D6F	DC	=A'MO'	LABEL
002088	06F5	1300	DC	X'1300'	EXPECTED STATUS FLAG
002089	06F6	0084	DC	X'84'	CCP CONTROL FLAG
002090	06F7	0041	DC	65	RANGE, 1ST FRAME
002091	06F8	0001	DC	1	RANGE, 2ND FRAME
002092	06F9	FEFF	DC	Z'FEFF'	XMIT CONTROL, FRAME 2, CHAR 1
002093			*		
002094	06FA	6D70	DC	=A'MP'	LABEL
002095	06FB	1500	DC	X'1500'	EXPECTED STATUS FLAG
002096	06FC	0084	DC	X'84'	CCP CONTROL FLAG
002097	06FD	0041	DC	65	RANGE, 1ST FRAME
002098	06FE	0002	DC	2	RANGE, 2ND FRAME
002099	06FF	FEFE	DC	Z'FEFE'	XMIT CONTROL, FRAME 2, CHAR 2
002100			*		
002101	0700	6D71	DC	=A'MQ'	LABEL
002102	0701	1500	DC	X'1500'	EXPECTED STATUS FLAG
002103	0702	0084	DC	X'84'	CCP CONTROL FLAG
002104	0703	0041	DC	65	RANGE, 1ST FRAME
002105	0704	0003	DC	3	RANGE, 2ND FRAME
002106	0705	FEFD	DC	Z'FEFD'	XMIT CONTROL, FRAME 2, CHAR 3
002107			*		
002108	0706	6D72	DC	=A'MR'	LABEL
002109	0707	1500	DC	X'1500'	EXPECTED STATUS FLAG
002110	0708	0084	DC	X'84'	CCP CONTROL FLAG
002111	0709	0041	DC	65	RANGE, 1ST FRAME
002112	070A	0004	DC	4	RANGE, 2ND FRAME
002113	070B	FEFC	DC	Z'FEFC'	XMIT CONTROL, FRAME 2, CHAR 4
002114			*		
002115	070C	6D73	DC	=A'MS'	LABEL
002116	070D	1300	DC	X'1300'	EXPECTED STATUS FLAG
002117	070E	0086	DC	X'86'	CCP CONTROL FLAG
002118	070F	0041	DC	65	RANGE, 1ST FRAME
002119	0710	0003	DC	3	RANGE, 2ND FRAME
002120	0711	FEFE	DC	Z'FEFE'	XMIT CONTROL, FRAME 2, CHAR 2
002121			*		
002122	0712	6D74	DC	=A'MT'	LABEL
002123	0713	1300	DC	X'1300'	EXPECTED STATUS FLAG
002124	0714	0086	DC	X'86'	CCP CONTROL FLAG
002125	0715	0041	DC	65	RANGE, 1ST FRAME
002126	0716	0004	DC	4	RANGE, 2ND FRAME
002127	0717	FEFD	DC	Z'FEFD'	XMIT CONTROL, FRAME 2, CHAR 3
002128			*		
002129	0718	6D75	DC	=A'MU'	LABEL
002130	0719	1300	DC	X'1300'	EXPECTED STATUS FLAG
002131	071A	0086	DC	X'86'	CCP CONTROL FLAG
002132	071B	0041	DC	65	RANGE, 1ST FRAME
002133	071C	0005	DC	5	RANGE, 2ND FRAME
002134	071D	FEFC	DC	Z'FEFC'	XMIT CONTROL, FRAME 2, CHAR 4
002135			*		
002136	071E	6D76	DC	=A'MV'	LABEL
002137	071F	1300	DC	X'1300'	EXPECTED STATUS FLAG
002138	0720	0084	DC	X'84'	CCP CONTROL FLAG
002139	0721	0042	DC	66	RANGE, 1ST FRAME
002140	0722	0001	DC	1	RANGE, 2ND FRAME
002141	0723	FEFF	DC	Z'FEFF'	XMIT CONTROL, FRAME 2, CHAR 1
002142			*		
002143			*		
002144	0724	6D77	DC	=A'MW'	LABEL
002145	0725	1500	DC	X'1500'	EXPECTED STATUS FLAG
002146	0726	0084	DC	X'84'	CCP CONTROL FLAG
002147	0727	0042	DC	66	RANGE, 1ST FRAME
002148	0728	0002	DC	2	RANGE, 2ND FRAME
002149	0729	FEFE	DC	Z'FEFE'	XMIT CONTROL, FRAME 2, CHAR 2
002150			*		
002151	072A	6D78	DC	=A'MX'	LABEL
002152	072B	1500	DC	X'1500'	EXPECTED STATUS FLAG
002153	072C	0084	DC	X'84'	CCP CONTROL FLAG
002154	072D	0042	DC	66	RANGE, 1ST FRAME
002155	072E	0003	DC	3	RANGE, 2ND FRAME
002156	072F	FEFD	DC	Z'FEFD'	XMIT CONTROL, FRAME 2, CHAR 3
002157			*		
002158	0730	6D79	DC	=A'MY'	LABEL
002159	0731	1300	DC	X'1300'	EXPECTED STATUS FLAG
002160	0732	0086	DC	X'86'	CCP CONTROL FLAG
002161	0733	0042	DC	66	RANGE, 1ST FRAME
002162	0734	0003	DC	3	RANGE, 2ND FRAME
002163	0735	FEFE	DC	Z'FEFE'	XMIT CONTROL, FRAME 2, CHAR 2
002164			*		

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002165 0736 6D7A DC =A'M2' LABEL
002166 0737 1300 DC X'1300' EXPECTED STATUS FLAG
002167 0738 0086 DC X'86' CCP CONTROL FLAG
002168 0739 0042 DC 66 RANGE, 1ST FRAME
002169 073A 0004 DC 4 RANGE, 2ND FRAME
002170 073B FEFD DC Z'FEFD' XMIT CONTROL, FRAME 2, CHAR 3
002171 *
002172 073C 6D31 DC =A'M1' LABEL
002173 073D 2300 DC X'2300' EXPECTED STATUS FLAG
002174 073E 0084 DC X'84' CCP CONTROL FLAG
002175 073F 0043 DC 67 RANGE, 1ST FRAME
002176 0740 0001 DC 1 RANGE, 2ND FRAME
002177 0741 FEFF DC Z'FEFF' XMIT CONTROL, FRAME 2, CHAR 1
002178 *
002179 0742 6D32 DC =A'M2' LABEL
002180 0743 2500 DC X'2500' EXPECTED STATUS FLAG
002181 0744 0084 DC X'84' CCP CONTROL FLAG
002182 0745 0043 DC 67 RANGE, 1ST FRAME
002183 0746 0004 DC 4 RANGE, 2ND FRAME
002184 0747 FEFC DC Z'FEFC' XMIT CONTROL, FRAME 2, CHAR 4
002185 *
002186 0748 6D33 DC =A'M3' LABEL
002187 0749 2300 DC X'2300' EXPECTED STATUS FLAG
002188 074A 0086 DC X'86' CCP CONTROL FLAG
002189 074B 0043 DC 67 RANGE, 1ST FRAME
002190 074C 0003 DC 3 RANGE, 2ND FRAME
002191 074D FEFE DC Z'FEFE' XMIT CONTROL, FRAME 2, CHAR 2
002192 *
002193 074E 6D34 DC =A'M4' LABEL
002194 074F 2500 DC X'2500' EXPECTED STATUS FLAG
002195 0750 0084 DC X'84' CCP CONTROL FLAG
002196 0751 0046 DC 70 RANGE, 1ST FRAME
002197 0752 0004 DC 4 RANGE, 2ND FRAME
002198 0753 FEFC DC Z'FEFC' XMIT CONTROL, FRAME 2, CHAR 4
002199 *
002200 0754 6D35 DC =A'M5' LABEL
002201 0755 2300 DC X'2300' EXPECTED STATUS FLAG
002202 0756 0086 DC X'86' CCP CONTROL FLAG
002203 0757 0046 DC 70 RANGE, 1ST FRAME
002204 0758 0005 DC 5 RANGE, 2ND FRAME
002205 0759 FEFC DC Z'FEFC' XMIT CONTROL, FRAME 2, CHAR 4
002206 *
002207 075A 6D36 DC =A'M6' LABEL
002208 075B 7100 DC X'7100' EXPECTED STATUS FLAG
002209 075C 0086 DC X'86' CCP FLAG
002210 075D 0047 DC 71 RANGE 1
002211 075E 0004 DC 4 RANGE 2
002212 075F FFBA DC Z'FFBA' XMIT CONTROL, FRAME 1, CHAR 68
002213 *
002214 0760 6D37 DC =A'M7' LABEL
002215 0761 1300 DC X'1300' EXPECTED STATUS FLAG
002216 0762 0086 DC X'86' DELAYED TURN ON OF RECEIVE
002217 0763 003F DC 63 RANGE 1
002218 0764 0005 DC 5 RANGE 2
002219 0765 FEFC DC Z'FEFC' XMIT CONTROL, FRAME 2, CHAR 4
002220 *
002221 * TEST M8 TESTS RECEIVER RE-SYNC
002222 *
002223 0766 6D38 DC =A'M8' LABEL
002224 0767 4130 DC Z'4130' EXPECTED STATUS FLAG
002225 0768 00E4 DC X'E4' CCP FLAG
002226 0769 0028 DC 40 RANGE 1
002227 076A 0004 DC 4 RANGE 2
002228 076B FE00 DC Z'FE00' XMIT CONTROL, FRAME 2, CHAR = DON'T CARE
002229 *
002230 *
002231 *
002232 076C 0000 DEX RESV 18,0 ROOM FOR MORE
002233 077E 0000 DC 0 STORAGE FOR INDEX
002234 -----
002235 *
002236 * START OF 3 FRAME TESTS
002237 *
002238 * THE FOLLOWING TESTS (EN -EP) ALL TRANSMIT 3 FRAMES
002239 * BEFORE RCV IS TURNED ON.
002240 *
002241 077F 9870 454E RCV=EN LDR $R1,=A'EN' REPORT TEST
002242 0781 9F00 11EA STR $R1,<ERMG+1 EXPECTED STATUS
002243 0783 9870 1150 LDR $R1,=Z'1150'
002244 0785 9F00 117B STR $R1,<EXST
002245 *
002246 0787 B380 0D3D * LNJ $B3,<FACDTA FILL ASCENDING DATA
002247 0789 8700 1174 CL <HEAD CLEAR HEADER FLAG
002248 078B AB80 0793 LAB $B2,<PAR17
002249 078D AF80 0ECF STB $B2,<PARPTR
002250 078F C380 0D80 LNJ $B4,<GHEAD GENERATE HEADER
002251 *
002252 *
002253 0791 A380 0A4A * LNJ $B2,<RET RECEIVE END TEST
002254 0793 000C PAR17 =X'0' RCV CONFIGURATION, 8 BITS
002255 0794 000C DC =X'0' XMIT CONFIG, 8 BITS
002256 0795 008C DC X'8C' FLAG IDLE BETWEEN FRAMES
002257 0796 0045 DC =X'45' LR2 CONTROL, TEST, XMIT
002258 0797 0084 DC X'84' DELAYED TURN ON OF RECEIVE
002259 0798 0000 DC 0
002260 0799 FD00 DC Z'FD00' RXMIT CONTROL AT FRAME 3
002261 *
002262 079A 1800 DC <SDb SEND BUFFER
002263 079B 0040 DC 64 RANGE IN BYTES
002264 079C 0040 DC X'40' VALID
002265 *
002266 079D 1840 DC <SDb+X'40' BUFFER 2
002267 079E 0002 DC 2 RANGE
002268 079F 0040 DC X'40' VALID
002269 *
002270 07A0 183C DC <SDb+60 BUFFER 3
002271 07A1 0002 DC 2 RANGE, FRAME 3
002272 07A2 0060 DC X'60' LAST, VALID
002273 *
002274 07A3 8386 JMP $B6
002275 *
002276 07A4 9870 454F RCV=EO LDR $R1,=A'EO' REPORT TEST
002277 07A6 9F00 11EA STR $R1,<ERMG+1

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002278 07A8 9870 1250          LDR $R1=Z'1250'    EXPECTED STATUS
002279 07AA 9F00 117B          STR $R1<EXST
002280 *
002281 07AC B380 0D3D          LNJ $B3<FACDTA    FILL ASCENDING DATA
002282 07AE 8700 1174          CL <HEAD          CLEAR HEADER FLAG
002283 07B0 AB80 07B8          LAB $B2<PAR18
002284 07B2 AFC0 071C          STB $B2,PARPTR
002285 07B4 C380 0D80          LNJ $B4,<GHEAD    GENERATE HEADER
002287 *
002288 07B6 A380 0A4A          LNJ $B2,<RET      RECEIVE END TEST
002289 07B8 000C          DC =X'1C'         RCV CONFIGURATION, 8 BITS
002290 07B9 000C          DC =X'1C'         XMIT CONFIG, 8 BITS
002291 07BA 008C          DC =X'8C'         FILL WITH FLAGS
002292 07BB 0045          DC =X'45'         LR2 CONTROL, TEST, XMIT
002293 07BC 0084          DC =X'84'         DELAYED TURN ON OF RECEIVE
002294 07BD 0000          DC 0
002295 07BE FD00          DC Z'FD00'       XMIT CONTROL, FRAME 1,
002296 *
002297 07BF 1800          DC <SDB          SEND BUFFER
002298 07C0 0040          DC 64            RANGE IN BYTES
002299 07C1 0040          DC X'40'         VALID
002300 *
002301 07C2 1840          DC <SDB+X'40'    BUFFER 2
002302 07C3 0003          DC 3             RANGE
002303 07C4 0040          DC X'40'         VALID
002304 *
002305 07C5 1860          DC <SDB+X'60'    BUFFER 3
002306 07C6 0002          DC 2             RANGE, FRAME 3
002307 07C7 0060          DC X'60'         LAST, VALID
002308 *
002309 07C8 8386          JMP $B6
-----
002310 RCV-EP LDR $R1=A'EP'     REPORT TEST
002311 07C9 9870 4550          STR $R1<ERMG+1  EXPECTED STATUS
002312 07CB 9F00 11EA          LDR $R1=Z'2530'
002313 07CD 9870 2530          STR $R1<EXST
002314 07CF 9F00 117B          *
002315 *
002316 07D1 B380 0D3D          LNJ $B3<FACDTA    FILL ASCENDING DATA
002317 07D3 8700 1174          CL <HEAD          CLEAR HEADER FLAG
002318 07D5 AB80 07D0          LAB $B2<PAR19
002319 07D7 AF80 0ECF          STB $B2,<PARPTR
002320 07D9 C380 0D80          LNJ $B4,<GHEAD    GENERATE HEADER
002321 *
002322 *
002323 07DB A380 0A4A          LNJ $B2,<RET      RECEIVE END TEST
002324 07DD 000C          DC =X'1C'         RCV CONFIGURATION, 8 BITS
002325 07DE 000C          DC =X'1C'         XMIT CONFIG, 8 BITS
002326 07DF 008C          DC =X'8C'         FILL WITH FLAGS
002327 07E0 0045          DC =X'45'         LR2 CONTROL, TEST, XMIT
002328 07E1 0084          DC =X'84'         DELAYED TURN ON OF RCV
002329 07E2 0000          DC 0
002330 07E3 FD00          DC Z'FD00'       XMIT CONTROL, FRAME 2
002331 *
002332 07E4 1800          DC <SDB          SEND BUFFER
002333 07E5 0043          DC 67            RANGE IN BYTES
002334 07E6 0040          DC X'40'         VALID
002335 *
002336 07E7 1840          DC <SDB+X'40'    BUFFER 2
002337 07E8 0002          DC 2             RANGE
002338 07E9 0040          DC X'40'         VALID
002339 *
002340 07EA 1860          DC <SDB+X'60'    BUFFER 3
002341 07EB 0002          DC 2             RANGE, FRAME 3
002342 07EC 0060          DC X'60'         LAST, VALID
002343 *
002344 07ED 8386          JMP $B6
-----
002345 *
002346 *
002347 *
002348 * MEASURE TEST CLOCK AND PRINT. (1ST PASS ONLY)
002349 *
002350 07EE 9870 2057          SPED-W LDR $R1=A' W'  REPORT TEST
002351 07F0 9F00 11EA          STR $R1<ERMG+1
002352 *
002353 07F2 9800 1182          LDR $R1<PASSC
002354 07F4 1900 07F7          BEZ $R1,<SPED1
002355 07F6 8386          JMP $B6          NOT FIRKST PASS
002356 *
002357 07F7 8980 1179          SPED1 CMZ <LOOP
002358 07F9 0902          BE >SPED2
002359 07FA 8386          JMP $B6          NOT FIRSI LOOP
002360 *
002361 * FILL SEND BUFFER WITH ZERO'S
002362 SPED2 CALL ZV$F,SDB,C0,R400
-----
002363 *
002364 07FB FBC0 0003          CL <CHSZ          SET FOR 8 BIT BYTES
002365 07FC D380 0000          LAB $B2<PAR21
002366 07FE 0F80          STB $B2,<PARPTR
002367 0809 A380 09E7          LNJ $B2,<SPTS
002368 0808 000C          DC X'1C'         SPEED TEST
002369 080C 000C          DC X'1C'         RCV CHAR = 8
002370 080D 000C          DC X'1C'         XMIT CHAR = 8 BITS
002371 080E 0047          DC =X'47'        FILL WITH ABORTS
002372 080F 0000          DC 0             LR2 CONTROL
002373 0810 0000          DC 0             FLAG
002374 0811 0000          DC 0             RC ACTION CHR, FRAME
002375 *
002376 0812 1800          DC <SDB          XMIT ACTION CHR, FRAME
002377 0813 0800          DC 2048          DATA ADDRESS
002378 0814 0060          DC X'60'         RANGE IN BYTES
002379 *
002380 0815 0F66          NUP >SPED2
002381 0816 8386          JMP $B6          VALID, LAST
002382 *
002383 *
002384 *
-----
002384 * RANDOM DATA TEST

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X

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002385
002386 0817 9870 5244
002387 0819 9F00 11EA
002388
002389
002390
002391 081B 9800 1182
002392 081D 9B70 0040
002393 081F AB00 1179
002394 0821 2F04
002395 0822 9A52
002396 0823 9F00 1189
002397 0825 9870 0100
002398 0827 9F00 0853
002399 0829 9870 07FE
002400 082B AB00 1192
002401 082D 2906
002402 082E A970 010E
002403 0830 0383
002404 0831 9F00 0853
002405
002406
002407
002408
002409 083A FB00 0003
002410 083C D380 0000
002411 083E 0F80
002412 083F 1800
002413 0840 118C
002414 0841 AB80 084B
002415 0843 AF80 0ECF
002416 0845 8700 1174
002417 0847 C380 0D80
002418
002419 0849 A380 08CD
002420 084B 000C
002421 084C 000C
002422 084D 000C
002423 084E 0047
002424 084F 0000
002425 0850 0000
002426 0851 0000
002427
002428 0852 1800
002429 0853 0100
002430 0854 0060
002431
002432 0855 8386
002433
002434
002435
002436
002437
002438 0856 1CEC
002439 0857 9F00 1169
002440 0859 8751
002441
002442 085A 9880 08AF
002443 085C 2C01
002444
002445 085D C2DD
002446 085E CF00 089D
002447 0860 C2DD
002448 0861 CF00 089C
002449 0863 C2DD
002450 0864 CF00 08A0
002451 0866 C2DD
002452 0867 CF00 089E
002453 0869 C2DD
002454 086A C780 11EA
002455 086C 4900 08AA
002456 086E C2DD
002457 086F C7A0 11EA
002458 0871 8700 1175
002459
002460 0873 8280 089D
002461 0875 0020
002462 0876 0580 0884
002463
002464 0878 9870 0304
002465
002466 087A 9F00 1174
002467 087C 8A80 1175
002468
002469 087E 8280 089C
002470 0880 0020
002471 0881 0583
002472 0882 8A80 1175
002473
002474
002475
002476 0884 FB00 0003
002477 0886 D380 0000
002478 0888 0F80
002479 0889 1800
002480 088A 118D
002481 088B 9F00 077E
002482 088D AB80 089C
002483 088F AF80 0ECF
002484 0891 C380 0D80

* RAN-RD LDR $R1,=A'RD'
STR $R1,<ERMG+1 REPORT TEST
*
* GENERATE RANDOM DATA
*
LDR $R1,<PASSC GET PASS COUNT
MUL $R1,=64
LDR $R2,<LOOP
MLV $R2,=4
ADD $R1,=$R2 GENERATE BASE FOR RAN GEN
STR $R1,<BASE
LDR $R1,=X'100' SET UP SHORT RANGE
STR $R1,<RD4
LDR $R1,=X'7FE' GET SPEED OF LINE
LDR $R2,>SPEED BRANCH IF NOT MEASURED YET
BLZ $R2,>RD3
CMR $R2,=270
BLE $R2
STR $R1,<RD4 SET UP LONG RANGE
*
RD3 CALL ZV$F1,BASE,MODE INITIALIZE
*
CALL ZV$FR,SDB,R85 FILL X'85' PSEUDO RAN
*
LAB $B2,<PAR20
STB $B2,<PARPTR
CL <HEAD CLEAR HEADER FLAG
LNJ $B4,<GHEAD GENERATE HEADER
*
*
PAR20 LNJ $B2,<LPTS LOOP TEST
DC =X'C' RCV CONFIG, 8 BITS
DC =X'C' XMIT CONFIG
DC X'C' FILL WITH ABORTS
DC =X'47' LR2 - RCV, XMIT, TEST
DC 0 ACTION FLAG
DC 0 RCV ACTION FRAME, CHAR
DC 0 XMIT ACTION FRAME, CHAR
*
RD4 DC <SDB DATA
DC X'100' RANGE
DC X'60' LAST,V
*
JMP $B6
-----
* PARTIAL BYTE, CRC TEST, BIT INVERSION TEST
*
* THE FOLLOWING TEST REPORTS ERRORS WITH LABEL "XZ" WHERE
* Z IS ANY CHAR. LOOK FOR ENTRY "XZ" IN TABLE "CRCTST" FOR
* SPECIFICS OF A FAILING TEST.
*
PCRC-X LDV $R1,=-20
PCRCREP STR $R1,<COUNT DO TEST 20 TIMES
CL =$R1 CLEAR INDEX TO PARAMETER TABLE
*
CRC-LP LAB $B1,<CRCTST
LDV $R2,=1
*
LLH $R4,$B1,+$R1 GET XMIT LR7
STR $R4,<PCRC1
LLH $R4,$B1,+$R1 GET RCV LR6
STR $R4,<PCRC2
LLH $R4,$B1,+$R1 GET FLAG
STR $R4,<PCRC3
LLH $R4,$B1,+$R1 GET PARTIAL BYTE INFO
STR $R4,<PCRC4
LLH $R4,$B1,+$R1 GET ERROR LABEL BYTE 1
STH $R4,<ERMG+1
BEZ $R4,<ENDCRC BR IF END OF LIST
LLH $R4,$B1,+$R1 GET ERROR LABEL BYTE 2
STH $R4,<ERMG+1,$R2
CL <IFLG CLEAR BIT INVERSION FLAG (XMIT)
*
LB <PCRC1,=X'20' GET TCB BIT
*
BBF <PCRC5 BRANCH IF NOT SET
*
LDR $R1,=Z'0304' LCF 4 LONG, TCB = 3
* IN THIS TEST THE ASSUMPTION IS MADE IF A TCB BIT IS SET,
* THEN THAT HALF OF THE LINE WILL HAVE BIT INVERSION.
STR $R1,<HEAD
INC <IFLG
*
LB <PCRC2,=X'20' GET TCB BIT (RCV)
*
BBF >PCRC5 BRANCH IF NOT SET
INC <IFLG SET BIT INVERSION FLAG TO 2
*
* GENERATE RANDOM DATA FOR CRC TEST
*
PCRC5 CALL ZV$FR,SDB,R10 FILL X'10' WORDS OF RANDOM DATA
*
STR $R1,<DEX STORE INDEX
LAB $B2,<PCRC2
STB $B2,<PARPTR
LNJ $B4,<GHEAD GENERATE HEADER
*

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002481
002482
002483 0893 C380 0D4D
002484
002485 0895 8980 1175
002486 0897 0903
002487 0898 C380 0E0A
002488 089A A380 08CD
002489 089C 0000
002490 089D 0000
002491 089E 0047
002492 089F 0000
002493 08A0 0000
002494 08A1 0000
002495 08A2 0000
002496
002497 08A3 1800
002498 08A4 001F
002499 08A5 0060
002500
002501 08A6 9800 077E
002502 08A8 0F80 085A
002503
002504 08AA 9800 1169
002505 08AC 1780 0857
002506 08AE 8386
002507
002508
002509
002510
002511
002512
002513
002514
002515
002516
002517
002518
002519
002520
002521
002522 08AF 0C0C
002523 08B0 080C
002524 08B1 5841
002525
002526
002527
002528 08B2 0C0C
002529 08B3 000F
002530 08B4 5842
002531
002532
002533 08B5 0808
002534 08B6 000B
002535 08B7 5843
002536
002537
002538 08B8 0404
002539 08B9 0007
002540 08BA 5847
002541
002542
002543 08BB 0000
002544 08BC 0003
002545 08BD 5848
002546
002547
002548
002549 08BE 2C00
002550 08BF 080D
002551 08C0 0000
002552
002553 08C1 2C00
002554 08C2 080B
002555 08C3 584A
002556
002557
002558 08C4 2C20
002559 08C5 000C
002560 08C6 584B
002561
002562 08C7 2C20
002563 08C8 0001
002564 08C9 584C
002565
002566 08CA 0000
002567
002568
002569
002570
002571
002572
002573
002574 08CD B380 0E32
002575 08CF 8F00 2063
002576 08D1 0642
002577 08D2 8753
002578 08D3 C380 0ED0
002579 08D5 0F82
002580 08D6 0F85
002581 08D7 8F80 2063
002582 08D9 0642
002583 08DA 8382
002584
002585 08DB BF00 1166
002586 08DD 8780 0BFB
002587 08DF B380 0BA3
002588
002589 08E1 9800 0C1B
002590 08E3 8756
002591 08E4 9970 0002

```

```

* GENERATE CRC'S AND CRC RESIDUES AND TACK ON END OF SEND DATA
*
* LNJ $B4,<GCRC
* PCRC7 CMZ <IFLG CHECK BIT INVERSION FLAG
* BE >PCRC8 BRANCH IF NOT SET
* LNJ $B4,<IVRT INVERT DATA
* LNJ $B2,<LPTS LOOP TEST
* DC 0 RCV LR6
* DC 0 XMIT LR7
* DC 0 FILL WITH ABORTS
* DC 0 LR2 CONTROL, TEST, XMIT
* PCRC3 DC =X'47' FLAG
* DC 0 XMIT ACTION FLAG, FRAME
* DC 0 RCV ACTION FLAG, FRAME
*
* DC <SDB
* DC X'1F' RANGE
* DC X'60' VALID
*
* LDR $R1,<DEX
* B <CRC-LP
*
* ENDCRC LDR $R1,<COUNT GET TEST COUNT
* BINC $R1,<PCRCREP DO AGAIN IF NOT DONE
* JMP $B6
*
* TABLE FOR CRC, PARTIAL BYTE SIZE TEST
*
* WORD 1 BITS 0 - 7 LRT, XMIT
* BITS 8 - 15 LR6, RCV
*
* WORD 2 BITS 0 - 7 CCP FLAG
* BITS 8 - 15 XMIT BS 0,1,2 FOR LAST BYTE
*
* TEST XA
* WORD 3 BITS 0 - 15 ASCII TEST IDENTIFIER
*
* TEST XA READS IN CRC RESIDUES BY SPECIAL RCV BYTE CODE OF "001"
*
* CRCTST DC Z'0C0C' XMIT, RCV = 8 BITS
* DC Z'080C' TEST, 8 BIT LAST
* DC Z'5841' XA
*
* TEST XB
*
* DC Z'0C0C' XMIT, RCV = 8 BITS
* DC Z'000F' 4 BIT LAST BYTE
* DC Z'5842' XB
*
* TEST XC
*
* DC Z'0808' XMIT, RCV = 7 BITS
* DC Z'000B' 3 BIT RESIDUE
* DC Z'5843' XC
*
* TEST XG
*
* DC Z'0404' XMIT, RCV = 6 BITS
* DC Z'0007' 2 BIT RESIDUE
* DC Z'5847' XG
*
* TEST XH
*
* DC Z'0000' XMIT, RCV = 5 BITS
* DC Z'0003' 3 BIT RESIDUE
* DC Z'5848' XH
*
* THE FOLLOWING HAVE XMIT BIT INVERSION
*
* TEST XI
* DC Z'2C00' 8 BITS, TCB FOR XMIT
* DC Z'080D' TEST 8 BIT LAST
* DC Z'0' XI TEMP!!! 5B 5849
*
* TEST XJ
* DC Z'2C00' 8 BITS, TCB FOR TRANSMIT
* DC Z'080B' TEST, 3 BIT RESIDUE
* DC Z'584A' XJ
*
* TEST XK
* DC Z'2C20' 8 BITS, TCB ON XMIT, RCV
* DC Z'000C' NO RESIDUE BYTE
* DC Z'584B'
*
* TEST XL
* DC Z'2C20' 8 BITS, TCB ON XMIT, RCV
* DC Z'0001' 5 BYTE RESIDUE
* DC Z'584C'
*
* RESV 3,0 END
*
*
*
*-----
* BASIC DATA LOOP TEST
*
* LPTS LNJ $B3,<PASS PASS PARAMETERS
* SAVE <SAVMAJ,=Z'0642'
*
* LCT2 CL = $R3 CLEAR CHANNEL
* LNJ $B4,<FLN FIND ACTIVE LINE
* B >LPTS2 NO MORE ACTIVE LINES
* B >LCI8
* LPTS2 RSTR <SAVMAJ,=Z'0642'
*
* JMP $B2 EXIT TEST
*
* LCT8 STR $R3,<CHAN STORE CHAN TO TEST
* CLH <XTMP SET TO NOT READ XMIT FW REV
* LNJ $B3,<LOAD LOAD MLCP AND EXECUTE CCP
*
* READ RCV STATUS TO $R5, XFER SHOULD BE OVER
*
* LDR $R1,<XRNG1 GET RANGE
* CL = $R6
* CMR $R1,=2

```



```

002592 08E6 0203
002593 08E7 E870 1010
002594 08E9 D956
002595 08EA 0903
002596 08EB E3C0 06C2
002597 08ED C380 1067
002598
002599
002600
002601 08EF C380 0F6B
002602 08F1 E870 0203
002603 08F3 9800 11EA
002604 08F5 9970 204F
002605 08F7 0905
002606 08F8 D956
002607 08F9 0903
002608 08FA E3C0 06B3
002609 08FC 8700 1176
002610 08FE 9C80 0C1A
002611 0900 9F80 092E
002612
002613
002614
002615 0902 8AD3
002616 0903 C3C0 075C
002617
002618 0905 9800 0C28
002619 0907 1900 0911
002620 0909 E870 1000
002621 090B C380 10D9
002622 090D D956
002623 090E 0903
002624 090F E3C0 069E
002625
002626 0911 C380 10D9
002627 0913 E870 1020
002628 0915 D956
002629 0916 0903
002630 0917 E3C0 0696
002631
002632
002633
002634 0919 C380 0F6B
002635 091B E870 0403
002636 091D E955
002637 091F 090B
002638 091E E3C0 068E
002639 0921 9800 1175
002640 0923 8700 1175
002641 0925 1D01
002642 0926 0983
002643 0927 C380 0E0A
002644
002645
002646
002647
002648
0929 FBC0 0003
092B D380 0000
092D 0F80
092E 0929
092F 1C00
0930 1176
0931 116C
0932 11BD
002649 0933 8980 11BD
002650 0935 0909
002651 0936 D800 11BF
002652 0938 E800 11BE
002653 093A F800 11BD
002654 093C E3C0 0671
002655 093E 8280 119B
0940 0001
002656 0941 0581 0019
002657 0943 9800 116C
002658 0945 1001
002659 0946 A080 0BEF
002660 0948 2041
002661 0949 C820 04AD
002662 094B A854
002663 094C C820 0E02
002664 094E CF40 0827
002665 0950 E290 1800
002666 0952 E500 1176
002667 0954 D290 1C00
002668 0956 D956
002669 0957 0901 0003
002670 0959 E3C0 0654
002671
002672 095B 8280 0BF7
095D 0800
095E 058D
002673
002674
002675
002676
002677 095F 8AD1
002678 0960 2CFC
002679 0961 E290 1800
002680 0963 D290 1C00
002681 0965 8AD1
002682 0966 D956
002683 0967 0903
002684 0968 E3C0 0645
002685 096A 27F7
002686
002687 096B 8800 1166
002688 096D 3E04
002689 096E 0F80 08D3
002690
002691
002692
002693
002694 0970 B380 0E32

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```

BL >LPTS1
LDR $R6,=X'1010'
CMR $R5,=$R6
BE >$+3
LNJ $B6,ERRRDB
LNJ $B4,<DLAYLG
BRANCH IF RANGE <2
RCV STATUS WRUNG
DELAY FOR ILS RUPT

* READ P VALUE FOR RCV
LNJ $B4,<RPVLU
LDR $R6,=X'203'
CMR $R1,<ERMG+1
BE >LPTS3
LNJ $R5,=$R6
LNJ >LPTS3
BRANCH IF TEST 0
BRANCH MEANS P IS GOOD
RCV P COUNTER BAD AFTER FRAME RCV
LNJ $B6,ERRRDB
CL <MASK
LDB $B1,<XADD1
STB $B1,<COMP+4+$AF
GET ADDRESS OF DATA

* READ TRANSMIT STATUS TO R5
INC =$R3
LNJ $B4,DLAY
GET XMIT CHANNEL
DELAY 25 MS

LDR $R1,<XADD2
BEZ $R1,<RSTLST
LDR $R6,=X'1000'
LNJ $B4,<INXT
CMR $R5,=$R6
BE >$+3
LNJ $B6,ERRRDB
STATUS WRONG, FIRST CCB

RSTLST LNJ $B4,<INXT
LDR $R6,=X'1020'
CMR $R5,=$R6
BE >$+3
LNJ $B6,ERRRDB
GET NEXT STATUS
SHOULD BE STATUS
COMPARE WITH IS
STATUS ERROR, TRANSMIT

* READ P VALUE FOR XMIT
LNJ $B4,<RPVLU
LDR $R6,=X'403'
CMR $R6,=$R5
BE >COMP
LNJ $B6,ERRRDB
PVALUE WRONG FOR CCB, XMIT
GET B1 INVERSION FLAG
CLEAR FLAG
CMV $R1,=1
BNE >COMP
LNJ $B4,<IVRT
BRANCH IF NOT SET
INVERT DATA

* COMPARE DATA
COMP CALL ZV$C,$,RTB,MASK,RANGE,ERAR

```

```

CMZ <ERAR
BE >TSTIC
LDR $R5,<ERAR+2
LDR $R6,<ERAR+1
LDR $R7,<ERAR
LNJ $B6,ERRRDB
LB <ODDFLG,=1
IS
SB
WORD NUMBER
DATA EKRROR IN XFER

BBF TSTID
LDR $R1,<RANGE
SOL $R1,1
LDH $R2,<PARBYT
SOR $R2,1
LDR $R4,<TCBBSZ,$R2
LDR $R2,=$R4
LDR $R4,<DMASK,$R2
STR $R4,MASK
LLH $R6,<SDB,$R1
AND $R6,<MASK
CMR $R5,<RTB,$R1
BE $R5,=$R6
LNJ $B6,ERRRDB
LAST DATA BYTE INCORRECT

TSTID LB <ACTIFLG,=Z'0800'
GET TEST BIT FLAG

BBF >TSTIE
BRANCH IF NO CRC TO CHECK

* CHECK CRC'S AND RESIDUES
INC = $R1
LDV $R2,=-4
LLH $R6,<SDB,$R1
LLH $R5,<RTB,$R1
INC = $R1
CMR $R5,=$R6
BE >TSTIF
LNJ $B6,ERRRDB
TSTIF BINC $R2,>TSTIG
ERROR IN CRC UR CRC RESIDUE

TSTIE LDR $R3,<CHAN
ADV $R3,=4
B <LCT2
GET TESTED CHANNEL
BUMP CHANNEL NUMBER

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-----
*
* UNDERUN, ABORT TEST
*
ABUND LNJ $B3,<PASS
PASS PARAMETERS

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002695 0972 8F00 2063          SAVE <SAVMAJ,=Z'0042'
002696 0974 0042
002697 0975 8753
002697 0976 C380 OED0          ABUND2 LNJ $B4,<FLN          FIND ACTIVE LINE
002698 0978 0F82                B >ABUND6                NO MORE LINES
002699 0979 0F85                B >ABUND3
002700 097A 8F80 2063          ABUND6 RSTR <SAVMAJ,=Z'0042'
002701 097C 0042
002702 097D 8382                JMP $B2
002703 097E B380 OBA3          * ABUND3 LNJ $B3,<LOAD          LOAD AND EXECUTE CHANNEL PROGRAM
002704
002705 * XFER SHOULD BE OVER, ABORT SHOULD BE SET (IF DATA + FCS > 24 BITS)
002706 *
002707 0980 8756                CL = $R6
002708 0981 7C03                LDV $R7,=3
002709 0982 9280 11EA          ABUND8 LLH $R1,<ERMG+1          GET TEST MESSAGE
002710 0984 1D4A                CMV $R1,=X'4A'
002711 0985 0982                BNE >ABUND8
002712 0986 7C05                LDV $R7,=5
002713 0987 8280 OBF7          ABUND8 LB <ACTFLG,=Z'1000'          OVERUN
002714 098A 0583
002715 098B E870 2000          ABUND7 BBF >ABUND7          BRANCH IF NO OVERUN
002716 098D 9800 OC1B        LDR $R6,=Z'2000'          SET OVERUN BIT
002717 098F 9957                LDR $R1,<XRNG1
002718 0990 0380 0994          CMR $R1,=$R7
002719 0992 E470 1230          BLE <ABUND4                BRANCH IF RANGE <3
002720 0994 D956                DR $R6,=X'1230'          SHOULD BE STATUS
002721 0995 0903          ABUND4 CMR $R5,=$R6          COMPARE WITH IS
002722 0996 E3C0 0617          BE >$+3
002723                LNJ $B6,ERRDB          RCV STATUS WRONG
002724 *
002725 * READ XMIT STATUS TO R5
002726 *
002727 0998 8AD3                INC = $R3
002728 0999 C380 1067          LNJ $B4,<DLAYLG          BUMP CHANNEL NUMBER
002729 099B C380 10D9          LNJ $B4,<INXT            DELAY 225 MS
002730 *
002731 * STATUS TEST FOR RECEIVE
002732 *
002733 099D 8280 OBF7          LB <ACTFLG,=Z'0500'          CHECK UNDERUN EXPECTED
002734 099F 0500
002735 09A0 0584
002736 09A1 E870 3020          BBF >ABUND1                SB STATUS
002737 09A3 0F83                LDR $R6,=Z'3020'
002738 09A4 E870 1020          B >ABUND5
002739 09A6 D956          ABUND1 LDR $R6,=Z'1020'          COMPLETE, LAST
002740 09A7 0903          ABUND5 CMR $R5,=$R6
002741 09A8 E3C0 0605          BE >$+3
002742 09AA CB88 OECF          LNJ $B6,ERRDB          XMIT STATUS BAD, SINGLE FRAME
002743 09AC 9844 0006          LAB $B4,*<PARPTR          GET POINTER TO PARAMETERS
002744 09AE 9470 FF00          LDR $R1,$B4.6          PICK UP XMIT ABORT COUNT
002745 09B0 8251          OR $R1,=Z'FF00'
002746 09B1 1EFE          NEG = $R1
002747 09B2 9F00 OC1B        ADV $R1,=-2
002748 09B4 7C03                STR $R1,<XRNG1          VALID DATA RANGE
002749 09B5 D280 11EA          LDV $R7,=3
002750 09B7 5D4A                LLH $R5,<ERMG+1          GET TEST LABEL
002751 09B8 0982                CMV $R5,=X'4A'
002752 09B9 7C05                BNE >ABUND9                BRANCH IF NOT TEST J
002753 09BA 9957          ABUND9 LDV $R7,=5
002754 09BB 0AA7                CMR $R1,=$R7
002755 09BC 1001                >UND2
002756 09BD 9F00 116C          SOL $R1,1
002757 *
002758 09BF 8700 1176          STR $R1,<RANGE
002759 *
002760 COMP1 CL <MASK
002761 CALL ZV$C,$,$,MASK,RANGE,ERAR
002762
002763
002764
002765
002766
002767
002768
002769
002770
002771
002772
002773
002774
002775
002776
002777
002778
002779
002780 09C1 FBC0 0003          X
002781 09C3 D380 0000
002782 09C5 0F80
002783 09C6 09C1
002784 09C7 09C1
002785 09C8 1176
002786 09C9 116C
002787 09CA 11BD
002788 09CB 8980 11BD          CMZ <ERAR
002789 09CD 0909                BE >UND3
002790 09CE D800 11BF          LDR $R5,<ERAR+2          IS
002791 09D0 E800 11BE          LDR $R6,<ERAR+1          SB
002792 09D2 E800 11BD          LDR $R6,<ERAR            WORD NUMBER
002793 09D4 E3C0 05D9          LNJ $B6,ERRDB          DATA ERROR
002794 09D6 9800 OC1B        LDR $R1,<XRNG1
002795 09D8 88D1                DEC = $R1
002796 09D9 8755                CL = $R5
002797 09DA D956                CMR $R5,=$R6
002798 09DB 0907                BE >UND2
002799 09DC E3C0 05D1          LNJ $B6,ERRDB          LAST BYTE TRANSFERED WRONG
002800 09DE 0F80 09E2          B <UND2
002801 09E0 D090 1C00          LDH $R5,<RTB,$R1
002802 09E2 B800 1166          LDR $R3,<CHAN          GET ACTUAL LAST BYTE
002803 09E4 3E04                ADV $R3,=4              GET TESTED CHANNEL
002804 09E5 0F80 0976          B <ABUND2                BUMP CHANNEL NUMBER
002805 *
002806 * -----
002807 *
002808 * SPEED TEST
002809 *
002810 09E7 B380 0E32          SPTS LNJ $B3,<PASS          PASS PARAMETERS
002811 09E9 8F00 2063          SAVE <SAVMAJ,=Z'0002'
002812 09EB 0002
002813 09EC 8753
002814 09ED C380 OED0          SPTS1 CL = $R3
002815 09EF 0F83                LNJ $B4,<FLN          FIND LINE NUMBER
002816 *
002817 09F0 0F80 09F6          B <SPTS2                NO MORE LINES
002818 09F2 8F80 2063          SPTS3 RSTR <SAVMAJ,=Z'0002'
002819 09F4 0002
002820 09F5 8382                JMP $B2
002821 *
002822 09F6 BF00 1166          SPTS2 STR $R3,<CHAN
002823 09F8 B380 OBA3          LNJ $B3,<LOAD
002824 09FA 9870 0804          LDR $R1,=2052
002825 09FC 9200 1183          SUB $R1,<RRNG          LOAD MLCP + EXECUTE CCP
002826 *
002827 * RANGE + 2 FLAGS + ILS
002828 * GETS NUMBER ACTUALLY SENT

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002794 09FE 1E03          ADV  $R1,=3          COMPENSATE FOR BUFFERING
002795 09FF 1880 0A02     CL   $R1,<SPED3     MAKE SURE +
002796 0A01 8751          CL   =SR1           4000 MSEC (TICKS)
002797 0A02 9F00 1168   SPED3 STR $R1,<CHRCNT REAL TIME CLOCK TICKS
002798 0A04 C870 01E0   LDR  $R4,=480     ELAPSED TIME
002799 0A06 C200 0000   SUB  $R4,<ZHRTCC  CONVERT KTC TICKS TO MSEC
002800 0A08 CF00 1167   STR  $R4,<ELPS    CALCULATE BITS/SEC
002801 0A0A 9F80 111E   LNJ  $B5,<TKSEC
002802 0A0C D380 1138   LNJ  $B5,<BPS
002803          * PRINT LINE SPEED
002804 0A0E 3041          SUR  $R3,1         ALIGN LINE SPEED
002805 0A0F BF00 1177   STR  $R3,<TPR     LINE
002806          CALL ZV$TC,LINE

002807 0A11 FBC0 0003          LDR  $R1,=FBC0    NUMBER
002808 0A13 D380 0000   X          CL   D380
002809 0A15 0F80          CL   0F80
002810 0A16 0A41          CL   0A41
002811 0A17 FBC0 0003          CALL ZV$TD,TPR
002812 0A19 D380 0000   X          CL   D380
002813 0A1B 0F80          CL   0F80
002814 0A1C 1177          CL   1177
002815 0A1D FBC0 0003          CALL ZV$TC,SPDMSG  BITS/SEC =
002816 0A1F D380 0000   X          CL   D380
002817 0A21 0F80          CL   0F80
002818 0A22 0A44          CL   0A44
002819 0A23 FBC0 0003          CALL ZV$TD,MSBS   BITS/SEC
002820 0A25 D380 0000   X          CL   D380
002821 0A27 0F80          CL   0F80
002822 0A28 1187          CL   1187
002823 0A29 1C5C          CL   1C5C
002824 0A2A 9F00 1177   LDV  $R1,=X'5C'   NULL
002825 0A2C 8700 1178   STR  $R1,<TPR
002826          CL   <TEMP
002827          CALL ZV$TD,TEMP,TPR  ADD ZERO ON END

002828 0A2E FBC0 0003          LDR  $R1,<MSBS    GET SPEED
002829 0A30 D380 0000   X          SUR  $R3,2       ALIGN FOR DAUGHT BD NUMB
002830 0A32 0F80          CL   0F80
002831 0A33 1178          CL   1178
002832 0A34 1177          CL   1177
002833          *
002834 0A35 9800 1187          LDR  $R1,<HCLS-1.$R3  STORE AWAY SPEED/10
002835 0A37 3042          SUR  $R3,2
002836 0A38 9F30 11A0          STR  $R1,<SPEED
002837 0A3A 9F00 1192          STR  $R1,<SPEED
002838          *
002839 0A3C B800 1166          LDR  $R3,<CHAN    GET BACK TESTED CHANNEL
002840 0A3E FE04          ADV  $R3,=4       BUMP CHANNEL NUMBER
002841 0A3F 0F80 09ED          B    <SPT51      DO NEXT CHANNEL
002842          *
002843 0A41 4C49 4E45 2400   LINE TEXT 'LINES'
002844 0A44 4249 5453 2F53   SPDMSG TEXT 'BITS/SEC = '$
002845 0A47 4543 203D 2024

-----
002846          *
002847          * RECEIVE END TEST
002848          *
002849 0A4A 0F00 0A52   RET  NOP <RET1
002850 0A4C 8F00 2063   RET  SAVE <SAVMAJ,=Z'0002'
002851 0A4E 0002          CL   =SR3
002852 0A4F B380 0E32          LNJ  $B3,<PASS    PASS PARAMETERS
002853 0A51 8753          CL   =SR3
002854 0A52 C380 0ED0   RET1 LNJ $B4,<FLN    FIND ACTIVE LINE
002855 0A54 0F83          B    >RET9
002856 0A55 0F80 0A5B          B    <RET2
002857 0A57 8F80 2063   RET9 KSTR <SAVMAJ,=Z'0002'
002858 0A59 0002          B    <RET2
002859 0A5A 8382          JMP  $B2
002860          *
002861 0A5B BF00 1166   RET2 STR $R3,<CHAN  STORE CHANNEL
002862          CALL ZV$F,STBL,CM1,C4  INITIALIZE STATUS TABLE
002863 0A5D FBC0 0003          LDR  $R1,=FBC0
002864 0A5F D380 0000   X          CL   D380
002865 0A61 0F80          CL   0F80
002866 0A62 0AEE          CL   0AEE
002867 0A63 1197          CL   1197
002868 0A64 1196          CL   1196
002869 0A65 B380 0BA3          LNJ  $B3,<LOAD    LOAD CHANNEL PROGRAM AND EXECUTE
002870          *
002871          * LOOP TO CHECK STATUS AND DATA FOR FRAMES SENT
002872          *
002873 0A67 1C01          LDV  $R1,=1
002874 0A68 9F00 1177   STR  $R1,<TPR
002875 0A6A 8752          CL   =SR2
002876          *
002877 0A6B F800 117B          LDR  $R7,<EXST    GET EXP STAT WORD
002878 0A6D 8756          CL   =SR6
002879 0A6E 7084          DOL  $R7,4       SHIFT LEFT DIGIT TO R6
002880 0A6F 6D04          CMV  $R6,=4     CHECK FOR NULL ON FIRST
002881 0A70 0985          BNE  >RET14
002882 0A71 8AD2          INC  =SR2
002883 0A72 EF00 1177   STR  $R6,<TPR
002884 0A74 0FF9          B    >FNULL
002885          *
002886 0A75 DF20 0AEE          STR  $R5,<STBL.$R2  STORE STATUS FOR REFERENCE
002887 0A77 D570 3250          AND  $R5,=Z'3250'  STRIP TO STATUS COMPLETE, OVRUN, EOF
002888 0A79 6D01          CMV  $R6,=1
002889 0A7A 0922          BE   >RET5
002890 0A7B EF00 1177   RETX STR $R6,<TPR  BRANCH IF GOOD DATA, STATUS
002891 0A7D EF54          STR  $R6,=SR4    STORE LAST STATE
002892 0A7E 8AD2          INC  =SR2
002893 0A7F 4D04          CMV  $R4,=4
002894 0A80 0900 0AD1          BE   <RET8
002895 0A82 4D03          CMV  $R4,=3
002896 0A83 0983          BNE  >RET3
002897 0A84 8756          CL   =SR6
002898 0A85 0F90          B    >RET11
002899 0A86 4D02          CMV  $R4,=2
002900 0A87 090C          BE   >RET13
002901 0A88 E870 3000          LDR  $R6,=Z'3000'  B = OVERRUN
002902 0A8A 4D05          CMV  $R4,=5

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002877 0A8B 090A          BE    >RET11
002878 0A8C E870 1210    LDR   $R6,=Z'1210'
002879 0A8E 4D06          CMV   $R4,=6
002880 0A8F 0906          BE    >RET11
002881 0A90 E870 3210    LDR   $R6,=Z'3210'
002882 0A92 0F83          B     >RET11
002883
*
002884 0A93 E870 3010    RET13 LDR   $R6,=Z'3010'
002885 0A95 D956          RET11 CMR   $R5,=$R6
002886 0A96 0900 0AD1      BE    <RET8
002887 0A98 E3C0 0515    RET4  LNJ   $B6,ERRDB
002888 0A9A 0F80 0AD1      B     <RET8
002889 0A9C E870 1010    RET5  LDR   $R6,=Z'1010'
002890 0A9E D956          CMR   $R5,=$R6
002891 0A9F 0903          BE    >RET6
002892 0AA0 E3C0 050D      LNJ   $B6,ERRDB
002893
* CHECK DATA
002894
*
002896 0AA2 CCA0 0AEA      RET6  LDB   $B4,<RADTBL,$R2
002897 0AA4 BCF4          LDB   $B3,+$B4
002898 0AA5 BF80 0AC2      STB   $B3,<CDATA+4+$AF
002899 0AA7 E804          LDR   $R6,$B4
002900 0AA8 6041          SOR   $R6,1
002901 0AA9 EF00 116C      STR   $R6,<RANGE
002902 0AAB 9852          LDR   $R1,=$R2
002903 0AAC E800 1177      LDR   $R6,<TPR
002904 0AAE 6D04          CMV   $R6,=4
002905 0AAF 0982          BNE   >RET10
002906 0AB0 88D1          DEC   $R1
002907 0AB1 BC90 0AEA      RET10 LDB   $B3,<RADTBL,$R1
002908 0AB3 8AD2          INC   $R2
002909 0AB4 C83          LDB   $B3,$B3
002910 0AB5 B8C4 0400      LAB   $B3,$B4,X'400'
002911 0AB7 BF80 0AC3      STB   $B3,<CDATA+4+2*$AF
002912 0AB9 8700 1176      CL   $MASK
002913 0ABB CF00 1177      STR   $R4,<TPR
002914
*
002915
* CDATA CALL
002916 0ABD FBC0 0003      CALL  ZV$C,$,$,MASK,RANGE,ERAR
002917 0ABF D380 0000
002918 0AC1 0F80
002919 0AC2 0ABD
002920 0AC3 0ABD
002921 0AC4 1176
002922 0AC5 116C
002923 0AC6 11BD
002924 0AC7 8980 11BD      CMZ   <ERAR
002925 0AC9 0908          BE    >RET8
002926 0ACA D800 11BF      LDR   $R5,<ERAR+2
002927 0ACC E800 11BE      LDR   $R6,<ERAR+1
002928 0ACE E3C0 04DF      LNJ   $B6,ERRDB
002929 0AD0 0000          HLT
002930 0AD1 9800 1192      RET8  LDR   $R1,<SPEED
002931 0AD3 9970 010E      CMR   $R1,=270
002932 0AD5 0303          BG    >RET15
002933 0AD6 C380 1067      LNJ   $B4,<DLAYLG
002934 0AD8 C380 1067      RET15 LNJ   $B4,<DLAYLG
002935 0ADA 8756          CL   $R6
002936 0ADB 7084          DOL   $R7,4
002937 0ADC 89D6          CMZ   $R6,=$R6
002938 0ADD 0908          BE    >RET7
002939 0ADE 7D04          CMV   $R7,=4
002940 0ADF 0900 0A75      LNJ   <RET14
002941 0AE1 C380 10D9      LNJ   $B4,<INXT
002942 0AE3 0F80 0A75      B     <RET14
002943 0AE5 B800 1166      RET7  LDR   $R3,<CHAN
002944 0AE7 3E04          ADV   $R3,=4
002945 0AE8 0F80 0A52      B     <RET1
002946
* POINTERS TO XMIT ADDRESS AND RANGE
002947
*
002948 0AFA OC1A          RADTBL DC <XADD1
002949 0AFB 0C28          DC <XADD2
002950 0AFD 0C33          DC <XADD3
002951 0AFE 0C3E          DC <XADD4
002952
* STATUS READ BACK STORED HERE
002953
*
002954 STBL  DC -1
002955 DC -1
002956 DC -1
002957 DC -1
002958
*-----*
002959
* INITIAL CONDITIONS TEST
002960
*
002961 0AF2 B380 0E32      TONTST LNJ   $B3,<PASS
002962 0AF4 8F00 2063      SAVE <SAVMAJ,=Z'0002'
002963 0AF6 0002
002964 0AF7 8753
002965 0AF8 C380 0ED0      TON1  CL   $R3
002966 0AFA 0F82          LNJ   $B4,<FLN
002967 0AFB 0F85          B     >TON2
002968
*
002969 0AFC 8F80 2063      TON2  RSTR <SAVMAJ,=Z'0002'
002970 0AFE 0002
002971 0AFF 8382          JMP   $B2
002972
*
* PUT XMIT IN FLAG IDLE, TURN ON RCV, DUMMY RCV, GET NO ILS RUPT
002973
*
002974 0B00 BF00 1166      TON3  STR   $R3,<CHAN
002975 0B02 3042          SOR   $R3,2
002976 0B03 9830 11B9      LDR   $R1,<ATLT,$R3
002977 0B05 8752          CL   $R2
002978 0B06 9970 21F6      CMR   $R1,=Z'21F6'
002979 0B08 0981 0003      BNE   TON10
002980 0B0A A870 00EB      LDR   $R2,=X'EB'
002981 0B0C AF00 0B9C      STR   $R2,<DSCI
002982 0B0E B800 1166      LDR   $R3,<CHAN
002983 0B10 9870 0020      LDR   $R1,=X'20'
002984 0B12 9780 0BFA      STH   $R1,<IFM
002985
*
002986
*
002987
*
002988
*
002989
*
002990
*
002991
*
002992
*
002993
*
002994
*
002995
*
002996
*
002997
*
002998
*
002999
*

```

X

BRANCH = MISSING FRAME
BRANCH IF ABORTED FRAME
IS ABORTED, OVERUN FRAME

GOOD STATUS ERROR - SHOULD BE IN R5
COMPLETE, EOF
GOOD IF BRANCH ERROR, STATUS, RCV

GET ADDRESS OF OUTPUT ADDRESS
GET ADDRESS
GET RANGE
GET RANGE IN WORDS

GET LAST STATE
SEE IF LAST STATE WAS NULL
SET TO GET LAST POINTER
GET RCV ADD -X'400'
BUMP FOR NEXT TIME
GET ADDRESS

SAVE LAST STATE

IS
SB
DATA ERRUR IN XFER

COMPARE FOR 2700 BAUD

LONG DELAY
DELAY 225MS

GET NEXT STATUS STATE
CHECK IF DONE
BRANCH IF DONE
TEST FOR NULL

INPUT NEXT STATUS

GET TESTED CHANNEL
BUMP CHANNEL NUMBER

R3 TRACKS CHANNEL
FIND ACTIVE LINE
NO MORE LINES

B = BH4DLE

RESTORE CHANNEL NUMB
INITIAL FLAG IDLE

```

002980 0B14 1C08          LDV      $R1,=8
002981 0B15 9780 0BFB      STH      $R1,<XTEMP
002982 0B17 B380 0BA3      LNJ      $B3,<LOAD          SET FLAG TO READ XMIT FW REV
                                LOAD CHANNEL PROGRAM AND EXECUTE
** READ P VALUE FOR RCV
**
LNJ      $B4,<RPVLU          READ P TO R5
LDR      $R6,=X'21A'        SB
CMR      $R6,=$R5
BE       >$+3
LNJ      $B6,ERRDB          P VALUE WRONG, AFTER START 10
** READ P VALUE FOR XMIT
**
INC      =$R3                GET XMIT CHANNEL
LNJ      $B4,<RPVLU          SB
LDR      $R6,=X'441'
CMR      $R6,=$R5
BE       >$+3
LNJ      $B6,ERRDB          WRONG P VALUE AFTER START, XMIT
** TURN ON TRANSMIT, BUT NOT RCV AND CHECK OPERATION
**
CLH      <IFM                SET FOR ABORT FILL
CLH      <PRCV                SET FOR RECEIVE START AT X'200'
LNJ      $B3,<LOAD          LOAD CHANNEL PROGRAM AND EXECUTE
** READ XMIT STATUS, SHOULD BE COMPLETE
**
LDR      $R6,=X'1020'
INC      =$R3                GET BACK XMIT
LNJ      $B4,<INXT            INPUT NEXT STATUS TO R5
CMR      $R6,=$R5
BE       >$+3
LNJ      $B6,ERRDB          WRONG XMIT STATUS
** CHECK P VALUE FOR RCV - SHOULD HAVE ILS ONLY
**
DEC      =$R3                GET BACK RECEIVE CHANNEL
LNJ      $B4,<RPVLU          READ P VALUE TO R5
LDR      $R6,=X'270'
CMR      $R6,=$R5
BE       >$+3
LNJ      $B6,ERRDB          INCORRECT RCV OPERATION
** CHECK P VALUE FOR XMIT, SHOULD HAVE COMPLETED
**
INC      =$R3                READ P TO R5
LNJ      $B4,<RPVLU          SB
LDR      $R6,=X'403'
CMR      $R6,=$R5
BE       >$+3
LNJ      $B6,ERRDB          XMIT CHANNEL PROG DIDN'T COMPLETE
** IF FIRST PASS, LOOP, PRINT BBHOLC FIRMWARE REV NUMBER
**
CMZ      <LOOP                BRANCH IF NOT FIRST LOOP
BNE      >TON6
CMZ      <PASSC                BRANCH IF NOT FIRST PASS
BNE      >TON6
** CALL ZV$TC,MESG4          PRINT BHCLA LINE
003043 0B51 FBC0 0003      X
003044 0B53 D380 0000
003045 0B55 0F80
003046 0B57 0B92
003047 0B58 2041          STR      $R3,=$R2
003048 0B59 AF00 1177      STR      $R2,1
                                STR      $R2,<TPR
                                CALL     ZV$TD,TPR          TYPE LINE NUMBER
003049 0B5B FBC0 0003      X
003050 0B5D D380 0000
003051 0B5F 0F80
003052 0B60 1177
003053 0B61 E380 0C9A      TON6   LNJ      $B6,<PREV
003054 0B63 B800 1166      LDR      $R3,<CHAN          PRINT REV NUMBER
                                GET TESTED <CHANNEL
** 1. ATTEMPT TO TRANSMIT WITH NO REQUEST TO SEND, TEST AND DIRECT CONNECT.
** 2. ATTEMPT TO TRANSMIT WITH NO DIRECT CONNECT OR TEST MODE
** 3. ATTEMPT TO TRANSMIT WITH BIT 2 OF LM2 = 1. (SHOULD INHIBIT CLOCK)
** PART 3 IS DONE FOR BH4DLD ONLY.
**
**
CL       <RTSFLG              CLEAR FLAG
LDR      $R1,<CBLOOP
BEZ      $R1,<TON9            IF NOT CUNN. LOOP, BRANCH
CL       =$R2
LAB      $B1,<PSB            ADDRESS OF P SHOULD BE TABLE
** TON8 LDR      $R1,<DSC,$R2  GET VALUE OF LR2
**
BEZ      $R1,<TON9            B = DONE
**
STH      $R1,<LR2CFG          FLAG TO INHIBIT RTS, DIR CONN
INC      <RTSFLG
LNJ      $B3,<LOAD          LOAD AND EXECUTE
** READ P FOR RECEIVE
**
LNJ      $B4,<RPVLU          SB
LDR      $R6,=X'270'
CMR      $R6,=$R5
BE       >$+3
LNJ      $B6,ERRDB          INVALID RCV REQUEST
** READ P FOR TRANSMIT
**
INC      =$R3                GET TRANSMIT CHANNEL
LNJ      $B4,<RPVLU          INPUT P VALUE
LDR      $R6,$B1,+$R2        GET SHOULD BE

```

```

003085 0B84 D956
003086 0B85 0283
003087 0B86 E3C0 0427
003088
003089 0B88 0F80 0B6E
003090
003091 0B8A B800 1166
003092 0B8C 3E04
003093 0B8D 1C45
003094 0B8E 9780 0BF1
003095 0B90 0F80 0AF8
003096 0B92 4843 4C41 2020
003096 0B95 4C46 4E45 2020
003097
003098 0B99 0000
003099
003100
003101 0B9A 008B
003102 0B9B 00C2
003103 0B9C 00EB
003104 0B9D 0000
003105
003106
003107
003108 0B9F 0425
003109 0BA0 0425
003110 0BA1 0425
003111 0BA2 0000
003112
003113
003114
003115
003116 0BA3 8F00 2073
003116 0BA5 2052
003117
003118
003119
003120 0BA6 8752
003121 0BA7 9800 1174
003122 0BA9 1806
003123 0BAA 9800 116A
003124 0BAC 9B80 0C91
003125 0BAE A811
003126
003127 0BAF 2008 003D
003128 0BB0 A470 0041
003129 0BB2 AF40 0041
003130 0BB4 0F00 0BBB
003131
003132 0BB6 9800 0C1B
003133 0BB8 8752
003134 0BB9 1B02
003135 0BBA 8AD2
003136 0BBB AF00 119B
003137 0BBD 9A00 0C29
003138 0BBF 1041
003139 0BC0 9F00 116C
003140
003141
003142
003143 0BC2 9800 1185
003144 0BC4 1900 0BD1
003145 0BC6 9800 0B99
003146 0BC8 1989
003147 0BC9 9800 0BF1
003148 0BCB 9570 FBFF
003149 0BCD 9470 4800
003150 0BCF 9F00 0BF1
003151 0BD1 8700 0B99
003152 0BD3 9800 1184
003153 0BD5 1900 0BDD
003154 0BD7 9800 0BF1
003155 0BD9 9570 F3FF
003156 0BDB 9F00 0BF1
003157
003158 0BDD B800 1166
003159 0BDF C380 0EEC
003160
003161
003162
003163
003164 0BE1 B380 10EB
003165 0BE3 0BE5
003166 0BE4 0FA7
003167
003168
003169
003170 0BE5 0206
003171 0BE6 0007
003172 0BE7 0426
003173 0BE8 0027
003174 0BE9 000C
003175 0BEA 000D
003176 0BED 002C
003177 0BEC 002D
003178 0BED 0023
003179 0BEE 0024
003180
003181
003182
003183 0BEF 0003
003184 0BF0 0004
003185 0BF1 C714
003186 0BF2 0017
003187 0BF3 0037
003188 0BF4 FF3D
003189 0BF5 003C
003190 0BF6 0019
003191 0BF7 003E
003192 0BF8 001D
003193 0BF9 001B
003194 0BFA 003F
    
```

```

CMR $R5,=$R6
BGE >$+3
LNJ $B6,ERRDB MORE THAN ONE XMIT REQUEST.
*
* B <TONB
*
* TON9 LDR $R3,<CHAN
ADV $R3,=4 BUMP LINE NUMBER
LDV $R1,<X145
STH $R1,<LR2CFG RESTORE CONFIGURATION
B <TON1 DO FOR NEXT HDLC LINE
MESG4 TEXT 'HCLA LINE $'

RTSFLG DC 0 FLAG
*
* LR2 VALUES TO OUTPUT
*
DSC DC Z'008B'
DC Z'00C2'
DSC1 DC Z'00EB' 0 IF BH4DLE
RESV Z,0
*
* P VALUE FOR XMIT
*
PSB DC Z'0425'
DC Z'0425'
DC Z'0425'
DC 0
-----
*
* SUBROUTINE TO SET UP CCB'S, LOAD CHANNEL PROGRAM, AND START EXECUTION
*
LOAD SAVE <SAV,=Z'2052' SAVE B3,B6,R2,B1
*
* FORM MASK OF ALL ONES EQUIVALENT TO CHAR SIZE
*
CL =R2
LDR $R1,<HEAD GET FRAME CONFIG INFO
BLZ $R1,>FMASK BRANCH IF SUPERVISORY CONTROL BIT SET
LDR $R1,>CHS2 GET CHAR SIZE
LAB $B1,<MSKTB1 GET ADDRESS OF TABLE OF MASKS
LDR $R2,$B1.$R1 GET MASK
*
* FMASK SOL $R2,B
OK $R2,=X'3D'
STR $R2,XTMSK STORE MASK WORD
NOP <LPI6
*
LDR $R1,<XRNG1
CL =R2
DEVN $R1,>LPT6
INC =R2
LPT6 STR $R2,<ODDFLG
ADD $R1,<XRNG2 TOTAL RANGE IN BYTES
SOR $R1,1 FORM RANGE IN WORDS
STR $R1,<RANGE
*
* MODIFY CONTROL IF CONNECTOR OR EXTERNAL LOOP
*
LDR $R1,<CBL0OP GET CONNECTOR LOOP FLAG
BEZ $R1,<LCT7
LDR $R1,<RTSFLG
BNEZ $R1,>LCT7 BRANCH FOR NO RTS, DIR. CONN.
LDR $R1,<LR2CFG GET LR2 INFO
AND $R1,=Z'FBFF' STRIP OFF TEST
OR $R1,=Z'4800' SET DIRECT CONNECT ,RTS
LCT7 STR $R1,<LR2CFG
CL <RTSFLG
LDR $R1,<XLOOP GET EXTERNAL LOOP FLAG
BEZ $R1,<LPT3
LDR $R1,<LR2CFG
AND $R1,=Z'F3FF' STRIP OFF TEST, DIRECT CONNECT
STR $R1,<LR2CFG
*
LPT3 LDR $R3,<CHAN
LNJ $B4,<GENITZ INITIALIZE
*
* SEND OUT LCT
*
LNJ $B3,<SETLCT
DC <LCT1 LCT TABLE FOR THIS TEST
B >TST1A
*
* LCT TABLE
*
LCT1 DC Z'0206' P, MSB
PRCV DC Z'0007' P, LSB
DC Z'0426' P, MSB, XMIT
DC Z'0027' P, LSB, XMIT
DC Z'000C' CHANNEL
DC Z'000D' LEVEL
DC Z'002C' CHANNEL
DC Z'002D' LEVEL
DC Z'0023' CRC RESIDUE 1, XMIT
DC Z'0024' CRC RESIDUE 2, XMIT
*
* CONFIGURABLE PARAMETERS
*
PARBYT DC X'0003'
RCVRES DC X'0004'
LR2CFG DC Z'C714'
LR6RCV DC Z'0017'
LR7XMT DC Z'0037'
XTMSK DC Z'FF3D'
TACC DC Z'003C'
UFMCNT DC X'0019'
ACTFLG DC Z'003E'
RAC DC Z'001D'
FRMCNT DC Z'001B'
IFM DC Z'003F'
BYTE SIZE FOR LAST BYTE XMIT
RESIDUE FOR LAST BYTE, RCV
LR2
CONFIG (LR6)
XMIT CONFIG (LR7)
WORD SIZE MASK
XMIT ACTION CHAR COUNT
ACTION FRAME COUNTER - XMIT
ACTION FLAG
RECEIVE ACTION CHAR COUNT
FRAME COUNT, RCV
FILL STATE, BIT 0 = IFM, BIT 1 = INITIAL
    
```

```

003195
003196 UBFb 0039
003197
003198
003199
003200
003201 UBFc 0018
003202 UBFd 0038
003203 UBFE 000E
003204 UBFF 002E
003205 UC00 001A
003206 UC01 001E
003207 UC02 001F
003208
003209 UC03 003B
003210 UC04 001C
003211 UC05 003A
003212
003213 UC06 0002
003214 UC07 0022
003215
003216 UC08 0000
003217
003218
003219
003220 UC0B 8AD3
003221 UC0C 9380 1095
003222 UC0E 11Ff
003223 UC0F 01AA
003224 UC10 0200
003225 UC11 0000
003226
003227
003228
003229 UC12 9380 1095
003230 UC14 12D4
003231 UC15 018A
003232 UC16 0400
003233 UC17 0000
003234
003235
003236
003237
003238 UC18 C380 10FF
003239 UC1A 1160
003240 UC1B 0000
003241 UC1C 0040
003242
003243 UC1D 8280 0C1C
003244 UC1F 0020
003245 UC20 0523
003246
003247
003248 UC21 9800 0C1C
003249 UC23 82D1
003250 UC24 0020
003251 UC25 051E
003252 UC26 C380 10FF
003253 UC28 1160
003254 UC29 0000
003255 UC2A 0060
003256
003257 UC2B 8708 0C28
003258 UC2D 8280 0C2A
003259 UC2F 0020
003260 UC30 0513
003261
003262
003263
003264 UC31 C380 10FF
003265 UC33 1160
003266 UC34 0000
003267 UC35 0060
003268
003269 UC36 8708 0C33
003270 UC38 8280 0C35
003271 UC3A 0020
003272 UC3B 0508
003273
003274 UC3C C380 10FF
003275 UC3E 1160
003276 UC3F 0000
003277 UC40 0060
003278
003279 UC41 8708 0C3E
003280 UC43 88D3
003281
003282
003283
003284
003285
003286 UC44 C380 10FF
003287 UC46 1C00
003288 UC47 0000
003289 UC48 0040
003290 UC49 8280 0C48
003291 UC4B 0020
003292 UC4C 0518
003293
003294 UC4D C380 10FF
003295 UC4F 1C00
003296 UC50 0000
003297 UC51 0040
003298 UC52 8280 0C51
003299 UC54 0020
003300 UC55 050F
003301
003302 UC56 C380 10FF
003303 UC58 1C00

```

```

* XTEMP DC Z'0039'
*
* CHANNEL PROGRAM WORK LOCATIONS
*
DC Z'0018' CHAR COUNTER, RCV
DC Z'0038' CHAR COUNTER, XMIT
DC Z'000E' LR5 STORAGE, RCV
DC Z'002E' LR5 STORAGE, XMIT
DC Z'001A' LR7, RCV
DC Z'001E' XMIT FW REV STORAGE
DC Z'001F' RCV FW REV STORAGE
*
DC Z'003B' XMIT FRAME COUNT
DC Z'001C' REC TEMP LOC
DC Z'003A' ERROR CODE
* SPARE
DC Z'0002'
DC Z'0022'
*
RESV 3,0
* SEND OUT CHANNEL PROGRAM
*
TST1A INC =$R3 GET XMIT CHANNEL
LNJ $B1,<SDATA SEND DATA
DC <CCP1 RCV CHANNEL PROGRAM
DC ((CCP2-CCP1)*2
DC X'200' RAM ADDRESS
DC 0 EVEN CPU ADDRESS
* SEND OUT XMIT CHANNEL PROGRAM
*
LNJ $B1,<SDATA XMIT CHANNEL PROGRAM
DC <CCP2 RANGE
DC ((CCP3-CCP2)*2 RAM ADDRESS
DC X'400' EVEN BYTE ADDRESS
*
* SET UP CCB'S FOR DATA XFER
*
LNJ $B4,<MCCB MAKE CCB FOR XMIT
DC <DUMMY DATA ADDRESS
XRNG1 DC 0 RANGE
XCON1 DC Z'0040' CCB CONTROL WORD
*
LB <XCON1,=Z'0020' GET LAST BIT
TESTT BBT >TESTM BRANCH IF SET
* GIVE SECOND CCB
*
LDR $R1,<XCON1 GET CONTROL WORD
LB =$R1,=Z'0020'
*
BBT >TESTM
LNJ $B4,<MCCB MAKE CCB
DC <DUMMY XADDRESS
XRNG2 DC 0 RANGE
XCON2 DC X'60' CONTROL
*
CL *<XADD2 CLEAR ADDRESS + CONTROL
*
LB <XCON2,=Z'0020' GET LAST BLOCK BIT
BBT >TESTM BRANCH IF SET
* GIVE 3D CCB
*
LNJ $B4,<MCCB MAKE CCB
DC <DUMMY
XRNG3 DC 0
XCON3 DC X'60'
*
CL *<XADD3
LB <XCON3,=Z'0020' GET LAST BLOCK BIT
BBT >TESTM BRANCH IF LAST BLOCK
* GIVE 4TH CCB
*
LNJ $B4,<MCCB
DC <DUMMY
XRNG4 DC 0
XCON4 DC X'60'
*
CL *<XADD4 CLEAR ADDRESS AND CONTROL
TESTM DEC =$R3 GET RCV CHAN BACK
*
* SET UP ONE RCV CCB FOR EACH
* XMIT CCB. A RCV CCB IS X'400' HIGHER IN MEMORY THEN CORRESPONDING
* XMIT CCB.
*
LNJ $B4,<MCCB MAKE CCB
DC <RTB RECEIVE BUFFER
RRNG1 DC 0 RANGE
RCON1 DC X'40' VALID
LB <RCON1,=Z'0020' GET LAST BIT
*
BBT >TESTY BRANCH IF SET
* 2ND RECEIVE
*
LNJ $B4,<MCCB MAKE CCB
DC <RTB ADDRESS
RRNG2 DC 0 RANGE
RCON2 DC X'40' VALID
LB <RCON2,=Z'0020' GET LAST BIT
*
BBT >TESTY BRANCH IF SET
* THIRD CCB
*
LNJ $B4,<MCCB
DC <RTB

```

```

003302 OC59 0000 RRRG3 DC 0
003303 OC5A 0040 RCON3 DC X'40' VALID
003304 OC5B 8280 0C5A LB <RCON3,=Z'0020' GET LAST BIT
003305 OC5E 0506 * MAKE BBT >TESTY BRANCH IF SET
003306 4TH CCB
003307 OC5F C380 10FF * RADD4 DC <RTD
003308 OC61 1C00 RRNG4 DC 0
003309 OC62 0000 RCON4 DC X'60' LAST, VALID
003310 OC63 0060 *
003311 *
003312 *
003313 *
003314 OC64 C380 0F02 * FILL RECEIVE WITH DEFAULT
003315 TESTY LNJ $B4,<FDFLT
003316 *
003317 *
003318 *
003319 OC66 9870 01E0 * SHORTEN TIMEOUT FOR ABORT CASES
003320 OC68 A870 0006 LDR $R1,=480
003321 OC6A A900 116C LDR $R2,=6
003322 OC6C 0203 CMR $R2,<RANGE
003323 OC6D 9870 0018 BL >TESTY BRANCH IF RANGE >6
003324 OC6F 9F00 0C8C LDR $R1,=24 200 MS DELAY
003325 STR $R1,<TIMEOUT
003326 *
003327 *
003328 *
003329 OC71 8AD3 * START I/O TO START XMIT CHANNEL PROG. INSURES
003330 OC72 C380 0F0B * START CONDITION IS IN DESIRED STATE
003331 OC74 0F82 TESTU INC =R3
003332 OC75 4000 LNJ $B4,<CHCT DO CHANNEL CONTROL
003333 * B >S+2
003334 OC76 88D3 * DC Z'4000' START I/O
003335 * DEC =R3 GET BACK RCV
003336 *
003337 *
003338 *
003339 OC77 8700 0000 * SYNCHRONIZE WITH RTC AND GIVE ENOUGH TIME FOR INITIAL ILS CHAR'S TO OCCUR
003340 OC79 1C06 X CL <ZHRTCI RTC RESET VALUE
003341 OC7A 9F00 0000 X LDV $R1,=6
003342 OC7C 9F00 0000 X STR $R1,<ZHRTCL LEVEL
003343 OC7E 0004 RTCN $R1,<ZHRTCC RTC
003344 OC7F 1C01 LDV $R1,=1
003345 OC80 9900 0000 X TESTX CMR $R1,<ZHRTCC
003346 OC82 0981 FFFD BNE TESTX BRANCH IF HASN'T CHANGED
003347 OC84 0005 RTCF SHUT OFF CLOCK
003348 *
003349 *
003350 OC85 C380 0F0B * START I/O TO START RCV CHANNEL PROGRAM
003351 OC87 0F82 LNJ $B4,<CHCT DO CHANNEL CONTROL
003352 OC88 4000 B >S+2
003353 * DC Z'4000' START I/O
003354 OC89 C380 0F1D * LNJ $B4,<TEST TEST FOR STATUS COMPLETE
003355 OC8B 0F82 B >S+2
003356 OC8C 01E0 DC 480 TIMEOUT AFTER 4 SEC OR 200 MS
003357 OC8D 8F80 2073 TIMOUT RSTR <SAV,=Z'2052'
003358 OC8F 2052 JMP $B3
003359 OC90 8383 *
003360 *
003361 *
003362 OC91 0000 * MASK TABLE
003363 OC92 0000 MSKTBL DC 0 MASK FOR 8 BITS
003364 OC93 0000 DC 0
003365 OC94 0000 DC 0
003366 OC95 0000 DC X'0'
003367 OC96 00E0 DC X'E0' 5 BIT
003368 OC97 00C0 DC X'C0' MASK FOR 6 BITS
003369 OC98 0080 DC X'80' MASK FOR 7 BITS
003370 OC99 0000 DC 0 EXTRA
003371 *****
003372 *
003373 *
003374 *
003375 *
003376 *
003377 *
003378 *
003379 *
003380 OC9A 8F00 11AE * SUBROUTINES ARE CODED HERE
003381 OC9C 1002 PREV SAVE <TMPSTR,=Z'1002' R3,B4
003382 OC9D 3B00 OCA0 BEVN $R3,<PREV5 BRANCH IF RCV CHANNEL
003383 OC9F 88D3 DEC =R3
003384 OCA0 8754 PREV5 =R4
003385 OCA1 C380 0F0B LNJ $B4,<CHCT DO CHANNEL CONTROL
003386 OCA3 0F82 B >S+2
003387 OCA4 0100 DC Z'0100' CCB LIST RESET
003388 OCA5 9800 11EA LDR $R1,<ERMG+1
003389 OCA7 9970 2A2A CMR $R1,=A'***' SEE IF MLCP REV
003390 OCA9 0904 BE >PREV2 BRANCH IF MLCP REV NO.
003391 OCA8 4F20 STR $R3,=R4
003392 OCA8 0F83 MLV $R4,=X'20' FORM OFFSET FOR CHANNEL
003393 OCA8 0F83 B >PREV1
003394 OCAF C380 0EEC PREV2 LNJ $B4,<GENITZ GENERAL INITIALIZE
003395 OCB1 9380 10BF PREV1 STR $R4,<REVLOC
003396 OCB3 1C00 DC <RTD READ DATA
003397 OCB4 0020 DC X'20' TO HERE
003398 OCB5 0000 DC 0 RANGE
003399 OCB6 0000 DC 0 ADDRESS
003400 * EVEN BYTE BOUNDRY
003401 * CALL ZVST,FREV PRINT FIRMWARE REV
003402 OC87 FBC0 0003 X
003403 OCB9 D380 0000
003404 OCB8 0F80
003405 OCB8 0D2D
003406 OCB8 BF00 1178 STR $R3,<TEMP
003407 OCBF 3043 SOR $R3,3 GET LINE NUMBER/4
003408 OCB8 BB80 1800 LAB $B3,<SDB
003409 OCB2 2CFF LDV $R2,=-1
003406 OCB3 9800 11EA LDR $R1,<ERMG+1 GET TEST LABEL
003407 OCB5 9970 2A2A CMR $R1,=A'***'

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003408 OCC7 091D          BE      >PREV3
003409 OCC8 2CFE          LDV     $R2,=-2
003410          CALL    ZV$T,XPRT          BRANCH IF MLCF REV NUMBER
                                DISPLAY RCV =

                                OCC9 FBC0 0003
                                OCCB D380 0000
                                OCCD 0F80
                                OCCE 11F3
                                X
003411 OCCF BB80 119E          LAB     $B3,<RHCFW1
003412 OCD1 9800 1C0F          LDR     $R1,<RTB+15
003413 OCD3 9F00 1C00          STR     $R1,<RTB
003414 OCD5 0F8F          B       >PREV3
003415          CALL    ZV$T,XPRT          DISPLAY XMIT =
                                PREV4

                                OCD6 FBC0 0003
                                OCD8 D380 0000
                                OCDA 0F80
                                OCDB 11EC
                                X
003416 OCDC 9800 1C0F          LDR     $R1,<RTB+15
003417 UCDE 1068          SAR     $R1,8
003418 UCDF 8251          NEG     = $R1
003419 UCE0 BB80 119C          LAB     $B3,<XHCFW1
003420 UCE2 9F00 1C00          STR     $R1,<RTB
                                *
                                * PREV3
003421          LDR     $R1,<RTB
003422 OCE4 9800 1C00          AND     $R1,=X'FF'
003423 OCE6 9570 00FF          STR     $R1,$B3.$R3
003424 OCE8 9F33          AND     $R1,=Z'00E0'
003425 OCE9 9570 00E0          AND     $R1,5
003426 UCED 1045          SUR     $R1,5
003427 UCFC 9F00 1177          STR     $R1,<TPR
003428 UCCE 1900 0CFC          BEZ    $R1,<PREV6
003429          CALL    ZV$T,D,TPR          IF NO UPPER BIT BRANCH
                                TYPE

                                OCF0 FBC0 0003
                                OCF2 D380 0000
                                OCF4 0F80
                                OCF5 1177
                                X
003430          CALL    ZV$T,DASH          TYPE DASH

                                OCF6 FBC0 0003
                                OCF8 D380 0000
                                OCFA 0F80
                                OCFB 0D3B
                                X
                                *
                                * PREV6
003431          LDR     $R1,<RTB
003432 UCFC 9800 1C00          AND     $R1,=Z'001F'
003433 UCFE 9570 001F          STR     $R1,<TPR
003434 UD00 9F00 1177          CALL    ZV$T,D,TPR          GET REV NUMB
                                STRIP TO LOW > BITS
                                PUT BACK
                                PRINT NUMBER

                                UD02 FBC0 0003
                                UD04 D380 0000
                                UD06 0F80
                                UD07 1177
                                X
003436 UD08 2780 0CD6          SIT13  BINC  $R2,<PREV4          GO BACK AND DO FOR XMIT
003437          *
003438          * IF PRINTING FIRMWARE REV, ALSO PRINT DATA SET STATUS
003439          *
                                LDR     $R3,<TEMP          GET BACK CHANNEL
                                LDR     $R1,<ERMG+1          GET TEST LABEL
                                CMR     $R1,=A'***'
                                BE      >SIT14
                                LDR     $R1,<RTB+7          BRANCH IF MLCF REV NO.
                                SUR     $R1,8          GET STATUS
                                STR     $R1,<TPR          GET STATUS
                                CALL    ZV$T,C,STPT          PRINT DATA SET STATUS

                                UD16 FBC0 0003
                                UD18 D380 0000
                                UD1A 0F80
                                UD1B 0D32
                                X
003448          CALL    ZV$THZ,TPR          PRINT VALUE

                                UD1C FBC0 0003
                                UD1E D380 0000
                                UD20 0F80
                                UD21 1177
                                X
003449 UD22 0F87          B       >SIT12
003450 UD23 9800 1C00          LDR     $R1,<RTB
003451 UD25 9570 00FF          AND     $R1,=Z'00FF'
003452 UD27 9F00 119A          STR     $R1,<MLC=FR          GET REV
                                STRIP
                                STORE MLC FIRMWARE REV NUMBER

                                *
                                * SIT12
003453          RSTR  <TMPSTR,=Z'1002'
003454          *
003455 UD29 8F80 11AE          JMP     $B6          RETURN
003456 UD2B 1002
003457 UD2C 8386
003458          *
003459          FREV  TEXT  ' FW REV$'
                                STPT  TEXT  'DATA SET STATUS =$'

                                UD30 2020 4657 2052
                                UD32 4441 5441 2053
                                UD35 4554 2053 5441
                                UD3B 5455 5320 3D24
                                DASH  TEXT  ' - $'
                                *
                                *
                                *
                                * FILL ASCENDING DATA LNJ $B3,<FACDTA
                                *
                                FACDTA SAVE <SAV2,=Z'6040'          R1,R2,B1

                                UD3D 8F00 202D
                                UD3F 6040
                                UD40 8751
                                UD41 9B80 1800
                                UD43 8752
                                UD44 A7DD
                                UD45 8AD2
                                UD46 9970 0400
                                UD48 09FC
                                UD49 8F80 202D
                                UD4B 6040
                                UD4C 8383
                                *
                                *
                                *
                                * GENERATE CRC FOR SEND BUFFER
                                *
                                GCRCL  LDV     $R1,=8
                                LDV     $R7,=-1
                                CL     = $R3
                                CHLUP  STR     $R1,=$R2          CHAR SIZE IS 8 FOR CRC TEST
                                LLH     $R5,<SDB.$R3          RANGE COUNTER
                                INC     = $R3          GET BYIE

                                UD50 9F50
                                UD51 D2B0 1800
                                UD53 8AD3

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003487
003488
003489
003490 OD54 3D1F
003491 OD55 098C
003492 OD56 9800 089E
003493 OD58 9570 000E
003494 OD5A 1041
003495 OD5B A810 04AD
003496 OD5D 2984
003497 OD5E 2C08
003498 OD5F 0F00 0D50
003499
003500 OD61 3D20
003501 OD62 0280 0D6F
003502
003503 OD64 2700 0D67
003504 OD66 0FEA
003505 OD67 E855
003506 OD68 E657
003507
003508 OD69 5041
003509 OD6A 7041
003510 OD6B 6B79
003511 OD6C F670 8408
003512 OD6E 0FF6
003513
003514 OD6F 8657
003515 OD70 1C01
003516 OD71 B857
003517 OD72 B790 180F
003518 OD74 3048
003519 OD75 B780 1810
003520 OD77 B870 00E2
003521 OD79 B790 1810
003522 OD7B B870 00F0
003523 OD7D B780 1811
003524 OD7F 8384
003525
003526
003527
003528
003529
003530
003531
003532
003533
003534
003535
003536
003537
003538
003539
003540
003541
003542
003543
003544 OD80 8F00 2073
OD82 FFFF
003545 OD83 AB80 1800
003546 OD85 DC80 00CF
003547
003548 OD87 8700 116A
003549 OD89 1C01
003550 OD8A 82C5 0001
OD8C 0040
003551 OD8D 0592
003552 OD8E C290 1174
003553 OD90 1C00
003554 OD91 4044
003555 OD92 88D4
003556 OD93 A092
003557 OD94 9954
003558 OD95 0907
003559
003560 OD96 8852
OD97 0001
003561 OD98 8B52
OD99 0080
003562 OD9A A7DE
003563 OD9B 0FF8
003564
003565 OD9C 8952
OD9D 0001
OD9E A7DE
003566
003567
003568
003569
003570 OD9F A092
ODA0 8852
ODA1 0001
003571 ODA2 A792
003572 ODA3 8280 1174
ODA5 8000
003573 ODA6 0589
003574 ODA7 A470 0001
003575 ODA9 8B52
ODAA 0080
003576 ODAB A792
003577 ODAC 1C08
003578
003579 ODAD 9F00 116A
003580
003581 ODAF 8AD1
003582 ODB0 82C5 0001
ODB2 0080
003583 ODB3 0585
003584
003585 ODB4 A870 00FF
003586 ODB6 A792
003587 ODB7 8AD1
003588
003589
003590 ODB8 82C5 0001

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```

* LOAD BYTE SIZE FOR LAST BYTE
*
CMV $R3,=X'1F'
BNE >GRC1 BRANCH IF NOT LAST BYTE
LDR $R1,<PCRC4 STRIP TO LAST BYTE INFO
AND $R1,=X'E'
SOR $R1,1
LDR $R1,<TCBBSZ,$R1 CONVERT BYTE CODE TO HEX
LNEZ $R2,>GRC1
LDV $R2,=8 SET FOR 8 BITS
NOP <CHLUP
*
GRC1 CMV $R3,=X'20'
BGE <RCEND
*
BITLUP BDEC $R2,<GRC2
>CHLUP DO NEXT CHAR
GRC2 LDR $R6,=$R5
XOR $R6,=$R7 R7 HAS RESIDUE
*
SOR $R5,1 SHIFT CHAR
SOR $R7,1 SHIFT RESIDUE
BEVN $R6,>BITLUP
XOR $R7,=Z'8408' OR WITH CCITT POLYNOMIAL
B >BITLUP
*
RCEND CPL =$R7 COMPLEMENT CRC
LDV $R1,=1
LDR $R3,=$R7
STH $R3,<SDB+X'F',$R1 STORE IN BUFFER
SOR $R3,8 GET LEFT HALF OF CRC
STH $R3,<SDB+X'10' STORE
LDR $R3,=Z'00E2'
STH $R3,<SDB+X'10',$R1
LDR $R3,=X'00F0'
STH $R3,<SDB+X'11'
JMP $B4
*
DATA HEADER ROUTINE
*
THIS SUBROUTINE FORMATS THE HEADER ON EACH FRAME BASED
ON THE FOLLOWING INPUTS:
*
TCB, AFX, CFX, + LCF FOR XMIT LR7
FLAG HEAD WHICH IS INTERPRETED AS FOLLOWS
*
BIT 0 = 1 MEANS SUPERVISORY CONTROL BIT SET
BITS 5-7 ARE THE TCB VALUE
BITS 8 - 11 ARE THE ADDRESS FIELD LENGTH
BITS 12-15 ARE THE LCF LENGTH
*
THE SUBROUTINE ALSO STRIPS XMIT DATA TO THE
BYTE SIZE SPECIFIED IN THE FOLLOWING PRIORITY:
SUPERVISORY BIT>TCB>LR7 BITS 5,6,7
*
GHEAD SAVE <SAV,Z'FFFF' SAVE ALL
*
LAB $B2,<SDB
LDB $B5,<PARPTR GET POINTER TO PARAMETER LIST
* ADDRESS FIELD
CL <CHSZ CLEAR CHAR SIZE
LDV $R1,=1
$B5.1,=X'40' GET AFX BIT
*
BBF >CFP IF NO AFX, GO TO CONTROL FIELD
LLH $R4,<HEAD,$R1 GET HEAD INFO
LDV $R1,=0
SOR $R4,4 GET ADDRESS FIELD LENGTH
DEC $R4
ADPR LDH $R2,$B2,$R1 PICK UP BYTE
CMR $R1,=$R4
BE >EADF
*
LBF = $R2,=1 SET CONTINUATION BIT = 0
LBC = $R2,=Z'0080' COMPLEMENT MSB TO MARK 8 BIT DATA
STH $R2,$B2,+$R1
B >ADPR
*
EADF LBT = $R2,=1 SET CONTINUATION BIT FOR LAST
STH $R2,$B2,+$R1
* CONTROL FIELD
CFP LDH $R2,$B2,$R1
LBF = $R2,Z'0001'
*
STH $R2,$B2,$R1 SET ITFF IND = 0
LB <HEAD,=Z'8000' GET CFX BIT
*
BBF >CFP1
OR $R2,=1 SET ITFF IND = 1
LBC = $R2,=Z'0080' SET MSB TO MARK 8 BIT DATA
*
STH $R2,$B2,$R1
LDV $R1,=8 BIT SET TO INDICATE AT 'DCHSZ' THAT
MASK IS 8 BIT
*
STR $R1,<CHSZ
*
CFP1 INC = $R1 BUMP TO NEXT BYTE
LB $B5.1,=X'80' GET CFX
*
BBF >TCBP
*
LDR $R2,=X'FF'
STH $R2,$B2,$R1 SET SECOND CONTROL WORD TO ALL ONES
INC $R1 BUMP FOR CONTROL FIELD EXTENSION
*
TCB BYTE PROCESSING
TCBP LB $B5.1,=X'20' GET TCB BIT INFORMATION

```

```

003591 00BA 0020
003592 00BB 0580 ODCC
003593 00BD A280 1174
003594 00BF A570 0007
003595 00C1 2D03
003596 00C2 0907
003597 00C3 8980 116A
003598 00C5 0980 ODCC
003599 00C7 AF00 116A
003600
003601 00C9 8952
003602 00CA 0080
003603 00CB A792
003604
003605 00CC 82C5 0001
003606 00CE 0001
003607 00CF 0580 ODE1
003608 00D1 C800 1174
003609 00D3 C570 000F
003610
003611 00D6 D292
003612 00D7 A954
003613 00D8 0906
003614 00D9 8855
003615 00DA 0080
003616 00DB D7DE
003617 00DC 8A22
003618 00DD 0FF9
003619 00DE 8955
003620 00DF 0080
003621 00E0 D7DE
003622
003623
003624 00E1 C800 116A
003625 00E3 4988
003626 00E4 A845 0001
003627 00E6 A570 000E
003628 00E8 2041
003629 00E9 C820 04AD
003630 00EB C570 0007
003631 00ED CF00 116A
003632
003633 00EF A800 116A
003634 00F1 D820 0E02
003635 00F3 DF00 1176
003636 00F5 A851
003637
003638 00F6 8754
003639 00F7 COA2
003640 00F8 C5F0 1176
003641 00FA C7EE
003642 00FB A970 0800
003643 00FD 09FA
003644 00FE 8F80 2073
003645 00E0 FFFF
003646 00E1 8384
003647
003648
003649 00E2 00FF
003650 00E3 0001
003651 00E4 0003
003652 00E5 0007
003653 00E6 000F
003654 00E7 001F
003655 00E8 003F
003656 00E9 007F
003657
003658
003659
003660
003661
003662 00EA 8F00 2073
003663 00EB FFFF
003664 00ED 2C05
003665 00EE F800 089C
003666 00EF F570 0007
003667 00E12 8700 1178
003668 00E14 9857
003669 00E15 4C01
003670 00E16 4000
003671 00E17 1C01
003672 00E18 8AD2
003673 00E19 D2A0 1800
003674 00E1B 2D20
003675 00E1C 0205
003676 00E1D 8F80 2073
003677 00E1F FFFF
003678 00E20 8384
003679 00E21 82D5
003680 00E22 0000
003681 00E23 9E54
003682 00E24 8A00 1178
003683 00E26 0000
003684 00E27 9E54
003685 00E28 8A00 1178
003686 00E2A 0000
003687 00E2B 1001
003688 00E2C 4041
003689 00E2D 9954
003690 00E2E 0380 0E21
003691 00E30 0F80 0E12
003692

```

```

* BBF <LCFP B = NO TCB
* LLH $R2,<HEAD GET TCB VALUE
AND $R2,=7 STRIP
CMV $R2,=3 CHECK FOR BIT INVERSION
BE >TCBP1 IF SO DON'T STORE CHAR SIZE
CMZ <CHSZ B = CHAR SIZE ALREADY SET
BNE <TCBP1
STR $R2,<CHSZ
* TCBP1 LBT = $R2,=X'80' SET LAST BIT
* STH $R2,$B2,$R1 PUT IN TCB
* * LCF PROCESSING
LCFP LB $B5.1,=X'01'
* BBF <DCHSZ B = NO LCF
LDR $R4,<HEAD
AND $R4,=X'0F'
LDV $R2,=1
* LCFP1 LLH $R5,$B2,$R1 GET BYTE
CMR $R2,=$R4 CHECK IF LAST
BE >ELCF
LBF = $R5,=X'80'
* STH $R5,$B2,+$R1
INC = $R2
B >LCFP1
* ELCF LBT = $R5,=X'80'
* STH $R5,$B2,+$R1
* * SET BYTE SIZE
* DCHSZ LDR $R4,<CHSZ GET CHAR SIZE
BNEZ $R4,>STDTA
LDR $R2,$B5.1 GET XMIT LR7
AND $R2,=X'E' STRIP TO BYTE SIZE
SOR $R2,1
LDR $R4,<TCBBSZ,$R2 PICK UP ACTUAL SIZE
AND $R4,=7
STR $R4,<CHSZ
* STRIP DATA TO SIZE
LDR $R2,<CHSZ
LDR $R5,<DMASK,$R2 GET MASK
STR $R5,<MASK
LDR $R2,=$R1 GET BYTE INDEX
*
* GDAT8 CL = $R4
LDH $R4,$B2,$R2 GET DATA
AND $R4,<MASK STRIP TO SIZE
STH $R4,$B2,+$R2 STORE BACK
CMR $R2,=X'800'
BNE >GDAT8 BRANCH IF MORE TO GO
RSTR <SAV,Z'FFFF'
JMP $B4
* * MASK TABLE
* DMASK DC X'FF' 8 BITS
DC 1 1 BIT
DC 3 2 BITS
DC 7 3 BITS
DC X'F' 4 BITS
DC X'1F' 5 BITS
DC X'3F' 6 BITS
DC X'7F' 7 BITS
*
* INVERT DATA ON A BYTE BY BYTE BASIS - ASSUMES LCF LENGTH =4
* RANGE OF X'1F'
* IVRT SAVE <SAV,=Z'FFFF'
LDV $R2,=5 BYTE INDEX
LDR $R7,<PCRC2 GET RCY CONFIG
AND $R7,=7 STRIP TO BYE SIZE
* BYTTZ CL <TEMP
LDR $R1,=$R7 NEW BYTE SIZE
LDV $R4,=1
SOL $R4,0 PUT 1 IN MSB OF BYTE
LDV $R1,=1
INC = $R2
LLH $R5,<SDB,$R2 GET BYTE
CMV $R2,=X'20'
BL >VERT
RSTR <SAV,=Z'FFFF'
JMP $B4 ALL DONE
* VERT LB = $R5,0 GET BIT LOW ORDER
SWR $R1,=$R4 GET MASK FOR HIGH ORDER
LBS <TEMP,0 PUT IN INVERTED POSITION
SWR $R1,=$R4
LBS <TEMP,0 SET BIT, LOWER ORDER
SOL $R1,1
SOR $R4,1 SET FOR NEXT BIT POSITION
CMR $R1,=$R4
BLE <VERT
B <BYTTZ DO NEXT BYTE
*
* PASS TEST PARAMETERS + FORMAT HEADER
*

```

003693	OE32	AF80	OECE	PASS	STB	\$B2,<PARPTR	STORE POINTER TO PARAMETERS
003694	OE34	C380	2000		LNJ	\$B4,<PLB	PRINT LABEL
003695				*			
003696	OE36	9872			LDR	\$R1,+\$B2	PICK UP RECEIVE CONFIGURATION
003697	OE37	1008			SOL	\$R1,8	
003698	OE38	9470	0017		OR	\$R1,=X'17'	
003699	OE3A	9F00	0BF2		STR	\$R1,<LR6RCV	
003700	OE3C	9872			LDR	\$R1,+\$B2	PICK UP XMIT CONFIGURATION
003701	OE3D	9F52			STR	\$R1,=\$R2	
003702	OE3E	A570	000E		AND	\$R2,=X'E'	XMIT CHAR SIZE
003703	OE40	1008			SOL	\$R1,8	
003704	OE41	9470	0037		OR	\$R1,=X'37'	
003705	OE43	9F00	0BF3		STR	\$R1,<LR7XMT	
003706	OE45	9802			LDR	\$R1,\$B2	GET LAST BYTE INFORMATION
003707	OE46	9570	000F		AND	\$R1,=X'F'	
003708	OE48	9780	0BF0	PASSD	STH	\$R1,<RCVRES	
003709	OE4A	1008			SOL	\$R1,8	
003710	OE4B	9470	0003		OR	\$R1,=3	
003711	OE4D	9F00	0BEF		STR	\$R1,<PARBYT	PARTIAL BYTE INFORMATION
003712	OE4F	9872		PASSA	LDR	\$R1,+\$B2	GET FILL MODE
003713	OE50	1008			SOL	\$R1,8	
003714	OE51	9470	003F		OR	\$R1,=X'3F'	
003715	OE53	9570	EOFF		AND	\$R1,=Z'EOFF'	STRIP TO PERTINENT BITS
003716	OE55	9F00	0BFA		STR	\$R1,<IFM	
003717	OE57	9872			LDR	\$R1,+\$B2	PICK UP LR2 CONTROL
003718	OE58	1008			SOL	\$R1,8	
003719	OE59	9470	0014		OR	\$R1,=X'14'	
003720	OE5B	9F00	0BF1		STR	\$R1,<LR2CFG	
003721				*			
003722	OE5D	9872			LDR	\$R1,+\$B2	GET ACTION FLAG
003723	OE5E	9780	0BF7		STH	\$R1,<ACTFLG	
003724	OE60	9082			LDH	\$R1,\$B2	RCV FRAME
003725	OE61	9570	003F		AND	\$R1,=X'3F'	
003726	OE63	9480	0BFA		ORH	\$R1,<IFM	
003727	OE65	9780	0BFA		STH	\$R1,<IFM	
003728	OE67	9872			LDR	\$R1,+\$B2	RCV ACTION CHAR COUNT
003729	OE68	9780	0BF8		STH	\$R1,<RAC	
003730	OE6A	9082			LDH	\$R1,\$B2	XMIT ACTION RCV FRAME
003731	OE6B	9780	0BF6		STH	\$R1,<UFMCNT	
003732	OE6D	9872			LDR	\$R1,+\$B2	
003733	OE6E	9780	0BF5		STH	\$R1,<TACC	XMIT ACTION CHAR COUNT
003734	OE70	9CF2			LDB	\$B1,+\$B2	PICK UP ADDRESS
003735	OE71	9F80	0C1A		STB	\$B1,<XADD1	
003736	OE73	CB01	0400		LAB	\$B4,\$B1,X'400'	
003737	OE75	CF80	0C46		STB	\$B4,<RADD1	
003738	OE77	9872			LDR	\$R1,+\$B2	PICK UP RANGE
003739	OE78	9F00	0C1B		STR	\$R1,<XRNG1	
003740	OE7A	A280	0BF7		LLH	\$R2,<ACTFLG	GET FLAG
003741	OE7C	82D2			LB	=\$R2,=Z'0008'	
003742	OE7D	0008					
003743	OE7E	0582			BBF	>PASSE	MAKE ROOM FOR CRC + RESIDUES
003744	OE7F	1E04			ADV	\$R1,=4	
003745	OE80	9F00	0C47	PASSE	STR	\$R1,<RRNG1	
003746				*			
003747	OE82	8780	0BE6		CLH	<PRCV	RECEIVE P COUNT
003748	OE84	8700	0C29		CL	<XRNG2	
003749	OE86	8700	0C28		CL	<XADD2	
003750	OE88	8700	0C2A		CL	<XCUN2	
003751	OE8A	8700	0C35		CL	<XCUN3	
003752	OE8C	8752			CL	=\$R2	
003753	OE8D	9872			LDR	\$R1,+\$B2	PICK UP CCB CONTROL 1
003754	OE8E	9F00	0C1C		STR	\$R1,<XCON1	
003755	OE90	9F00	0C48		STR	\$R1,<RCUN1	
003756	OE92	82D1			LB	=\$R1,=Z'0020'	CHECK LAST BLOCK BIT
003757	OE93	0020					
003758	OE94	0583			BBF	>CCBA	BRANCH IF NOT LAST CCB
003759	OE95	0F80	0ECE		B	<PASEND	DONE
003760				*			
003761	OE97	9CF2		CCBA	LDB	\$B1,+\$B2	PICK UP ADDRESS
003762	OE98	9F80	0C28		STB	\$B1,<XADD2	
003763	OE9A	CB01	0400		LAB	\$B4,\$B1,X'400'	
003764	OE9C	CF80	0C4F		STB	\$B4,<RADD2	
003765	OE9E	9872			LDR	\$R1,+\$B2	RANGE
003766	OE9F	9F00	0C29		STR	\$R1,<XRNG2	
003767	OEA1	9F00	0C50		STR	\$R1,<RRNG2	
003768	OEA3	A872			LDR	\$R2,+\$B2	CONTROL WORD
003769	OEA4	AF00	0C2A		STR	\$R2,<XCON2	
003770	OEA6	AF00	0C51		STR	\$R2,<RCON2	
003771	OEA8	8754			CL	=\$R4	
003772	OEAA	82D2			LB	=\$R2,=Z'0020'	
003773	OEAB	0020					
003774	OEAC	0523			BBT	>PASEND	BRANCH IF LAST BLOCK
003775	OEAD	9CF2			LDB	\$B1,+\$B2	
003776	OEAF	9F80	0C33		STB	\$B1,<XADD3	GET RECEIVE ADDRESS
003777	OEAE	CB01	0400		LAB	\$B4,\$B1,X'400'	
003778	OEAE	CF80	0C58		STB	\$B4,<RADD3	
003779	OEAE	9872			LDR	\$R1,+\$B2	RANGE
003780	OEAE	9F00	0C34		STR	\$R1,<XRNG3	
003781	OEAE	9F00	0C59		STR	\$R1,<RRNG3	
003782	OEAE	9872			LDR	\$R1,+\$B2	
003783	OEAE	9F00	0C35		STR	\$R1,<XCON3	
003784	OEAE	9F00	0C5A		STR	\$R1,<RCON3	
003785	OEAE	82D1			LB	=\$R1,=Z'0020'	
003786	OEAE	0020					
003787	OEAE	0500	0ECE		BBT	<PASEND	BRANCH IF LAST CCB
003788	OEAE	9CF2			LDB	\$B1,+\$B2	
003789	OEAE	9F80	0C3E		STB	\$B1,<XADD4	
003790	OEAE	CB01	0400		LAB	\$B4,\$B1,X'400'	
003791	OEAE	CF80	0C61		STB	\$B4,<RADD4	
003792	OEAE	9872			LDR	\$R1,+\$B2	
003793	OEAE	9F00	0C3F		STR	\$R1,<XRNG4	
003794	OEAE	9F00	0C62		STR	\$R1,<RRNG4	
003795	OEAE	9872			LDR	\$R1,+\$B2	DUMMY IO BUMP RETURN
003796	OEAE	8383			JMP	\$B3	
003797				*			
003798	UECF	0000					CURRENT PARAMETER POINTER
003799				*			
003800				*			FIND ACTIVE HULC CHANNEL. START SEARCHING WITH
003801				*			CHANNEL NUMBER CONTAINED IN R3.
				*			
				*	LNJ	\$B4,FLN	

003899	UF39	C380	0EE8	LNJ	\$B4,<CGSCH		
003900	UF3B	8055		IO	=\$R5,=\$R4	INPUT STATUS	
	UF3C	0054					
003901	UF3D	0774		BIOT	>TESTZ	BRANCH MEANS TRY AGAIN	
003902	UF3E	E380	0FBA	LNJ	\$B6,<ERRMB	INPUT STATUS WAS NAK'ED	
003903	UF40	0000		HLT			
003904				*			
003905				*			
003906				*			
003907				*			
003908	UF41	8F00	202D	INRNG	SAVE <SAV2,=Z'4909'		
	UF43	4909					
003909	UF44	9870	9999	TESTZ1	LDR \$R1,=Z'9999'	DEFAULT	
003910	UF46	9F00	1183	STR	\$R1,<RRNG	RESIDUAL RANGE	
003911	UF48	C800	11A3	LDR	\$R4,<CONT2	FC	
003912	UF4A	C380	0EE8	LNJ	\$B4,<CGSCH	OR IN CHANNEL	
003913	UF4C	8000	1183	IO	<RRNG,=\$R4	INPUT RANGE	
	UF4E	0754					
003914	UF4F	0700	0F53	BIOT	<TESTZ2		
003915	UF51	E380	0FBA	LNJ	\$B6,<ERRMB	COMMAND WAS NAK'ED	
003916	UF53	9800	1167	LDN	\$R1,<ELP5	GET TIMEOUT VALUE	
003917	UF55	0005		RTCF		TURN OFF RTC	
003918	UF56	9200	0000	SUB	\$R1,<ZHRGCC	SUBTRACT PRESENT	
003919	UF58	C380	0F5E	LNJ	\$B4,<UPTM	UPDATE TIME TOTAL	
003920				*			
003921	UF5A	8F80	202D	RSTR	<SAV2,=Z'4909'		
	UF5C	4909					
	UF5D	8384		JMP	\$B4	EXIT	
003922				*			
003923				*			
003924				*			
003925				*			
003926	UF5E	9A00	1198	UPTM	ADD \$R1,<TTOT		
003927	UF60	9F00	1198	STR	\$R1,<TTOT		
003928	UF62	1041		SOR	\$R1,1		
003929	UF63	9900	1191	CMR	\$R1,<HRTZ	COMPARE WITH TICKS FOR 1 SEC	
003930	UF65	0205		BL	>UPTM1	CLEAR TOTAL	
003931	UF66	8700	1198	CL	<TTOT		
003932	UF68	8A80	1199	INC	<SEC		
003933	UF6A	8384		UPTM1	JMP \$B4		
003934				*			
003935				*			
003936				*			
003937				*			
003938				*			
003939	UF6B	8F00	204D	RPVLU	SAVE <SAV4,=Z'5858'	R1,3,4 B1,3,4	
	UF6D	5858					
003940	UF6E	1C3A		LDV	\$R1,=X'3A'		
003941	UF6F	9780	1055	STH	\$R1,<DMPLOC	SET TO READ ERROR CODE	
003942	UF71	B380	10EB	LNJ	\$B3,<SETLCT	SEND OUT BYTE DIRECTOR	
003943	UF73	1055		DC	<DMPLOC		
003944	UF74	B380	108D	LNJ	\$B3,<INBYTE	INPUT BYTE	
003945	UF76	DF00	1162	STR	\$R5,<ERRCD	STORE	
003946				*			
003947	UF78	8756		CL	=\$R6		
003948	UF79	0956		CMR	\$R5,=\$R6		
003949	UF7A	0903		BE	>LPTS4		
003950	UF7B	E3C0	0032	LNJ	\$B6,<ERRDB	FATAL ERROR DURING CCP	
003951				*			
003952	UF70	9870	0006	LPTS4	LDR \$R1,=6		
003953	UF7F	3B03		BEVN	\$R3,>PVLU2	BRANCH IF READ	
003954	UF80	88D3		DEC	=\$R3	GET READ CHANNEL	
003955	UF81	1C26		LDV	\$R1,=38		
003956	UF82	9780	1055	STH	\$R1,<DMPLOC		
003957	UF84	B380	10EB	LNJ	\$B3,<SETLCT	SEND BYTE DIRECTOR	
003958	UF86	1055		DC	<DMPLOC		
003959	UF87	B380	108D	LNJ	\$B3,<INBYTE	INPUT BYTE	
003960	UF89	DF00	1163	STR	\$R5,<PVLU	LSB OF P	
003961	UF8B	8AD1		INC	=\$R1		
003962	UF8C	9780	1055	STH	\$R1,<DMPLOC		
003963	UF8E	B380	10EB	LNJ	\$B3,<SETLCT	SEND BYTE DIRECTOR	
003964	UF90	1055		DC	<DMPLOC		
003965				*			
003966	UF91	B380	108D	LNJ	\$B3,<INBYTE	INPUT BYTE	
003967	UF93	5048		SOR	\$R5,8		
003968	UF94	D400	1163	OR	\$R5,<PVLU	FORM LSB + MSB	
003969	UF96	DF00	1163	STR	\$R5,<PVLU		
003970				*			
003971	UF98	D570	0FFF	AND	\$R5,=Z'0FFF'	STRIP TO 12 BITS	
003972	UF9A	8F80	204D	RSTR	<SAV4,=Z'5858'		
	UF9C	5858					
	UF9D	8384		JMP	\$B4		
003973				*			
003974				*			
003975				*			
003976				*			
003977				*			
003978	UF9E	8F40	109E	DSSTA	DATA SET STATUS		
	UFA0	0008		SAVE	SAV3,=Z'0008'	SAVE B4	
003979	UFA1	C800	11AD	LDR	\$R4,<CONT12	GET FUNCTION CODE	
003980	UFA3	C380	0EE8	LNJ	\$B4,<CGSCH	OR IN CHANNEL NUMBER	
003981	UFA5	8055		IO	=\$R5,=\$R4	INPUT DATA SET STATUS	
	UFA6	0054					
003982	UFA7	0703		BIOT	>\$+2+\$AF		
003983	UFA8	E380	0FBA	LNJ	\$B6,<ERRMB	COMMAND WAS NAK'ED	
003984	UFAA	8FC0	1092	RSTR	SAV3,=Z'0008'		
	UFAC	0008					
003985	UFAD	8384		JMP	\$B4	DONE	
003986				*			
003987				*			
003988				*			
003989	UFAL	8F00	2053	ERRDB	SAVE <SAV5,=Z'FFFF'		
	UFBD	FFFF					
003990	UFBI	9870	4442	LDR	\$R1,=A'DB'		
003991	UFB3	9F00	11E9	STR	\$R1,<ERMG	SET UP ERR MESSAGE FOR DAUGHT. BOATER	
003992	UFB5	9853		LDR	\$R1,=\$R3	GET CHANNEL NUMBER	
003993	UFB6	1041		SOR	\$R1,1	CONVERT TO LINE	
003994	UFB7	9F00	1165	STR	\$R1,<LNBM		
003995	UFB9	0F8D		B	>ERROR		
003996				*			
003997	UFBA	8F00	2053	ERRMB	SAVE <SAV5,=Z'FFFF'		
	UFBC	FFFF					
003998	UFBD	9870	4D42	LDR	\$R1,=A'MB'		
003999	UFBF	9F00	11E9	STR	\$R1,<ERMG	SET ERROR MESSAGE FOR MOTHER	
004000	UFCl	1CFF		LDV	\$R1,=-1		

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004001 OFC2 9F00 1165
004002 OFC4 0F80 OFC6
004003
004004
004005
004006
004007
004008 OFC6 BF00 1166
004009 OFC8 1CF0
004010 OFC9 C866
004011 OFCA C866
004012 OFCB EF80 OFD2
004013
004014 OFCD FBC0 0003
004015 OFCF D380 0000
004016 OFD1 0F80
004017 OFD2 0FCD
004018 OFD3 11E9
004019 OFD4 A800 1165
004020 OFD6 280D
004021 OFD7 FBC0 0003
004022 OFD9 D380 0000
004023 OFDB 0F80
004024 OFDC 105C
004025 OFDD FBC0 0003
004026 OFDF D380 0000
004027 OFE1 0F80
004028 OFE2 1165
004029
004030 OFE3 8751
004031 OFE4 A810 101E
004032 OFE6 2900 OFF4
004033 OFEB AF00 1055
004034 OFEA B380 10EB
004035 OFEC 1055
004036
004037 OFED B380 108D
004038 OFEF 5048
004039 OFF0 DF10 103A
004040 OFF2 8AD1
004041 OFF3 OFF1
004042
004043 OFF4 9B80 103A
004044 OFF6 9800 1180
004045 OFF8 9F00 1177
004046 OFFA 1CF8
004047
004048 OFFB C871
004049 OFFC 4008
004050 OFFD D871
004051 OFFE C455
004052
004053 OFFF CF00 1178
004054
004055 1001 FBC0 0003
004056 1003 D380 0000
004057 1005 0F80
004058 1006 1178
004059 1007 1177
004060 1008 A800 1181
004061 100A AF00 1177
004062 100C 8AD1
004063 100D 89D1
004064 100E 0801 FFEC
004065 1010 0300 1019
004066 1012 A800 1180
004067 1014 AF00 1177
004068 1016 9B80 205B
004069 1018 5CF7
004070
004071 1019 C871
004072 101A 5780 OFFF
004073 101C 0F80 1057
004074
004075 101E 0637
004076 101F 0737
004077 1020 2637
004078 1021 2737
004079 1022 0E37
004080 1023 2E37
004081 1024 3A37
004082 1025 1437
004083 1026 1A37
004084 1027 3E37
004085 1028 1837
004086 1029 3837
004087 102A 1B37
004088 102B 3B37
004089 102C 1737
004090 102D 0337
004091 102E 3F37
004092 102F 1D37
004093 1030 1937
004094 1031 3C37
004095 1032 1E37
004096 1033 1F37
004097 1034 0437
004098 1035 3D37
004099 1036 0000
004100
004101
004102
004103
004104 103A 0000
004105 103B 0000
    
```

```

STR $R1,<LNBM SET LINE NUMBER TO NULL
B <ERROR GO REPORT ERROR
*
* EERROR PRINT ROUTINE
*
*EELNJ $B7,<ERROR
*
ERROR STR $R3,<CHAN INITIALIZE LOOP COUNTER
LDV $R1,=-16 DECREMENT $B6
LDR $R4,=$B6 DO AGAIN
LDR $R4,=$B6 STORE ERROR ADDRESS FOR ERROR CALL
STB $B6,<ERR1+4*$AF
ERR1 CALL ZV$ER,$,ERMG
X
004014 OFD4 A800 1165 LDR $R2,<LNBM GET LINE NUMBER
004015 OFD6 280D BLZ $R2,>ERR6
004016 OFD7 FBC0 0003 CALL ZV$1,LINEP PRINT "LINE"
004017 OFD9 D380 0000
004018 OFDB 0F80
004019 OFDC 105C CALL ZV$TD,LNBM PRINT THE LINE NUMBER
004020 OFDD FBC0 0003
004021 OFDF D380 0000
004022 OFE1 0F80
004023 OFE2 1165
*
* ERROR PARAMETER ROUTINE
*
ERR6 CL =$R1 INDEX
ERRP1 LDR $R2,<LOCTAB,$R1 GET BYTE ADDRESS
BEZ $R2,<PPAR BRANCH IF DONE
STR $R2,<DMPLOC OUTPUT BYTE ADDRESS
LNJ $B3,<SETLCT
DC <DMPLOC
*
LNJ $B3,<INBYTE INPUT BYTE
SOR $R5,8 SHIFT BYTE OVER
STR $R5,<OUTTAB,$R1 STORE VALUE
INC =$R1
B >ERRP1 DO NEXT
*
* PRINT ERROR PARAMETERS
*
PPAR LAB $B1,<OUTTAB GET ADDRESS OF OUTPUT TABLE
LDR $R1,<CRLF CR/LF
STR $R1,<TPR
LDV $R1,=-8 NUMB ENTRIES / LINE
*
* FIRST LINE GETS CHAN PROG INFO
*
PCPV LDR $R4,+$B1
SOL $R4,8
LDR $R5,+$B1
OR $R4,=$R5 FORM VALUE
*
PVL STR $R4,<TEMP
CALL ZV$THZ,TEMP,TPR
X
004050 1008 A800 1181 LDR $R2,<SPACE
004051 100A AF00 1177 STR $R2,<TPR
004052 100C 8AD1 INC =$R1
004053 100D 89D1 CMZ =$R1
004054 100E 0801 FFEC BAL PCPV
004055 1010 0300 1019 BG <GREG
004056 1012 A800 1180 LDR $R2,<CRLF
004057 1014 AF00 1177 STR $R2,<TPR
004058 1016 9B80 205B LAB $B1,<SAV5+7*$AF+1
004059 1018 5CF7 LDV $R5,=-9
004060
004061 1019 C871
004062 101A 5780 OFFF
004063 101C 0F80 1057 GREG LDR $R4,+$B1
B BINC $R5,<PVL
<ERR5 DONE
*
* VALUES TO INPUT
*
LOCTAB DC Z'0637'
DC Z'0737'
DC Z'2637'
DC Z'2737'
DC Z'0E37'
DC Z'2E37'
DC Z'3A37'
DC Z'1437'
DC Z'1A37'
DC Z'3E37'
DC Z'1837'
DC Z'3837'
DC Z'1B37'
DC Z'3B37'
DC Z'1737'
DC Z'0337'
DC Z'3F37'
DC Z'1D37'
DC Z'1937'
DC Z'3C37'
DC Z'1E37'
DC Z'1F37'
DC Z'0437'
DC Z'3D37'
RESV 4,0
*
* TABLE OF VALUES TO READ IN
*
OUTTAB DC 0 P MSB, RCV
DC 0 P LSB, RSV
    
```

```

004096 103C 0000          DC      0          P MSB, XMIT
004097 103D 0000          DC      0          P LSB, XMIT
004098 103E 0000          DC      0          LR5 STATUS, RCV
004099 103F 0000          DC      0          LR5 STATUS, XMIT
004100 1040 0000          DC      0          ERROR CODE
004101 1041 0000          DC      0          LR2 CONTROL WORD
004102 1042 0000          DC      0          RCV LR/
004103 1043 0000          DC      0          CCP FLAG
004104 1044 0000          DC      0          CHAR COUNT (NEG) RCV
004105 1045 0000          DC      0          CHAR COUNT (NEG) XMIT
004106 1046 0000          DC      0          FRAME COUNT (NEG), RCV
004107 1047 0000          DC      0          FRAME COUNT (NEG), XMIT
004108 1048 0000          DC      0          RCV CONFIG
004109 1049 0000          DC      0          SIZE OF LAST XMIT BYTE
004110 104A 0000          DC      0          FILL, RCV ACTION FRAME COUNT
004111 104B 0000          DC      0          RCV ACTION CHAR COUNT
004112 104C 0000          DC      0          XMIT ACTION FRAME COUNT
004113 104D 0000          DC      0          XMIT ACTION CHAR COUNT
004114 104E 0000          DC      0          XMIT FW REV
004115 104F 0000          DC      0          RCV FW REV
004116 1050 0000          DC      0          SIZE OF RESIDUE EXPECTED
004117 1051 0000          DC      0          UNUSED BIT MASK
004118 1052 0000          RESV    3,0
004119
004120 1055 0037          * DMPLOC DC      Z'0037'          BYTE ADDRESS TO OUTPUT
004121 1056 0000          DC      0
004122 1057 8FC0 0FFB          ERR5  RSTR    SAV1,=Z'FFFF'          RESTORE REGISTERS
004123 1058 0000          HLT
004124 1058 8386          JMP      $B6          HALT AFTER ERROR
004125
004126 105C 2020 204C 494E          * LINEP TEXT      ' LINES'
004127 105F 4524
004128 1060 8F00 201D          * DLAY  SAVE      <SAV1,=Z'CC8C'
004129 1063 9800 1191          LDR      $R1,<HRTZ
004130 1065 0001 1044          SUR      $R1,4          SET FOR CLOCK/16
004131 1066 0F87          B        >DLAY2
004132
004133          * LONG DELAY (APPROX 225 MS)
004134
004135 1067 8F40 0FB5          * DLAYLG SAVE     SAV1,=Z'CC8C'
004136 106A 9800 1191          LDR      $R1,<HRTZ
004137 106C 1041          SUR      $R1,1          GET CLUCK/4
004138 106D DB80 0000          X        LAB      $B5,<ZHPPFR          CLEAR B5
004139 106F DF80 0001          X        STB      $B5,<ZH1SAZ+$AF          ZERO OUT LEV 1 INTERRUPT VECTOR
004140 1071 9F00 0000          X        STR      $R1,<ZHRTCC          SET RTC CURRENT VALUE
004141 1073 C380 0F5E          LNJ      $B4,<UPTM          UPDATE TIME
004142 1075 8751          CL
004143 1076 9F00 0000          X        STR      $R1,<ZHRTCI          RTC RESET VALUE
004144 1078 1C01          LDV      $R1,=1
004145 1079 9F00 0000          X        STR      $R1,<ZHRTCL
004146 107B C800 11A7          LDR      $R4,<CONT6          INPUT STATUS FUNCTION
004147 107D C380 0EE8          LNJ      $B4,<CGSCH          PUT IN CHAN NUMBER
004148
004149 107F 0004          *
004150 1080 8980 0000          X        RTCN          TURN ON RTC TIMER
004151 1082 8055          DLAY1  CMZ      <ZHRTCC
004152 1083 0054          IO      $R5,=$R4          INPUT STAT
004153
004154 1084 DF00 115F          * HANG HERE IF MLCC DOES NOT RESPOND WITH A SECOND HALF READ
004155 1086 0A00 1080          STR      $R5,<STAT          STORE LAST STATUS READ
004156 1088 0005          BAG      <DLAY1          WAIT TILL DONE
004157 1089 8FC0 0F93          RTCF     RSTR    SAV1,=Z'CC8C'          TURN OFF TIMER
004158 108B CC8C 8384          JMP      $B4          RETURN
004159
004160          * INPUT BYTE
004161 108D C800 11AB          * INBYTE LDR      $R4,<CONT10          INBYTE FC
004162 108F C380 0EE8          LNJ      $B4,<CGSCH          OR IN CHAN
004163 1091 8055          ERRP2  IO      $R5,=$R4          INPUT BYTE
004164 1092 0054
004165 1093 07FE          BLOF   >ERRP2
004166 1094 8383          JMP      $B3
004167
004168          * BLOCK WRITE DATA TO RAM
004169
004170          * LNJ      $B1,<SDATA          LOCATION OF DATA
004171          * DC      DATA          NUMBER OF DATA BYTES
004172          * DC      RANGE          RAM ADDRESS
004173          * DC      RAMAD          RAM ADDRESS
004174          * DC      EVEN          0 = EVEN BYTE CPU ADDRESS
004175          * BIT 15 = 1 FOR ODD BYTE START
004176
004177          * R3 MUST CONTAIN THE CHANNEL NUMBER
004178
004179 1095 8F00 201D          * SDATA  SAVE     <SAV1,=Z'FFBF'          SAVE ALL BUT b1
004180 1097 FFBF          LDB      $B5,+$B1          GET ADDRESS OF DATA
004181 1098 DC11          LDR      $R4,+$B1          GET RANGE
004182 1099 C871          STR      $R4,<SPRG1          STORE RANGE
004183 109A CF00 10A7          SPRG4  STB      $B5,<SPRG5
004184 109C DF80 10A6          LDR      $R2,+$B1          GET RAM ADDRESS
004185 109E A871          STR      $R2,<SPRG2
004186 109F AF00 10A8          LDR      $R1,+$B1          LOAD START BYTE INDEX
004187 10A1 9871          AND      $R1,=X'7FFF'
004188 10A2 9570          LNJ      $B4,<MCCB          FORM CLB
004189 10A4 C380 10FF          DC      <DUMMY          CPU ADDRESS
004190 10A6 1160          DC      0          RANGE
004191 10A8 0000          SPRG1  DC      0          RAM ADDRESS
004192 10A9 0000          SPRG2  DC      0          GIVE CHANNEL CONTROL
004193 10AB 0F82          LNJ      $B4,<CHCT
004194 10AC 0400          B        >$+2
004195          DC      X'400'          BLOCK WRITE
004196
004197          * PROGRAM ARRIVES HERE FROM SDATA OR RDATA AS WELL AS SPRG.
004198          * SPRG3 LNJ      $B4,<TEST          WAIT FOR STATUS COMPLETE, OR
004199 10AD C380 0F1D          B        >$+2
004200 10AF 0F82          DC      12          100 MS TIMEOUT
004201 10B1 82D5          LB      $R5,=X'1000'          GET STATUS COMPLETE BIT

```



```

004201 10B2 1000
004202 10B3 0503
004203 10B4 E380 OFBA
004204 10B6 82D5
004205 10B7 0007
004206 10B8 0583
004207 10B9 E380 OFBA
004208 10BB 8F80 201D
004209 10BD FFBF
004210 10BE 8381
004211 *****
004212 * BLOCK READ FROM RAM.- CHAN. NUMBER MUST BE IN R3.
004213 *
004214 * LNJ $B1,<RDATA
004215 * DC INBUFF INPUT BUFFER ADDRESS
004216 * DC RANGE NUMBER OF BYTES
004217 * DC RAMAD RAM ADDRESS
004218 * DC EVEN 0 = EVEN BYTE CPU ADDRESS
004219 * BIT 1 = 1 FOR ODD BYTE ADDRESS
004220 * BIT 0 = 1 FOR NO DELAY AFTER STARTING
004221 10BF 8F00 201D
004222 10C1 FFBF
004223 10C2 DCF1
004224 10C3 DF80 10D0
004225 10C5 C871
004226 10C6 CF00 10D1
004227 10C8 9871
004228 10C9 9F00 10D2
004229 10CB 9871
004230 10CC 9570 7FFF
004231 10CE C380 10FF
004232 10D0 1160
004233 10D1 0000
004234 10D2 0000
004235 10D3 C380 OF0B
004236 10D5 0F82
004237 10D6 0800
004238 10D7 0F81 FF05
004239
004240
004241 10D9 8F00 201D
004242 10DB 0008
004243 10DC C800 11A5
004244 10DE C380 0EEB
004245 10E0 8055
004246 10E1 0054
004247 10E2 0703
004248 10E3 E380 OFBA
004249 10E5 0F00 10D9
004250 10E7 8FC0 0F35
004251 10E9 0008
004252 10EA 8384
004253
004254
004255
004256
004257 10EB 8F40 0F41
004258 10ED E8E0
004259 10EE 9CF3
004260 10EF 8751
004261 10F0 A811
004262 10F1 8AD1
004263 10F2 2985
004264 10F3 8FC0 0F39
004265 10F5 E8E0
004266 10F6 8383
004267 10F7 C800 11A6
004268 10F9 C380 0EEB
004269 10FB 8052
004270 10FC 0054
004271 10FD 07FE
004272 10FE 0FF2
004273
004274
004275
004276
004277
004278
004279
004280
004281
004282
004283
004284 10FF 8F00 202D
004285 1101 FDF4
004286 1103 ACF4
004287 1104 D874
004288 1105 A874
004289 1106 DEB4
004290 1108 C800 11A2
004291 1109 C380 0EEB
004292 110A 8182
004293 110B 0054
004294 110C 0703
004295 110E E380 OFBA
004296 1110 C800 11A4
004297 1112 C380 0EEB
004298 1114 8052
004299 1115 0054
004300 1116 0703
004301 1117 E380 OFBA
004302 1119 CED5

BBT >$+2+$AF
LNJ $B6,<ERRMB
LB =R5,=7
STATUS COMPLETE NOT SET AFTER BLOCK WRITE

BBF >$+2+$AF
LNJ $B6,<ERRMB
RSTR <SAV1,=Z'FFBF'
ERROR ; PARITY, MEMORY, OR RESOURCES

JMP $B1

RDATA SAVE <SAV1,=Z'FFBF'
SAVE EVERYTHING BUT B1.

LDB $B5,+$B1
STB $B5,<RDTA1
LDR $R4,+$B1
STR $R4,<RDTA3
LDR $R1,+$B1
STR $R1,<RDTA2
LDR $R1,+$B1
AND $R1,=X'7FFF'
LNJ $B4,<MCCB
DC <DUMMY
RDTA1 DC 0
RDTA3 DC 0
RDTA2 DC 0
LNJ $B4,<CHCT
B >$+2
DC X'800'
B SPRG3
FORM CCB
CPU ADDRESS
RANGE
RAM ADDRESS
ISSUE CHANNEL CONTROL
BLOCK READ
EXIT

*
* INPUT NEXT STATUS TO R5
*
INXT SAVE <SAV1,=Z'0008'
B4

LDR $R4,<CONT4
LNJ $B4,<CGSCH
IO =R5,=R4
GET CONTROL WORD FOR INPUT NEXT STATUS
MODIFY FOR CHANNEL
GET NEXT STATUS

BIOT >$+2+$AF
LNJ $B6,<ERRMB
INPUT NEXT STATUS WAS NAK'ED

*
NOP <INXT
RSTR SAV1,=Z'0008'
JMP $B4

* SET UP LCT FOR CHANNEL SPECIFIED IN R3
*
* LNJ $B3,<SETLCT
* -DC TABLE
*
SETLCT SAVE SAV2,=Z'E8E0'
R1,<R2,<R4,<B2,<B1

LDB $B1,+$B3
CL =R1
GET ADDRESS OF TABLE

* LCT4
LDR $R2,$B1.$R1
INC =R1
BNEZ $R2,>LCT5
RSTR SAV2,=Z'E8E0'
GET BYTE TO OUTPUT
BRANCH IF NOT AT END OF TABLE

JMP $B3
RETURN

* LCT5
LDR $R4,<CONT5
LNJ $B4,<CGSCH
LCT3 IO =R2,=R4
FUNCTION CODE FOROUT LCT BYTE
FORM IO CONTROL WORD
OUTPUT BYTE

BIOF >LCT3
B >LCT4
CHECK IF TAKEN
GET NEXT BYTE

*
*
* CCB FORMATION
*
* $R3 - CONTAINS CHANNEL WANTED
*
* LNJ $B4,<MCCB
* DC CPU ADDRESS
* DC RANGE IN BYTES
* DC RAM ADDRESS NUMBER OR CHANNEL CONTROL WORD.
*
*
MCCB SAVE <SAV2,=Z'FDF4'
SAVES $B1,$B3,$B2,$B5,$R7,$R5,$R4,R2,6 $R1

LDB $B2,+$B4
LDR $R5,+$B4
LDR $R2,+$B4
SWB $B5,=$B4
LDR $R4,<CONT1
LNJ $B4,<CGSCH
IOLD $B2,=$R4,=$R5
LOAD $B2 WITH CPU ADDRESS
GET RANGE
PUT RAM ADDRESS IN $R2
ALLOW $B4 TO BE USE IN SUBR. CALL
LOAD $R4 WITH I/O CONTROL WORD
OUTPUT ADDRESS AND RANGE

BIOT >$+2+$AF
LNJ $B6,<ERRMB
LDR $R4,<CONT3
LNJ $B4,<CGSCH
IO =R2,=$R4
ERROR, IOLD WAS NAK'ED
LOAD $R4 WITH I/O CONTROL WORD
PUT I/O CONTROL WORD IN $R4
OUTPUT MLCC RAM ADDRESS

BIOT >$+2+$AF
LNJ $B6,<ERRMB
SWB $B4,=$B5
ERROR, OUTPUT CONTROL WAS NAK'ED
SWAP FOR SUBR. RETURN

```

004300 111A 8F80 202D
 004301 111C FDF4
 004302 111D 8384
 004303
 004304
 004305
 004306 111E 8F00 201D
 004307 1120 C080
 004308 1121 CF51
 004309 1122 CB00 1193
 004310 1124 CF00 1186
 004311 1128 C851
 004312 1127 CB00 1194
 004313 1129 C370 000A
 004314 1128 9B00 1195
 004315 1127 9370 0064
 004316 1127 CA00 1186
 004317 1131 CA51
 004318 1132 F000 1186
 004319 1134 8F80 201D
 004320 1136 C080
 004321 1137 8385

RSTR <SAV2,=Z'FDF4' RESTORE REGS.
 JMP \$B4

*
 *
 *

CONVERT RTC TICKS TO MILSEC.

TKSEC SAVE <SAV1,=Z'C080' SAVE REGS.
 STR \$R4,=\$R1 DUPLICATE VALUE IN \$R1
 MUL \$R4,<DIV1
 STR \$R4,<TLOC
 LDR \$R4,=\$R1
 MUL \$R4,<DIV2
 DIV \$R4,=10
 MUL \$R1,<DIV3
 DIV \$R1,=100
 ADD \$R4,<TLOC
 ADD \$R4,=\$R1
 STR \$R4,<TLOC
 RSTR <SAV1,=Z'C080'
 JMP \$B5

*
 *
 *

CONVERT MS TO BITS/SEC/10 (GIVEN CHAR. SIZE & NUMBER)

004322
 004323
 004324
 004325
 004326
 004327 1138 8F00 201D
 004328 113A 9F80
 004329 113B 8756
 004330 113C B800 1168
 004331 1140 F870 03E8
 004332 1142 7F08
 004333 1143 F300 1186
 004334 1145 FF55
 004335 1146 DB53
 004336 1147 DF00 1187
 004337 1149 70D0
 004338 114A 7F0A
 004339 114B F354
 004340 114C FF55
 004341 114D DB53
 004342 114E D370 000A
 004343 1150 DA00 1187
 004344 1152 70D0
 004345 1153 7F0A
 004346 1154 F354
 004347 1155 FB53
 004348 1156 F370 0064
 004349 1158 FA53
 004350 1159 F000 1187
 004351 115B 8F80 201D
 004352 115D 9F80
 004353 115E 8385

BPS SAVE <SAV1,=Z'9F80' SAVE REGS.
 CL \$R6 CLEAR \$R6 FOR DIVIDE
 LDR \$R3,<CHRCNT LOAD \$R3 WITH NUMBER OF CHAR.
 DIV \$R3,=10 GET CHAR/10
 LDR \$R7,=1000 CONVERSION FACTOR FOR MS TO C.
 MLV \$R7,=8 MLV MULTIPLY BY CHARACTER SIZE
 DIV \$R7,<TLOC DIVIDE BY ACTUAL TIME IN MS
 STR \$R7,=\$R5
 MUL \$R5,=\$R3
 STR \$R5,<MSBS
 DOR \$R7,16 SHIFT REMAINDER DOWN
 MLV \$R7,=10
 DIV \$R7,=\$R4
 STR \$R7,=\$R5
 MUL \$R5,=\$R3
 DIV \$R7,=100
 ADD \$R7,=\$R5
 STR \$R7,<MSBS
 RSTR <SAV1,=Z'9F80'
 JMP \$B5

CONSTANTS AND STORAGE LOCATIONS

004354
 004355
 004356
 004357
 004358
 004359
 004360 115F 0000
 004361 1160 0000
 004362 1161 0000
 004363 1162 0000
 004364 1163 0000
 004365 1164 0000
 004366 1165 0000
 004367 1166 0000
 004368 1167 0000
 004369 1168 0000
 004370 1169 0000
 004371 116A 0000
 004372
 004373 116B 0000
 004374 116C 0000
 004375 116D 0000
 004376 116E FFFF
 004377 116F 0016
 004378 1170 0000
 004379 1171 FC00
 004380 1172 5555
 004381 1173 0400
 004382 1174 0000
 004383 1175 0000
 004384 1176 0000
 004385 1177 0000
 004386 1178 0000
 004387 1179 0000
 004388 117A 1C00
 004389 117B 0001
 004390 117C 0001
 004391 117D 0303
 004392 117E 0000
 004393 117F 0000
 004394 1180 8020
 004395 1181 2020
 004396 1182 0000
 004397 1183 0000
 004398 1184 0000
 004399 1185 0000
 004400 1186 0000
 004401 1187 0000
 004402 1188 0000
 004403 1189 0000
 004404 118A 0000
 004405 118B 0400
 004406 118C 0085
 004407 118D 0010

STAT DC 0 LAST STATUS STORED HERE
 DUMMY DC 0 DUMMY
 SPFLG DC 0 NON ZERO FOR 72 KB
 ERRCD DC 0 ERROR VALUE
 PVLU DC 0 CCP P COUNTER
 FRST DC 0 FRST TIME FLAG
 LNBM DC 0 LINE NUMBER
 CHAN DC 0 CHANNEL STORED HERE
 ELPS DC 0 ELAPSED TIME
 CHRCNT DC 0 CHARACTER COUNT
 COUNT DC 0 COUNTER FOR TEST LOOPS
 CHSZ DC 0 CHARACTER SIZE STORAGE
 *
 IMASK DC 0 MASK FOR CHANNELS PRESENT
 RANGE RESV 1,0
 DEVID RESV 1,0 DEVICE ID
 ALLONE RESV 1,-1
 C16 DC X'16'
 ADSTR RESV 1,0 MS 6 BITS OF DEVICE ADDRESS
 DADD DC Z'FC00' DEFAULT DEVICE ID
 DFLT DC Z'5555' DEFAULT VALUE
 DRNG DC X'400' DEFAULT RANGE
 HEAD DC 0 HEADER INFORMATION STORAGE
 IFLG DC 0 DATA INVERSION FLAG
 MASK DC 0 MASK
 TPR DC 0 TEMPORARY STORAGE
 TEMP DC 0 TEMPORARY STORAGE
 LOOP DC 0 LOOP COUNTER
 INBUF DC <RTB
 EXST DC 0
 ERF DC 0 EXPECTED STATUS FLAG
 PAT DC 1 MAKE 0 FOR SHORT ERROR PRINTOUT
 QFLG DC X'0303' DATA PATTERN
 PFLAG DC 0 QUICK MODE FLAG
 CRLF DC Z'8020' PRINT TEST LABEL FLAG
 SPACE DC Z'2020'
 PASSC DC 0 PASS COUNTER
 RRNG DC 0 RESIDUAL RANGE
 XLOOP DC 0 EXTERNAL LOOP FLAG
 CBLOOP DC 0 CONNECTOR LOOP FLAG
 TLOC DC 0 ELAPSED TIME IN MS
 MSBS DC 0 BITS/SEC
 STPTR DC 0 POINTER TO FRAME STATE TABLE
 BASE DC 0 RAN NUMBER BASE
 MODE DC 0 RAN NUMBER MODE
 K400 DC X'400'
 R85 DC X'85'
 R10 DC X'10'

```

004408 118E 0000
004409 118F 0008
004410 1190 0046
004411 1191 003C
004412 1192 0000
004413 1193 0000
004414 1194 0000
004415 1195 0000
004416 1196 0004
004417 1197 FFFF
004418 1198 0000
004419 1199 0000
004420 119A 0000
004421 119B 0000
004422
004423
004424
004425 119C FFFF
004426 119D FFFF
004427
004428 119E FFFF
004429 119F FFFE
004430
004431
004432
004433 11A0 FFFF
004434 11A1 FFFF
004435
004436
004437
004438
004439
004440
004441
004442
004443
004444
004445 11A2 0009
004446 11A3 000C
004447 11A4 000F
004448 11A5 001A
004449 11A6 000B
004450 11A7 0018
004451 11A8 0005
004452 11A9 0026
004453 11AA 0001
004454 11AB 001E
004455 11AC 0003
004456 11AD 001C
004457
004458
004459
004460
004461
004462 11AE 0000
004463
004464 11B8 0000
004465
004466 11B9 FFFF
004467
004468 11BD 0000
004469
004470
004471
004472
004473
004474 11C1 4248 434C 4120
11C4 5445 5354
004475
004476 11C6 2044 434D 5333
11C9 2C20 5341 462D
4120
004479 11CD 2020 4A55 4C20
11D0 3234 2C20 3139
3738 2400
004480 11D5 4248 434C 4120
11D8 4348 414E 4E45
4C28 5329 2400
004481 11DE 4E45 5854 2400
004482 11E1 5057 5220 4652
11E4 4551 2028 485A
2920 2400
004483 11E9 7878 2020 2400
004484 11EC 2054 5241 4E53
11EF 4D49 5420 3D20
2400
004485 11F3 2052 4356 203D
11F6 2024
004486 11F7 204C 494E 4520
11FA 2400
004487 11FB 2049 4420 3D20
11FE 2400
004488
004489
004490
004491
004492
004493
004494
004495
004496
004497
004498
004499
004500
004501
004502
004503 11FF
004504 11FF E007
004505
004508 11FF E007
004509
004510 0202

```

```

CO DC 0
C8 DC 8
C70 DC 70
HRTZ DC 60
SPEED DC 0
DIV1 DC 0
DIV2 DC 0
DIV3 DC 0
C4 DC 4
CM1 DC -1
TTOT DC 0
SEC DC 0
MLC-FR DC 0
ODDFLG DC 0
*
* HDLC FIRMWARE REV'S FOLLOW. FFFF IS DEFAULT VALUE IN THE TABLE
*
XHCFW1 DC -1
XHCFW2 DC -1
RHCFW1 DC -1
RHCFW2 DC -2
*
* BHCLA LINE SPEEDS FOLLOW. FFFF IS THE DEFAULT VALUE
*
HCLS-1 DC -1
HCLS-2 DC -1
*
*
* CONTROL WORDS FOR IO OPERATIONS
*
I/O CONTROL WORDS
*
CONT1 DC Z'0009'
CONT2 DC Z'000C'
CONT3 DC Z'000F'
CONT4 DC X'1A'
CONT5 DC Z'000B'
CONT6 DC Z'0018'
CONT7 DC X'5'
CONT8 DC X'26'
CONT9 DC Z'0001'
CONT10 DC X'1E'
CONT11 DC X'13'
CONT12 DC X'1C'
*
* STORAGE AREAS
*
TMPSTR RESV 10,0
*
ISA2 DC 0
*
ATLT RESV 4,-1
*
ERAR RESV 4,0
*
SAVE AREA -MAJOR TEST
*
MESSAGES
*
TITLE TEXT 'BHCLA TEST'
IFZ ($AF-2),LAF1
TEXT 'DCMS3, SAF-A '
DATE TEXT ' JUL 24, 1978$'
MSG1 TEXT 'BHCLA CHANNEL(S)$'
MSG2 TEXT 'NEXT$'
MSG5 TEXT 'PWR FREQ (HZ) $'
ERMG TEXT 'XX $'
XPRT TEXT ' TRANSMIT = $'
RPRT TEXT ' RCV = $'
IDMSG TEXT ' LINE $'
EQ TEXT ' ID = $'
*
* -VC-VH-VA-VN-VN-VE-VL PROGRAMS GO HERE
*
*
*
*
* SOURCE = DCMS3A4-CCP
*
* MAR 24, 1978
*
* BROADBAND CHANNEL FUNCTIONAL PROGRAM, RECEIVE
*
CCP1 ORG X'200'
EQU $
B RCV1
*
* LAST LOC LAST
LAST EQU X'0202'

```

```

PWR FREQ
MSEC
.1 MS
.01 MS
TOTAL OF TICKS
TOTAL SECONDS
MLCC FIRMWARE REV STORED HERE
NON ZERO FOR ODD RANGE
XMIT FW REV, LA 1
XMIT FW REV, LA 2
RCV FW LA 1
RCV FW REV LA 2
SPEED/10, LA 1
SPEED/10, LA 2
IOLD FUNCTION CODE
INPUT RANGE FUNCTION CODE
OUTPUT CCB CONTROL FUNCTION CODE
INPUT NEXT STATUS FUNCTION CODE
OUTPUT BYTE INTO LCT FUNCTION CODE
INPUT STATUS FUNCTION CODE
OUTPUT CHANNEL CONTROL FUNCTION CODE
INPUT ID FUNCTION CODE
OUTPUT MLCC CONTROL FUNCTION CODE
INPUT BYTE
OUTPUT INTERRUPT CONTROL FUNCTION CODE
INPUT DATA SET STATUS
ACTIVE LINES TABLE
ERROR BUFFER

```

```

004511
004512
004513
004514 1200 0190
004517
004518 1201 0BE6
004521 1202 0195
004522
004523
004524
004525
004526
004527
004528
004529 0208
004530
004533 1203 503E
004534
004537 1204 9308
004538
004541 1205 9411
004542
004545
004546
004547 1206 3690
004550
004553 1207 0632
004554
004555
004556
004557
004558 0212
004559
004560
004561
004562
004563
004564
004565
004568 1208 0125
004569
004572 1209 510E
004573
004576 120A A051
004577
004580
004581
004582 0219
004583
004584 120B 1F01
004585
004586
004587
004588
004589
004592 120C 2751
004593
004596 120D 1A92
004597
004600 120E 80E1
004601
004604 120F 0690
004605
004608 1210 01E6
004609 1211 0177
004612
004613
004614
004615
004616 0226
004617
004620
004621 1212 2751
004624
004625 1213 1A92
004628
004629 1214 80E1
004632
004633
004634 1215 0690
004637
004638 1216 0DE6
004641 1217 016B
004642
004643
004644 0232
004645
004648
004649 1218 2551
004652
004653 1219 0E93
004656
004657 121A 08F2
004660
004661 121B 0690
004664
004665 121C 02E6
004668 121D 015F
004669
004670
004671 023E
004672
004675 121E E506
004676
004679 121F 9003
004680
004683 1220 E601
004684
004685
004686 0245
004687
004690 1221 58A0
004691

*
*      WAIT  END
*      LD    =11
*
*      JUMP  EREND
*
*      DC    EREND-X'0207'
*
*-----*
*
* RCV INITIALIZATION, RESYNC
*
*
*      LOC   RCV1
RCV1 EQU LD X'0208'
*      LD    X'3E'
*
*      AND   =X'8'
*
*      OR    =X'11'
*
*      OUT   6
*
*      LD    =6
*
*      OUT   2
*
*      LOC   RCV2
RCV2 EQU LD X'0212'
*      WAIT
*
*-----*
*
* READ FW REV
*
*
*      IN    5
*
*      ST    X'E'
*
*      RECV  0
*      ST    X'1F'
*
*      LOC   RCV3
RCV3 EQU LD X'0219'
*      WAIT
*
*-----*
*
* PROCESS THE INITIAL ILS RUPT
*
*
*      IN    7
*      ST    X'1A'
*
*      C     =X'80'
*
*      BET   RCV4
*
*      LD    =1
*
*      JUMP  EREND
*
*      DC    EREND-X'0225'
*
*
*      LOC   RCV4
RCV4 EQU LD X'0226'
*      IN    7
*      ST    X'1A'
*
*      C     =X'80'
*
*      BET   RCV4A
*
*      LD    =13
*
*      JUMP  EREND
*
*      DC    EREND-X'0231'
*
*
*      LOC   RCV4A
RCV4A EQU LD X'0232'
*      IN    5
*      ST    X'E'
*
*      AND   =8
*
*      BZF   RCV5
*
*      LD    =2
*
*      JUMP  EREND
*
*      DC    EREND-X'023D'
*
*
*      LOC   RCV5
RCV5 EQU LD X'023E'
*      BART  RCV6
*
*      LD    =3
*
*      JUMP  EREND
*
*
*      LOC   RCV6
RCV6 EQU LD X'0245'
*      RECV  0
*
*      BARF  RCV7
*
*
* OF FRAMES
*
* CRI AFTER LAST MESSAGE
*
* GET CONTROL FLAG
*
* STRIP TO READ CRC BIT
*
* INITIALIZATION PARAMETER
*
* TEST, RCV ON
*
* INPUT DATA SET STATUS
*
* INPUT FW REV + ENABLE RECV
*
* READ LR7 AGAIN
*
* BRANCH IF ADAPT RDY = 1
*
* ERROR CODE
*
* DUMMY TO ADVANCE

```

```

004694 1222 F506
004695
004698 1223 9009
004699
004702 1224 E601
004703
004704
004705      024D
004706
004709 1225 5025
004710
004713 1226 510E
004714
004717 1227 9304
004718
004721 1228 E206
004722
004723
004726 1229 9004
004727
004730 122A E601
004731
004732
004733
004734
004735      0259
004736
004737 122B 4450
004740
004741
004742
004743
004744
004745 122C 1793
004748
004749 122D 20E2
004752
004753 122E 0750
004756
004757 122F 1795
004760
004761 1230 04E0
004764
004765
004766      0265
004767
004768 1231 0350
004771
004772
004773      0267
004774
004777 1232 1736
004778
004779
004780
004781
004784 1233 5014
004785
004788
004789
004790      026B
004791
004792
004793 1234 3290
004796
004797 1235 FF51
004800
004801      026F
004802
004803 1236 1B01
004804
004805
004806
004807
004808
004809
004810
004811
004812      0270
004813
004816 1237 503E
004817
004820 1238 9390
004821
004824 1239 F204
004825
004828 123A E600
004829
004830
004831
004832
004833      0279
004834
004835 123B 2C50
004838
004839 123C 3F94
004842
004843 123D C052
004846
004847 123E 1BF1
004850
004851
004852
004853
004854
004855 123F 0C50
004858
004859 1240 1D52
004862
004863 1241 1BF1
004866
004867 1242 0690
004870

*      LD      =9
*      JUMP    EREND          ADAP RDY DIDN'T RESET
*
*      LOC     RCV7
RCV7 EQU X'024D'
*      IN      5              IN DATA SET STAT
*      ST      X'E'
*      AND     =X'4'         STRIP TO READY
*      BZT    RCV8
*
*      LD      =4
*      JUMP    EREND
*
* OUTPUT RCV CONFIGURATION
*
*      LOC     RCV8
RCV8 EQU X'0259'
*      LD      X'17'
*
* IF TCB IS SET MODIFY BYTE SIZE SPECIFIED
* BY LR6 (SHOULD BE DON'T CARE)
*
*      AND     =X'20'         TCB BIT
*      BZT    DOCFG          B = NO TCB
*      LD      X'17'         GETVALUE
*      XOR     =4           CHANGE BIT 5
*      B      OUTIT
*
*      LOC     DOCFG
DOCFG EQU X'0265'
*      LD      X'17'
*
*      LOC     OUTIT
OUTIT EQU X'0267'
*      OUT     6
*
* TURN ON XMIT, RCV, OR BOTH
*
*      LD      X'14'         PICK UP CONTROL
*      OUT     2
*
*      LOC     DLOUP
DLOUP EQU X'026B'
*
*      LD      =X'FF'
*      ST      X'1B'         SET INITIAL FRAME COUNT
*
*      LOC     XYZ
XYZ EQU X'026F'
*      WAIT
*
* -----
* DECIDE TO DO LONG OR SHORT DATA LOOP
*
*
*      LOC     LPI
LPI EQU X'0270'
*      LD      X'3E'         GET FLAG
*
*      AND     =X'90'         STRIP TO OVERUN BIT
*      BZF    LUP2          BRANCH IF LONG LOOP
*      JUMP   RCV10         DD SHURI LOOP
*
* LONG DATA LOOP - 1ST SEE IF TIME FOR ANY SPECIAL ACTION
*
*      LOC     LUP2
LUP2 EQU X'0279'
*      LD      X'3F'         GET ACTION FRAME
*
*      OR      =X'C0'         MAKE 8 BITS
*      C      X'1B'         COMPARE WITH ACTUAL
*      BEF    RCV9          B = NO OVRUN NOW
*
* -----
* SPECIAL ACTION
*
*      LD      X'1D'         GET ACTION CHAR
*      C      X'18'         COMPARE WITH ACTUAL
*      BEF    RCV9          B = NO OVERUN NOW
*      LD      =X'20'
*      JUMP   RDLAY         DELAY ABOUT 100 MS

```

```

004871 1243 20E6
004874 1244 0090
004875
004876
004877
004878
004879
004880
004881 028C
004882
004883
004884 1245 2751
004885
004886 1246 1AF2
004887
004888 1247 22A0
004889
004890 1248 1150
004902
004903 1249 1805
004904
004907 124A 5118
004908
004911 124B F306
004912
004913 029A
004914
004917 124C 9005
004918
004921 124D E600
004922
004923 029F
004924
004925 124E FEE5
004928
004929 124F D901
004930
004933 1250 E0D6
004934
004935
004936
004937
004938
004939 02A4
004940
004943 1251 2751
004944
004947 1252 1AF2
004951
004954 1253 0AA0
004955
004956
004957 1254 11E3
004960
004961 1255 EEE5
004964
004965 1256 F601
004966
004969 1257 E0F3
004970
004971
004972
004973
004974
004975
004976
004977 02B2
004978
004981 1258 93F0
004982
004985 1259 9220
004986
004989 125A F10C
004990
004993 125B 503E
004994
004997 125C 9308
004998
005001 125D F20A
005002
005005 125E 9010
005006
005009 125F E600
005010
005011
005012 02C3
005013
005014
005015
005016
005017
005018
005019
005020 1260 DA92
005023
005024 1261 10F1
005027
005028 02C7
005029
005030 1262 5290
005033
005034 1263 1054
005037
005038 1264 1151
005041
005042
005043
005044
005045
005046
005047 1265 1150

-----
* DC RDLAY-X'028B'
*-----
* NO SPECIAL ACTION
*
* RCV9 LOC RCV9
* EQU X'028C'
* IN 7 GET STATUS
* ST X'1A'
*
* BZF RC5 BRANCH IF STATUS TO READ
*
* RECV 0
*
* ST LD X'18'
*
* DEC
*
* ST X'18' UPDATE CHAR COUNT
*
* BLCF NEXT
*
* ER-5 LOC ER-5
* EQU X'029A'
* LD =5 MISSED CLOSED FLAG
*
* JUMP EREND
*
* NEXT LOC NEXT
* EQU X'029F'
* BART LUP2
*
* WAIT
*
* B LUP2
*-----
* MINIMUM LOOP
*
* RCV10 LOC RCV10
* EQU X'02A4'
* IN 7
* ST X'1A'
*
* BZF RC5 B = SOME STATUS TO READ
*
* RECV 0
*
* ST BLCT ER-5 BR = MISSED CLOSE FLAG
*
* BART RCV10
*
* WAIT
*
* B RCV10
*-----
* COME HERE WHEN NON -ZERO LR7 FOUND IN DATA LOOP
*
* RC5 LOC RC5
* EQU X'02B2'
* AND =X'F0' STRIP OFF RESIDUE
*
* C =X'20' CHECK FOR DIAGNOSTIC STATUS
*
* BEF NOTEST BR IF NOT DIAGNOSTIC CRC MODE
*
* LD X'3E' GET FLAG
*
* AND =X'8' READ CRC BIT
*
* BZF NOTESM
*
* LD =X'10'
*
* JUMP EREND ERROR, DIAGNOSTIC STATUS WHEN NOT
* IN DIAGNOSTIC MODE
*
* NOTEST LOC NOTEST
* EQU X'02C3'
*-----
* CHECK FOR NORMAL CLOSING STATUS, BITS 0 - 4
*
*
*
* C =X'10'
*
* BEF JMP2 BRANCH IF ERROR
*
* NOTESM LOC NOTESM
* EQU X'02C7'
* LD =X'10'
*
* OR X'11'
*
* ST X'11' SET CLOSE BIT
*-----
* CHECK THAT LAST BYTE SIZE IS AS EXPECTED
*
*
* LD X'1A' GET STATUS

```

```

005050 * AND =X'F' STRIP TO RESIDUE BITS
005051 1266 1A93 *
005054 * C 4 *...AND PARTIAL BYTE BIT
005055 * COMPARE WITH WHAT SHOULD BE
005056 1267 0F52 *
005059 * BET RCV12
005060 1268 04E1 *
005063 * LD =10
005064 1269 0690 *
005067 * JUMP EREND RESIDUE WRONG FOR LAST BYTE
005068 126A 0AE6
005071 126B 00C3 * DC EREND-X'02D9'
005072 * *...OR PARTIAL BYTE BIT WRONG
005073 *
005074 * RCV12 LOC RCV12
005075 EQU X'02DA'
005078 * RECV 0
005079 * ST
005080 126C A011 *
005083 126D 5018 * LD X'18'
005084 *
005085 * DEC X'18' DEC CHAR COUNT
005086 126E 0551 * ST
005089 * LD =X'10'
005090 126F 1890 *
005093 * OR X'11'
005094 1270 1054 * ST X'11' SET CLOSE BIT
005097 *
005098 1271 1151 * LD X'3E' GET FLAG
005101 *
005102 1272 1150 * AND =X'8' STRIP TO READ CRC DIAGNOSTIC BIT
005105 *
005106 1273 3E93 * BZT RCV11
005109 *
005110 1274 08E2 *
005113 * WAIT
005114 1275 0E01 *
-----
005115 *
005116 * RECEIVE CRC RESIDUES IF IN CRC DIAGNOSTIC MODE
005117 *
005118 * RECV 0 GETS RES BYTE 1
005119 * ST
005122 1276 A011 *
005123 * WAIT
005124 * RECV 0 GETS RES BYTE 2
005125 * ST
005128 1277 01A0 *
005129 * WAIT
005130 *
005131 1278 1101 *
005132 * RECV 0 GETS XMIT CRC BYTE 1
005133 * ST
005136 1279 A011 *
005137 * WAIT
005138 * RECV 0 GETS XMIT CRC BYTE 2
005141 127A 01A0 *
005142 * ST
005143 * WAIT
005144 127B 1101 *
005145 *
005146 02FA * RCV11 LOC RCV11
005147 EQU X'02FA'
005148 * GNB
005149 *
-----
005150 *
005151 * END OF FRAME PROCESSING
005152 *
005153 *
005154 * EOFP LOC EOFP
005155 EQU LD X'02FB'
005156 127C 0250 * X'1B' GET FRAME COUNT
005159 *
005160 127D 1805 * DEC
005161 *
005164 127E 511B * ST X'1B'
005165 *
005168 127F F434 * BLBF RC92 BRANCH IF MORE DATA COMING
005169 *
-----
005170 *
005171 *
005172 * LAST FRAME
005173 * LOC LFRM
005174 0302 * EQU X'0302'
005175 *
005176 * LD X'3F' GET FILL MODE
005179 1280 503F *
005180 * AND =X'80'
005183 1281 9380 *
005184 * BZF RC91 BRANCH IF FLAG IDLE
005187 1282 F22B *
005188 * BART EOFR
005191 1283 E502 *
005192 *
005193 * WAIT
005194 * LOC EOFR
005195 030B * EQU X'030B'
005198 1284 0127 * IN 7 GET STATUS
005199 *
005202 1285 511A * ST X'1A'
005203 *
005206 1286 9280 * C =X'80'
005207 *
005210 * RECV 0
005211 1287 A0E1 * BET RC91 DUMMY RECV FOR ILS
005214 * * DONE WITH FRAMES
005215 *
005216 * LOC RC90
005217 1288 2090 * EQU X'0313'
005220 * LD =6
005221 1289 06E6 * JUMP EREND SHOULD HAVE ILS SET
005222 128A 0085 *
005225 * DC EREND-X'0317'
005226 * LOC JMP2
    
```

```

005227      0318      JMP2  EQU  X'0318'
005228      *          JUMP  STATER
005231      128B  E600      *
005232      *-----*
005233      *
005234      * DELAY ROUTINE USED TO INDUCE OVERRUN
005235      *
005236      * RDLAY
005237      *
005238      *          LOC  RDLAY
005239      RDLAY EQU  X'031B'
005240      *          ST  X'1C'          STORE COUNT
005241      128C  4551      *
005242      * RCCPD1
005243      *          LOC  RCCPD1
005244      RCCPD1 EQU  X'031D'
005245      *          LD  =0
005246      031D
005247      128D  1C90      *RCCPD2
005248      *          LOC  RCCPD2
005249      RCCPD2 EQU  X'031F'
005250      *          DEC
005251      031F
005252      128E  0005      *
005253      *          AND  =X'FF'
005254      128F  93FF      *
005255      *          AND  =X'FF'
005256      1290  93FF      *
005257      *          AND  =X'FF'
005258      1291  93FF      *
005259      *          BZF  RCCPD2
005260      1292  F2F8      *
005261      *          LD  X'1C'
005262      1293  501C      *
005263      *          DEC
005264      *          ST  X'1C'
005265      1294  0551      *
005266      *          BZF  RCCPD1
005267      1295  1CF2      *
005268      *          JUMP  RCV9          END OF DELAY ROUTINE
005269      1296  EFE6
005270      1297  FF5B      *
005271      *          DC  RCV9-X'0331'
005272      *-----*
005273      *
005274      * LAST FRAME, FLAG IDLE
005275      *
005276      *          LOC  RC91
005277      RC91  EQU  X'0332'
005278      *          JUMP  LAST
005279      1298  E6FE      *
005280      *-----*
005281      *
005282      * INTERFRAME STATE
005283      *
005284      *          LOC  RC92
005285      RC92  EQU  X'0335'
005286      *          BART RCNXT
005287      1299  CEE5      *
005288      *          WAIT
005289      129A  0201      *
005290      *          LOC  RCNXT
005291      RCNXT EQU  X'0338'
005292      *          IN  7
005293      *          ST  X'1A'          GET RCV STATUS
005294      129B  2751      *
005295      *          LD  X'3F'          XMIT CONFIG.
005296      129C  1A50      *
005297      *          AND  =X'80'
005298      129D  3F93      *
005299      *          BZF  RC89          BRANCH IF FLAG IDLE
005300      129E  80F2      *
005301      *-----*
005302      *
005303      * ABORT IDLE STATE. CHECK FOR ILS STATUS RECEIVED.
005304      * ILS STATUS RECEIVED.
005305      *
005306      *          LD  X'1A'
005307      129F  1850      *
005308      *          NOP
005309      12A0  1A00      *
005310      *          C  =X'80'
005311      12A1  9280      *
005312      *          BET  RC88          BRANCH IF ILS
005313      12A2  E106      *
005314      *          LD  =7
005315      *          SHOULD BE ILS STATUS, IS NOT
005316      12A3  9007      *
005317      *          JUMP  EREND
005318      12A4  E600      *
005319      *          LOC  RC88
005320      RC88 EQU  X'034D'
005321      *          REC  0          DUMMY REC  V
005322      12A5  50A0      *
005323      *          WAIT
005324      *-----*
005325      *
005326      * CRI TO GET HERE IS 1ST DATA FOR NEXT FRAME
005327      *
005328      *          BART  RC89
005329      12A6  01E5      *
005330      *          LD  =8
005331      12A7  0890      *
005332      *          ST  X'3A'
005333      12A8  0851      *
005334      *          JUMP  EREND          ADAP READY NOT SET AFTER DATA CRI
005335      12A9  3AE6
005336      12AA  0045      *
005337      *          DC  EREND-X'0357'
005338      *
005339      * FIRST DATA, NEXT FRAME
005340      *
005341      *          LOC  RC89
005342      *
005343      *
005344      *
005345      *
005346      *
005347      *
005348      *
005349      *
005350      *
005351      *
005352      *
005353      *
005354      *
005355      *
005356      *
005357      *
005358      *
005359      *
005360      *
005361      *
005362      *
005363      *
005364      *
005365      *
005366      *
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005371      *
005372      *
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005374      *
005375      *
005376      *
005377      *
005378      *
005379      *
005380      *
005381      *
005382      *
005383      *
005384      *
005385      *
005386      *
005387      *
005388      *
005389      *
005390      *
005391      *
005392      *
005393      *
005394      *
005395      *

```



```

005396      0358      RC89  EQU  X'0358'
005397      *          *
005398      *          LD    =0
005401 12AB  9000      *          *
005402      *          ST    X'18'      CLEAR CHAR COUNT
005405 12AC  5118      *          *
005406      *          JUMP  XYZ
005409 12AD  E6FF      *
005410      *-----*
005411      *
005412      *
005413      * COME HERE FOR STATUS OTHER THAN CLOSE
005414      *
005415      *          LOC   STATER
005416      STATER EQU  X'035F'
005417      *          LD    X'1A'      GET STATUS
005418 12AE  1150      *          *
005421      *          AND   =X'10'    STRIP TO EOF
005422 12AF  1A93      *          *
005425      *          BZT   STAT1
005426 12B0  10E2      *          *
005429      *          LD    =X'10'
005430 12B1  0790      *          *
005433      *          OR    X'11'
005434 12B2  1054      *          *
005437      *          ST    X'11'
005438 12B3  1151      *          *
005441      *          LOC   STAT1
005442      STAT1 EQU  X'036B'
005443      *          LD    X'1A'
005444 12B4  1150      *          *
005447      *          AND   =X'80'    STRIP TO ABORT/IDLE
005448 12B5  1A93      *          *
005451      *          BZT   STAT2
005452 12B6  80E2      *          *
005455      *          LD    =X'2'
005456 12B7  0790      *          *
005459      *          OR    X'10'
005460 12B8  0254      *          *
005463      *          ST    X'10'
005464 12B9  1051      *          *
005467      *          LOC   STAT2
005468      STAT2 EQU  X'0377'
005469      *          LD    X'1A'
005470 12BA  1050      *          *
005473      *          AND   =X'40'    STRIP TO OVER RUN
005474 12BB  1A93      *          *
005477      *          BZT   STAT3
005478 12BC  40E2      *          *
005481      *          LD    =X'20'
005482 12BD  0790      *          *
005485      *          OR    X'10'
005486 12BE  2054      *          *
005489      *          ST    X'10'
005490 12BF  1051      *          *
005493      *          LOC   STAT3
005494      STAT3 EQU  X'0383'
005495      *          LD    X'1A'
005496 12C0  1050      *          *
005499      *          AND   =X'20'    STRIP TO FCS ERROR
005500 12C1  1A93      *          *
005503      *          BZT   STAT4
005504 12C2  20E2      *          *
005507      *          LD    =X'40'
005508 12C3  0790      *          *
005511      *          OR    X'11'
005512 12C4  4054      *          *
005515      *          ST    X'11'
005516 12C5  1151      *          *
005519      *          LOC   STAT4
005520      STAT4 EQU  X'038F'
005521      *          RECV  0      DUMMY TO ADVANCE
005524 12C6  11A0      *          *
005525      *          BLCT  STAT5    CHECK BUFFER NOT FULL
005528 12C7  E302      *          *
005529      *          ST    STAT5
005530      *          LOC   STAT5
005531      STAT5 EQU  X'0393'
005532      *          GNB
005533 12C8  1102      *          *
005534      *          BLBF  RC93    BRANCH IF MORE COMING
005537 12C9  F404      *          *
005538      *          JUMP  LFRM
005541 12CA  E6FF      *          *
005542      *          LOC   RC93
005543      RC93  EQU  X'0399'
005544      *          JUMP  EOFP
005545 12CB  6AE6      *          *
005548 12CC  FF60      *          DC    EOFP-X'039B'
005549      *-----*
005550      *
005551      *
005552      * FATAL ERROR ROUTINE
005553      *
005554      *          LOC   EREND
005555      EREND EQU  X'039C'
005556      *          ST    X'3A'
005559 12CD  513A      *          *
005560      *          LD    =0
005563 12CE  9000      *          *
005564      *          OUT   2      SHUT EVERYTHING OFF
005567      *          NOP
005568 12CF  3200      *          *
005569      *          NOP
005570      *          NOP
005571 12D0  0000      *          *
005572      *          NOP
005573      *          LOC   LUP
005574      LUP   EQU  X'03A5'
005575      *          WAIT
005576 12D1  0001      *          *
005577      *          B     LUP
005580 12D2  E0FE

```

```

005581 *      NOP
005582 *      NOP
005583 12D3 0000
005584 *-----*
005585 *-----*
005586 *-----*
005587 *
005588 * BROADBAND CHANNEL FUNCTIONAL PROGRAM , XMIT
005589 *
005590 *
005591 *      ORG      X'400'
005592 *      CCP2   EQU  $
005593 *      B      XMIT21
005594 12D4  E007
005595 *
005596 *
005597 *
005598 *      LOC      LAST1
005599 *      EQU     X'0402'
005600 *      WAIT
005601 *      LD      =12
005602 12D5  0190
005603 *
005604 *      JUMP    EREND
005605 *
005606 12D6  0CE6
005607 12D7  FF95
005608 *
005609 *      DC      EREND-X'0407'
005610 *
005611 *-----*
005612 *
005613 * DECIDE TO READ FW REV OR NOT
005614 *
005615 *
005616 *      LOC      XMIT21
005617 *      EQU     X'0408'
005618 *      LD      X'39'
005619 *
005620 *      BZF    RREV
005621 12D8  5039
005622 *
005623 *      BZF    RREV
005624 *
005625 12D9  F20F
005626 *
005627 *-----*
005628 *
005629 * DON'T READ FW REV.
005630 *
005631 *
005632 *
005633 *      LD      =X'45'
005634 *
005635 *      OUT    2
005636 *      LD      =X'18'
005637 *
005638 *      OUT    7
005639 *
005640 *      NOP
005641 *      NOP
005642 12DA  9045
005643 *
005644 *      LD      =0
005645 *
005646 *      SEND   0
005647 *      WAIT
005648 *
005649 *      B      XREV
005650 12DB  3290
005651 *
005652 *
005653 *
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005739 *
005740 *
005741 *
005742 *
005743 *
005744 *
005745 *
005746 *
005747 *

```

CRI AFTER XMIT TURNED OFF

GET READ REV FLAG
B = READ FW REVTON, RQS OFF
TEOF OFF

DUMMY OUTPUT

TEST, XMIT
EOF BIT

DUMMY OUTPUT

READ STAT

STRIP TO UND RUN BIT
IF ZERO DONE
GET FIRMWARE REVSHUT OFF XMIT
GET IDLE STATE

```

005748 12EF 3250
005751
005752 12F0 3F93
005755
005756 12F1 20E2
005759
005760 12F2 0390
005763
005764 043F
005765
005768 12F3 1036
005769
005770
005771
005772
005773
005774
005775 12F4 0190
005778
005781 12F5 1C37
005782
005785 12F6 9000
005786
005789
005790 12F7 6001
005791
005792
005793 12F8 0000
005794
005795
005796 12F9 0000
005797
005798
005799 12FA 0000
005800
005801
005802
005803
005804
005805 044E
005806
005807
005808
005809 12FB 0150
005812
005813 12FC 3793
005816
005817 12FD 10E2
005820
005821
005822
005823
005824
005825 12FE 0750
005828
005829 12FF 3793
005832
005833 1300 0EE0
005836
005837
005838
005839
005840
005841 045B
005842
005843 1301 0F50
005846
005847
005848
005849
005850
005851 1302 3793
005854
005855 1303 20E2
005858
005859 1304 0750
005862
005863 1305 3795
005866
005867 1306 04E0
005870
005871
005872 0467
005873
005874 1307 0350
005877
005878
005879
005880
005881
005882
005883 0469
005884
005887 1308 3737
005888
005891 1309 503F
005892
005895 130A 9380
005896
005897
005898 130B 0707
005899
005902 130C 543F
005903
005906 130D 9360
005907
005908
005911 130E 0736
005912
005915 130F 503B
005916
005917
005918 1310 0551

```

```

*      AND      =X'20'      STRIP TO PERTINENT BIT
*      BZT      XSTAR      BRANCH IF ABURT IDLE
*      LD       =X'10'      SET FUR FLAG IDLE
*STAR   LOC     XSTAR
EQU     EQU     X'043F'
OUT     OUT     6      SET FILL MODE
*      WAIT
*-----*
* GET HERE BY CRI AFTER RCV SIDE TURNS ON XMIT
*      LD       =X'1C'
*      OUT      7      TEOF, 0 FILLS
*      LD       =0
*      SEND     0      DUMMY SEND
*      WAIT
*      NOP
*      NOP
*      NOP
*      NOP
*-----*
* START OF FRAME INITIALIZATION
*STAR   LOC     SFRM
EQU     EQU     X'044E'
WAIT
*      LD       X'37'
*      AND      =X'10'      STRIP TO EOF BIT
*      BZT      SFRM3      B = NO EOF BIT SET
*      INITIAL EOF BIT IS SET
*      LD       X'37'      GET CONFIG WURD AGAIN
*      AND      =X'E'      STRIP TO BYTE SIZE INFO
*      B        SFRM2
*      INITIAL EOF BIT NOT SET
*STAR   LOC     SFRM3
SFRM3   EQU     EQU     X'045B'
LD      LD      X'37'
*      IF TCB SPECIFIED GIVE SCREWED UP BYTE SIZE TO LR7.
*      TCB SHOULD OVERRIDE THIS
*      AND      =X'20'      STRIP TO TCB BIT
*      BZT      SFRM1
*      LD       X'37'      GET VALUE FOR LR7
*      XOR      =4      INVERI BIT 5
*      B        SFRM2
*STAR   LOC     SFRM1
SFRM1   EQU     EQU     X'0467'
LD      LD      X'37'
*-----*
* SET UP XMIT CONFIGURATION
*STAR   LOC     SFRM2
SFRM2   EQU     EQU     X'0469'
OUT     OUT     7      TRANSMIT CONFIGURATION
*      LD       X'3F'      GET FILL MODE AND DELAY XMIT BIT
*      AND      =X'80'
*      SR
*      SR
*      OR       X'3F'
*      AND      =X'60'
*      SR
*      OUT     6      SET INTER-FILL MODE
*      LD       X'3B'
*      DEC
*      ST      X'3B'      DEC FRAME COUNT

```

```

005921
005922
005923
005924
005925      047B
005926
005927
005928      1311 3B50
005931
005932      1312 3E93
005935
005936      1313 83E2
005939
005940
005941
005942
005943
005944      0481
005945
005946
005947      1314 3E50
005950
005951      1315 1952
005954
005955      1316 3BF1
005958
005959
005960      1317 2250
005963
005964      1318 3C52
005967
005968      1319 38F1
005971
005972
005973
005974
005975      131A 1C50
005978
005979      131B 3E93
005982
005983      131C 02E2
005986
005987      131D 0790
005990
005993      131E 9037
005994
005995
005996      131F 10E0
005999
006000
006001      0499
006002
006003      1320 5250
006006
006007      1321 3E93
006010
006011      1322 01E2
006014
006015      1323 0690
006018
006019      1324 14E6
006022      1325 00DA
006023
006024      04A4
006025
006026
006027      1326 0000
006028
006029
006030      1327 0000
006031
006032
006033
006034
006035
006036      04A8
006037
006040      1328 5038
006041
006042
006043      1329 0551
006046
006047      132A 3893
006050
006051      132B 1092
006054
006055      132C 0010
006056
006059      132D F103
006060
006063      132E 543D
006064
006065      04B6
006066
006069      132F E310
006070
006073
006074      1330 60E5
006077
006078      1331 C701
006079
006082      1332 E0C4
006083
006084
006085
006086
006087
006088      04BE
006089
006090
006091      1333 10E3
006094
006097      1334 0760

*
* SHOULD WE TAKE LONG OR SHORT DATA LOOP?
*
* LOC      TDATA
TDATA EQU X'047B'
*
* LD      X'3E'          GET FLAG
*
* AND     =X'83'
*
* BZT    TDATA9          BR = 00 MINIMUM LOOP
*
*-----
*
* LONG DATA LOOP, 1ST SEE IF ANY SPECIAL ACTION TO BE TAKEN.
*
* LOC      TXYZ
TXYZ EQU X'0481'
*
* LD      X'19'          ACTION FRAME
*
* C       X'3B'          COMPARE WITH ACTUAL
*
* BEF    TDATA3          B = NOT THIS FRAME
*
* LD      X'3C'          GET CHAR
*
* C       X'38'
*
* BEF    TDATA3          B = NOT THIS CHAR
*
* IT IS THE CHAR AND FRAME TO TAKE SPECIAL ACTION.
*
* LD      X'3E'
*
* AND     =X'2'          STRIP TO ABORT
*
* BZT    TDATA2          B = NO ABORT
*
* LD      =X'90'          ABORT
*
* OUT    7              OUT CONFIG
*
* LD      B             TDATA10
*
* B       TDATA10       GO TO END OF FRAME PROCESSING
*
* LOC      TDATA2
TDATA2 EQU LD X'0499'
*
* LD      X'3E'
*
* AND     =1            STRIP TO DELAY FLAG
*
* BZT    TDATA5          DON'T DELAY HERE
*
* LD      =20
*
* JUMP   XDLAY          DELAY 100 MS
*
* DC      XDLAY-X'04A3'
* LOC      TDATA5
TDATA5 EQU X'04A4'
*
* NOP
*
* NOP
*
* NOP
*
* PATCH SPECIAL ACTION HERE
*
*-----
*
* NO SPECIAL ACTION
*
* LOC      TDATA3
TDATA3 EQU LD X'04A8'
*
* LD      X'38'
*
* DEC    ST             X'38'          DEC CHAR COUNT
*
* AND     =16
*
* C       =0
*
* LD
*
* BEF    TDATA4
*
* OR     X'3D'          PUT IN LEADING SPACES 1/3 OF TIME
*
* LOC      TDATA4
TDATA4 EQU X'04B6'
*
* BLC    EOF
*
* SEND   0
*
* BART   TXYZ          MORE SPACE IN BUFFER
*
* WAIT
*
* B      TXYZ
*
*-----
*
* MINIMUM LOOP
*
* LOC      TDATA9
TDATA9 EQU LD X'04BE'
*
* LD      BLC          EOF
*
* SEND   0

```

```

006098      *      BART  TDAT9
006101      1335  E5FB      *
006102      *      WAIT
006103      *      B      TDAT9
006104      1336  01E0
006107
006108
006109
006110
006111
006112
006113      04C7
006114
006115      1337  F851
006116
006117      1338  3950
006118
006119      1339  0393
006120
006121      133A  0F51
006122
006123      133B  2450
006124
006125      133C  1952
006126
006127      133D  3BE1
006128
006129
006130
006131
006132
006133
006134
006135
006136
006137
006138
006139
006140
006141
006142
006143
006144
006145
006146
006147      133E  0B00
006148
006149
006150      133F  0000
006151
006152
006153      1340  0050
006154
006155      1341  2494
006156
006157      1342  10E0
006158
006159
006160
006161      1342  10E0
006162
006163      04DF
006164
006165
006166      1343  0950
006167
006168      1344  3794
006169
006170      1345  1093
006171
006172
006173
006174
006175
006176
006177
006178
006179
006180
006181
006182
006183
006184      1346  3054
006185
006186      04E7
006187
006188
006189      1347  2437
006190
006191      1348  5039
006192
006193
006194
006195      04EA
006196
006197
006198
006199
006200
006201      1349  6050
006202
006203      134A  3805
006204
006205      134B  5138
006206
006207
006208
006209
006210
006211
006212
006213
006214
006215      134C  503F
006216
006217      134D  9340
006218
006219      134E  E218
006220
006221
006222
006223
006224
006225      134F  E600
006226
006227
006228
006229
006230
006231
006232
006233
006234
006235
006236
006237      04F9
006238
006239      1350  8A50
006240
006241      1351  1B92
006242
006243      1352  FFF1
006244
006245
006246
006247
006248
006249      1353  3F50
006250
006251      1354  18F2
006252
006253
006254
006255
006256
006257      1355  3B50
006258
006259      1356  3F93
006260
006261      1357  8051
006262
006263      1358  3F07
006264
006265
006266
006267
006268
006269
006270
006271
006272
006273
006274
006275
006276      1359  0707

```

```

*      BART  TDAT9
*      WAIT
*      B      TDAT9
-----
*
* END OF FRAME ROUTINE
*
*      LOC      EOF
EOF EQU X'04C7'
*      ST      X'39'      TEMP STORAGE
*
*      LD      3      GET LAST BYTE INFO
*
*      AND     =X'F'   STRIP TO BYTE SIZE AND PARTIAL BYTE
*
*      ST      X'24'
*
*      LD      X'19'   GET CONTROL FRAME NUMBER
*
*      C      X'3B'   CHECK WITH ACTUAL
*
*      BET     EOF1   B = IS CONTROL FRAME
*
* IF IT IS A CONTROL FRAME PICK UP LAST CONTROL WORD
* FROM X'37'. OTHERWISE SEND ONLY EOF + BYTE SIZE.
*
*      NOP
*
*      NOP
*
*      NOP
*
*      LD      X'24'   GET BYTE SIZE
*
*      OR      =X'10'  SET EOF BIT
*
*      B      EOF2
*
*      LOC      EOF1
EOF1 EQU X'04DF'
*      LD      X'37'   GET XMIT CONFIG INFO
*
*      OR      =X'10'  PUT IN EOF
*
*      AND     =X'30'  STRIP OFF BYTE SIZE
-----
*
* SEND OUT CLOSE FLAG
*
*      OR      X'24'   PUT IN BYTE SIZE FOR LAST
*
*      LOC      EOF2
EOF2 EQU X'04E7'
*      EQU     X'04E7'  TELL HDLC TO SEND CLOSE FLAG
*      OUT     7
*
*      LD      X'39'
*
*      LOC      TDAT10
TDAT10 EQU X'04EA'
*      EQU     X'04EA'
*      SEND   0
*      LD      X'38'
*
*      DEC
*
*      ST      X'38'   DEC CHAR COUNT
*
* TEST TO SEE IF "DELAY" XMIT BIT IS ON.
*
*      LD      X'3F'
*
*      AND     =X'40'  GET DELAY XMIT BIT
*
*      BZT     TDAT12  B = NOT SET
*
* GIVE DELAY FOR TIME FOR XMIT TO OCCUR ID "DELAY XMIT" BROKEN
*
*      JUMP   XDLAY1
*
* CHECK RCV FRAME + CHAR COUNT TO INSURE NO CHAR HAVE BEEN
* RECEIVED WHILE "DELAY XMIT" WAS ON.
*
*      LOC      TDAT13
TDAT13 EQU X'04F9'
*      EQU     X'04F9'
*      LD      X'1B'   GET RCV FRAME COUNT
*
*      C      =X'FF'
*
*      BEF     TONER   B = ERROR, FRAME RCV'D WITH
*
*      LD      X'18'   XMIT DELAY BIT ON
*
*      BZF     TONER   GET RCV CHAR COUNT
*
*      LD      X'3F'   B = ERROR, CHAR RCV WITH
*
*      AND     =X'80'  XMIT DELAY BIT ON
*
*      ST      X'3F'   GET CONFIGURATION WORD
*
*      SR
*
*      SR

```

```

006277          *          OUT      6
006280          *
006281          *-----*
006282          *
006283          * FIRST WAIT AFTER SENDING EOF, LAST DATA CHAR
006284          *
006285          *          LOC      TDAT12
006286          *          EQU      X'050D'
006287          *          WAIT
006288          135A 3601
006289          *
006290          *          IN      5
006293          *          ST      X'2E'
006294          135B 2551
006297          *          AND      =X'1'          STRIP TO UNDERRUN BIT
006298          135C 2E93          *          BZF      XUR          BRANCH IF UNDERRUN
006301          *
006302          135D 01F2          *          LOC      TDAT6
006305          *          EQU      X'0515'
006306          *          GNB
006307          *
006308          135E 4602          *          LD      =0
006309          *
006312          135F 9000          *          ST      X'38'          CLEAR CHAR COUNT
006313          *
006316          1360 5138          *-----*
006317          *
006318          * TEST IF CONTROL FLAG IS SET TO TURN ON RCV AFTER
006319          * A SPECIFIED XMIT FRAME.
006320          *
006321          *          LD      X'3E'          GET FLAG
006322          1361 503E          *          AND      =4          STRIP TO DELAY TURN ON OF RCV
006323          *
006324          1362 9304          *          BZT      TDAT11
006325          *
006326          1363 E212          *          TURN ON RCV
006327          *
006328          *          LD      X'19'          ACTION FRAME
006329          *          C      X'3B'          COMPARE WITH ACTUAL
006330          *          BEF      TDAT11          B = NOT THIS FRAME
006331          *          LD      =20          DELAY TO ALLOW LAST CHAR'S
006332          *          BS      CCPDLY          TO GET TO RCVR
006333          *          LD      X'14'          LR2 CONTROL
006334          *          OR      =2          PUT IN RCV BIT
006335          *          ST      X'14'
006336          *
006337          *          OUT      2
006338          *-----*
006339          *
006340          * TEST FOR LAST FRAME XMITTED.
006341          *
006342          *          LOC      TDAT11
006343          *          EQU      X'0531'
006344          *          BLBF      TDAT7
006345          136C 32F4          *-----*
006346          *
006347          * END OF TRANSMISSION - SHUT OFF XMIT
006348          *
006349          *          LOC      TEND
006350          *          EQU      X'0533'
006351          *          LD      X'14'          GET LR2 CONTROL
006352          136D 1050          *          AND      =X'FE'          STRIP OFF XMIT ON
006353          *
006354          136E 1493          *          ST      X'14'
006355          *
006356          136F FE51          *          OUT      2
006357          *
006358          1370 1432          *          JUMP      LAST1
006359          *
006360          1371 E6FE          *-----*
006361          *          LOC      TONER
006362          *          EQU      X'053D'
006363          *          LD      =14          ERROR CODE
006364          *          JUMP      EREND          ERROR, DELAYED XMIT DIDN'T FUNCTION
006365          *
006366          *          DC      EREND=X'0541'
006367          *-----*
006368          *
006369          * INTERFRAME STATE
006370          *
006371          *          LOC      TDAT7
006372          *          EQU      X'0542'
006373          *          LD      X'3E'          GET FLAG
006374          1375 503E          *          AND      =X'20'          CHECK FOR INTERFRAME DELAY
006375          *          BZT      TDAT8          B = NO DELAY
006376          1376 9320          *
006377          1377 E210          * INTERFRAME DELAY ROUTINE. NOTE THAT 'PAUSE FEATURE' OF MLCP
006378          * PREVENTS THIS DELAY FROM "HOGGING" OUT RCV.
006379          *          LD      =20
006380          *          BS      CCPDLY          DELAY 100 MS
006381          *
006382          * ROUTINE TO ISSUE RCV RE-SYNC IF SPECIFIED.
006383          * THIS OCCURS AFTER DELAY BETWEEN FRAMES
006384          *
006385          *          LD      X'3E'          GET FLAG
006386          137A 503E          *
006387          *
006388          *
006389          *
006390          *
006391          *
006392          *
006393          *
006394          *
006395          *
006396          *
006397          *
006398          *
006399          *
006400          *
006401          *
006402          *
006403          *
006404          *
006405          *
006406          *
006407          *
006408          *
006409          *
006410          *
006411          *
006412          *
006413          *
006414          *
006415          *
006416          *
006417          *
006418          *
006419          *
006420          *
006421          *
006422          *
006423          *
006424          *
006425          *
006426          *
006427          *
006428          *
006429          *
006430          *
006431          *
006432          *
006433          *
006434          *
006435          *
006436          *
006437          *
006438          *
006439          *
006440          *
006441          *
006442          *
006443          *
006444          *
006445          *
006446          *
006447          *
006448          *
006449          *
006450          *
006451          *
006452          *
006453          *
006454          *
006455          *

```

```

006456
006459 137B 9340
006460
006463 137C E206
006464
006467
006468
006469 137D 3355
006472
006473 137E 3E51
006476
006477 0557
006478
006479 137F 3EE6
006482 1380 FEF5
006483
006484
006485
006486
006487
006488 055A
006489
006492 1381 5030
006493
006495 1382 9420
006496
006500 1383 5130
006501
006504 1384 5120
006505
006508 1385 E0B2
006509
006510
006511
006512
006513
006514
006515 0564
006516
006519 1386 5139
006520
006521
006522 0566
006523
006526 1387 9000
006527
006528
006529 0568
006530
006531
006532 1388 0593
006535
006536 1389 FF93
006539
006540 138A FF93
006543
006544 138B FFF2
006547
006548 138C F850
006551
006552 138D 3905
006553
006556 138E 5139
006557
006560 138F F2EF
006561
006562
006563 1390 0600
006564
006565
006566
006567 1391 0000
006568
006569
006570
006571
006572 057D
006573
006574 1392 00F0
006577
006578 1393 E6E6
006581 1394 FF27
006582
006583
006584 0582
006585
006588 1395 F0E1
006589
006592 1396 E6FF
006593
006594 1397 7300
006595
006596
006597 1398 0000
006598
006599 1399
006600
006601 1399 0000
006602
006603
006604
006605
006606
006607
006608
006609
006610
006611
006612
006613
006614 0200
006615 139A
006616
    
```

```

* AND =X'40' RCV RE=SYNC
* BZT TDAT8 B = NU RE=SYNC
* OUT 3 RE=SYNC
* XOR X'3E' RESET BIT
* ST X'3E' STORE BACK
* LOC TDAT8
TDAT8 EQU X'0557'
* JUMP SFRM
DC SFRM-X'0559'
* -----
* * UNDERUN OCCURED
* *
* * LOC XUK
XUR EQU LD X'055A'
* X'30'
* OR =X'20'
* ST X'30' STORE UNDERUN BIT IN CP STATUS
* ST X'20' SET BIT IN STATUS
* B TDAT6
* -----
* * DELAY ROUTINE
* *
* * CCPDLY LOC CCPDLY
CCPDLY EQU ST X'0564'
* X'39' STORE COUNT
* CCPD1 LOC CCPD1
CCPD1 EQU LD X'0566'
* =0
* CCPD2 LOC CCPD2
CCPD2 EQU DEC X'0568'
* AND =X'FF'
* AND =X'FF'
* AND =X'FF'
* BZF CCPD2
* LD X'39'
* DEC
* ST X'39'
* BZF CCPD1
* RET END OF ROUTINE
* NOP
* NOP
* NOP
* -----
* * LOC XDLAY
XDLAY EQU BS X'057D'
* CCPDLY
* JUMP TDAT3
DC TDAT3-X'0581'
* * LOC XDLAY1
XDLAY1 EQU BS X'0582'
* CCPDLY DELAY
* JUMP TDAT13
* NOP
* NOP
* NOP
* * LOC $
CCP3 EQU $
* NOP
* -----
* *
* *
* * DATA SET STATUS CHANNEL PROGRAM
* *
* * ORG X'200'
* * D50 LOC D50
D50 EQU X'0200'
* * CCP4 EQU $
* * NOP
    
```

```

006617 *      NOP
006618 139A 0000 *      NOP
006619 *      NOP
006620 *      NOP
006621 139B 0000 *      NOP
006622 *      NOP
006623 *      NOP
006624 139C 0000 *      LD      =0
006625 *      LD      =0
006628 139D 9000 *      BS      GSTAT      OUTPUT CONTROL AND INPUT STATUS
006629 *      BS      GSTAT
006632 139E F032 *      C        =0
006633 *      C        =0
006636 139F 9200 *      BET     DS1
006637 *      BET     DS1
006640 13A0 E103 *      BS      BADS      BAD STATUS
006641 *      BS      BADS
006644 13A1 F025 *      BS      BADS
006645 *      BS      BADS
006646 *      DS1
006647 *      DS1   LOC     DS1
006648 *      DS1   EQU     X'0210'
006649 *      DS1   WAIT
006650 *      DS1   LD      =X'80'
006651 13A2 0190 *      BS      GSTAT      OUTPUT CONTROL AND INPUT STATUS
006654 13A3 80F0 *      BS      GSTAT
006655 13A3 80F0 *      C        X'3E'      10 FOR BHCLA1, 90 FOR BHCLA2
006658 13A4 2752 *      C        X'3E'
006659 13A4 2752 *      BET     DS2
006662 13A5 3EE1 *      BET     DS2
006663 13A5 3EE1 *      BS      BADS      BAD STATUS
006666 13A6 03F0 *      BS      BADS
006667 13A6 03F0 *      BS      BADS
006670 *      DS2
006671 *      DS2   LOC     DS2
006672 *      DS2   EQU     X'021B'
006673 *      DS2   WAIT
006674 *      DS2   LD      =X'40'
006676 13A7 1A01 *      LD      =X'40'
006679 13A8 9040 *      BS      GSTAT      OUTPUT CONTROL AND INPUT STATUS
006680 *      BS      GSTAT
006683 13A9 F01C *      C        X'3F'      ED FOR BHCLA1, 60 FOR BHCLA2
006684 *      C        X'3F'
006687 13AA 523F *      BET     DS3
006688 *      BET     DS3
006691 13AB E103 *      BS      BADS      BAD STATUS
006692 13AB E103 *      BS      BADS
006695 13AC F00F *      BS      BADS
006696 13AC F00F *      BS      BADS
006697 *      DS3
006698 *      DS3   LOC     DS3
006699 *      DS3   EQU     X'0226'
006700 *      DS3   WAIT
006701 *      DS3   LD      =X'3C'
006702 13AD 0190 *      BS      GSTAT      OUTPUT CONTROL AND INPUT STATUS
006705 13AE 3CF0 *      BS      GSTAT
006706 13AE 3CF0 *      C        =X'0'
006709 *      C        =X'0'
006710 13AF 1192 *      BET     DS4
006713 *      BET     DS4
006714 13B0 00E1 *      BS      BADS      BAD STATUS
006717 *      BS      BADS
006718 13B1 03F0 *      BS      BADS
006721 *      BS      BADS
006722 *      DS4
006723 *      DS4   LOC     DS4
006724 *      DS4   EQU     X'0231'
006725 *      DS4   WAIT
006726 13B2 0401 *      B        DS0
006727 *      B        DS0
006730 13B3 E0CD *      B        DS0
006731 *      B        DS0
006732 *      B        DS0
006733 *      B        DS0
006734 *      B        DS0
006735 *      B        DS0
006736 13B4 5010 *      B        DS0
006737 *      B        DS0
006740 *      B        DS0
006741 *      OR      =X'4'
006744 13B5 9404 *      OR      =X'4'
006745 *      OR      =X'4'
006748 13B6 5110 *      ST      X'10'
006749 *      ST      X'10'
006750 *      RET
006751 *      RET
006752 *      ROUTINE TO OUTPUT CONTROL AND INPUT STATUS
006753 *      ROUTINE TO OUTPUT CONTROL AND INPUT STATUS
006754 *      GSTAT
006755 *      GSTAT LOC     GSTAT
006756 *      GSTAT EQU     X'023B'
006757 *      GSTAT OUT
006758 *      GSTAT 2
006759 13B7 0632 *      GSTAT 2      OUTPUT CONTROL
006760 *      GSTAT 2
006761 *      NOP
006762 13B8 0090 *      LD      =0
006763 *      LD      =0
006765 *      STDLY LOC     STDLY
006766 *      STDLY EQU     X'023F'
006767 *      STDLY DEC
006768 13B9 0005 *      STDLY X'023F'
006769 *      STDLY X'023F'
006772 13BA F2FE *      BZF     STDLY      DELAY
006773 *      BZF     STDLY
006774 *      BZF     STDLY
006775 *      BZF     STDLY
006776 13BB 0000 *      NOP
006777 *      NOP
006778 *      NOP
006779 *      NOP
006780 *      IN      5
006781 *      IN      5
006783 13BD 2551 *      ST      X'2E'      INPUT STATUS
006784 *      ST      X'2E'
006787 *      AND     =X'F7'      STRIP OFF ADAPTER READY

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990	991C	1017	1018C	1042	1043C	1068	1069C	1109	1110C
1113	1154b	1139	1140C	1142	1157C	1173	1191C	1195	11245
1246C	1252	1251	1250C	1257C	1258	1290	1234C	1292	1293C
1326	1328C	1329	1330C	1331	1333	1346	1347C	1336C	1337C
1338C	1340	1340	1341C	1342	1343B	1351	1352C	1440	1441C
1442	1443C	1481	1482C	1483	1484C	1521	1522C	1523	1524C
1571	1572C	1573	1574C	1575	1577	1578	1580	1581C	1582
1583C	1584	1585	1586C	1587	1588B	1589	1590C	1592	1593
1594C	1595	1597C	1598	1599C	1713	1714C	1715	1716C	1717
1755C	1789	1790C	1791	1792C	1816	1817C	1818	1819C	1843
1844C	1845	1846C	1870	1871C	1872	1873C	1912C	1915	1918
1920	1922	1924	1926	1928C	1935	1936C	1947	1948C	1974
2241	2242C	2243	2244C	2276	2277C	2278	2279C	2311	2312C
2313	2314C	2350	2351C	2353	2354B	2386	2387C	2391	2392
2395	2396C	2397	2398C	2399	2404C	2438	2439C	2440C	2445
2447	2449	2451	2453	2456	2463	2466C	2476C	2501	2504
2505B	2589	2591	2603	2604	2618	2619B	2639	2641	2657
2658	2665	2667	2677C	2679	2680	2681C	2709	2710	2716
2717	2741	2742	2743C	2744	2745C	2751	2753	2754C	2764
2765C	2771	2792	2793	2794	2795B	2796C	2797C	2810	2811C
2815	2817C	2818C	2847	2848C	2902	2906C	2907	2922	2923
2971	2973	2978	2979C	2980	2981C	3059	3060B	3064	3066B
3068C	3093	3094C	3121	3122B	3123	3125	3132	3134B	3137
3138	3139C	3143	3144B	3145	3146B	3147	3148	3149	3150C
3152	3153B	3154	3155	3156C	3248	3249	3319	3323	3324C
3339	3340C	3341C	3343	3344	3387	3388	3406	3407	3412
3413C	3416	3417	3418C	3420C	3422	3423	3424C	3425	3426
3427C	3428B	3432	3433	3434C	3441	3442	3444	3445	3446C
3448	3451	3452C	3467C	3470C	3472	3480	3484C	3492	3493
3494	3495	3515	3517C	3521C	3549	3552	3553	3556	3557
3562C	3566C	3569	3571C	3576C	3577	3579C	3581C	3586	3587C
3602C	3611	3613C	3620C	3636	3668	3671	3680B	3682C	3684
3686	3696	3697	3699C	3699C	3700	3701C	3703	3704	3705C
3706	3707	3708C	3709	3710	3711C	3712	3713	3714	3715
3716C	3717	3718	3719	3720C	3722	3723C	3724	3726	3727
3727	3728	3729C	3730	3731C	3732	3733C	3734	3736	3737C
3744	3745C	3752	3754C	3755	3764	3765C	3766	3767	3768C
3779C	3780	3781C	3782	3783	3789	3790C	3791	3792	3793
3813	3815	3819	3883C	3884C	3885	3886C	3887	3888	3889C
3900C	3909	3910C	3916	3918	3926	3927C	3928	3929	3930
3941C	3952	3955	3956C	3961C	3962C	3990	3991C	3992	3993C
3994C	3998	3999C	4000	4001C	4009	4021C	4022	4030C	4031C
4037	4038C	4039	4052C	4053	4129	4130	4136	4137	4140C
4142C	4143C	4144	4145C	4185	4186	4225	4226C	4227	4228
4259C	4261	4262C	4307C	4310	4313	4314	4316	6813	6814C
4713C	720	785	791	793C	794B	1166	1167	1255	1259B
1323C	1326	1337	1339	1342	1348C	1371	1568C	1571	1582
1584	1587	1592	1595	1596C	1597C	1600C	1601C	1602	1606C
1628	1915	1916B	1917C	1918	1919C	1920	1921C	1922	1923C
1924	1925C	1926	1927C	1939	1940	1943	1944C	1945	1946C
2393	2394	2395	2400	2401B	2402	2443	2457C	2659	2660
2661	2662	2663	2678	2685B	2849C	2856C	2860C	2866C	2896
2902	2908C	2972C	2975	2976C	3044C	3045	3046C	3061C	3064
3084	3120C	3125	3127	3128	3129C	3133C	3135C	3136C	3320
3321	3405	3409	3436B	3469C	3470C	3471C	3484C	3495	3496B
3497	3503B	3556	3560	3561	3562C	3565	3566C	3569	3570
3571C	3574	3575	3576C	3585	3586C	3593	3594	3595	3599C
3601	3602C	3609	3612	3616C	3626	3627	3628	3629	3633
3634	3636	3639	3641C	3642	3663	3672C	3673	3674	3701C
3702	3740	3741	3751C	3767	3768C	3769C	3771	4014	4015B
4022	4023B	4024C	4050	4051C	4056	4057C	4183	4184C	4261
4263B	4287	4296							
839C	1119C	1126B	1127C	1134C	1138	1139	1141	1181C	1182B
1183C	1189	1197	1322C	2576C	2583C	2615C	2687	2688	2696C
2726C	2772	2773	2782C	2790C	2804	2805C	2816	2817C	2820
2821	2833C	2840C	2935	2936	2958C	2969C	2970	2971	2977
2994C	3010C	3019C	3028C	3044C	3049	3082C	3091	3092	3158
3220C	3280C	3329C	3334C	3381B	3382C	3390C	3402C	3403	3424C
3440	3482C	3485	3486C	3490	3500	3516	3517C	3518	3519C
3520	3521C	3522	3523C	3808	3810	3812	3817C	3821	3832
3833	3834	3953B	3954C	3992	4008C	4329	4330	4335	4341
4347									
699	700C	789	791	792	1180	1185	1333	1334C	1377
1578C	2445	2446C	2447	2448C	2449	2450C	2451	2452C	2453
2454C	2455B	2456	2457C	2661	2662	2663	2665C	2798	2799
2800C	2862C	2867	2869	2872	2876	2879	2913C	3383C	3390C
3291	3294C	3527	3594	3595C	3597	3607	3608	3612	3624
3625B	3629	3630	3631C	3638	3639	3640	3641C	3669	3670
3680C	3682C	3685	3686	3770C	3833	3842	3844	3866	3868
3898	3900	3911	3913	3979	3981	4010	4011	4043	4044
4046	4048C	4061	4146	4151	4161	4163	4180	4181C	4223
4224C	4242	4244	4267	4269	4289	4291	4294	4296	4307C
4308	4309C	4310	4311	4312	4315	4316	4317C	4339	4346
727	728	740C	749	752	753	754C	756	765C	776
1172	1174	1185	1191B	2594	2606	2622	2628	2636	2651
2667	2668	2680	2682	2720	2737	2747	2748	2760	2766C
2767	2771	2860C	2861	2885	2890	2918	2988	2997	3012
3022	3031	3076	3085	3485	3505	3508	3611	3614	3615C
3619	3620C	3634	3635C	3673	3679	3864	3865	3868	3903
3900	3945C	3948	3960C	3967	3968	3969C	3971	3981	4029
4030C	4045	4046	4059	4062B	4151	4153C	4163	4200	4203
4244	4286	4291	4334C	4335	4336C	4340C	4341	4342	4343
4349									
688C	692C	698C	1173	1174	1190C	2590C	2593	2594	2602
2606	2622	2627	2628	2628	2635	2636	2652	2665	2666
2668	2679	2682	2707C	2715	2719	2720	2734	2736	2737
2761	2762	2767	2852C	2854	2857C	2862	2864C	2865C	2871C
2875	2878	2881	2884	2885	2889	2890	2899	2900	2901C
2903	2904	2919	2927C	2929	2987	2988	2996	2997	3009
3012	3021	3022	3030	3031	3075	3076	3084	3085	3505
3506	3510B	3947C	3948	4328C					
690	691	693	694C	695	696	697C	778	2653	2708
2712	2717	2746	2750	2751	2851	2853	2928	2931	2932
3506	3509	3511	3514C	3516	3664	3665	3668	4331	4332
4333	4334C	4337	4338	4339	4340C	4344	4345	4346	4347
4348									
2694	ABUND	1616b							
2736	ABUND1	2733B							
2697	ABUND2	2774b							
2703	ABUND3	2699b							
2720	ABUND4	2718b							
2737	ABUND5	2735b							
2700	ABUND6	2698b							

3097	RTSFLG	4388	3069C	3145	3151C																
812	RUT1	808B																			
836	RUT4	810B																			
802	RUTBG	807	823B	817B	832B																
824	RUTBGA	821B																			
828	RUTBGB	825B																			
819	RUTEND	815B																			
720	SALT	774B																			
6830	SAV	3116C	3357	3544C	3644	3662C	3676														
6824	SAV1	3807C	3820	4128C	4135C	4156	4178C	4206	4220C	4241C	4249										
		4306C	4318	4327C	4351																
6825	SAV2	3466C	3474	3839C	3849	3863C	3872	3881C	3908C	3921	4257C										
		4264	4284C	4300																	
6826	SAV3	3978C	3984																		
6827	SAV4	3939C	3972																		
6828	SAV5	3989C	3997C	4058	4122																
6829	SAVMAJ	1111C	1115	2575C	2580	2695C	2700	2781C	2787	2831C	2837										
		2957C	2963																		
4178	SDATA	1128B	3221B	3229B																	
6800	SDB	955	967	981	1007	1019	1033	1059	1085	1251	1274										
		1308	1367	1463	1467	1504	1508	1544	1548	1624	1737										
		1741	1772	1810	1837	1864	1891	1966	1970	2262	2266										
		2270	2297	2301	2305	2332	2336	2340	2362	2376	2408										
		2424	2475	2497	2665	2679	3404	3468	3485	3517C	3519C										
		3521C	3523C	3545	3673																
		853C	3932C																		
4419	SEC	1145B	1148B	1154B	3164B	3942B	3957B	3963B	4025B												
4257	SEFLCT	6480	6481	6482																	
5805	SEFRM	5856	5857	5859																	
5872	SEFRM1	5834	5835	5843	5868	5869	5874														
5883	SEFRM2	5818	5819	5825																	
5841	SEFRM3	3449B																			
3455	SEFRM4																				
3436	SEFRM5																				
3450	SEFRM6																				
4395	SPACE	4050																			
2825	SPDM5G	2808																			
2357	SPEED1	2354B																			
2362	SPEED2	2358B	2380																		
2797	SPEED3	2795B																			
2350	SPEED-W	861B																			
4412	SPEED	865	2400	2818C	2922																
4362	SPFLG	869C	872C	921																	
4189	SPRG1	4181C																			
4190	SPRG2	4184C																			
4197	SPRG3	4236B																			
4182	SPRG4																				
4188	SPRG5	4182C																			
4206	SPRG7																				
2780	SPTS	2367B																			
2783	SPTS1	2822B																			
2790	SPTS2	2786B																			
2787	SPTS3	2784B																			
4360	STAT	4153C																			
5442	STAT1	5427	5428	5430																	
5468	STAT2	5453	5454	5456																	
5494	STAT3	5479	5480	5482																	
5520	STAT4	5505	5506	5508																	
5531	STAT5	5526	5527	5528																	
5416	STAT6	5229	5230	5231	5241																
2948	STBL	2841	2860C																		
6766	STDLY	6770	6771	6772																	
3630	STDTA	3625B																			
850	STLUOP	666B	843B	856																	
662	STOP	674C	851C	933C	934B	937B															
3459	STPT	3447																			
4402	STPTR																				
665	STRT	667	6831																		
1290	SUP-G	883B																			
3489	TACC	3733C																			
1428	TACBSZ	1532C	1577	2661	3495	3629															
3590	TCEB52	3532B																			
3601	TCEB1	3583B																			
6198	TCEB10	3597B	3598B	6003																	
6377	TDAT11	6331	6332	6333	6346	6347	6348														
6286	TDAT12	6224	6225	6226																	
6237	TDAT13	6590	6591	6592	6594																
6001	TDAT2	5984	5985	5987																	
6036	TDAT3	5956	5957	5960	5969	5970	5975	6579	6580	6581											
6065	TDAT4	6057	6058	6059																	
6024	TDAT5	6012	6013	6015																	
6306	TDAT6	6506	6507	6508																	
6425	TDAT7	6380	6381	6389																	
6477	TDAT8	6435	6436	6437	6461	6462	6463														
6088	TDAT9	5937	5938	5947	6099	6100	6101	6105	6106	6115											
5925	TDATA																				
4386	TEMP	2812C	2813	3402C	3440	3667C	3681	3683	4048C	4049											

CURRENT	29A7	
*LOC DEFS		
ZHCOMM	0000	
*DCMS3	0000	REV A
ZHPFR	0000	
ZHTSA	0002	
ZHNTSA	0010	
ZHRTCL	0014	
ZHRTCC	0015	
ZHRTCL	0016	
ZHWDTC	0017	
ZHMERK	001F	
ZH1AFb	0020	
ZHTH29	0063	
ZHTH2b	0064	
ZHTH27	0065	
ZHTH26	0066	
ZHTH25	0067	
ZHTH24	0068	
ZHTH23	0069	
ZHTH22	006A	
ZHTH21	006B	
ZHTH20	006C	
ZHTH19	006D	
ZHTH18	006E	
ZHTH17	006F	
ZHMEMP	006F	
ZHTH16	0070	
ZHLEKK	0070	
ZHTH15	0071	
ZHNRES	0071	
ZHTH14	0072	
ZHPMEM	0072	
ZHTH13	0073	
ZHP-OP	0073	
ZHTH12	0074	
ZHTH11	0075	
ZHTH10	0076	
ZHTH9	0077	
ZHTH8	0078	
ZHTH7	0079	
ZHTH6	007A	
ZHOVFL	007A	
ZHTH5	007B	
ZHOP-N	007B	
ZHTH4	007C	
ZHTH3	007D	
ZHSC-N	007D	
ZHTH2	007E	
ZHTRC	007E	
ZHTH1	007F	
ZHMCL	007F	
ZHISA2	0080	
ZHIVBS	0080	
ZHIVBS	0080	
*ZV\$TH	2083	
ZV\$TD	2088	
ZV\$IH	2089	
ZV\$TH2	20AB	
*ZV\$E	20D3	
ZV\$F	20D3	
*ZV\$IH	20E1	
ZV\$ID	20E6	
ZV\$IH	20E1	
ZV\$IAU	20EB	
ZV\$--2	2103	
ZV\$--3	2115	
*ZV\$PCH	217A	
ZV\$PCH	217A	
*ZV\$T	227C	REV. 5.0
ZV\$UC	2299	
ZV\$IC	2285	
ZV\$T	227C	
ZV\$U	228E	
*ZV\$IA	22AD	REV. 7
ZV\$IA	22B0	
ZV\$ABF	2361	
ZV\$AKG	235F	
ZV\$--1	231C	
ZV\$IAV	22AE	
*ZV\$FK	236C	
ZV\$FI	238E	
ZV\$FK	236C	
ZV\$FS	23b1	
ZV\$FRA	23bE	
ZV\$FRX	23bF	
ZV\$FRK	2383	
ZV\$FKb	23C0	
ZV\$FRM	23bD	
*ZV\$C	23C3	REV. 5
ZV\$C	23C3	
ZV\$CU	23E6	
*ZV\$ER	23F7	REV. 5.0
ZV\$ER	23F7	
ZV\$TA	2423	
ZV\$--U	240A	
*ZV\$BRK	2467	
ZV\$BRK	2467	
*ZV\$GP	2481	
ZV\$GP	2481	
ZV\$--4	24A1	
*ZV\$HA	24AD	
ZV\$HA	24AD	
ZV\$H2	24B7	
ZV\$HS	24B2	
*ZV\$HD	24E6	
ZV\$HD	24E6	
*ZV\$RD	2518	REV. 7
ZV\$RD	2518	
ZV\$IZ	2552	
ZV\$TTY	252B	
ZV\$BKF	2540	
ZV\$SV1	26ED	

ZV\$SV3	270D
ZV\$AF	2529
ZV\$SV2	26FD
ZV\$OTP	25BF
ZV\$TID	252A
ZV\$CF2	2534
ZV\$TK	2530
ZV\$RAR	2531
ZV\$ST1	2535
ZV\$RCC	2536
ZV\$BUD	252C
ZV\$ULB	2538
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T+V