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* TITLE CMCS1,*REV B*
* CACHE MEMORY TEST
* PART NO.
* CMCX1 60134336-002
* CMCS1 60134337-002
* CMCL1 60134338-002
*
* DESCRIPTION
* -----
* THIS T & V PROGRAM VERIFIES PROPER OPERATION OF THE LEVEL-6 CACHE MEMORY
* SUBSYSTEM. PROVIDES A FIRST LEVEL OF DIAGNOSIS WHEN FAILURES ARE DETECTED
* AND MAKES FACILITIES AVAILABLE TO SUPPORT EXTENSIVE PROBLEM INVESTIGATIONS
*
* NOTE: FOR MAIN MEMORY SIZE GREATER THAN 64K WORDS, THE CMCL1(LAF)
* PROGRAM MUST BE USED. THE SYSTEM SHOULD BE SET TO LAF MODE.
* OTHERWISE, CMCS1 WILL ONLY USE UP TO 64K LOCATIONS.
*
* THE SUBSYSTEM OPTIONS SUPPORTED BY THIS PROGRAM ARE:
*
* BCHE006A CACHE MEMORY CONTROLLER
* BCHE002A CACHE ARRAY PAC (2K WORDS)
* BCHE004A CACHE ARRAY PAC (4K WORDS)
*
* REVISION HISTORY
* -----
* A DEC 1977 ORIGINAL RELEASE
* B APR 1978
*
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* PROPRIETARY TO AND THE EXCLUSIVE PROPERTY OF HONEYWELL INFORMATION SYSTEMS
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* IT MUST NOT BE REPRODUCED IN WHOLE OR IN PART; AND IT SHALL NOT BE DIS-
* CLOSED TO ANY OTHER PARTY WITHOUT THE PRIOR WRITTEN CONSENT OF HONEYWELL.
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000039 / PROGRAM PREPARATION:
000040 * -----
000041 * THE ROOT SOURCE OF THIS PROGRAM, AFTER THE ADDITION OF THE APPROPRIATE
000042 * TITLE AND END STATEMENTS, WAS PROCESSED BY THE HOST RESIDENT ASSEMBLER
000043 * TO CREATE EITHER SHORT OR LONG ADDRESS FORM (SAF OR LAF) OBJECT TEXT
000044 * AND LISTING. THE OBJECT TEXT WAS FURTHER PROCESSED BY THE HOST
000045 * RESIDENT LINKER USING THE APPROPRIATE CONSULE ZV$LIB LIBRARY TO CREATE
000046 * A PUNCH SEGMENT CONTAINING AN EXECUTABLE MODULE. THE ASSEMBLY LISTING
000047 * WAS AUGMENTED WITH CRJSS REFERENCE DATA, PLUS THE LOAD MAP FROM THE
000048 * LINKER TO CREATE A LIST SEGMENT.
000049 *          ROUT      SAF      LAF
000050 *          ---      ---      ---
000051 *          NAME      CMCX1      CMCS1      CMCL1
000052 *          DOCUMENT  60134336-002  60134337-002  60134338-002
000053 *
000054 * DISTRIBUTION
000055 * -----
000056 * THE ELEMENTARY ITEMS SUBMITTED TO THE T & V PROGRAM DISTRIBUTION CENTER
000057 * WERE THE EXECUTABLE LINKED IMAGES, ON DISKETTE, OF PPSI AND PPPLI, AND
000058 * MAGNETIC TAPE IMAGES OF THE AUGMENTED LISTINGS.
000059 *
000060 * REPRODUCTIONS OF THE EXECUTABLE LINKED IMAGES MAY BE AS DUPLICATE CARD
000061 * DECKS OR AS A MEMBER OF A MULTIPLE MEMBER FILE. IN THE MOST FREQUENT
000062 * CASE, IT WILL BE FOUND AS MEMBER "SW" (SAF) OR "LW" (LAF) WITHIN FILE
000063 * "PROGFILE" OF A DISKETTE VOLUME ENTITLED "DIAGS".
000064 *
000065 * DISTRIBUTION OF THE LISTINGS, WHICH SHOULD BE AVAILABLE IF ANY COMPLEX
000066 * MAINTENANCE OR REPAIR IS TO BE PERFORMED, IS NORMALLY AS A PRINTED COPY.
000067 *
000068 * ROUTINE DEMONSTRATION
000069 * -----
000070 * A MINIMUM SATISFACTORY TEST FOR NORMAL OPERATION MAY BE OBTAINED BY
000071 * RUNNING A SINGLE PASS OF THE T&V PROGRAM WITH ALL THE AVAILABLE MAIN
000072 * MEMORY LOCATIONS.
000073 *
000074 * STORAGE
000075 * -----
000076 * THIS PROGRAM REQUIRES AT LEAST 8 K WORDS OF MAIN MEMORY AND WILL USE
000077 * ALL AVAILABLE MEMORY UP THROUGH MEMORY SIZE.
000078 *
000079 * OPERATION
000080 * -----
000081 * LOAD AND START (OR RESTART) THE PROGRAM. THE PROGRAM IDENTIFICATION WILL
000082 * BE DISPLAYED ON THE CONSULE. THE INITIAL START WILL ALSO DISPLAY:
000083 *
000084 * THE ZV$LIB REVISION NUMBER
000085 * THE ADDRESS FORM (SAF OR LAF)
000086 * I/O EQUIPMENT DETECTED IN THE SYSTEM
000087 * MEMORY SIZE
000088 *
000089 * THIS DISPLAY MUST BE VERIFIED BY THE OPERATOR. THIS DISPLAY IS OMITTED
000090 * ON RESTARTS.
000091 *
000092 * THE CONSULE SEARCH RULES ARE: FIND THE CONSULE WITH THE LOWEST CHANNEL
000093 * NUMBER CONNECTED THRU AN MDC CONTROLLER. IF THERE IS NO CONSULE ON AN
000094 * MDC, THEN SEARCH FOR A TERMINAL WITH THE HIGHEST CHANNEL NUMBER ASSIGNED
000095 * TO AN ACLA ADAPTER ON AN MLC CONTROLLER. IF NO ASYNC ADAPTER IS FOUND,
000096 * THEN GO TO THE FULL CONTROL PANEL.
000097 *
000098 * THERE ARE THREE CONSULE CHANNEL OPTIONS DETERMINED BY THE VALUE OF LO-
000099 * CATION "ZV$TTY".
000100 *
000101 * IF ZV$TTY EQUALS (0000), SEARCH FOR A CONSULE.
000102 * IF ZV$TTY EQUALS (FFFF), ASSUME THERE IS NO CONSULE.
000103 * IF ZV$TTY EQUALS NEITHER (0000), NOR (FFFF), THEN IT IS THE CONSULE CHAN-
000104 * NEL NUMBER. NOTE: DEFAULT IS TO SEARCH FOR A CONSULE.
000105 *
000106 * ALL CONSULE I/O IS EVEN PARITY. IF CONSULE IS ON MLC, IT MUST BE ASYNC
000107 * AND THE BAUD RATE SET AT 1200 TO MATCH THE PROGRAM SUPPLIED RATE. IF IT
000108 * IS NECESSARY TO CHANGE THE PROGRAM BAUD RATE, THEN THE NEW BAUD RATE
000109 * CODE SHOULD BE PUT INTO LOCATION "ZV$BUD" IN HEX. THE TERMINAL BAUD RATE
000110 * MUST BE SET TO MATCH THIS NEW BAUD RATE. THE CORRECT HEX VALUE MAY BE
000111 * OBTAINED FROM THE FOLLOWING TABLE.
000112 *
000113 * -----*
000114 *          BAUD RATE TABLE          *
000115 *          *                          *
000116 *          *                          *
000117 *          *                          *
000118 *          *-----*
000119 *          *  ACLA I.D.      (2118) (2110)      (2108)  *
000120 *          *  BAUD-RATE      *                  *
000121 *          *  50             0                  1          *
000122 *          *  75             1                  2          *
000123 *          *  110            2                  3          *
000124 *          *  134            3                  4          *
000125 *          *  150            4                  5          *
000126 *          *  200            5                  ---        *
000127 *          *  300            6                  7          *
000128 *          *  600            7                  8          *
000129 *          *  900            ---                ---        *
000130 *          *  1050           8                  9          *
000131 *          *  1200           9                  ---        *
000132 *          *  1800           10 (A)             10 (A)       *
000133 *          *  2000           11 (B)             ---        *
000134 *          *  2400           12 (C)             11 (B)       *
000135 *          *  3600           ---                12 (C)       *
000136 *          *  4800           13 (D)             13 (D)       *
000137 *          *  7200           ---                14 (E)       *
000138 *          *  9600           14 (E)             15 (F)       *
000139 *          *  19200          15 (F)             ---        *
000140 *
000141 * TO MAKE ANY OF THE ABOVE CHANGES, LOAD AND HALT THE PROGRAM BEFORE EX-
000142 * ECUTION. INSERT CHANGE THEN EXECUTE. MEMORY LOCATIONS OF "ZV$TTY" AND
000143 * "ZV$BUD" MAY BE FOUND IN MAP AT END OF LISTING.
000144 * CONSULT LEVEL-6 T&V MANUAL "AW94" FOR DETAILS ON HOW TO LOAD THE TESTS.
000145 *
000146 * THE FOLLOWING IS A TYPICAL RESULT OF LOADING AND STARTING TO RUN THE
000147 * PROGRAM.
000148 *
000149 * CMCS1  CACHE MEMORY TEST
000150 * APR 1978  REV. B
000151 * ZV$LIB REV. 6.0
000152 * ZV$AF= 1 <2>

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* WDT
* CHAN DEVC ID
* 0400 DSK1 2010
* 0480 DSK1 2010
* 0580 CUR 2008
* 1200 DISC 2330
* 1280 DISC 2330
* 1300 LPT 2000
* 1380 CUNS 2019
* MEMORY LOW 00002B2D
* MEMORY HIGH 00003FFF 16K
*
* USE
* ---
* THE PROGRAM WILL FIRST CHECK THE SIZE OF THE MAIN MEMORY. IF THE MAIN
* MEMORY SIZE IS GREATER THAN 64K WORDS AND A SHORT ADDRESS FORM (SAF)
* PROGRAM IS USED, THE PROGRAM WILL PRINT:
*
* MEMORY GREATER THAN 64K, CHANGE TO CMCL1 PROGRAM, OTHERWISE, WILL
* ONLY USE UP TO 64K LOCATIONS.
*
* THE OPERATOR SHOULD SET THE SYSTEM TO LAF MODE AND RELOAD WITH THE CMCL1
* TEST PROGRAM.
*
* THE PROGRAM THEN WILL CHECK WHETHER THE CACHE IS PRESENT OR NOT. IF THE
* CACHE IS PRESENT, THE PROGRAM WILL PRINT:
*
* CACHE PRESENT
*
* IF THE CACHE IS NOT CONFIGURED, THE PROGRAM WILL PRINT:
*
* CACHE NOT PRESENT
*
* THE OPERATOR SHOULD VERIFY THE SYSTEM IF THIS MESSAGE IS PRINTED.
*
* THE PROGRAM THEN CHECK THE SIZE OF THE CACHE ARRAY. THE CACHE SIZE
* CAN BE EITHER 2K (2048) OR 4K (4096) WORDS. IF THE CACHE SIZE IS 2K,
* THE PROGRAM WILL PRINT:
*
* CACHE SIZE IS 2K
*
* IF THE CACHE SIZE IS 4K, THE PROGRAM WILL PRINT:
*
* CACHE SIZE IS 4K
*
* ERROR REPORTING
* ---
* DETECTED ERRORS ARE REPORTED AS FOLLOWS:
*
* ERR CODE AT AAAA
* REPLACE BBBB
*
* WHERE:
* CODE = ERROR CODE WITH UNIQUE FIRST FOUR CHARACTERS
* AAAA = LOCATION OF TEST IN PROGRAM WHERE ERROR WAS.
* BBBB = MOTHER BOARD OR DAUGHTER BOARD
*
* NOTE: THIS PROGRAM CAN BE OPERATED WITHOUT A CONSOLE. ERROR-DATA IS
* LIMITED TO "CODE" IN REGISTERS "R1" AND "R2", AND "AAAA" IN REGISTER "B2"
* ALL ENTRIES MUST BE MADE VIA REGISTER "R1". THE PROCESS IS FURTHER EXPLAIN
* ED IN MANUAL "AW94" ENTITLED "LEVEL-6 SYSTEM CHECKOUT AND T&V MANUAL".
*
* END OF PASS REPORT
* ---
* WHILE RUNNING, PASS COUNTS WILL BE REPORTED ON THE CONSOLE (IF PRESENT)
* FOR EXAMPLE:
*
* CMCS1 PS 1
* CMCS1 PS 2
* CMCS1 PS 3
*
* .
* .
* .
* CMCS1 PS 15
* 3 MIN DONE
* CMCS1 PS 16
*
* .
* .
* .
*
* THE PASS COUNT MAY ALSO BE OBSERVED IN B6 (IN DECIMAL).
*
* THE PROGRAM WILL PRINT "3 MIN DONE" AFTER 3 MINUTES TEST IS COMPLETED.
* THE TEST WILL BE CONTINUED UNTILL MANUALLY STOPPED.
*
* WHILE THE PROGRAM IS RUNNING, THE OPERATOR CAN STOP THE TEST ANY
* TIME BY PUSHING THE STOP BUTTON ON THE OPERATOR'S PANNEL. THE
* TEST CAN BE RESTARTED AT EO = 100 AND HIT THE EXECUTE BUTTON.

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000255 / THIS PROGRAM RUNS AT LEVEL 4
000256 *
000257 CTRL LINK ZV$TH
000258 CTR LINK ZV$IH
000259 XLOC ZV$AF
000260 XLOC ZV$SHK
000261 XLOC ZV$SHRU
000262 XLOC ZV$SLK
000263 XLOC ZV$TTY
000264 XLOC ZV$UIH
000265 XLOC ZHCOMM
000266 XLOC ZHTSA
000267 XLOC ZHNTSA
000268 XLOC ZHRTCI
000269 XLOC ZHRTCC
000270 XLOC ZHRTCL
000271 XLOC ZHTH15
000272 *
000273 * XLOC ZHTH17
000274 *
000275 XLOC ZHISAZ
000276 0004 X ZHISA4 EQU ZHISAZ+4*$AF
000277 *
000278 0007 X ZHTSA7 EQU ZHTSA+4+2*$AF
000279 *
000280 0000 ZERU EQU $ ZERO+X*100
000281 0100 ZERU ORG
000282 *
000283 *
000284 *
000285 0100 0F00 0000 START NOP <ZERU SAF, LAF CHECK
000286 0102 0F00 0000 NOP <ZERU
000287 0104 7C02 LDV $R7,=2 DISABLE CACHE
000288 0105 E870 0300 LDR $R6,=Z*0300 CHANNEL NO.
000289 0107 0011 DC X*11
000290 0108 8740 045F CL PASCNT+$AF-1 CLEAR PASS COUNT
000291 010A 0F84 B >START1
000292 *
000293 *
000294 010B 0F7F DONE NOP >$-1 3 MINUTE TESTING COMPLETE
000295 *
000296 010C 0F81 0101 B NTPAS CONTINUE TEST
000297 *
000298 *
000299 *
000300 010E 0F86 START1 B >INSRT BECOMES "NOP" AFTER 1ST EXECUTION
000301 010F 0F80 0001 START2 CALL ZV$RD INITIALIZE
000302 0111 0380 0000 X B >START3 CONTINUE
000303 *
000304 *
000305 INSTRT CALL ZV$RD,MSG1 INITIAL START, INITIALIZE AND PRINT
000306 0114 FBC0 0003 X
000307 0116 D380 0000
000308 0118 0F80
000309 0119 0491
000310 011A 9840 0023 LDR $R1,NOP PROGRAM NAME AND SYSTEM RESOURCES.
000311 011C 9F40 FFF1 STR $R1,START1 INSERT "NOP" SO ONLY DO ONCE.
000312 011E 0FF1 B >START2
000313 *
000314 011F 9BC0 0421 START3 LAB $B1,SECTSA PUT ADDRESS OF SECONDARY TRAP SAVE
000315 0121 9F80 0000 X STB $B1,<ZHTSA AREA IN LC X'0002'. SEE COMMENT
000316 * AT "SECISA".
000317 *
000318 *
000319 0123 8740 0417 CL ISA4 CLEAR TSA POINTER
000320 0125 8740 0415 CL ISA4+$AF-1
000321 *
000322 * CHANGE TO LEVEL 4
000323 *
000324 LAB $B3,ISA4+$AF STORE POINTER TO INTERRUPT SAVE
000325 0127 BBC0 0414 X STB $B3,<ZHISA4 AREA FOR LEVEL 4.
000326 0129 BF80 0004 LAB $B3,ISA4 ADDRESS FOR LEVEL 4 INTERRUPT.
000327 012B BBC0 0007 LAB $B3,ISA4
000328 012D BF80 0411 STB $B3,ISA4P
000329 012F 8E70 8004 LEV =Z*8004
000330 *
000331 *
000332 *
000333 *
000334 *
000335 *
000336 *
000337 0131 0F80 0000 X B <ZV$UIH SCHEDULE LEVEL 4,SUSPEND LEVEL (0),
000338 * SCAN AND DISPATCH. FIRST SAVE CONTEXT
000339 * COMES HERE ON LEVEL CHANGE FROM 0 TO 4 OF CURRENT RUNNING LEVEL (0) AND
000340 * RESTORE CONTEXT OF HIGHEST ACTIVE
000341 * LEVEL (4).
000342 * ADDRESS OF THIS LOCATION GOES TO
000343 * P-REG WORD OF ZV$ISA.
000344 *
000345 *
000346 *
000347 *
000348 *
000349 *
000350 * INITIALIZE PASS COUNT
000351 0133 EC80 056E X I4A1 LDB $B6,<NULL ** B6 RESERVED FOR PASS COUNT DISPLAY **
000352 *
000353 * INITIALIZE REAL TIME CLOCK FOR LATER TURN ON
000354 *
000355 *
000356 0135 9870 FFFF X LDR $R1,=Z'FFFF' MAX. VALUE
000357 0137 9F00 0000 X STR $R1,<ZHRTCC RTC CURRENT
000358 0139 9F00 0000 X STR $R1,<ZHRTCI RTC INITIAL
000359 013B 1C3F X LDV $R1,=63 LOWEST PRIORITY. WON'T INTERRUPT
000360 013C 9F00 0000 X STR $R1,<ZHRTCL RTC LEVEL
000361 *
000362 *
000363 *
000364 *
000365 *
000366 013E 0F7F NOP NOP >$-1
000367 *
000368 *
000369 *
000370 013F 9800 0000 X LDR $R1,<ZV$AF
000371 0141 1D02 CMV $R1,=2 LAF?
000372 0142 0911 BE >CLOCK YES
000373 0143 8980 0000 X CMZ <ZV$SHRU NO. MEMORY SIZE GREATER THAN 64K?
000374 0145 090E BE >CLOCK NO
000375 0146 FBC0 0003 X CALL ZV$TC,MSG2 YES. MEMORY SIZE GREATER THAN 64K.
000376 0148 D380 0000
000377 014A 0F80
000378 014B 04A9
000379 *
000380 014C FBC0 0003 CALL ZV$TC,MSG2B

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014L 0380 0000      X
0150 0F80
0151 04D2
000357 *
000358 *      NOP      >$-1      PRINT MESSAGE:
000359 *      *      CHANGE TO CALL(LAF) TEST PROGRAM
000360 *      *      AND SET THE SYSTEM TO LAF MODE
000361 *      *      OTHERWISE, THE PROGRAM WILL
000362 *      *      ONLY USE UP TO 64K LOCATIONS
000363 *
000364 0153 0004      CLOCK      RTCN      TURN ON CLOCK
000365 *
000366 *
000367 *
000368 *      TEST TO SEE IF CACHE IS PRESENT
000369 *      *****
000370 *
000371 *
000372 0154 AB80 0171      TCP10      LAB      $B2,<TCP20      SET UP IV15
000373 0156 AF80 0000      STB      $B2,<ZHTH15
000374 0158 8740 040D      CL      CAFLG      CLEAR CACHE FLAG
000375 *
000376 015A 7C08      LDV      $R7,=8      INITIALIZE CACHE
000377 015B E870 0300      LDR      $R6,=Z'0300'  CHANNEL NO.
000378 015D 0011      DC      X'11'      WILL TRAP IF CACHE IS NOT PRESENT
000379 *
000380 015L 7C02      LDV      $R7,=2      DISABLE CACHE
000381 015F 0011      DC      X'11'
000382 *
000383 0160 89C0 0405      CMZ      CAFLG      CACHE PRESENT?
000384 0162 0988      BNE     >TCP15      NO
000385 *
000386 *
000387 *      CALL      ZV$TC,MSGZA      YES. PRINT "CACHE PRESENT"
0163 FBC0 0003
0165 0380 0000      X
0167 0F80
0168 04E2
000388 0169 0F8B
000389 *
000390 *      b      >DC55
000391 *
016A FBC0 0003      TCP15      CALL      ZV$TC,MSG3      PRINT "CACHE NOT PRESENT"
016C 0380 0000      X
016E 0F80
016F 04E9
000392 0170 0000
000393 *      HLT
000394 0171 8AC0 03F4      TCP20      INC      CAFLG      CACHE IS NOT PRESENT. SET FLAG
000395 0173 0003      KTT      RETURN
000396 *
000397 *
000398 *
000399 *      DETERMINE CACHE SIZE
000400 *      *****
000401 *
000402 *
000403 *      CHECK NO HIT FAULT TEST MODE
000404 *
000405 0174 AB80 0192      DCS5      LAB      $B2,<DCS10      SET UP IV15 FOR UNAVAILABLE RESOURCES
000406 0176 AF80 0000      STB      $B2,<ZHTH15
000407 *
000408 0178 8740 03E1      CL      TEMPA      CLEAR NO HIT FLAG
000409 017A BCC0 001A      LDB      $B3,DCS20      TEST ADDR
000410 *
000411 017C 7C08      LDV      $R7,=8      INITIALIZE CACHE
000412 017D E870 0300      LDR      $R6,=Z'0300'  CHANNEL NO.
000413 017F 0011      DC      X'11'
000414 *
000415 0180 F870 00C0      LDR      $R7,=Z'00C0'  SET TEST MODE ON
000416 0182 0011      DC      X'11'
000417 0183 C803      LDR      $R4,$B3      READ. WILL CAUSE NO HIT FAULT TRAP
000418 *
000419 0184 89C0 03D5      CMZ      TEMPA      TRAP? = 0 NO TRAP, = 1 TRAP
000420 0186 0990      BNE     >DCS30      YES
000421 *      CALL      ZV$ER,DC1,MSGDC1      NO. ERROR "DC1"
0187 FBC0 0003
0189 0380 0000      X
018B 0F80
018C 0190
018D 050F
000422 018E 03C0 02B4      DC1      LANJ      $B5,PK2      PRINT "REPLACE ARRAY PAC"
000423 0190 0F7F      NOP      >$-1      ** ERROR **
000424 *      *      COMPARATOR LOGIC FAULT
000425 0191 0F85      b      >DCS30
000426 *
000427 *
000428 0192 8AC0 03C7      DCS10      INC      TEMPA      TRAP. SET NO HIT FAULT FLAG
000429 0194 0003      KTT
000430 0195 0003 1FFE      DCS20      DC      <ZHCOMM+X'1FFE'  TEST ADDR
000431 *
000432 *
000433 *      NOW CHECK THE CACHE SIZE
000434 *
000435 *
000436 0196 AB80 01CD      DCS30      LAB      $B2,<DCS40      SET UP IV15
000437 0198 AF80 0000      STB      $B2,<ZHTH15
000438 *
000439 *
000440 019A 8740 03D1      CL      CASIZE      PRESTORE 4K AS LAST LOCATION ON CACHE
000441 019C A870 0FFF      LDR      $R2,=Z'0FFF'  WILL MODIFY IF CACHE SIZE IS NOT 4K
000442 019E AF40 03CE      STR      $R2,CASIZE+1  CLEAR CACHE SIZE FLAG
000443 01A0 8740 03CA      CL      SIZEFLG
000444 01A2 7C08      LDV      $R7,=8      INITIALIZE CACHE
000445 01A3 E870 0300      LDR      $R6,=Z'0300'  CHANNEL NO.
000446 01A5 0011      DC      X'11'
000447 01A6 F870 00C0      LDR      $R7,=Z'00C0'  SET NO-HIT FAULT MODE ON
000448 01A8 0011      DC      X'11'
000449 01A9 9CC0 0026      LDB      $B1,DCS50      TEST ADDR TO CHECK WHETHER THE
000450 *      *      CACHE SIZE IS 4K
000451 *      *      READ. WILL TRAP IF CACHE SIZE
000452 *      *      IS LESS THAN 4K (4095)
000453 *

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000454 01AC 89C0 03BE          CMZ  SIZFLG          TRAP?
000455 01AE 0924          BE   >DC570          NO TRAP.  CACHE SIZE IS 4K
000456 01AF 8740 03BB          CL   SIZFLG          TRAP.  RESET SIZE FLAG
000457 01B1 9CC0 001F          LDB  $B1,DCS60       TEST ADDR TO CHECK WHETHER THE
000458 *                               *                               *
000459 01B3 C801          *   LDR  $R4,$B1     *   CACHE SIZE IS 2K
000460 *                               *                               *
000461 01B4 89C0 03B6          CMZ  SIZFLG          TRAP.  WILL TRAP IF CACHE SIZE
000462 01B6 0923          BE   >DC580          IS LESS THAN 2K (2047)
000463 *                               *                               *
000464 01B7 8740 03B3          CL   SIZFLG          TRAP.  CACHE SIZE IS LESS THAN 2K
000465 01B9 8740 03A3          CL   TEMP2          SET ADDR TO 0
000466 01BB 8740 03A2          CL   TEMP2+1
000467 01BD 9CC0 039B          LDB  $B1,TEMP1-$AF+2
000468 01BF C801          LDR  $R4,$B1
000469 *                               *                               *
000470 01C0 89C0 03AA          CMZ  SIZFLG          READ LOCATION 0.  WILL TRAP IF
000471 01C2 0921          BE   >CPP10         *   CACHE DAUGHTER BOARD IS NOT PRESENT
000472 *                               *                               *
000473 *                               *                               *
000474 *                               *                               *
000475 01C3 FBC0 0003          CALL ZV$ER,DC2,MSGDC2  TRAP.  ERROR "DC2"
000476 01C5 D380 0000          *                               *
000477 01C7 0F80          *                               *
000478 01C8 01CA          *                               *
000479 01C9 0511          *                               *
000480 01CA 0F7F          *                               *
000481 *                               *                               *
000482 *                               *                               *
000483 01CB 0F81 0017          DC2  NOP   >$-1     ** ERROR **
000484 *                               *                               *
000485 *                               *                               *
000486 *                               *                               *
000487 01CB 0F81 0017          B    CPP10          CHECK WHETHER DAUGHTER BOARD IS
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000489 *                               *                               *
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000492 *                               *                               *
000493 *                               *                               *
000494 01D2 FBC0 0003          *                               *
000495 01D4 D380 0000          *                               *
000496 01D6 0F80          *                               *
000497 01D7 04F2          *                               *
000498 01D8 0F8B          *                               *
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000508 01D9 FBC0 0003          *                               *
000509 01DB D380 0000          *                               *
000510 01DD 0F80          *                               *
000511 01DE 04F8          *                               *
000512 01DF A870 07FF          *                               *
000513 01E1 AF40 038B          *                               *
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000527 01E3 AB80 020D          *                               *
000528 01E5 AF80 0000          *                               *
000529 01E7 AF80 0000          *                               *
000530 01E9 AFC0 0000          *                               *
000531 01EB ACC0 0000          *                               *
000532 01ED 9CC0 0380          *                               *
000533 01EF 9C80 0000          *                               *
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000540 01F1 C801          *                               *
000541 01F2 CF40 0367          *                               *
000542 01F4 8601          *                               *
000543 01F5 C671          *                               *
000544 01F6 4DFF          *                               *
000545 01F7 0989          *                               *
000546 01F8 9DD2          *                               *
000547 01F9 0278          *                               *
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000554 01FA 9C80 0000          *                               *
000555 01FC 9FC0 0373          *                               *
000556 01FE 0F81 0010          *                               *
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000554 *****
000555 *
000556 *
000557 020E 0F7F NXTPAS NOP >$-1
000558 *
000559 *
000560 *
000561 *
000562 * ROUND ROBIN TEST
000563 *****
000564 *
000565 *
000566 020F AB80 023C RRT10 LAB $B2,<RRT40 SET UP IV 15 FOR UNAVAILABLE RESOURCES
000567 0211 AF80 0000 X STB $B2,<ZHTH15
000568 *
000569 0213 8740 0353 CL TRPFLG
000570 0215 ACC0 0357 LDB $B2,CASIZE-$AF+2 CLEAR TRAP FLAG
000571 0217 8740 0340 CL TEMP1 LAST LOCATION ON CACHE
000572 0219 8740 033F CL TEMP1+1
000573 021B 9CC0 033D LDB $B1,TEMP1-$AF+2 INITIALIZE ADDR TO 0
000574 *
000575 021D E870 0300 LDR $R6,=Z'0300' CHANNEL NO.
000576 021F 7C08 LDV $R7,=8 INITIALIZE CACHE
000577 0220 0011 DC X'11'
000578 0221 F870 00C0 LDR $R7,=Z'00C0' SET TO NO HIT MODE
000579 0223 0011 DC X'11'
000580 0224 C871 RRT20 LDR $R4,+$B1 READ. BUMP ADDR. WILL TRAP IF
000581 * ROUND ROBIN FAULT IS DETECTED
000582 *
000583 0225 89C0 0341 CMZ TRPFLG TRAP?
000584 0227 0901 FFFC BE RRT20 NO TRAP. CONTINUE TEST
000585 0229 8740 033D CL TRPFLG TRAP
000586 022B 9DD2 CMB $B1,=$B2 ADDR LESS THAN CACHE SIZE?
000587 022C 0383 BLE >RRT30 NO. REPORT ERROR
000588 *
000589 *
000590 *
000591 022D 0F81 0011 B CLT10 GO TO NEXT TEST
000592 *
000593 *
000594 RRT30 CALL ZV$ER,KR1,MSGR<1 ERROR "KR1"
000595 *
000596 022F FBC0 0003 X
000597 0231 D380 0000
000598 0233 0F80
000599 0234 0238
000600 0235 0523
000601 0236 D3C0 0202 KR1 LNJ $B5,PR1 PRINT "REPLACE MOTHER BOARD"
000602 0238 0F7F * NUP >$-1 ** ERROR **
000603 * ROUND ROBIN RAM ERROR
000604 0239 7C08 LDR $R7,=8 REPLACE MOTHER BOARD
000605 023A 0011 DC X'11' INITIALIZE CACHE
000606 023B 0FE9 B >RRT20 CONTINUE TEST
000607 *
000608 *
000609 *
000610 * COMPARATOR LOGIC TEST
000611 *****
000612 *
000613 *
000614 023F AB80 0275 CLT10 LAB $B2,<CLT50 SET IV15
000615 0241 AF80 0000 X STB $B2,<ZHTH15
000616 0243 9CC0 0329 LDB $B1,CASIZE-$AF+2
000617 0245 89F1 CMZ +$B1 INITIALIZE ADDR TO CACHE SIZE + 1
000618 0246 0005 RLCF TURN OFF CLOCK
000619 0247 7C08 LDV $R7,=8 INITIALIZE CACHE
000620 0248 E870 0300 LDR $R6,=Z'0300' CHANNEL NO.
000621 024A 0011 DC X'11'
000622 024B F870 00C0 CLT20 LDR $R7,=Z'00C0' SET NO HIT TEST MODE ON
000623 024D 0011 DC X'11'
000624 024E C801 LDR $R4,+$B1 READ. WILL CAUSE NO HIT FAULT TRAP
000625 *
000626 024F 7C02 LDV $R7,=2 DISABLE CACHE
000627 0250 0011 DC X'11'
000628 0251 0004 RTCN TURN ON CLOCK
000629 0252 89C0 0307 CMZ TEMPA TRAP? = 0 - NO TRAP, = 1 - TRAP
000630 0254 0924 BE >CLT60 NO TRAP. REPORT ERROR
000631 0255 8740 0304 CL TEMPA CLEAR TRAP FLAG
000632 0257 9FC0 0301 CLT30 STB $B1,TEMP1-$AF+2 INC. ADDR BY 1K TO NEXT ROW IN THE DIRECTORY
000633 0259 E840 02FE LDR $R6,TEMP1
000634 025B F840 02FD LDR $R7,TEMP1+1
000635 025D FA70 0400 ADD $R7,=Z'0400'
000636 025F 8ED6 CAD =$R6
000637 0260 EF40 02F7 STR $R6,TEMP1
000638 0262 FF40 02F6 STR $R7,TEMP1+1
000639 0264 9CC0 02F4 LDB $B1,TEMP1-$AF+2
000640 0266 9D80 0000 CMB $B1,<ZV$LK ADDR REGISTER OVERFLOW?
000641 0268 0201 001B BL DPT10 YES
000642 026A 9DC0 0305 CMB $B1,MEMHI-$AF+2 ADDR GREATER THAN MAIN MEMORY HIGH?
000643 026C 0301 0017 BG DPT10 YES
000644 026E 0005 RTCF NO. TURN OFF CLOCK
000645 026F E870 0300 LDR $R6,=Z'0300' CHANNEL NO.
000646 0271 7C03 LDV $R7,=3 ENABLE CACHE
000647 0272 0011 DC X'11'
000648 0273 0F81 FFD7 B CLT20 CONTINUE TEST
000649 *
000650 *
000651 0275 8AC0 02E4 CLT50 INC TEMPA TRAP. SET NO HIT FAULT FAULT FLAG
000652 0277 0003 RTT RETURN
000653 *
000654 CLT60 CALL ZV$ER,CL1,MSGCL1 ERROR "CL1"
000655 0278 FBC0 0003 X
000656 027A D380 0000
000657 027C 0F80
000658 027D 0281
000659 027E 0513
000660 027F D3C0 01C3 CL1 LNJ $B5,PR2 PRINT "REPLACE ARRAY PAC"
000661 0281 0F7F * NUP >$-1 ** ERROR **

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000659 02b2 0f81 FFD4
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000671 0284 9870 1000
000672 0286 8740 02D1
000673 0288 9F40 02D0
000674 028A 9CC0 02CE
000675 028C 5C01
000676 028D DF01
000677 028E E870 0300
000678 0290 7C08
000679 0291 0011
000680 0292 C871
000681 0293 7C02
000682 0294 0011
000683 0295 C955
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000685 0297 5011
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000694 029A 9CC0 0000 P
000695 029C E870 0300
000696 029E 7C08
000697 029F 0011
000698 02A0 DF01
000699 02A1 C801
000700 02A2 7C02
000701 02A3 0011
000702 02A4 C955
000703 02A5 0990
000704 02A6 5011
000705 02A7 5D01
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000713 02AC D380 0000
000714 02AE 0F80
000715 02AF 02B3
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000717 02B1 D3C0 0187
000718 02B3 0F7F
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000722 02B7 D380 0000
000723 02B9 0F80
000724 02BA 02BE
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000735 02C0 E870 0300
000736 02C2 7C08
000737 02C3 0011
000738 02C4 7C02
000739 02C5 0011
000740 02C6 AB80 0342 X
000741 02C8 AF80 0000
000742 02CA 9870 1000
000743 02CC 8740 028B
000744 02CE 9F40 028A
000745 02D0 9CC0 0288
000746 02D2 9FC0 0290
000747 02D4 AB80 054D
000748 02D6 1C00
000749 02D7 2CF5
000750 02D8 E812
000751 02D9 EF5D
000752 02DA 27FE
000753 02DB 9870 1005
000754 02DD 8740 027F
000755 02DF 9F40 027E
000756 02E1 1E06
000757 02E2 9F40 0276
000758 02E4 CCC0 0274
000759 02E6 9CD4

*
* COMPARATOR LOGIC FAULT
* REPLACE ARRAY PAC
* CONTINUE TEST
*
*
* DATA PATH TEST
* *****
*
* FIRST TEST DATA PATH IN CACHE REPLACEMENT CYCLE TO ISOLATE THE DATA PATH
* FAULT FROM CACHE SERVICE CYCLE AND CACHE UPDATE CYCLE.
*
DPT10 LDR $R1,=Z'1000' INITIALIZE ADDR TO 4K+1
CL TEMP1
STR $R1,TEMP1+1
LDB $B1,TEMP1-$AF+2
LDV $R5,=1 DATA = 0001
DPT20 STR $R5,$B1 WRITE
LDR $R6,=Z'0300' CHANNEL NO.
LDV $R7,=8 INITIALIZE CACHE
DC X'11'
LDR $R4,+$B1 READ. BUMP ADDR
LDV $R7,=2 DISABLE CACHE
DC X'11'
CMR $R4,=$R5 COMPARE DATA
BNE >DPT60 REPORT ERROR IF DATA DON'T MATCH
DPT30 SCL $R5,1 SHIFT DATA 1 BIT LEFT
CMV $R5,=1 ALL 16 BIT DATA PATH TESTED?
BNE >DPT20 BRANCH TO DPT20 IF NOT
*
* DATA PATH TEST IN CACHE SERVICE CYCLE
*
*
DPT50 LDB $B1,ZV$LR USE MAIN MEMORY LOW AS TEST LOCATION
LDR $R6,=Z'0300' CHANNEL NO.
LDV $R7,=8 INITIALIZE CACHE
DC X'11'
DPT60 STR $R5,$B1 WRITE
LDR $R4,$B1 READ
LDV $R7,=2 DISABLE CACHE
DC X'11'
CMR $R4,=$R5 COMPARE DATA
BNE >DPT90 REPORT ERROR IF DATA DON'T MATCH
DPT60 SCL $R5,1 SHIFT DATA 1 BIT LEFT
CMV $R5,=1 ALL 16 BITS DATA PATH TESTED?
BNE >DPT50 BRANCH TO DPT50 IF NOT
B >CRT10 DATA PATH TEST DONE
*
*
DPT80 CALL ZV$EK,DP1,MSGDP1 ERROR "DP1"
*
*
DP1 LNJ $B5,$R1 PRINT "REPLACE MOTHER BOARD"
NOP >$-1 ** ERROR **
* DATA PATH ERROR (CACHE REPLACEMENT CYCLE)
* REPLACE MOTHER BOARD
*
B >DPT30 CONTINUE NEXT BIT (TOTAL 16 BITS) TEST
*
*
DPT90 CALL ZV$EK,DP2,MSGDP2 ERROR "DP2"
*
*
DP2 LNJ $B5,$R1 PRINT "REPLACE MOTHER BOARD"
NOP >$-1 ** ERROR **
* DATA PATH ERROR (CACHE SERVICE CYCLE)
* REPLACE MOTHER BOARD
* CONTINUE NEXT BIT (TOTAL 16 BIT) TEST
*
*
* CACHE RAM TEST
* *****
*
*
CRT10 LDR $R6,=Z'0300' CHANNEL NO.
LDV $R7,=8 INITIALIZE CACHE
DC X'11'
LDV $R7,=2 DISABLE CACHE
DC X'11'
LAB $B2,<CRT150 SET UP FOR UNEXPECTED REV TRAP
STB $B2,<ZHTH17
LDR $R1,=Z'1000' INITIAL ADDR
CL TEMP1
STR $R1,TEMP1+1
LDB $B1,TEMP1-$AF+2
STB $B1,TEMP5-$AF+2
LAB $B2,<BUFF1 MEMORY ADDR FOR DATA FROM BUFF1
LDV $R1,=0 SAVE INITIAL ADDR
LDV $R2,=-11 BUFF1 STORES DATA
DPT20 LDR $R6,$B2,$R1 INDEX
STR $R9,$B1,$R1 COUNT NO. OF LOCATIONS IN BUFF1
BINC $R2,>CRT20 MOVE DATA FROM BUFF1 TO MEMORY
LDR $R1,=Z'1005'
CL TEMP2
STR $R1,TEMP2+1
ADV $R1,=6 MMM SOURCE ADDR
STR $R1,TEMP1+1
LDB $B4,TEMP1-$AF+2
LDB $B1,=$B4 STARTING ADDR

```



```

000760 02E7 1EF9          ADV  $R1,=-7
000761 02E8 9F40 027C    STR  $R1,TEMP6+1      "MMM" ADDR
000762 02EA 8740 0279    CL   TEMPO
000763 02EC 89C0 027E    CMZ  SIZFLG          CACHE SIZE = 4K?
000764 02EE 0981 003A    BNE  CRT110         NO
000765 02F0 E840 026C    LDR  $R6,TEMP2      SOURCE ADDR
000766 02F2 F840 026B    LDR  $R7,TEMP2+1
000767 02F4 FA70 0FFB    ADD  $R7,Z'0FFB'    ADD 4091 TO SOURCE ADDR
000768 02F6 EF40 0268    STR  $R6,TEMP3      DEST. ADDR
000769 02F8 FF40 0267    STR  $R7,TEMP3+1
000770 02FA 9870 1FF6    LDR  $R1,Z'1FF6'
000771 02FC 9F40 0264    STR  $R1,TEMP4
000772 02FE 7EFF          CRT30 ADV  $R7,=-1
000773 02FF FF40 0259    STR  $R7,TEMP1+1
000774 0301 ECC0 0257    LDB  $B6,TEMP1-$AF+2  ENDING ADDR
000775 0303 DB70 CA5A    LDR  $R5,Z'CA5A'    DATA
000776 0305 DF71          CRT40 STR  $R5,+$B1
000777 0307 5011          SCL  $R5,1          CLOSE SHIFT DATA 1 BIT LEFT
000778 0309 9DD6          CMB  $B1,=$B6      ADDR GREATER THAN ENDING ADDR?
000779 030B 03FD          BLE  >CRT40        NO
000780
000781
000782
* SET UP FOR MEMORY TO MEMORY MOVE
*
000783 0309 ACC0 0254          LDB  $B2,TEMP2-$AF+2  SOURCE ADDR
000784 030B 2C00          LDV  $R2,=0          DISPLACEMENT
000785 030C BCC0 0253          LDB  $B3,TEMP3-$AF+2  DEST. ADDR
000786 030E 3C00          LDV  $R3,=0          DISPLACEMENT
000787 030F 9840 0251          LDR  $R1,TEMP4      NO. OF BYTES TO MOVE
000788 0311 9CD4          LDB  $B1,=$B4      STARTING ADDR
000789 0312 DB70 CA5A          LDR  $R5,Z'CA5A'    DATA
000790 0314 8740 0245          CL   TEMPA          CLEAR MMM FLAG
000791 0316 FCC0 024C          LDB  $B7,TEMP5-$AF+2
000792 0318 0005          CRT50 RTCF          TURN OFF CLOCK
000793 0319 D387          LNJ  $B5,$B7        JUMP TO CACHE ENABLE
000794
* CACHE IS ENABLED. START MEMORY TO MEMORY MOVE OPERATION. TEST
* PATTERNS ARE WRITTEN INTO CACHE. READ 1 LOCATION. WILL TRAP
* AND BRANCH TO REPORT ERROR IF DATA ERROR OCCURES. IF NO ERROR
* IS DETECTED, DISABLE CACHE AND RETURN.
*
000799
000800 031A 0004          RTCN          TURN ON CLOCK
000801 031B 89C0 023E          CMZ  TEMPA          MMM FLAG = 0?
000802 031D 0901 001B          BE   CRT120         YES
000803 031F C955          CRT60 CMR  $R4,=$R5      COMPARE DATA
000804 0320 0981 002D          BNE  CRT160         DATA DO NOT MATCH. REPORT ERROR
000805 0322 9871          CRT70 LDR  $R1,+$B1      DUMMY TO BUMP ADDR
000806 0323 5011          SCL  $R5,1          CLOSE SHIFT DATA 1 BIT LEFT
000807 0324 9DD6          CMB  $B1,=$B6      ADDR GREATER THAN ENDING ADDR?
000808 0325 0381 FFF2          BLE  CRT50         NO
000809 0327 0F81 0032          B    RDT10         DONE
000810
*
000811
000812 0329 E840 0233          CRT110 LDR  $R6,TEMP2      SOURCE ADDR
000813 032B F840 0232          LDR  $R7,TEMP2+1
000814 032D FA70 0800          ADD  $R7,Z'0800'    ADD 2K TO SOURCE ADDR
000815 032F EF40 022F          STR  $R6,TEMP3      DEST. ADDR
000816 0331 FF40 022E          STR  $R7,TEMP3+1
000817 0333 9870 1000          LDR  $R1,Z'1000'
000818 0335 9F40 022B          LDR  $R1,TEMP4
000819 0337 0F81 FFC6          B    CRT30         NO. OF BYTES TO MOVE = 4K
000820
*
000821 0339 ACC0 022B          CRT120 LDB  $B2,TEMP6-$AF+2  MMM ADDR
000822 033B AB40 FE02          LDR  $R2,NOP        OVERWRITE MMM WITH NOP
000823 033D AF02          STR  $R2,$B2
000824 033E 8AC0 021B          INC  TEMPA          SET MMM FLAG
000825 0340 0F81 FFDE          B    CRT60
000826
*
000827          CRT150 CALL  ZV$ER,CRT1,MSGCR1  ERROR "CR1"
000828
000829 0342 FBC0 0003          X
000830 0344 D380 0000
000831 0346 0F80
000832 0347 034B
000833 0348 0519
000834 0349 D3C0 00F9          CR1  LNJ  $B5,PK2    PRINT "REPLACE ARRAY PAC"
000835 034B 0F7F          NUP  >$-1          ** ERROR **
000836
*
000837          B    RDT10
000838
*
000839          CRT160 CALL  ZV$ER,CRT2,MSGCR2  ERROR "CR2"
000840
000841 034E FBC0 0003          X
000842 0350 D380 0000
000843 0352 0F80
000844 0353 0357
000845 0354 051B
000846 0355 D3C0 00ED          CR2  LNJ  $B5,PK2    PRINT "REPLACE ARRAY PAC"
000847 0357 0F7F          NUP  >$-1          ** ERROR **
000848
*
000849          CACHE DATA ERROR
000850          DATA FROM CACHE UNEQUAL DATA SHOULD BE
000851          R4 = DATA FROM CACHE
000852          R5 = DATA SHOULD BE
000853          CONTINUE TEST
000854
*
000855          REPLACEMENT LOGIC AND DIRECTORY TEST
000856          *****
000857
*
000858          RDT10 LDR  $R6,Z'0300'    CHANNEL NO.
000859 035A 7C02          LDV  $R7,=2          DISABLE CACHE
000860 035B 0011          DC   X'11'
000861 035D 9C80 0000          LDB  $B1,<ZV$LR     ADDR = MAIN MEMORY LOW
000862 035F 9FC0 01F8          STB  $B1,TEMP1-$AF+2
000863 0361 9840 01F6          LDR  $R1,TEMP1+1
000864 0363 9F71          RDT20 STR  $R1,+$B1
000865 0365 1E01          ADV  $R1,=1          WRITE, BUMP ADDR
000866 0367 9DB0 0000          CMB  $B1,<ZV$LR     ADDR REGISTER OVERFLOW?
000867 0369 0204          BL   >RDT25         YES
000868 036B 9DC0 0206          CMB  $B1,MEMHI-$AF+2  ADDR GREATER THAN MAIN MEMORY HIGH?
000869 036D 03F9          BLE  >RDT20        NO

```

```

000863
000864 036C ACC0 0203
000865 036E AFC0 01EA
000866 0370 9840 01E8
000867 0372 1EF5
000868 0373 9F40 01E5
000869 0375 ACC0 01E3
000870 0377 BB80 0540
000871 0379 1C00
000872 037A 2CF5
000873 037B B813
000874 037C BF5E
000875 037D 27FE
000876 037E AFC0 01DA
000877 0380 9840 01D8
000878 0382 1E04
000879 0383 9F40 01D5
000880 0385 BCC0 01D3
000881 0387 9840 FDB6
000882 0389 9F03
000883 038A 8740 01D2
000884 038C 8740 01D1
000885
000886
000887
000888 038E 9CC0 01CF
000889 0390 BCD1
000890
000891 0391 89F1
000892
000893 0392 E870 0300
000894 0394 7C08
000895 0395 0011
000896
000897 0396 0005
000898
000899 0397 D382
000900
000901
000902
000903
000904 0398 0004
000905 0399 D801
000906 039A 9DC0 0077
000907 039C 0904
000908 039D C955
000909 039E 0981 0067
000910 03A0 9FC0 01B8
000911 03A2 E840 01B5
000912 03A4 F840 01B4
000913 03A6 7081
000914 03A7 EF40 01B0
000915 03A9 FF40 01AF
000916 03AB 9CC0 01AD
000917 03AD 9DD2
000918 03AE 0301 0003
000919 03B0 9DD3
000920 03B1 09E5
000921
000922
000923
000924
000925 03B2 9CC0 01AB
000926 03B4 8740 01AF
000927 03B6 3C00
000928
000929
000930
000931 03B7 E870 0300
000932 03B9 7C08
000933 03BA 0011
000934
000935 03BB 0005
000936 03BC D382
000937
000938
000939
000940
000941 03BD 0004
000942 03BE D871
000943
000944 03BF 9DEC 0053
000945 03C1 0904
000946 03C2 C955
000947 03C3 0981 0036
000948 03C5 89C0 019E
000949 03C7 0981 001C
000950 03C9 9DD2
000951 03CA 0201 FFF0
000952 03CC 8AD3
000953 03CD 8AC0 0196
000954 03CF 3D01
000955 03D0 0901 0019
000956 03D2 8740 0185
000957 03D4 8740 0184
000958 03D6 9CF0 0559
000959 03D8 9853
000960 03D9 9B70 0400
000961 03DB 1EFF
000962 03DC 8740 017B
000963 03DE 9F40 017A
000964 03E0 C000 0178
000965 03E2 0F81 FFD8
000966
000967
000968
000969 03E4 9DD4
000970 03E5 0381 FFD5
000971 03E7 3D03
000972 03E8 0281 002B
000973
000974
000975

*
RDT25 LDB $B2, MEMH1-$AF+2
STB $B2, TEMP1-$AF+2
LDR $R1, TEMP1+1
ADV $R1, #-11
STR $R1, TEMP1+1
LDB $B2, TEMP1-$AF+2
LAB $B3, <BUFF1
LDV $R1, #0
LDV $R2, #-11
LDR $R3, $B3, $R1
STR $R3, $B2, +$R1
RDT30 LDR $R2, >RDT30
STR $R2, TEMP1-$AF+2
BINC STB $B2, TEMP1-$AF+2
LDR $R1, TEMP1+1
ADV $R1, #4
STR $R1, TEMP1+1
LDB $B3, TEMP1-$AF+2
LDR $R1, NOP
STR $R1, $B3
CL TEMP2
CL TEMP2+1

*
* FIRST TEST THE ADDRESS PATH AND THE REPLACEMENT LOGIC
*
LDB $B1, TEMP2-$AF+2
LDV $B3, $B1
*
CMZ +$B1
*
LDR $R6, =Z'0300'
LDV $R7, #8
DC X'11'
*
RDT33 RTCF
*
LNJ $B5, $B2
*
* CACHE IS ENABLED. READ CACHE. DISABLE CACHE. RETURN.
*
RTCN
LDR $R5, $B1
LDR $R1, LOC15
CMB $R4, $R5
BE >RDT35
CMR $R4, $R5
BNE RDT180
RDT35 STB $B1, TEMP1-$AF+2
LDR $R6, TEMP1
LDR $R7, TEMP1+1
DUL $R7, #1
STR $R6, TEMP1
STR $R7, TEMP1+1
LDB $B1, TEMP1-$AF+2
CMB $B1, $B2
BG RDT37
CMB $B1, $B3
BNE >RDT33
*
* NOW TEST ALL LEVELS (2 OR 4) IN THE CACHE DIRECTORY
*
RDT37 LDB $B1, TEMP2-$AF+2
CL TEMP6
LDV $R3, #0
*
*
LDR $R6, =Z'0300'
LDV $R7, #8
DC X'11'
*
RDT40 RTCF
LNJ $B5, $B2
*
* CACHE IS ENABLED. READ CACHE. DISABLE CACHE. RETURN.
*
RTCN
LDR $R5, +$B1
*
CMB $B1, LOC16
BE >RDT50
CMR $R4, $R5
BNE RDT170
RDT50 CMZ TEMP6
BNE RDT110
CMB $B1, $B2
BL RDT40
INC $R3
INC TEMP6
CMV $R3, #1
BE RDT120
CL TEMP1
CL TEMP1+1
LDB $B1, TEMP1-$AF+2
LDR $R1, $R3
MUL $R1, #1024
ADV $R1, #-1
CL TEMP1
STR $R1, TEMP1+1
LDB $B4, TEMP1-$AF+2
B RDT40
*
*
RDT110 CMB $B1, $B4
BLE RDT40
CMV $R3, #3
BGE ULT10
*
*
*

```

CALCULATE LOCATION TO STORE
"CACHE ENABLE" INSTRUCTION

BUFF1 STORES "CACHE ENABLE", ETC.
INDEX
COUNT NO. OF LOCATIONS IN BUFF1
MOVE CONTENTS IN BUFF1 TO MEMORY

OVERWRITE MMM WITH NOP

INITIALIZE ADDR TO 0
FOR ADDR REGISTER OVERFLOW CHECK IN
SAF MODE
DUMMY TO BUMP ADDR

CHANNEL NO.
INITIALIZE CACHE

TURN OFF CLOCK

JUMP TO CACHE ENABLE

CACHE IS ENABLED. READ CACHE. DISABLE CACHE. RETURN.

TURN ON CLOCK
READ MAIN MEMORY WITH CASH DISABLED
LOCATION 15 IS THE REAL TIME CLOCK
CURRENT VALUE

REPORT ERROR
SHIFT ADDR 1 BIT LEFT

ADDR GREATER THAN "CACHE ENABLE" ADDR?
YES
BI OVERFLOW?
NO

NOW TEST ALL LEVELS (2 OR 4) IN THE CACHE DIRECTORY

REINITIALIZE ADDR TO 0
INITIALIZE FLAG
INITIALIZE COUNTER A

CHANNEL NO.
INITIALIZE CACHE

TURN OFF CLOCK
JUMP TO CACHE ENABLE

CACHE IS ENABLED. READ CACHE. DISABLE CACHE. RETURN.

TURN ON CLOCK
READ MAIN MEMORY WITH CACHE DISABLED
POST INC. ADDR
LOCATION 15 IS THE REAL TIME CLOCK
CURRENT VALUE
COMPARE CACHE DATA TO MAIN MEMORY DATA
DATA DO NOT MATCH. REPORT ERROR
FLAG = 0?
NO
ADDR LESS THAN "CACHE ENABLE" ADDR?
YES. TEST NEXT LOCATION
NO. INC. COUNTER A
SET FLAG
COUNTER A = 1?

SET ADDR TO 0
CALCULATE ENDING ADDR

ADDR GREATER THAN ENDING ADDR?
NO
COUNTER A
GO TO NEXT TEST IF DONE

000976	03EA	E840	0172	RDT120	LDR	\$R6,TEMP2		
000977	03EC	F840	0171		LDR	\$R7,TEMP2+1		
000978	03EE	FA70	03FF		ADD	\$R7,=Z'03FF'		
000979	03F0	EF40	016C		STR	\$R6,TEMP2		
000980	03F2	FF40	016B		STR	\$R7,TEMP2+1		UPDATE INITIAL ADDR
000981	03F4	9CC0	0169		LDB	\$B1,TEMP2-\$AF+2		REINITIALIZE ADDR
000982	03F6	8740	016D		CL	TEMP6		RESET FLAG
000983	03F8	0F81	FFC2		B	RDT40		
000984					*			
000985	03FA	FBC0	0003	RDT170	CALL	ZV\$ER,RD1,MSGRD1		ERROR "RD1"
	03FC	D380	0000					
	03FE	0F80						
	03FF	0403						
	0400	051D						
000986	0401	D3C0	0041	RD1	LNJ	\$B5,PR2		PRINT "REPLACE ARRAY PAC"
000987	0403	0F7F			NOP	>\$-1		** ERROR **
000988					*			DIRECTORY ERROR
000989					*			R4 = DATA IS
000990					*			R5 = DATA SHOULD BE
000991	0404	0F81	FFC0		B	RDT50		CONTINUE TEST
000992					*			
000993				RDT180	CALL	ZV\$ER,RD2,MSGRD2		ERROR "RD2"
000994								
	0406	FBC0	0003					
	0408	D380	0000					
	040A	0F80						
	040B	040F						
	040C	051F						
000995	040D	D3C0	002B	RD2	LNJ	\$B5,PR1		PRINT "REPLACE MOTHER BOARD"
000996	040F	0F7F			NOP	>\$-1		** ERROR **
000997					*			REPLACEMENT LOGIC ERROR
000998					*			
000999	0410	0F81	FF8F		B	RDT35		CONTINUE TEST
001000					*			
001001					*			
001002					*			
001003	0412		0000	LOC15	DC	<ZHRICC		
001004					*			
001005	0413		0001	LOC16	DC	<ZHRICC+1		
001006					*			
001007					*			
001008					*			
001009					*			
001010					*			
001011					*			
001012					*			
001013					*			
001014					*			
001015	0414	9CC0	0000	ULT10	LDB	\$B1,ZV\$LR		ADDR = MAIN MEMORY LOW
001016	0416	9FC0	0142		STB	\$B1,TEMP1-\$AF+2		
001017	0418	0840	0140		LDR	\$R5,TEMP1+1		DATA = ADDR
001018	041A	E870	3000		LDR	\$R6,=Z'3000'		CHANNEL NO.
001019	041C	7C06			LDB	\$R7,=8		INITIALIZE CACHE
001020	041D	0011			DC	X'11'		
001021	041E	0F01		ULT20	STR	\$R5,\$B1		WRITE
001022	041F	C871			LDR	\$R4,+ \$B1		READ, BUMP ADDR
001023	0420	C955			CMR	\$R4,= \$R5		COMPARE DATA
001024	0421	098D			BNE	>ULT40		DATA DON'T MATCH. REPORT ERROR
001025	0422	5E01			ADV	\$R5,=1		BUMP DATA
001026	0423	9D80	0000		CMR	\$B1,<ZV\$LR		ADDR REGISTER OVERFLOW?
001027	0425	0204			BL	>ULT30		YES
001028	0426	9DC0	0149		CMR	\$B1,MEMHI-\$AF+2		ADDR GREATER THAN MAIN MEMORY HIGH?
001029	0428	03F6			BLE	>ULT20		NO. TEST NEXT LOCATION
001030	0429	F870	0002	ULT30	LDR	\$R7,=2		YES. DISABLE CACHE
001031	042B	0011			DC	X'11'		
001032	042C	0F81	0027		B	EOP		DONE
001033					*			
001034					*			
001035	042E	FBC0	0003	ULT40	CALL	ZV\$ER,U11,MSGU1		ERROR "U11"
	0430	D380	0000					
	0432	0F80						
	0433	0437						
	0434	0521						
001036	0435	D3C0	0003	U11	LNJ	\$B5,PR1		PRINT "REPLACE MOTHER BOARD"
001037	0437	0F7F			NOP	>\$-1		** ERROR **
001038					*			UPDATE LOGIC FAULT
001039					*			REPLACE MOTHER BOARD
001040	0438	0FE6			B	>ULT20		CONTINUE TEST
001041					*			
001042					*			
001043					*			
001044					*			
001045					*			
001046					*			
001047					*			
001048					*			
001049					*			
001050					*			
001051					*			
001052					*			
001053					*			
001054	0439	DFC0	0121	PR1	STB	\$B5,TEMPC		SAVE B5
001055					CALL	ZV\$TC,MSGCB		PRINT "REPLACE MOTHER BOARD"
	043B	FBC0	0003					
	043D	D380	0000					
	043F	0F80						
	0440	0525						
001056	0441	8388	055B		JMP	*<TEMPC		RETURN
001057					*			
001058	0443	DFC0	0117	PR2	STB	\$B5,TEMPC		SAVE B5
001059					CALL	ZV\$TC,MSGAP		PRINT "REPLACE DAUGHTER BOARD"
	0445	FBC0	0003					
	0447	D380	0000					
	0449	0F80						
	044A	0532						
001060	044B	8388	055B		JMP	*<TEMPC		RETURN
001061					*			
001062					*			
001063					*			
001064					*			
001065					*			

```

001066
001067
001068
001069 044D 9BC0 0003
001070 044F 9F80 0000 X
001071
001072
001073 0451 8AC0 0115
001074 0453 0003
001075
001076
001077
001078
001079
001080
001081
001082
001083
001084 0454 8AC0 0113
001085
001086
001087
001088 0456 F840 0111
001089 0458 A870 0003
001090 045A 8756
001091 045B F370 000A
001092 045D EAD5
001093 045E 000F
001094 045F 5054
001095 0460 277A
001096 0461 DF40 0108
001097 0463 ECC0 0106
001098
001099
001100
001101 0465 9800 0000 X
001102 0467 9970 D20F
001103
001104
001105
001106
001107
001108
001109
001110 0469 0319
001111
001112 046A 1CFF
001113 046B 9F00 0000 X
001114
001115
001116
001117
001118
001119 047F 0004 FC8A
001120 0480 0F81
001121
001122
001123
001124
001125
001126
001127 048E 0004
001128
001129
001130 048F 0F81 FD7E
001131
001132
001133
001134
001135
001136
001137
001138
001139
001140 0491 636D 6373 3120
001141 0494 2063 6163 6865
001142 206D 656D 6F72
001143 7920 7465 7374
001144
001145 049D 2020 2032 3120
001146 04A0 4170 7220 3139
001147 3738 2020 2052
001148 4556 2E20 4224
001149 04A9 6D65 6D6F 7279
001150 04AC 2067 7265 6174
001151 04AF 6572 2074 6861
001152 04B2 6E20 3634 6B2C
001153 2063 6861 6E67
001154 6520 746F 2063
001155 6D63 6C31 202C
001156 0D0A 2020 7365

* SUBROUTINE TO SET UP FOR RED OR PARITY ERRORS
*
* LAB $B1,TH17C SET UP FOR UNEXPECTED RED OR PARITY
* STB $B1,<ZHTH17 ERRORS
*
* TH17C INC TRPFLG SET RED TRAP FLAG
* RTT RETURN
*
*
* END OF PASS
*****
*
* EOP INC PASCNT+$AF-1 BUMP PASS COUNT
*
* DISPLAY PASS COUNT IN B6 IN DECIMAL
*
* LDR $R7,PASCNT+$AF-1
* LDR $R2,=3
DIGIT CL =$R6
* DIV $R7,=10
* SRM $R6,=$R5,=Z'000F'
*
* SCR $R5,4
* BDEC $R2,>DIGIT
* STR $R5,B6DCML+1
* LDB $B6,B6DCML-$AF+2 ** B6 RESERVED FOR PASS COUNT DISPLAY **
*
* TYPE PASS COUNT AND "DONE" IF 3 MINUTES ARE UP
*
* LDR $R1,<ZHRTC NO, ARE 3 MINUTES UP?
* CMR $R1,=Z'D20F' AB9F = 43935(DEC) = 65535-21600(DEC).
* AB9F IS THE NORMAL REAL TIME CLOCK VALUE
* D20F IS THE CORRECTED CLOCK VALUE
* BECAUSE THE CLOCK IS TURNED OFF WHEN
* THE CACHE IS ENABLED
*
* CLOCK SET INITIALLY TO FFFF = 65535(DEC). 21600 TICKS = 3 MINUTES.
*
* BG >GLEN 3 MINUTES NOT UP YET
*
* LDV $R1,=-1 SET UP FOR NEXT 3 MIN INTERVAL
* STR $R1,<ZHRTC
* CALL ZV$TC,MSG6 TYPE PASS MESSAGE
*
* CALL ZV$TD,PASCNT+$AF-1 TYPE THE COUNT
*
* CALL ZV$TC,MSG7 TYPE "3 MIN DONE"
*
*
* RTCN B DONE ** 3 MIN TESTING COMPLETE **
*
* TYPE PASS COUNT
*
* GLEN CALL ZV$TC,MSG6 TYPE PASS MESSAGE
*
* CALL ZV$TD,PASCNT+$AF-1 TYPE THE COUNT
*
*
* RTCN TURN ON THE CLOCK
*
* B NXTPAS REPEAT TEST FOR NEXT PASS
*
*
* MESSAGES
*****
*
* MSG1 IFZ $AF=Z,LAF1
* TEXT *CMCS1 CACHE MEMORY TEST*
*
* LAF1 NULL
* SAF1 NULL
* TEXT * 21 APR 1978 REV. B5*
*
* MSG2 TEXT *MEMORY GREATER THAN 64K. CHANGE TO CMCL1 ,*,Z'0D0A*
* * SET TO LAF MODE. OTHER WISE, WILLS*

```

		7420	746F	206C		
		6166	206D	6F64		
		652E	2020	6F74		
		6865	7220	7769		
		7365	2C20	7769		
		6C6C	2400			
001148	04D2	2020	6F6E	6C79	MSG2B	TEXT ' ONLY USE UP TO 64K LOCATIONS!\$'
	04D5	2075	7365	2075		
		7020	746F	2036		
		346B	206C	6F63		
		6174	696F	6E73		
		2124				
001149	04E2	6361	6368	6520	MSG2A	TEXT 'CACHE PRESENT\$'
	04E5	7072	6573	656E		
		7424				
001150	04E9	6361	6368	6520	MSG3	TEXT 'CACHE NOT PRESENT\$'
	04EC	6E6F	7420	7072		
		6573	656E	7424		
001151	04F2	6361	6368	6520	MSG3A	TEXT 'CACHE SIZE IS 4K\$'
	04F5	7369	7A65	2069		
		7320	346B	2400		
001152	04FB	6361	6368	6520	MSG3B	TEXT 'CACHE SIZE IS 2K\$'
	04FE	7369	7A65	2069		
		7320	326B	2400		
001153					MSG6	IFZ \$AF=2,LAF2
001154	0504	636D	6373	3120	TEXT	'CMCS1' P5\$'
	0507	2070	7324			
001155					LAF2	NULL
001158					SAF2	NULL
001159	0509	3320	6D69	6E20	MSG7	TEXT '3 MIN DUNES\$'
	050C	646F	6E65	2400		
001160	050F	6463	3124		MSGDC1	TEXT 'DC1\$'
001161	0511	6463	3224		MSGDC2	TEXT 'DC2\$'
001162	0513	636C	3124		MSGCL1	TEXT 'CL1\$'
001163	0515	6470	3124		MSGDP1	TEXT 'DP1\$'
001164	0517	6470	3224		MSGDP2	TEXT 'DP2\$'
001165	0519	6372	3124		MSGCR1	TEXT 'CR1\$'
001166	051B	6372	3224		MSGCR2	TEXT 'CR2\$'
001167	051D	7264	3124		MSGRD1	TEXT 'RD1\$'
001168	051F	7264	3224		MSGRD2	TEXT 'RD2\$'
001169	0521	756C	3124		MSGUL1	TEXT 'UL1\$'
001170	0523	7272	3124		MSGRR1	TEXT 'RR1\$'
001171	0525	7265	706C	6163	MSGCB	TEXT 'REPLACE CONTROLLER BOARDS\$'
	0528	6520	636F	6E74		
		726F	6C6C	6572		
		2062	6F61	7264		
		2400				
001172	0532	7265	706C	6163	MSGAP	TEXT 'REPLACE ARRAY PAC\$'
	0535	6520	6172	7261		
		7920	7061	6324		
001173					*	
001174					*	
001175					*	
001176					** RESERVED AREA	
001177					*****	
001178					*	
001179					*	
001180					*	
001181					** INTERRUPT SAVE/RESTORE AREA FOR LEVEL 4	
001182					* (RESTORED ON LEVEL CHANGE FROM 0,1 OR 3 TO 4)	
001183					*	
001184					ISA4	RESV \$AF00 TRAP SAVE AREA POINTER
001185	053B	0000			RESV 1,0	I.D. OF INTERRUPTING DEVICE
001186	053C	0000			RESV 1,0	RUPT SAVE MASK 1
001187	053D	0000			RESV 1,0	RUPT SAVE MASK 2
001188	053E	0000			ISA4P DC <T4A1	P-REG
001189	053F	0133			DC X'4000'	S-REG, SEI PRIVILEGE MODE
001190	0540	4000				FOR INTERRUPT HANDLER
001191					*	
001192					*	
001193					** SECONDARY TRAP SAVE AREA	
001194					*	
001195					SECTSA	RESV 12,0 LINKED TO TSA AT X'0002' TO PREVENT
001196	0541	0000			*	LEVEL 2 INTERRUPT WHEN TRAP AND
001197					*	RECORDS MACHINE STATE IN CASE OF
001198					*	DOUBLE (UNEXPECTED) TRAP.
001199					*	
001200					*	
001201					*	
001202					*	
001203					*	
001204	054D	E870			BUFF1	DC Z'E870' CACHE ENABLE
001205	054E	0300			DC Z'0300'	
001206	054F	7C03			DC Z'7C03'	
001207	0550	0011			DC Z'0011'	
001208	0551	0008			DC Z'0008'	MEMORY TO MEMORY MOVE
001209	0552	C801			DC Z'C801'	READ (DATA IN R4, ADDR IN B1)
001210	0553	E870			DC Z'E870'	CACHE DISABLE
001211	0554	0300			DC Z'0300'	
001212	0555	7C02			DC Z'7C02'	
001213	0556	0011			DC Z'0011'	
001214	0557	8385			DC Z'8385'	RETURN
001215					*	
001216					*	
001217					*	
001218	0558	0000			TEMP1	RESV 2,0
001219	055A	0000			TEMPA	RESV 1,0
001220	055B	0000			TEMPC	RESV 2,0
001221	055D	0000			TEMP2	RESV 2,0
001222	055F	0000			TEMP3	RESV 2,0
001223	0561	0000			TEMP4	RESV 1,0
001224	0562	0000			TEMP5	RESV 2,0
001225	0564	0000			TEMP6	RESV 2,0
001226	0566	0000			CAFLG	RESV 1,0
001227					*	
001228	0567	0000			TRPFLG	RESV 1,0 =0 IF CACHE IS PRESENT
001229					*	=1 IF NOT
001230	0568	0000			PASCNT	RESV \$AF,0 =0 IF NO TRAP OCCURED
001231					*	=1 IF TRAPS
001232	0569	0000			B6DCML	RESV 2,0 PASS COUNTER
001233					*	
001234					*	
001235	056B	0000			SI2FLG	RESV 1,0 =0 IF CACHE SIZE IS 4K

001236
 001237
 001238 056C 0000
 001239
 001240 056E 0000
 001241
 001242 056F 0000
 001243
 001244 0571 0000
 001245
 001246
 001247
 001248
 001249
 001250 0573 636F 7079 7269
 0576 6768 7420 3139
 3737 2C20 6279
 2068 6F6E 6579
 7765 6C6C 2069
 6E63 2E24

*
 * CASIZE RESV 2,0
 * NULL RESV \$AF,0
 * MEMHI RESV 2,0
 * PLOW RESV 2,0
 *
 *
 *
 *
 *
 *
 *

=1 IF CACHE SIZE IS 2K
 STORES LAST CACHE ADDR
 MEMORY HIGH IF PROM IS PRESENT
 PROM ISI LOCATION

TEXT 'COPYRIGHT 1977, BY HONEYWELL INC. \$'

001251
 001252
 001253 0584 0100
 U000 ERR COUNT
 TITLE CMCS1, *REV B*

END CMCS1, START

U000	ERR	COUNT	TITLE CMCS1, *REV B*	278	290C	315C	319	467	527C	540C	542C	570
			\$AF	276	290C	315C	319	467	527C	540C	542C	570
				273	616	632C	639	642	674	745	746C	758
				783	785	791	821	855C	861	864	865C	869
				880	888	910C	916	925	958	964	981	1016C
				1084C	1088	1096	1115	1124	1139	1142	1153	1156
				1230	1240							1185
			\$B1	310	311C	448	450	457	459	467	468	513
				516	518C	519	522	526	527C	531	536	540C
				542C	573	580	586	616	617	624	632C	640
				642	674	676C	680	694	698C	699	745	746C
				759	776C	778	788	805	807	854	855C	857C
				861	888	889	891	905	906	910C	916	917
				925	942	944	950	958	969	981	1015	1016C
				1022	1026	1028	1069	1070C				1021C
			\$B2	372	373C	405	406C	436	437C	508	509C	510
				512	522	566	567C	570	586	614	615C	740
				747	750	783	821	823C	864	865C	869	874C
				899B	917	936B	950					876C
				319	320C	321	322C	409	417	785	870	873
			\$B3	882C	889	919						880
				758	759	788	964	969				
			\$B4	422B	595B	655B	712B	721B	793B	828B	835B	899B
			\$B5	986B	995B	1036B	1054C	1058C				936B
			\$B6	337	774	778	807	1096				
			\$B7	791	793B							
			\$R1	306	307C	341	342C	343C	344	345C	350	351
				533	534	671	673C	742	744C	750	751C	753
				755C	756	757C	760	761C	770	771C	787	817
				818C	856	857C	858	866	867	868C	871	873
				877	878	879C	881	882C	959	960	961	963C
				1102	1112	1113C						1101
			\$R2	440	441C	498	499C	749	752B	784	822	823C
				875B	1089	1094B						872
			\$R3	786	873	874C	927	952C	954	959	971	
			\$R4	417	450	459	468	516	517C	519	520	531
				536	541	580	624	680	683	699	702	532
				946	1022	1023						908
			\$R5	675	676C	683	685	686	698C	702	704	705
				776C	777	789	803	806	905	908	942	775
				1021C	1023	1025	1092	1093	1095C			1017
			\$R6	289	377	412	444	575	620	633	636	637C
				677	695	735	750	751C	765	768C	812	815C
				893	911	914C	931	976	979C	1018	1090C	1092
			\$R7	287	376	380	411	415	443	446	576	578
				619	622	626	634	635	638C	646	678	681
				700	736	738	766	767	769C	772	773C	696
				816C	852	894	912	913	915C	932	977	681
				1019	1030	1088	1091					813
				1095C	1096							814
				1232	1204							980C
				B6UCML	747							
				BUFF1	374C							
				1226	439C	394C						
				1238	441C	499C	570	616				
				656	654							
				364	352B	354B						
				614	591B							
				622	648B							
				632	659B							
				651	614							
				654	630B							
				CMCS1	1	1253						
				508	471B	478b	494B					
				516	523B	537b						
				531	521B							
				540	535B							
				546	508	510						
				829	827							
				836	834							
				735	707B							
				812	764B							
				821	802B							
				827	740							
				834	804B							
				750	752B							
				772	819B							
				776	779B							
				792	808B							
				803	825B							
				805	842B							
				423	421							
				475	474							
				428	405							
				430	409							
				436	420B	425B						
				438	436							
				405	388B							
				487	448							
				490	457							
				493	455B							

	ZHRTCL	270	345C									
	ZHTH15	271	373C	406C	437C	509C	567C	615C				
	ZHTH17	273	511C	741C	1070C							
	ZHTSA	266	278	311C								
278	ZHTSA7											
	ZV\$AF	259	350									
	ZV\$EK	421B	474B	594B	654B	711B	720B	827B	834B	985B	994B	
		1035B										
	ZV\$HR	260	512	526								
	ZV\$HKU	261	353									
	ZV\$KD	262	514	640	694	854	859	1015	1026			
	ZV\$TC	301B	305B									
		355B	356B	387B	391B	493B	497B	1055B	1059B	1114B	1116B	
		1123B										
	ZV\$TD	1115B	1124B									
	ZV\$TTY	263										
	ZV\$UIH	264	328B									

178 LABELS
 761 REFERENCES
 1253 RECORDS
 1 U FLAGS
 2 M FLAGS
 4 N FLAGS
 6 CROSS REF VERSION L - 24 SEPT, 1976
 MS LINKER VERSION 5.00 04/27/78 1715.1 EST THU
 LINK MAP FOR CMCS1

	START	0100	
	LOW	0000	
	HIGH	0CF2	
	CURRENT	0CF3	
*LOC DEFS			
ZHCOMM	0000		
*CMCS1	0000	REV B	
ZHPFR	0000		
ZHTSA	0002		
ZHNTSA	0010		
ZHRTCL	0014		
ZHRTCC	0015		
ZHRTCL	0016		
ZHWDTC	0017		
ZHMERC	001F		
ZHIAFB	0020		
ZHTH29	0063		
ZHTH28	0064		
ZHTH27	0065		
ZHTH26	0066		
ZHTH25	0067		
ZHTH24	0068		
ZHTH23	0069		
ZHTH22	006A		
ZHTH21	006B		
ZHTH20	006C		
ZHTH19	006D		
ZHTH18	006E		
ZHTH17	006F		
ZHMEMP	006F		
ZHTH16	0070		
ZHLERR	0070		
ZHTH15	0071		
ZHNRES	0071		
ZHTH14	0072		
ZHPMEM	0072		
ZHTH13	0073		
ZHP-OP	0073		
ZHTH12	0074		
ZHTH11	0075		
ZHTH10	0076		
ZHTH9	0077		
ZHTH8	0078		
ZHTH7	0079		
ZHTH6	007A		
ZHUVFL	007A		
ZHTH5	007B		
ZHOP-N	007B		
ZHTH4	007C		
ZHTH3	007D		
ZHSC-N	007D		
ZHTH2	007E		
ZHTRC	007E		
ZHTH1	007F		
ZHMCL	007F		
ZHTSA2	0080		
ZHIVBS	0080		
ZHTVBS	0080		
*ZV\$TH	0584		
ZV\$TD	0589		
ZV\$TH	0584		
ZV\$TH2	05AC		
*ZV\$IH	05D4		
ZV\$ID	05D9		
ZV\$IAU	05DE		
ZV\$---	05F6		
ZV\$---	0608		
*ZV\$ER	066D	REV. 5.0	
ZV\$ER	066D		
ZV\$TA	0699		
ZV\$--U	0680		
*ZV\$GP	06DD		
ZV\$GP	06DD		
ZV\$--4	06FD		
*ZV\$HA	0709		
ZV\$HA	0709		
ZV\$HZ	0713		
ZV\$HS	070E		
*ZV\$T	0742	REV. 5.0	
ZV\$TC	074B		
ZV\$T	0742		
ZV\$QC	075F		
ZV\$Q	0754		
*ZV\$HD	0773		
ZV\$HD	0773		
*ZV\$IA	07A5	REV. 7	

ZV\$IA	07A8	
ZV\$ARG	0857	
ZV\$ABF	0859	
ZV\$--L	0814	
ZV\$IAV	07A6	
*ZV\$RU	0884	REV. 7
ZV\$RU	0884	
ZV\$UIH	0894	
ZV\$AF	0875	
ZV\$HRU	088D	
ZV\$HR	0893	
ZV\$LK	0890	
ZV\$DA1	0873	
ZV\$HM	08DA	
ZV\$SV1	0A39	
ZV\$SV3	0A59	
ZV\$TTY	0877	
ZV\$SV2	0A49	
ZV\$UTP	090B	
ZV\$TID	0876	
ZV\$CFZ	0880	
ZV\$TK	087C	
ZV\$KAK	087D	
ZV\$ST1	0881	
ZV\$KCC	0882	
ZV\$BUD	0878	
ZV\$ULB	0884	
ZV\$KCB	0885	
ZV\$NSR	0889	
ZV\$STR	0887	
ZV\$IZ	089E	
ZV\$HRL	088E	
ZV\$LRU	088F	
ZV\$LRL	0890	
ZV\$HBD	0891	
ZV\$CF1	087F	
ZV\$BKS	088B	
ZV\$BKF	088C	
ZV\$--S	0896	
ZV\$KMD	0874	
ZV\$MCP	0892	
HIBAUD	0891	
ZV\$RAW	087E	
ZV\$RUT	0A95	
ZV\$CTL	087B	
ZV\$BI	09B6	
ZV\$TST	0AEB	
ZV\$MDC	0ABF	
ZV\$K99	0CB0	
ZV\$ISA	0899	
ZV\$ZRU	0918	
ZV\$D\$H	091A	
ZV\$CPU	087A	
ZV\$K50	08F8	
ZV\$R60	0903	
ZV\$RT	0BFA	
ZV\$ALL	0879	
*NLCHPG	0CC2	T+V
MLCHPG	0CC2	
ENDCHP	0CF3	
*UNLINK	MODULE(S)	
ZV\$TC		
ZV\$T		

