

IDENTIFICATION

Product Code: MAINDEC 9A-D1FA-D (D)  
Product Name: Extended Memory Address Test  
Date Created: December 29, 1967  
Maintainer: Diagnostics Group  
Author: J. W. Richardson

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1912  
The following is a list of the  
names of the persons who were  
present at the meeting held  
at the residence of Mr. J. H. [unclear]  
on the 15th day of [unclear] 1912.

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1. Abstract

The PDP-9 Extended Memory Address Test tests all of memory not occupied by the program to make sure that each location can be uniquely addressed. This is performed by a series of five tests. The first three tests write the address and complement address of each memory location into itself, and then check each location to make sure each is correct. The fourth test slides a one through a word of zeroes in each bank, and test five writes and reads a pattern designed to detect adjacent bit positions shorted within the memory stack. Any one or a combination of, the five tests is selectable with ACS 3,4,5,6, and 7.

The program relocates automatically from bank to bank, and tests all of core memory from each bank.

2. Requirements

2.1 Equipment

A PDP-9 equipped with a minimum of 8K of core memory.

2.2 Storage

The program requires 4476(8) locations of core memory. Locations 100 through 6007 are used.

3. Loading Procedure

3.1 Method

The tape supplied is punched in the HRI mode.

Place the tape in the reader.

Set the address switches to 100.

Press I/O RESET, and then READ-IN.

At the completion of loading, the program header will be printed, followed by a halt with the PC = 4477.

4. Starting Procedure

After the header has been printed, set ACS 14 through 17 to indicate the amount of memory to be tested, and press CONTINUE. The program will run until stopped by the operator with ACS 0 on a 1.

4.1 Control Switch Settings

Immediately after the program header, a halt occurs so the operator may signify the amount of core memory to be tested. This is done by placing the first bank to be tested in ACS 14 and 15, and the last bank in ACS 16 and 17. The values should equal bank numbers, i.e., bank 0 = 00; bank 1 = 01; bank 2 = 10; bank 3 = 11. Press CONTINUE after setting the test limits in ACS 14 - 17. The combinations recognized by the program are listed below.

<u>Test Memory Bank to Memory Bank</u>			ACS			
			<u>14</u>	<u>15</u>	<u>16</u>	<u>17</u>
(8K)	0	0	0	0	0	0
(16K)	0	1	0	0	0	1
(24K)	0	2	0	0	1	0
(32K)	0	3	0	0	1	1
(8K)	1	1	0	1	0	1
(16K)	1	2	0	1	1	0
(24K)	1	3	∅	1	1	1
(8K)	2	2	1	∅	1	∅
(16K)	2	3	1	∅	1	1
(8K)	3	3	1	1	1	1

Unless other than normal operation is desired, place all other ACS down.

The program recognizes ACS 14 and 15 as the lowest order 8K bank to be tested, and ACS 16 and 17 as the

highest order 8K bank to be tested. The lower limit value must not exceed the higher limit value. When this occurs the message "L.L. EXCEEDS U.L." is printed, and a halt occurs with the PC = 2064. To recover, correct the ACS settings and press CONTINUE.

The amount of core memory tested at one time when using a PDP-9 without extended memory is 4K. The program automatically relocates to test the area occupied by the program.

The amount of extended memory tested at one time equals the amount selected by ACS 14 through 17, minus 4K. If 32 K of core memory is to be tested the actual amount tested at one time is 28K. The 4K field occupied by the program is not tested until program relocation takes place.

#### 4.2 Starting Address

The program may be restarted from location 100 of the memory bank containing the program (as indicated by extended program counter bits 3 and 4). If the program happens to be in the higher 4K field (as indicated by MB or PC bit 5 on a 1), the starting address is 10100.

#### 4.3 Program Action

After setting the ACS and pressing CONTINUE, or after a restart, the program initializes various constants and then senses the ACS and checks for invalid switch settings. If all is in order the lower and upper limit addresses of memory are determined from ACS 14 through 17.

The program next performs the five tests, and then determines the next area to relocate to.

#### 4.3.1 Program Relocation

Program relocation depends entirely upon the amount of core memory being tested. Under certain conditions the program will not relocate at all, but will remain in the current bank to perform all five tests (see below). For PDP-9s equipped with extended memory, the program normally first relocates to the highest order memory bank under test. From there it relocates to the next lower bank (after performing all five tests). The program keeps relocating to the next lower memory bank until it reaches the lowest order bank under test. The testing and relocation cycle is then repeated. This cycle continues until stopped by the operator with ACS 0. The program will always occupy the lower 4K field (00000 to 07777) of any memory bank.

The program will not relocate if any of the conditions described below exist.

- a. A forced relocation has been made (section 8.2.7).
- b. The program is currently located in an upper 4K field (10000 to 17777), and one or more banks other than the one containing the program is being tested.
- d. An error was detected in all of the available lower 4K areas under test.
- e. ACS 9 is on a one to inhibit relocation (section 8.2.5).

The EPC indicators will indicate the current bank containing the program.

If the program is testing a single 8K memory bank, which contains the program, the upper 4K field is first tested, and then the lower 4K field is tested after relocation to the upper 4K. The program relocates back to the lower 4K field after testing is completed. Bit 5 of the MB will glow brightly when the program is in the upper 4K.

The program will not relocate if any of these conditions exist:

- a. A forced relocation has been made (section 8.2.7).
- b. An error was detected in the opposite 4K field.
- c. ACS 9 is on a 1 to inhibit relocation (section 8.2.5).

One complete pass of the program is defined as the execution of all five tests on all available memory from each memory bank or from each 4K field in the case of a single bank being tested.

The program will print "END" after all tests (or those selected) have been performed from one memory bank. If 16K is being tested, one complete pass would be indicated by END being printed twice, and four times if 32K is being tested.

If the program is to be run for lengthy periods, the operator may inhibit the message by placing a NOP instruction in locations 215 and 216. These locations ordinarily contain JMS CRLF and JMS PNXT, respectively.

## 5. Operating Procedure

### 5.1 Operational Switch Settings

See sections 4.1, and 8.2.

### 5.2 Operator Action

- a. Load the program as described in section 3.1.
- b. Specify the test limits in ACS 14 through 17 (section 4.1).
- c. Press CONTINUE. The program will run until stopped by the operator.

The program should be stopped with ACS  $\emptyset$  rather than the STOP key, because the program may be performing a relocation when STOP is pressed, resulting in various constants and addresses being modified. Restarting the program would give unpredictable results.

The PC will = 2064 when halting with ACS  $\emptyset$  on a 1. To recover place ACS  $\emptyset$  down, and press CONTINUE. ACS changes may be made after the halt, in which case the program will execute the new functions specified. If no changes are made, the routine which was halted will be resumed. ACS  $\emptyset$  is sensed at the end of each test routine, and after each error print-out.

### 5.3 Subroutine Abstracts

The program executes a series of five tests on core memory. Each test writes a unique pattern, and checks each location for error.

Test 1 writes the value of each memory location into itself, from the lowest order to the highest order bank



under test. The address pattern is then read, and checked for error, in the same direction, i.e., from the lowest to highest bank. The pattern is then read and checked for error in the reverse direction, i.e., from the highest bank to the lowest bank. This sequence is repeated twice before test 2 is initiated. Test 1 may be run by itself by placing ACS 3 on a 1.

Tests 2,3,4 and 5 write and read their patterns into one bank at a time. The rest of memory will contain an all 1's pattern. After each pattern is written and read, the rest of memory is checked to make sure that its pattern has not changed. After one bank has been tested the next higher bank in sequence will contain the pattern, and the rest of memory will equal all 1's. This is continued for one test until all banks have contained the test pattern. The next test in sequence will then be initialized. All of memory is set 1's before the next bank in sequence is tested.

Test 2 first writes 1's into all of memory and then writes the complement address of each memory location into itself. The only bits which are complemented are bits 5 through 17. Bits 3 and 4 are not complemented. Test 2 writes the pattern in one bank in the forward direction, and then reads and checks for error in the forward and reverse directions. The rest of memory is then read and checked for error (in the forward direction only). After each bank is tested in this manner, test 3 is initialized. Test 2 may be run alone by placing

ACS 4 on a 1.

Test 3 first writes 1's into all of memory, and then writes the complement value of each address into itself. It differs from test 2 in that all 18 bits are complemented. The pattern is written once in the forward direction in one bank. The rest of memory is then read and checked for error (in the forward direction). The bank with the address pattern is then read once in the forward and reverse directions, after which the rest of memory is again checked.

The same bank with the address pattern is then reset to all 1's, and the same address pattern is then written in the reverse direction. The rest of memory is then checked; the bank with the pattern is read in forward and reverse directions, and finally the rest of memory is again checked. The next higher bank in sequence will be tested in the same manner. After all banks have been tested in this manner, test 4 is initialized. Test 3 may be run alone by placing ACS 5 on a 1.

Test 4 first writes 1's into all of memory, and then writes a sliding 1 pattern into one bank. Each location of the bank will contain a word of all 0's except for one bit position. The bit set is rotated one place to the left for each memory location, starting with bit 17. The following test sequence is repeated 18 times, resulting in every bit in each memory location being set.

- a. Write all 1's into all of memory.
- b. Write a sliding one pattern into one bank.
- c. Read and test rest of memory.

- d. Read and test the bank with the pattern in the forward direction only.
- e. Read and test rest of memory.
- f. Repeat steps b through e 17 more times before testing the next sequential bank in the same manner.

After all banks have been tested with a sliding 1, test 5 is initialized. Test 4 may be run alone by placing ACS 6 on a 1.

Test 5 first writes ones into all of memory. A pattern consisting of 4 words of 0's followed by 4 words of 1's is then written into one bank. The following sequence is then executed.

- a. Write the pattern in one bank in the forward direction.
- b. Read the bank with the pattern in the forward direction.
- c. Read again in the reverse direction.
- d. Read rest of memory.
- e. Write the complement pattern in the forward direction, and repeat steps b, c and d, then do step f.
- f. Write the pattern in the reverse direction, and do steps b, c and d, then do step g.
- g. Write the complement pattern in the reverse direction, and do steps b, c and d, then setup to repeat a through g on the next higher bank in sequence.

After all banks have been tested, the program then relocates automatically and starts over with test 1. Test 5 may be run alone by placing ACS 7 on a 1.

6. Errors

6.1 Error Print-outs and Description

Immediately after program load the header shown below is printed.

TEST OCTAL ADR. GOOD BAD BANK WITH PAT.

Where:

TEST = the current test which detected an error.

OCTAL ADR. = the memory location which contains the data in error.

GOOD = what the data should have been in that location.

BAD = the data as read from that location.

BANK WITH PAT. = the current bank under test which contains the pattern of the failing test. For test 1, this will equal "ALL", since test 1 writes an address pattern into all memory under test. For test 2,3,4 or 5, it will equal 0,1,2 or 3, depending on the amount of core memory available for test.

Example:

TEST	OCTAL ADR.	GOOD	BAD	BANK WITH PAT.
1	060100	060100	060000	ALL
2	060100	077677	076677	3
3	060100	777677	776677	3
4	023000	000002	000003	1
5	047777	777777	000000	2

TEST	OCTAL ADR.	GOOD	BAD	BANK WITH PAT.
5	047776	777777	000000	2
5	047775	777777	000000	2
5	017777	777777	000000	2

During test 1 address 100 in bank 3 was found to be in error. From the example, it can be seen that bit 11 was dropped. Bit 8 was dropped during test 2 in the same address, and also during test 3. Bit 17 was picked up at location 3000 in bank 1 during the sliding 1 test. Three consecutive addresses in bank 2 were in error during test 5. The test was reading in the reverse direction at the time, because the addresses are printed in descending order. Also, when checking rest of memory, location 17777 in bank 0 was found to be incorrect.

After each print-out the program continues with the next memory location to test.

ACS 0 may be raised, during an error print-out, to halt the program after print-out is completed. Press CONTINUE to resume the test in progress, or to execute new ACS settings made.

ACS 1 may be raised any time to inhibit all error print-outs (section 8.2.2).

ACS 2 on a 1 will cause the TTY BELL to ring on each error. Print-outs will be inhibited (section 8.2.3).

ACS 10 may be raised, during print-out, if the operator wishes to test selected areas of memory, or if certain addresses are to be suppressed. See section 8.2.6.

7. Restrictions

7.1 Starting Restrictions

Start from location 100 of the memory bank containing the program.

7.2 Operating Restrictions

Don't use the STOP key to halt the program. Place ACS 0 on a 1.

8. Miscellaneous

8.1 Execution Time

The time required to test one 8K bank with all 5 tests is approximately 2 1/2 minutes.

8.2 Applications

For operating convenience, and as an aid to troubleshooting, each ACS is assigned a unique function. The ACS assignments and their effect on the program are described below. Please note that it is important that the program should be halted before changing the ACS, unless otherwise specified in the descriptions below. Otherwise, the program may be destroyed. Halting the program with ACS 0 is preferred, rather than with the STOP key. Using the STOP key may result in a halt while the program is in the process of relocating, which is disastrous.

8.2.1 Halt After Test or Error - ACS 0

Raising ACS 0 at any time while the program is running will cause a halt after the current test is completed. The PC will = 2064. ACS settings may then be changed if desired. Press CONTINUE to recover. If no ACS changes, the program will resume the test which was interrupted. If ACS changes were made, the new settings are stored,

and executed.

Raising ACS 0 during an error type-out will cause a halt at the same location mentioned above. Proceed exactly as described in the above paragraph.

#### 8.1.2 Inhibit Error Typeout - ACS 1

Raising ACS 1 causes all error typeouts to be inhibited. All other message typeouts are not inhibited. ACS 1 may be raised while the program is running.

#### 8.1.3 Bell on Error - ACS 2

ACS 2 on a 1 causes the program to ring the TTY BELL whenever an error occurs. This is convenient when testing with power supply margins. ACS 2 has precedence over ACS 1 if both should happen to be up. ACS 2 may be raised while the program is running.

#### 8.2.4 Test Selection - ACS 3 through 7

Any one, or any combination of tests may be executed by raising any one or any combination of ACS 3 through 7. ACS 3 specifies test 1; ACS 4, test 2; ACS 5, test 3; ACS 6, test 4; ACS 7, test 5. The test specified by the most significant ACS will be executed first.

If all five ACS are down, all five tests are executed in order starting with test 1.

Program relocation is not effected, regardless of the ACS settings.

ACS 3 through 7 should not be changed while the program is running.

#### 8.2.5 Inhibit Program Relocation - ACS 9

The program normally relocates automatically as indicated by MB indicators 3,4 and 5. To retain the

program in its current 4K field, raise ACS 9 at any time.

#### 8.2.6 Request Keyboard Input - ACS 10

ACS 10, when up, indicates to the program that the operator wishes to select one test, along with one or more addresses to be suppressed, and that one or more blocks of addresses are to be tested, disregarding all other addresses. ACS 10 is recognized only immediately after an error print-out, restarting from 100, or after pressing CONTINUE after a halt with ACS 0.

The program will print the following information, waiting for input from the keyboard after each line:

TEST # -

SUPPRESS -

BLOCK #1 -

BLOCK #2 -

The program expects input information for each line. Typing only a carriage return indicates that the function represented by that line is not wanted. The next line will be printed. An explanation of each line follows.

TEST # - Type the test number desired (1 to 5). Any number less than 1 or greater than 5 is an error, and a ? will be printed, followed by TEST # - being printed again. If no particular test is wanted, type a carriage return only. The pattern used by the last test in progress will be used. In the case of no tests being previously run, test 1 will be used. TEST # is used



only when one or more blocks are specified. If no blocks are selected, TEST # is ignored.

SUPPRESS - Error print-outs for one or more individual addresses, or a block of consecutive addresses may be suppressed. The addresses typed must be 5 digit octal numbers. Up to 256 addresses, individual or a block, may be suppressed. Any amount over 256 will be ignored by the program.

Suppression of individual addresses is indicated by separating each 5 digit address with a colon, terminating the line with a carriage return after the last address or colon. A block is indicated by typing the first address of the block, and the last address of the block, separating the two by a comma. An automatic carriage return is provided after the second address is typed. The individual or block of addresses do not have to be typed in numerical order.

Any input which differs from the above will result in a ? being printed, followed by SUPPRESS - being printed again. Typing a carriage return only indicates no addresses are to be suppressed.

BLOCK #1 and BLOCK #2 - Any length block of consecutive addresses to be tested may be indicated in either BLOCK #1 or BLOCK #2, or both. The block limits must not overlap the program or exceed the amount of memory available. If the limits of either block overlap the program a message will be printed giving the location of the program. The

current line will be re-printed, and the operator must then specify new limits. Indicate the block to be tested by typing the first address and last address of the block, separating the two 5 digit addresses with a comma.

The program handles the blocks to be tested as if each were a separate memory bank. That is, if test 1 were selected, each address within either block would contain its own value. If any other test were selected, BLOCK 1 would contain the pattern, and BLOCK 2 would contain an all 1's pattern. The rest of memory outside of the block limits is ignored. Refer to section 5.2, Subroutine Abstracts, for a description of methods of testing, and patterns generated by each test.

When two blocks are selected, their limits should not overlap when tests 2, 3, 4 or 5 are selected. BLOCK 2 will always contain an all 1's pattern with these tests, and error print-outs will occur if both block limits overlap.

If only one block is selected, the pattern will be written in that block, and the rest of memory is ignored. A single block may be indicated after BLOCK 1 or BLOCK 2; one of the lines being left blank.

If no blocks are to be tested type a carriage return after BLOCK #1 and BLOCK #2. The program will start over with test 1, or the test selected in ACS 3-7.

If a block is selected, program relocation will not take place.

Press the RUBOUT key if a typing error was made.

The routine will start over with "TEST # -".

If the pattern generated by test 5 is to be used, the operator may select one address, or a block of  $2^8$  (8) addresses with no restriction. Any amount over  $2^8$  (8) addresses must be in multiples of  $2^8$  (8). This is necessary due to the method of generating and reading the pattern. If a block of more than  $2^8$  addresses is not a multiple  $2^8$ , invalid error print-outs occur indicating an error at every fourth location within the block. This restriction applies to block #1 when two blocks are selected, or to a single block selected.

Examples:

Assume the program to be located in the lower 4K field of bank 1.

A. TEST # - 5  
SUPPRESS -  
BLOCK #1 - ~~00077~~<sup>00100</sup>,01100  
BLOCK #2 -

The pattern generated by test 5 will be written and checked for error from ~~00077~~<sup>00100</sup> to 01100 of bank 0. The rest of memory will contain an all 1's pattern, and will be ignored by the program since only one block is specified. All errors will be printed.

B. TEST # - 5  
SUPPRESS -  
BLOCK #1 - ~~00077~~<sup>00100</sup>,01100  
BLOCK #2 -

The program will perform exactly as described in example A.

C. TEST # - 3  
SUPPRESS  
BLOCK #1 - 3777,36000  
BLOCK #2 - 0000,16000

The pattern generated by test 3 will be written and checked for error in the first block from 36000 to 37777. An all 1's pattern will be written and checked for error in the second block from 00000 to 16000.

D. TEST # -

SUPPRESS - 377777:36100:35000

BLOCK #1 - 00000,16000

BLOCK #2 - 36000,37777

The pattern used by the last test in progress will be written in BLOCK 1, and an all 1's pattern in BLOCK 2 (note that the blocks are reversed from example C).

Any error at the two addresses 36100 or 37777 will not be printed. Address 35000 is meaningless since it is located outside the test limits.

E. TEST # - 4

SUPPRESS - 36000,37777

BLOCK #1 - 00000,16000

BLOCK #2 - 36000,37777

All errors encountered in BLOCK 2 will not be printed.

F. TEST # - 8

?

TEST # - 1

SUPPRESS - 036000

?

SUPPRESS - 36000,37777

BLOCK #1 - 40100,40100

BLOCK #2 - 20000,30100

BLOCK #2 - LIMITS OVERLAP PROGRAM

PROGRAM IS LOCATED IN LOWER 4K FIELD OF BANK 1

BLOCK #2 - 30000,30100

Example F indicates program response to format errors. The first, TEST #, is self-explanatory, since there are only 5 tests to choose from. The second, SUPPRESS, was in error because the number exceeds 5 digits. The program is assumed to be in the lower 4K of bank 1 for these examples, and the first address selected for BLOCK 2 is within this area. The example shows the error message printed for BLOCK 2, followed by the operator's correction.

G. TEST # - 2  
SUPPRESS - 10000,10377  
BLOCK #1 -  
BLOCK #2 -

The program will not loop on test 2, but will re-start with test 1, or the test specified in ACS 3-7. TEST # is used only when one or more blocks are specified. All of memory specified in ACS 14-17 will be tested.

Any errors detected within the block indicated after SUPPRESS will not be printed, regardless of which test is running.

To return to normal program operation type a carriage return after each of the four lines, or restart the program from 100.

#### 8.2.7 Program Relocation - ACS 11 and 12

The operator may relocate the program to any 4K field in any memory bank by specifying the desired bank in ACS 16 and 17, and the 4K field in ACS 11. Use the following procedure.

- a. Halt the program with ACS 0
- b. Place ACS 12 up to indicate a program relocation.
- c. Place ACS 11 up if an upper 4K field is wanted (10000 to 17777), or down for a lower 4K field (00000 to 07777).
- d. Place in ACS 16 and 17 the desired memory bank (00=bank 0; 01=bank 1; 10=bank 2; 11=bank 3).
- e. Press CONTINUE. The program will relocate to the specified area, and a halt will occur in the present area with the PC = .
- f. Place ACS 11 and 12 down, and reset ACS 14 - 17 to the desired value. Any other ACS may be changed at this time.
- g. Press CONTINUE to execute the program in the new 4K area.

The program will not automatically relocate again until it is restarted from address 100.

Before the relocation takes place the new area to be occupied is first checked to see if errors have been previously detected. If an error was detected the message "ERROR IN SELECTED 4K" is printed. A halt then occurs with the PC = 2064. The operator may then change the destination, or press CONTINUE to ignore the warning, and relocate to that area.

8.2.8 Upper and Lower Core Memory Testing Limits - ACS 14 - 17

ACS 14 and 15 specify to the program the first (or lowest order) memory bank to test, and ACS 16 and 17 the last (or highest order) bank. A single bank may be tested by setting ACS 14 and 15 to equal ACS 16 and 17.

If the 8K bank under test does not contain the program relocation will not take place. However, testing more than 8K allows relocation to take place.

9. Program Description

The Extended Memory Address test is intended for use with PDP9's equipped with the extended memory option, but may be used on basic 8K PDP-9's as well. A total of five tests are executed by the program. Each test writes a unique pattern into core memory and then checks for error. The first test writes the value of each memory address into that address, with all available banks containing the pattern. The remaining four tests write their patterns into one 8K bank at a time, with rest of memory containing an "all ones" pattern. The patterns were chosen so as to detect word and digit driver errors, as well as shorted wires within any bank (see section 5.2).

Control of the program is given to the operator by means of the ACS. The operator may halt the program, inhibit error print-outs, substitute the TTY BELL for error indication, halt after print-out, select any one or group of tests, inhibit program relocation, specify any single address or group of addresses to be suppressed,

any single address or up to two blocks of addresses for testing, relocate the program to any 4K area, and vary the number of 8K banks to test. See section 8.2 for the ACS designations for the above functions.

The program automatically relocates after performing all specified tests on the amount of core memory selected.

10. Listing



```

/IOT DEFINITIONS TO KEEP MACRO-9 HAPPY
/
700001 CLSF=700001
700004 CLOF=700004
700044 CLON=700044
/
700101 RSF=700101
700102 RCF=700102
700112 RRR=700112
700104 RSA=700104
700144 RSR=700144
/
700201 PSF=700201
700202 PCF=700202
700204 PSA=700204
700244 PSR=700244
/
700301 KSF=700301
700312 KRR=700312
/
700401 TSF=700401
700402 TCF=700402
700406 TLS=700406
/

```

```

.TITLE XMADR9
/PDP-9 EXTENDED MEMORY ADDRESS TEST. HRI LOAD ADDRESS
/=100. PLACE LOWEST BANK # TO TEST IN ACS 14-15; HIGHEST
/BANK # TO TEST IN ACS 16-17.

```

```

AP100
/
00100 700002
00101 700004
00102 204550
00103 045511
00104 144756
00105 100106
00106 000000
00107 200106
00110 505076
00111 044770
00112 044771
00113 760000
00114 044760
00115 045022
00116 045012
00117 045014
/
00120 202310
00121 044757
00122 202536
00123 042537
00124 707704
00125 760000
00126 062537

```

```

/
BEGIN IOF
CLOF
LAC DLMT
DAC KEND+402 /752525 IS AT END OF TABLE
D7M FLAGS /CLEAR PROGRAM FLAGS
JMS .+1 /STORE EPC
0
LAC .-1
AND K60K /MASK BITS 3 AND 4
DAC FLOAD /BANK WHICH 1ST HAS PROGRAM
DAC FLOADA /CURRENT BANK WITH PROGRAM
LAW
DAC ERWRD+1
DAC LAST
DAC BLOC1
DAC BLOC3
/
LAC ERTBL
DAC ERWRD
LAC STRL /SETUP POINTERS
DAC SUPTBL /SUPPRESSION TABLE
LEM
LAW
DAC* SUPTBL

```

00127 750004  
00130 044773  
00131 505047  
00132 744200  
00133 603354

```

/
/RETURN TO STOVER AFTER ANY ACS CHANGES DURING
/RUNNING.
/
STOVER LAS /READ ACS
DAC MCWA /PROGRAM CONTROL WORD - CHANGES -
AND K40 /MASK BIT 12
SZA:CLL /DO A FORCED MOVE IF NO SKIP
JMP FCDMV /RELOCATE
/
.EJECT
```

/ NOW DETERMINE TFST LIMITS AND TESTS

00134	707704	LEM	
00135	204773	LAC MCWA	/CONTROL WORD C(ACS)
00136	505044	AND K3	/MASK 16 AND 17
00137	044774	DAC ULBNK	/C(ULBNK) = HIGHEST RANK #
00140	204773	LAC MCWA	
00141	505045	AND K14	/MASK 14 AND 15
00142	742020	RTR	
00143	740001	CMA	
00144	345043	TAD K1	/2'S COMPLEMENT
00145	344774	TAD ULBNK	/SUBTRACT LOW RANK # FROM HIGH #
00146	741100	SPA	/LOWER BETTER RE LESS THAN HIGHER
00147	603567	JMP HEXED	/IT IS NOT. PRINT ERROR MESSAGE
00150	204773	LAC MCWA	/CONTROL WORD
00151	505046	AND K17	/MASK L.L. AND U.L.
00152	340320	TAD BASE	/ADD BASE & LIMITS AND JMP* ON
			/THE SUM
00153	040224	DAC LOCN	
00154	707704	LEM	/EXTEND OFF
00155	620224	JMP* LOCN	/DETERMINE ADDRESS TEST LIMITS

.EJECT

/ RETURN HERE AFTER SETTING UP ADDRESS LIMITS

00156	204773	EXTST	LAC MCWA	/CONTROL WORD
00157	505051		AND K200	/MASK BIT 10
00160	740200		SZA	/KEYBOARD INPUT IF NO SKIP
00161	602312		JMP KYBRD	/GO ACCEPT INPUT
00162	204773		LAC MCWA	/PROGRAM CONTROL WORD (ACS)
00163	505077		AND K76K	/K76K = 076000
00164	741200		SNA	/NO SKIP = NO INDIVIDUAL TESTS
00165	600314		JMP DOALL	/SETUP TO RUN ALL TESTS
00166	205075	EXAM1	LAC K40K	/K40K = 040000
00167	504773		AND MCWA	
00170	740200		SZA	/NO SKIP = DO TEST 1
00171	600321		JMP TST1	
00172	205073	EXAM2	LAC K20K	
00173	504773		AND MCWA	
00174	740200		SZA	/NO SKIP = DO TEST 2
00175	600461		JMP TST2	
00176	205072	EXAM3	LAC K10K	
00177	504773		AND MCWA	
00200	740200		SZA	/NO SKIP = DO TEST 3
00201	600730		JMP TST3	
00202	205071	EXAM4	LAC K4K	/K4K = 004000
00203	504773		AND MCWA	
00204	740200		SZA	/NO SKIP = DO TEST 4
00205	601151		JMP TST4	
00206	205070	EXAM5	LAC K2K	
00207	504773		AND MCWA	
00210	740200		SZA	/NO SKIP = DO TEST 5
00211	601323		JMP TST5	
00212	204474		LAC END	/END=PEND
00213	045032		DAC PRNT	
00214	707704		LEM	
00215	104537		JMS CRLF	/CR,LF
00216	103010		JMS PNXT	/PRINT END
00217	750004		LAS	
00220	505065		AND K400	
00221	740200		SZA	
00222	600127		JMP STOVER	
00223	603031		JMP MOVE	/DONE ALL TESTS, NOW GO DETERMINE /DEST'N FOR RELOCATING PROGRAM

.EJECT

00224	000000		0	
00225	600245	LOCN	JMP AD01	/TEST BANK 0 TO 0
00226	600251		JMP AD02	/TEST BANK 0 TO 1
00227	600253		JMP AD03	/TEST BANK 0 TO 2
00230	600255		JMP AD04	/TEST BANK 0 TO 3
00231	740040		HLT	/SHOULD NEVER GET HERE
00232	600257		JMP AD05	/TEST BANK 1 TO 1
00233	600263		JMP AD06	/TEST BANK 1 TO 2
00234	600265		JMP AD07	/TEST BANK 1 TO 3
00235	740040		HLT	/SHOULD NEVER GET TO HERE,
00236	740040		HLT	/OR HERE,
00237	600267		JMP AD08	/TEST BANK 2 TO 2
00240	600273		JMP AD09	/TEST BANK 2 TO 3
00241	740040		HLT	/SHOULD NEVER GET HERE,
00242	740040		HLT	/OR HERE
00243	740040		HLT	/OR HERE
00244	600275		JMP AD10	/TEST BANK 3 TO 3
00245	205103	/		
00246	044775	AD01	LAC K17S	/K17S = 17777
00247	044776		DAC LAST1	/U.L. FOR BANK 0
00250	600302		DAC LAST2	
			JMP LOWA	/SETUP L.L. TO = BANK 0
			.EJECT	

```

/
00251 205104 / AD02 LAC K37S /K37S = 37777; U.L. FOR BANK 1
00252 600246 JMP AD01+1

/
00253 205105 / AD03 LAC K57S /K57S = 57777; U.L. FOR BANK 2
00254 600246 JMP AD01+1

/
00255 205106 / AD04 LAC K77S /K77S = 77777; U.L. FOR BANK 3
00256 600246 JMP AD01+1

/
00257 205104 / AD05 LAC K37S /K37S = 37777; U.L. FOR BANK 1
00260 044775 DAC LAST1 /U.L. (UPPER LIMIT) TO TEST
00261 044776 DAC LAST2
00262 600310 JMP LOWB /SETUP L.L. TO = BANK 1

/
00263 205105 / AD06 LAC K57S /K57S = 57777; U.L. FOR BANK 2
00264 600260 JMP AD05+1

/
00265 205106 / AD07 LAC K77S /K77S = 77777; U.L. FOR BANK 3
00266 600260 JMP AD05+1

/
00267 205105 / AD08 LAC K57S /K57S = 57777; U.L. FOR BANK 2
00270 044775 DAC LAST1 /U.L. TO TEST
00271 044776 DAC LAST2
00272 600312 JMP LOWC /SETUP L.L. TO = BANK 2

/
00273 205106 / AD09 LAC K77S /K77S = 77777; U.L. FOR BANK 3
00274 600270 JMP AD08+1

/
00275 205106 / AD10 LAC K77S /K77S = 77777; U.L. FOR BANK 3
00276 044775 DAC LAST1
00277 044776 DAC LAST2
00300 205076 LAC K60K /L.L. = BANK 3 ALSO
00301 600303 JMP LOWA+1 /STORE AND EXIT

/
00302 205042 / LOWA LAC K0 /BANK 0
00303 045001 DAC FIRST1
00304 045002 DAC FIRST2
00305 345103 TAD K17S /ADD 17777
00306 044777 DAC LSTLOC /EXIT AND START TESTING
00307 600156 JMP EXTST

/
00310 205073 / LOWB LAC K20K /BANK1
00311 600303 JMP LOWA+1

/
00312 205075 / LOWC LAC K40K /BANK 2
00313 600303 JMP LOWA+1

/
.EJECT
    
```

```

/
/SFTUP TO RUN ALL TESTS
/
00314      204773
00315      245077
00316      044773
00317      600321
00320      000225

DOALL      LAC MCWA          /CONTROL WORD (ACS)
           XOR K76K         /SET ALL TEST SWITCHES
           DAC MCWA
           JMP TST1         /START TEST 1

/
BASE       LOCN+1
/
/TEST 1. EACH LOCATION WILL CONTAIN ITS OWN
/VALUE. ALL OF MEMORY SPECIFIED WILL CONTAIN
/THE PATTERN.
/
00321      102035
00322      760261
00323      045003
00324      205001
00325      045000
00326      101674
00327      777776
00330      045004
00331      777775
00332      045005
00333      707702
00334      205000
00335      065000
00336      445004
00337      600336
00340      777776
00341      045004
00342      445005
00343      600334
00344      205000
00345      544777
00346      600351
00347      445000
00350      600331
00351      760000
00352      545012
00353      741000
00354      603775
00355      445000
00356      101636
00357      101674
00360      600331

TST1       JMS WRT1S        /WRITE 1'S INTO ALL OF MEMORY
           LAW 261
           DAC TNUM        /TEST NUMBER
           LAC FIRST1      /FIRST1 = C(ACS 14,15)
           DAC MEMADR      /ADR. COUNTER
           JMS CBANK       /SEE IF TESTED BANK HAS PROGRAM

WBLK1      LAW -2
           DAC RPETE       /DELAY COUNTER
           LAW -3
           DAC WRCNT       /COUNTS 3 TIMES FOR EACH ADDRESS
           EEM             /EXTEND ON

WLOP1      LAC MEMADR
           DAC* MEMADR     /WRITE C(MEMADR) INTO SAME ADDRESS
           ISZ RPETE       /DELAY 6 US
           JMP .-1
           LAW -2
           DAC RPETE       /RESTORE COUNT
           ISZ WRCNT
           JMP WLOP1
           LAC MEMADR
           SAD LSTLOC      /TOTAL 17 US BETWEEN WRITES
           JMP .+3
           ISZ MEMADR      /INCREMENT ADDRESS COUNT
           JMP WLOP1-3     /WRITE IN NEXT
           LAW
           SAD BLOC1       /NO BLOCK IF = LAW
           SKP
           JMP BLKA1
           ISZ MEMADR      /SETUP FOR BLOCK 2
           JMS NXTBNK      /INCREMENT ADDRESS COUNT
           JMS CBANK       /SETUP FOR NEXT BANK
           JMP WLOP1-3
/
.EJECT

```

```

/
/READ AND CHECK FOR ERROR. READ FROM LO BANK
/TO HI BANK AND THEN DECREMENT FROM HI TO LO.
/REPEAT THE SEQUENCE TWICE BEFORE FINISHING.
/
READ1   LAW -2
        DAC LOOPT           /COUNT # OF TIMES READ BACKWARD
        LAC FIRST1        /FIRST 1 = C(ACS 14,15)
        DAC MEMADR        /ADDRESS COUNTER
        JMS CRANK         /SEE IF TESTED BANK HAS PROGRAM
RLOP1   LAW -24
        DAC RPFTE         /LOOP 5 TIMES ON EACH READ
        FEM              /EXTEND ON
        LAC MEMADR
        DAC PATR
        LAC* MFADR
        SAD MEMADR        /CHECK FOR ERROR
        SKP              /O.K.
        JMS ERROR        /PRINT INFO
        ISZ RPFTE        /LOOP 5 TIMES ON EACH ADDRESS
        JMP RLOP1+5      /READ SAME ONE AGAIN

00401   205000           LAC MEMADR
00402   544777           SAD LSTLOC           /LSTLOC = LAST ADR. OF TESTED BANK
00403   600406           JMP .+3
00404   445000           ISZ MEMADR         /SETUP FOR NEXT BANK
00405   600366           JMP RLOP1
00406   760000           LAW
00407   545012           SAD BLOC1         /NO BLOCKS IF = LAW
00410   741000           SKP
00411   604016           JMP RLKB1        /SETUP FOR BLOCK 2
00412   445000           ISZ MEMADR        /INCREMENT ADR. COUNTER
00413   101636           JMS NXTBNK
00414   101674           JMS CRANK
00415   600366           JMP RLOP1
00416   600422           JMP RBAK1
00417   445006           LOOP1 ISZ LOOPT   /NOW READ FROM LAST TO FIRST
00420   600363           JMP READ1+2      /DONE IF 0
00421   600172           JMP EXAM2        /READ FORWARD ONCE MORE
                          /ALL DONE. SEE IF TEST 2 HAS
                          /BEEN SELECTED.

.EJECT

```



```

/
/READ ALL OF MEMORY FROM HI BANK TO LO BANK
/
00422      204775      RBAK1      LAC LAST1          /VERY LAST LOC. IN HIGH BANK
00423      044777      DAC LSTLOC        /ADDRESS COUNT
00424      505076      AND K60K
00425      045000      DAC MEMADR        /FIRST LOC. IS LAST TESTED
00426      101744      JMS CKRAK         /SEE IF BANK CONTAINS PROGRAM
00427      707702      EEM               /EXTEND ON
00430      777754      BAK1      LAW -24
00431      045004      DAC RPFTE         /COUNTS 20 TIMES
00432      204777      LAC LSTLOC
00433      045007      DAC PATR
00434      224777      LAC* LSTLOC       /READ ONE
00435      544777      SAD LSTLOC        /COMPARE
00436      741000      SKP               /O.K.
00437      102070      JMS ERRORA        /PRINT INFO
00440      445004      ERR1A      ISZ RPETE         /SKIP AFTER 20 READS
00441      600434      JMP BAK1+4
00442      204777      LAC LSTLOC        /COUNT
00443      545000      SAD MEMADR        /DONE ONE BANK IF EQUAL
00444      600451      JMP .+5
00445      777777      LAW -1
00446      344777      TAD LSTLOC        /DECREMENT ADDRESS COUNT
00447      044777      DAC LSTLOC
00450      600430      JMP BAK1          /READ IN DESCENDING ORDER
00451      760000      LAW
00452      545012      SAD BLOC1         /NO BLOCKS IF = LAW
00453      741000      SKP
00454      604043      JMP BLKC1         /SETUP FOR BLOCK 2
00455      102012      JMS NXRAK         /SETUP FOR NEXT BANK
00456      101744      JMS CKRAK         /SEE IF NEXT HAS PROGRAM
00457      600430      JMP BAK1          /READ
00460      600417      JMP LOOP1         /READ FORWARDS AGAIN

```

.EJECT

/  
 /TEST 2. REVERSE THE CONTENTS OF EACH ADDRESS.  
 /LOC. 0 WILL = 17777; LOC. 1 = 17776, ETC. WRITE  
 /AND READ THE PATTERN IN ONE BANK AT A TIME.  
 /AFTER TESTING ONE BANK MAKE SURE REST OF  
 /MEMORY HAS NOT CHANGED.  
 /

00461	102035	TST2	JMS WRT1S	/WRITE 1'S INTO AL OF MEMORY
00462	205001		LAC FIRST1	/FIRST1 = FIRST BANK
00463	045000		DAC MEMADR	/ADDRESS COUNTER
00464	045010		DAC PATBNK	/PATBNK = BANK THAT HAS PATTERN
00465	345103		TAD K17S	/K17S = 017777
00466	045007	LOP2	DAC PATR	/CONTAINS DATA TO BE WRITTEN
00467	101674		JMS CBANK	/SEE IF TESTED BANK HAS PROGRAM
00470	777776		LAW -2	
00471	045004		DAC RPETE	/DELAY COUNTER
00472	777775		LAW -3	
00473	045005		DAC WRCNT	/COUNTS 3 TIMES FOR EACH ADDRESS
00474	707702		EEM	/EXTEND ON
00475	205007	WLOP2	LAC PATR	
00476	065000		DAC MEMADR	/WRITE C(PATR)
00477	445004		ISZ RPETE	/DELAY 6 US
00500	600477		JMP .-1	
00501	777776		LAW -2	
00502	045004		DAC RPETE	/RESTORE COUNT
00503	445005		ISZ WRCNT	
00504	600475		JMP WLOP2	/17 US TOTAL BETWEEN WRITES
00505	205000		LAC MEMADR	
00506	544777		SAD LSTLOC	/LSTLOC = LAST LOC. OF ONE BANK
00507	600515		JMP CKBA	/GO READ ANN CHECK THIS BANK
				/FROM LO TO HI
00510	445000		ISZ MEMADR	/INCREMENT ADDRESS
00511	777777		LAW -1	/-1
00512	345007		TAD PATR	/DECREMENT COUNT PATTERN
00513	045007		DAC PATR	
00514	600472		JMP WLOP2-3	/DO NEXT IN SEQUENCE
		/		
00515	760000	CKBA	LAW	
00516	545012		SAD BLOC1	/NO BLOCKS IF = LAW
00517	600534		JMP RDFWD	/NONE. READ FORWARD
00520	205012		LAC BLOC1	
00521	045000		DAC MEMADR	/1ST OF BLOCK 1
00522	740001		CMA	
00523	505103		AND K17S	/CLEAR BITS 0-4
00524	045007		DAC PATR	/COMPARE WORD
00525	205012		LAC BLOC1	
00526	505076		AND K60K	
00527	245007		XOR PATR	/PUT BANK # ON PATR
00530	045007		DAC PATR	
00531	205013		LAC BLOC2	/LAST OF BLOCK 1
00532	044777		DAC LSTLOC	
00533	600543		JMP RBLK2	/READ BLOCK 1 FORWARD

.EJECT

/  
 /READ THE BANK WITH THE PATTERN FROM 0  
 /TO HI THEN HI TO LO, THEN REST OF MEMORY  
 /

00534	760262	RDFWD	LAW 262	
00535	045003		DAC INJM	
00536	205010		LAC PATBNK	/PATRNK = BANK WITH PATTERN
00537	045000		DAC MEMADR	/ADDRESS COUNTER
00540	345103		TAD K17S	/K17S = 017777
00541	045007		DAC PATR	/PATR = DATA FOR COMPARISON
00542	101674		JMS CBANK	/SEE IF TESTED BANK HAS PROGRAM
00543	777776	RBLK2	LAW -2	
00544	045004		DAC RPETE	/DELAY COUNTER
00545	777775		LAW -3	
00546	045005		DAC WRCNT	/COUNTS 3 TIMES FOR EACH ADR.
00547	707702		EEM	/EXTEND ON
00550	225000	FWD2	LAC* MEMADR	/READ
00551	545007		SAD PATR	/COMPARE
00552	741000		SKP	/O.K.
00553	102100		JMS ERROR	/PRINT INFO
00554	445004		ISZ RPETE	/DELAY 6 US
00555	600554		JMP .-1	
00556	777776		LAW -2	
00557	045004		DAC RPETE	/RESTORE COUNT
00560	445005		ISZ WRCNT	/17 US TOTAL
00561	600550		JMP FWD2	
00562	205000		LAC MEMADR	
00563	544777		SAD LSTLOC	/LSTLOC = LAST LOC. IN TESTED BANK
00564	600572		JMP CKRB	/READ THIS BANK HI TO LO
00565	445000		ISZ MEMADR	/INCREMENT ADDRESS
00566	777777		LAW -1	
00567	345007		TAD PATR	/DECREMENT COUNT PATTERN
00570	045007		DAC PATR	
00571	600545		JMP FWD2-3	/DO NEXT IN SEQUENCE

.EJECT

00572 760000  
 00573 545012  
 00574 600611  
 00575 205012  
 00576 045000  
 00577 205013  
 00600 044777  
 00601 740001  
 00602 505103  
 00603 045007  
 00604 205013  
 00605 505076  
 00606 245007  
 00607 045007  
 00610 600615

CKRB

LAW  
 SADR BLOC1 /NO HLOCK IF = LAW  
 JMP RDHAK /READ HI TO LO  
 LAC BLOC1  
 DAC MEMADR /1ST OF BLOCK 1  
 LAC BLOC2  
 DAC LSTLOC /LAST OF BLOCK 1  
 CMA  
 AND K17S /CLEAR BITS 0-4  
 DAC PATR  
 LAC BLOC2  
 AND K60K  
 XOR PATR /PUT BANK # ON PATR  
 DAC PATR  
 JMP BAK2-1 /READ BLOCK 1 BACKWARD  
  
 .EJECT

```

/
/READ THE BANK WITH THE PATTERN FROM HI TO LO
/AND THEN READ REST OF MEMRY.
/
00611 205010 RDRAK LAC PATBNK
00612 045007 DAC PATR /PATR = DATA FOR COMPARISON
00613 045000 DAC MEMADR /MEMADR = LAST LOC. TO TEST
00614 101744 JMS CKRAK /SFF IF TEST BANK HAS PROGRAM
00615 707702 FEM /EXTEND ON
00616 777754 BAK2 LAW -24
00617 045004 DAC RPFTE
00620 224777 LAC* LSTLOC /READ
00621 545007 SAD PATR /COMPARE
00622 741000 SKP /O.K.
00623 102070 JMS ERRORA /PRINT INFO
00624 445004 ISZ RPFTE
00625 600620 JMP ,-5
00626 204777 LAC LSTLOC
00627 545000 SAD MEMADR /DONE IF LSTLOC = LOWEST ADR.
00630 600636 JMP CKRC /READ & TEST REST OF MEMORY
00631 445007 ISZ PATR /INCREMENT COUNT PATTERN
00632 777777 LAW -1
00633 344777 TAD LSTLOC /DECREMENT ADDRESS COUNT
00634 044777 DAC LSTLOC
00635 600616 JMP BAK2 /READ NEXT IN DESCENDING ORDER

/
CKBC LAW
00636 760000 SAD BLOC1
00637 545012 JMP REST
00640 600642 JMP BLKA2
00641 604073 .EJECT

```

/  
 /NOW READ REST OF MEMORY AND MAKE SURE IT  
 /EQUALS 777777, THEN WRITE THE ADDRESS PATTERN  
 /IN NEXT SEQUENTIAL BANK.  
 /

00642	204775	REST	LAC LAST1	/SEE IF MORE THAN 1 BANK SELECTED
00643	505076		AND K60K	/MASK BITS 3 AND 4
00644	545001		SAD FIRST1	/ONLY 1 SELECTED IF EQUAL
00645	600176		JMP EXAM3	/SEE IF TEST 3 IS SELECTED
00646	205001		LAC FIRST1	/FIRST1 = 1ST LOCATION TO TEST
00647	045000	REST1	DAC MEMADR	/ADDRESS COUNTER
00650	345103		TAD K17S	/K17S = 01777
00651	044777		DAC LSTLOC	/LAST ADDRESS IN FIRST BANK
00652	201146		LAC KNXPT	/KNXPT = LOCATION NXTPAT
00653	045011		DAC EXIT	/(EXIT) = NXTPAT
00654	205000		LAC MEMADR	
00655	505076		AND K60K	/MASK BITS 3 AND 4
00656	545010		SAD PATBNK	/SEE IF BANK HAS ADDRESS PATTERN
00657	600662		JMP .+3	
00660	100670		JMS REST2	/READ REST OF MEMORY
00661	600654		JMP .-5	
00662	204777		LAC LSTLOC	
00663	544776		SAD LAST2	/IS THE BANK THE HIGHEST SELECTED
00664	600176		JMP EXAM3	/YES. SEE IF TEST 3 IS SELECTED
00665	205010		LAC PATBNK	/EQUALS BANK WITH ADDRESS PATTERN
00666	345073		TAD K20K	/ADD 8K TO ADDRESS
00667	600647		JMP REST1	

.EJECT

```

/
/READ ALL OF MEMORY EXCEPT BANK WITH ADDRESS PATTERN
/
00670      000000
00671      101674
00672      777777
00673      045007
00674      707702
00675      225000
00676      545007
00677      741000
00700      102100
00701      205000
00702      544777
00703      600706
00704      445000
00705      600675
00706      760000
00707      545012
00710      741000
00711      600716
00712      445000
00713      101636
00714      740000
00715      620670
00716      707704
00717      625011

REST2      0
           JMS CBANK           /SEE IF TESTED BANK HAS PROGRAM
           LAW -1             /777777
           DAC PATR          /COMPARE CONSTANT
           EEM
ALL1       LAC* MEMADR        /READ ONE
           SAD PATR          /MUST = 777777
           SKP               /O.K.
           JMS ERROR         /PRINT INFO
           LAC MEMADR
           SAD LSTLOC        /SEE IF READ ONE BANK
           JMP .+3           /YES
           ISZ MEMADR        /INCREMENT ADDRESS COUNT
           JMP ALL1
           LAW
           SAD BLOC1         /NO BLOCKS IF = LAW
           SKP
           JMP .+5           /EXIT
           ISZ MEMADR        /INCREMENT ADDRESS
           JMS NXTBNK        /SETUP FOR NEXT BANK IN SEQUENCE
           NOP
           JMP* RFST2
           LEM               /EXTEND OFF.
           JMP* EXIT         /EXIT TO NXTPAT, RTN3, RTN4, OR RTN5

/
/SETUP TO WRITE PATTERN IN NEXT BANK
/
NXTPAT     JMS WRT1S          /WRITE 1'S INTO ALL OF MEMORY
           LAC PATBNK        /CURRENT BANK WITH ADDRESS PATTERN
           TAD K20K          /ADD BK TO IT
           DAC PATBNK
           DAC MEMADR        /ADDRESS COUNTER
           TAD K17S          /K17S = 017777
           DAC LSTLOC        /LAST LOCATION OF NEXT BANK
           JMP LOP2          /JMP BACK AND WRITE THE
                           /ADDRESS PATTERN IN NEXT BANK

/
.EJECT

```

/  
 /TEST 3. WRITE COMPLEMENT ADDRESSES INTO ONE  
 /BANK AND 777777 IN ALL OTHER BANKS. WRITE  
 /IN BOTH DIRECTIONS (LO TO HI AND HI TO LO). AND  
 /READ IN BOTH DIRECTIONS BEFORE READING REST  
 /OF MEMORY

00730	102035			
00731	205001	TST3	JMS WRT1S	/WRITE 1'S INTO ALL OF MEMORY
00732	045000		LAC FIRST1	/FIRST BANK TO TEST
00733	045010		DAC MEMADR	/ADDRESS COUNTER
00734	345103		DAC PATBNK	/EQUALS BANK WITH ADDRESS PATTERN
00735	044777		TAD K17S	/AND 017777
00736	101674	LOP3	DAC LSTLOC	/LSTLOC = LAST LOC. OF TESTED BANK
00737	777776		JMS CBANK	/SEE IF TESTED BANK HAS PROGRAM
00740	045004		LAW -2	
00741	777775		DAC RPETE	/DELAY COUNTER
00742	045005		LAW -3	
00743	707702		DAC WRCNT	/COUNTS 3 TIMES FOR EACH ADDRESS
00744	205000	WLOP3	EEM	/EXTEND ON
00745	740001		LAC MEMADR	
00746	065000		CMA	/COMPLEMENT ADDRESS
00747	445004		DAC* MEMADR	/WRITE INTO SAME ADDRESS
00750	600747		ISZ RPETE	
			JMP .-1	/DELAY 6 US
00751	777776		LAW -2	
00752	045004		DAC RPETE	
00753	445005		ISZ WRCNT	
00754	600744		JMP WLOP3	/18 US TOTAL BETWEEN WRITES
00755	205000		LAC MEMADR	
00756	544777		SAD LSTLOC	/LSTLOC = LAST LOC. OF TESTED BANK
00757	600762		JMP .+3	
00760	445000		ISZ MEMADR	/INCREMENT ADDRESS COUNT
00761	600741		JMP WLOP3-3	/WRITE IN NEXT
00762	760000		LAW	
00763	545012		SAD BLOC1	/NO BLOCK IF = LAW
00764	741000		SKP	
00765	604124		JMP RLKA3	
00766	100772		JMS REST3	/READ REST OF MEMORY
00767	101023		JMS RFWD3	/READ LO TO HI FROM TESTED BANK
00770	100772		JMS REST3	/READ REST OF MEMORY AGAIN
00771	601104		JMP TST3A	/WRITE HI TO LO IN TESTED BANK

.EJECT



```

/
/SETUP TO READ REST OF MEMORY, THEN READ AND
/TEST THE BANK WITH ADDRESS PATTERN
/
00772      000000      REST3      0
00773      760263      LAW 263
00774      045003      DAC TNUM      /TEST NUMBER
00775      204775      LAC LAST1     /SEE IF MORE THAN 1 BANK SELECTED
00776      505076      AND K60K      /MASK BITS 3 AND 4
00777      545001      SAD FIRST1    /ONLY 1 SELECTED IF EQUAL
01000      620772      JMP* REST3    /NO MORE TO READ
01001      205001      RTN3          /FIRST1 = 1ST LOCATION TO TEST
01002      045000      REST3A       /ADDRESS COUNTER
01003      345103      TAD K17S      /K17S = 017777
01004      044777      DAC LSTLOC    /LAST ADDRESS IN FIRST BANK
01005      201147      LAC KRTN3     /KRTN3 = LOCATION RTN3
01006      045011      DAC EXIT      /EXIT = RTN3
01007      205000      LAC MEMADR    /MASK BITS 3 AND 4
01010      505076      AND K60K      /SEE IF BANK HAS ADDRESS PATTERN
01011      545010      SAD PATBNK    /YES
01012      601015      JMP .+3       /NO, READ REST OF MEMORY
01013      100670      JMS REST2
01014      601007      JMP .-5
01015      204777      LAC LSTLOC
01016      544776      SAD LAST2     /IS THE BANK THE HIGHEST SELECTED
01017      620772      JMP* REST3    /YES. EXIT
01020      205010      LAC PATBNK
01021      345073      TAD K20K
01022      601002      JMP REST3A    /ADD 8K TO ADDRESS

.EJECT

```

/NOW READ THE BANK WITH ADDRESS PATTERN FROM  
 /LO T HI, THEN HI TO LO AND THEN RECHECK REFT  
 /OF MEMORY.  
 /

01023	000000	RFWD3	0	
01024	205010		LAC PATBNK	/BANK WITH ADDRESS PATTERN
01025	045000		DAC MEMADR	/ADDRESS COUNTER
01026	345103		TAD K17S	/AKD 017777
01027	044777		DAC LSTLOC	/LAST LOCATION TO TEST
01030	101674		JMS CBANK	/SEE IF THE BANK HAS PROGRAM
01031	707702		FEM	/EXTEND ON
01032	205000	FWD3	LAC MEMADR	
01033	740001		CMA	
01034	045007		DAC PATR	/DATA FOR COMPARISON
01035	225000		LAC* MEMADR	/READ ONE
01036	545007		SAD PATR	/COMPARE
01037	741000		SKP	/O.K.
01040	102100		JMS ERROR	/PRINT INFO
01041	205000		LAC MEMADR	
01042	544777		SAD LSTLOC	/SEE IF DONE ONE BANK
01043	601046		JMP .+3	/READ BACKWARD
01044	445000		ISZ MEMADR	/INCREMENT ADDRESS COUNT
01045	601032		JMP FWD3	/READ NEXT IN SEQUENCE
01046	760000		LAW	
01047	545012		SAD BLOC1	/NO BLOCK IF = LAW
01050	601052		JMP RBAK3	/READ BACKWARD
01051	604145		JMP BLK3	

.EJECT

/  
 /NOW READ SAME BANK FROM HI TO LO, AND THEN  
 /RECHECK REST OF MEMORY  
 /

01052 205010  
 01053 045000  
 01054 101744  
 01055 707702  
 01056 777770  
 01057 045004  
 01060 204777  
 01061 740001  
 01062 045007  
 01063 224777  
 01064 545007  
 01065 741000  
 01066 102070  
 01067 445004  
 01070 601063  
 01071 204777  
 01072 545000  
 01073 601100  
 01074 777777  
 01075 344777  
 01076 044777  
 01077 601056  
 01100 760000  
 01101 545012  
 01102 621023  
 01103 604152

RBAK3 LAC PATBNK /CURRENT BANK WITH ADDRESS PATTERN  
 DAC MEMADR /MEMADR = LAST LOC. TO \*FST  
 JMS CKBAK /SEE IF TESTED BANK HAS PROGRAM  
 FEM /EXTEND ON  
 BAK3 LAW -10  
 DAC RPFTE /ADDRESS COUNT  
 LAC LSTLOC /DATA FOR COMPARISON  
 CMA /COMPARE  
 DAC PATR /D.K.  
 SAD PATR /PRINT INFO  
 SKP  
 JMS ERRORA  
 ISZ RPFTE  
 JMP .-5  
 LAC LSTLOC /DONE IF LOWEST ADDRESS  
 SAD MEMADR  
 JMP .+5  
 LAW -1 /DECREMENT ADDRESS COUNT  
 TAD LSTLOC  
 DAC LSTLOC /READ NEXT IN DESCENDING ORDER  
 JMP BAK3  
 LAW /NO BLOCK IF = LAW  
 SAD BLOC1 /EXIT AND RECHECK REST OF MEMOR  
 JMP\* RFD03  
 JMP BLK03

.EJECT

/  
 /TEST 3A. WRITE SAME PATTERN IN SAME BANK  
 /FROM HI TO LO.  
 /

01104	102035	TST3A	JMS WRT1S	/WRITE 1'S INTO ALL OF MEMORY
01105	205010		LAC PATBNK	/CURRENT BANK WITH ADDRESS PATTERN
01106	045000		DAC MEMADR	/MEMADR = LAST LOC. TO TEST
01107	101744		JMS CKRAK	/SEE IF TESTED BANK HAS PROGRAM
01110	707702		FEM	
01111	204777	BAK3A	LAC LSTLOC	/DATA TO BE WRITTEN
01112	740001		CMA	/COMPLEMENT
01113	064777		DAC* LSTLOC	/PUT IN SAME ADDRESS
01114	204777		LAC LSTLOC	
01115	545000		SAD MEMADR	/DONE IF = LOWEST ADDRESS
01116	601123		JMP .+5	
01117	777777		LAW -1	
01120	344777		TAD LSTLOC	/DECREMENT ADDRESS COUNT
01121	044777		DAC LSTLOC	
01122	601111		JMP BAK3A	/WRITE NEXT IN DESCENDING ORDER
01123	760000		LAW	
01124	545012		SAD BLOC1	/NO BLOCK IF = LAW
01125	741000		SKP	
01126	604124		JMP RLKA3	
01127	100772		JMS REST3	/SETUP TO CHECK REST OF MEMORY
01130	101023		JMS RFWD3	/GO READ LO TO HI; HI TO LO
01131	100772		JMS REST3	/RECHECK REST OF MEMORY THEN
				/SETUP TO WRITE THE PATTERN IN
				/NEXT SEQUENTIAL BANK

/  
 /SETUP FOR NEXT  
 /

01132	205010	NXPT3	LAC PATBNK	/CURRENT BANK
01133	345103		TAD K17S	/ADD 1777
01134	544775		SAD LAST1	/CHECK FOR LAST
01135	600202		JMP EXAM4	/ALL DONE. SEE IF TEST 4 SELECTED
01136	102035		JMS WRT1S	/WRITE 1'S INTO ALL OF MEMORY
01137	205010		LAC PATBNK	
01140	345073		TAD K20K	/ADD 8K TO CURRENT ADDRESS
01141	045010		DAC PATBNK	/BANK WITH ADR. PATTERN
01142	045000		DAC MEMADR	/ADDRESS COUNTER
01143	345103		TAD K17S	/K17S = 017777
01144	044777		DAC LSTLOC	/LAST LOC. TO WRITE
01145	600736		JMP LOP3	/START NEW BANK

01146	000720	KNXPT	NXTPAT
01147	001000	KRTN3	RTN3
01150	001215	KRTN4	RTN4

/XMADR9 - TAPE 2

/

/TEST 4. SLIDE A 1 THRU ONE BANK. REPEAT  
 /18 TIMES PER BANK TO CHECK EACH BIT POSITION.  
 /REST OF MEMORY WILL CONTAIN ALL 1'S. CHECK  
 /REST OF MEMORY AFTER EACH WRITE AND READ  
 /IN THE BANK BEING TESTED.

01151	760264	/	LAW 264	
01152	045003	TST4	DAC TNUM	/TEST NUMBER
01153	102035		JMS WRT1S	/WRITE 1'S INTO ALL OF MEMORY
01154	205001		LAC FIRST1	/1ST LOCATION TO TEST
01155	045000		DAC MEMADR	/ADDRESS COUNTER
01156	045010		DAC PATBNK	/BANK WITH BIT PATTERN
01157	345103		TAD K17S	/ADD 01777
01160	044777		DAC LSTLOC	/LAST LOC. OF TESTED BANK
01161	101674	LOP4	JMS CBANK	/SEE IF TESTED BANK HAS PROGRAM
01162	205043		LAC K1	/K1=1
01163	045033		DAC BITN	
01164	205033		LAC BITN	/BITN SAVES CURRENT STARTING BIT POSITION
01165	045007		DAC PATR	
01166	744000		CLL	
01167	707702		FEM	
01170	205007	WLOP4	LAC PATR	
01171	065000		DAC* MEMADR	/WRITE THE BIT
01172	740010		RAL	/ROTATE TO NEXT BIT POSITION
01173	045007		DAC PATR	
01174	205000		LAC MEMADR	
01175	544777		SAD LSTLOC	/DONE ONE BANK IF EQUAL
01176	601201		JMP .+3	
01177	445000		ISZ MEMADR	/INCREMENT ADDRESS COUNT
01200	601170		JMP WLOP4	/WRITE IN NEXT IN SEQUENCE
01201	760000		LAW	
01202	545012		SAD BLOC1	/NO BLOC IF=LAW
01203	741000		SKP	
01204	604206		JMP RLK44	
01205	101211		JMS REST4	/SETUP TO READ REST OF MEMORY
01206	101240		JMS RFWD4	/READ LO TO HI FROM TESTED BANK
01207	101211		JMS REST4	/READ REST OF MEMORY AGAIN
01210	601274		JMP CK18B	/SEE IF ALL 18 BITS HAVE BEEN TESTED
				/GO ON TO NEXT BANK IF SO.

.EJECT

```

/
/SETUP TO READ REST FOR MEMRY, THEN READ AND TEST
/THE BANK WITH BIT PATTERN.
/
REST4 0
LAC LAST1 /LAST LOC. TO TEST IN HI BANK
AND K60K /MASK BITS 3 AND 4
SAD FIRST1 /ONLY ONE BANK SELECTED IF EQUAL
RTN4 JMP* REST4 /NO MORE TO READ
LAC FIRST1 /1ST LOCATION TO TEST
REST4A DAC MEMADR /ADDRESS COUNTER
TAD K17S /ADD 017777
DAC LSTLOC /LAST LOC. TO TEST
LAC KRTN4 /KRTN4 = LOCATION RTN4
DAC EXIT /(EXIT) = RTN4
AND K60K /MASK BITS 3 AND 4
SAD PATBNK /SEF IF BANK HAS BIT PATTERN
JMP .+3 /YES
JMS REST2 /READ REST OF MEMORY
JMP .-5
LAC LSTLOC
SAD LAST2 /IS THE BANK THE HIGHEST SELECTED
JMP* REST4 /YES. EXIT
LAC PATBNK
TAD K20K /ADD 8K TO ADDRESS
JMP REST4A

```

.EJECT

/NOW READ THE BANK WITH BIT PATTERN FROM  
/LO TO HI, THEN RECHECK REST OF MEMORY

01240	000000	/		
01241	205010	RFWD4	0	
01242	045000		LAC PATBNK	/BANK WITH BIT PATTERN
01243	345103		DAC MEMADR	/ADDRESS COUNTER
01244	044777		TAD K17S	/ADD 017777
01245	101674		DAC LSTLOC	/LAST LOCATION TO TEST
01246	205033		JMS CBANK	/SEE IF THE BANK HAS PROGRAM
01247	045007		LAC BITN	/FIRST BIT POSITION
01250	744000		DAC PATR	
01251	707702		CLL	
01252	225000	FWD4	EEM	
01253	545007		LAC* MEMADR	/READ ONE
01254	741000		SAD PATR	/COMPARE
01255	102100		SKP	/O.K.
01256	205007		JMS ERROR	/PRINT INFO
01257	741200		LAC PATR	
01260	744002		SNA	/RESTORE LINK IF AC=0
01261	740010		STL	
01262	045007		RAL	/ROTATE TO NEXT BIT POSITION
01263	205000		DAC PATR	
01264	544777		LAC MEMADR	
01265	601270		SAD LSTLOC	/DONE ONE BANK IF EQUAL
01266	445000		JMP .+3	
01267	601252		ISZ MEMADR	/INCREMENT ADDRESS
01270	760000		JMP FWD4	/READ NEXT IN SEQUENCE
01271	545012		LAW	
01272	621240		SAD BLOC1	/NO BLOCK IF = LAW
01273	604227		JMP* RFWD4	
			JMP BLKC4	
			.EJECT	

/  
/SEE IF EACH BIT POSITION HAS BEEN TESTED  
/

01274	205033	CK18R	LAC BITN	
01275	545100		SAD K400K	/DONE 18 IF = 400000
01276	601307		JMP NXPT4	/SETUP FOR NEXT BANK
01277	744010		RCL	/SETUP FOR NEXT POSITION
01300	045033		DAC BITN	
01301	205010		LAC PATBNK	/BANK WITH BIT PATTERN
01302	045000		DAC MEMADR	/ADR. COUNT
01303	345103		TAD K17S	
01304	044777		DAC LSTLOC	/LAST LOCATION TO TEST
01305	101674		JMS CBANK	/SEE IF IT HAS PROGRAM
01306	601164		JMP WLOP4-4	/START SAME BANK WITH NEW BIT

/  
/SETUP FOR NEXT BANK WITH BIT PATTERN

01307	205010	NXPT4	LAC PATBNK	/CURRENT BANK
01310	345103		TAD K17S	/ADD 017777
01311	544775		SAD LAST1	/ALL BANK HAD BIT PATTERN IF EQUAL
01312	600206		JMP EXAM5	/ALL DONE SEE IF TEST 5 SELECTED
01313	102035		JMS WRT1S	/WRITE 1'S INTO ALL OF MEMORY
01314	205010		LAC PATBNK	/BANK JUST FINISHED
01315	345073		TAD K20K	/ADD 8K TO IT
01316	045010		DAC PATBNK	
01317	045000		DAC MEMADR	/ADDRESS COUNTER
01320	345103		TAD K17S	/ADD 017777
01321	044777		DAC LSTLOC	/LAST LOC. TO TEST
01322	601161		JMP LOP4	/START NEW BANK

/  
.EJECT



/  
 /TEST 5. WRITE A PATTERN CONSISTING OF 4 WORDS  
 /OF 0'S FOLLOWED BY 4 WORDS OF 1'S. FROM LO TO HI.  
 /AND THEN HI TO LO IN EACH BANK. READ THE PAT-  
 /TERN THE SAME WAY. CHECK REST OF MEMORY AFTER  
 /EACH WRITE OR READ SEQUENCE. COMPLEMENT THE  
 /PATTERN AND REPEAT THE SEQUENCE.  
 /

01323	102035	TST5	JMS WRT1S	/WRITE 1'S INTO ALL OF MEMORY
01324	760265		LAW 265	
01325	045003		DAC TNUM	/TEST NUMBER
01326	205001		LAC FIRST1	/1ST TO TEST
01327	045000		DAC MEMADR	/ADDRESS COUNTER
01330	045010		DAC PATBANK	/BANK WITH PATTERN
01331	205034		LAC PCW	/PATTERN CONTROL WORD = 036074
01332	045036		DAC CNTRL	/SAVF
01333	101674	LOP5	JMS CBANK	/SEE IF BANK HAS PATTERN
01334	707702		FEM	
01335	205036		LAC CNTRL	/CNTRL=036074 OR 741700
01336	045007		DAC PATR	
01337	777760		LAW -20	
01340	045006		DAC LOOPT	/COUNT 16 SHIFTS
01341	777774	WLOP5	LAW -4	
01342	045004		DAC RPFTE	/COUNTS 4 WRITES
01343	205007		LAC PATR	
01344	744010		RCL	
01345	045007		DAC PATR	
01346	751400		SZL:CLA	/WRITE 0'S IF LINK =0
01347	740001		CMA	/WRITE 1'S
01350	065000		DAC* MFMADR	/WRITE
01351	445004		ISZ RPFTE	
01352	601350		JMP .-2	/WRITE 4 TIMES BEFORE SKIP
01353	205000		LAC MEMADR	
01354	544777		SAD LSTLOC	/DONE WRITING 1 BANK IF EQUAL
01355	601362		JMP .+5	/NONE
01356	445000		ISZ MEMADR	/INCREMENT ADDRESS
01357	445006		ISZ LOOPT	/16 SHIFTS IF SKIP
01360	601341		JMP WLOP5	
01361	601335		JMP LOP5+2	/RESTORE LOOPT AND PATR
01362	760000		LAW	
01363	545012		SAD BLOC1	/NO BLOCK IF = LAW
01364	741000		SKP	
01365	604266		JMP BLKA5	
01366	101431		JMS RFWD5	/READ LO TO HI FROM TESTED BANK
01367	101402		JMS REST5	/READ REST OF MEMORY AGAIN

.EJECT

```

/ NOW WRITE COMPLEMENT. (4 WORDS = 1'S, 4 WORDS = 0)
/
01370      205035      LAC PCWA
01371      545036      SAD CNTRL           /PCWA = 741700
01372      601542      JMP TST5A          /ALREADY DONE COMPLEMENT
01373      045036      DAC CNTRL
01374      205010      LAC PATBNK        /BANK WITH PATTERN
01375      045000      DAC MEMADR
01376      345103      TAD K17S
01377      044777      DAC LSTLOC
01400      601333      JMP LOP5          /WRITE COMPLEMENT

/
01401      001406      KRTN5            RTN5
/
/
/ READ REST OF MEMORY
/
01402      000000      REST5            0
01403      204775      LAC LAST1        /LAST LOC. IN HIGHEST BANK
01404      505076      AND K60K         /MASK 4 AND 4
01405      545001      SAD FIRST1       /ONLY 1 BANK SELECTED IF EQUAL
01406      621402      RTN5            JMP* REST5       /NONE TO READ
01407      205001      LAC FIRST1       /FIRST TO TEST
01410      045000      REST5A          DAC MEMADR       /ADDRESS COUNTER
01411      345103      TAD K17S         /ADD 017777
01412      044777      DAC LSTLOC       /LAST LOC. TO TEST
01413      201401      LAC KRTN5        /KRTN5 = LOCATION RTN5
01414      045011      DAC EXIT
01415      205000      LAC MEMADR
01416      505076      AND K60K
01417      545010      SAD PATBNK       /SEE IF BANK HAS PATTERN
01420      601423      JMP .+3          /YES
01421      100670      JMS REST2        /READ REST OF MEMORY
01422      601415      JMP .-5
01423      204777      LAC LSTLOC
01424      544776      SAD LAST2
01425      621402      JMP* REST5       /IS THE BANK THE HIGHEST SELECTED
01426      205010      LAC PATBNK       /YES. EXIT
01427      345073      TAD K20K         /ADD 8K TO ADDRESS
01430      601410      JMP REST5A       /AND SETUP FOR ANOTHER BANK

/
.EJECT

```

```

/READ THE BANK WITH WORD PATTERN FROM LO TO HI,
/THEN HI TO LO, AND THEN RECHECK REST OF MEMORY
/
01431 000000 RFWD5 0
01432 205010 LAC PATBNK /BANK WITH WORD PATTERN
01433 045000 DAC MEMADR /ADDRESS COUNTER
01434 345103 TAD K17S /ADD 017777
01435 044777 DAC LSTLOC
01436 101674 JMS CBANK /SEE IF BANK HAS PROGRAM
01437 205036 RBLK5 LAC CNTRL /CNTRL = 036074 OR 741700
01440 045037 DAC COMPR
01441 777760 LAW -20
01442 045006 DAC LOOPT /COUNT 16 SHIFTS
01443 777770 FWD5 LAW -10
01444 045004 DAC RPFTE /READ 8 TIMES
01445 707702 EEM
01446 205037 LAC COMPR
01447 744010 RCL
01450 045037 DAC COMPR
01451 751400 SZL:CLA /READ 0'S IF LINK = 0
01452 740001 CMA /READ 1'S
01453 045007 DAC PATR /USED FOR COMPARISON
01454 225000 LAC* MEMADR /READ
01455 545007 SAD PATR /COMPARE
01456 741000 SKP /0,K
01457 102100 JMS ERROR /PRINT INFO
01460 445004 ERR5 ISZ RPFTE /SKIP AFTER 8 READS
01461 601454 JMP .-5
01462 205000 LAC MEMADR
01463 544777 SAD LSTLOC /DONE 1 BANK IF EQUAL
01464 601471 JMP .+5
01465 445000 ISZ MEMADR /INCREMENT ADDRESS COUNT
01466 445006 ISZ LOOPT /DONE 16 SHIFTS IF 0
01467 601443 JMP FWD5
01470 601437 JMP FWD5-4 /RESTORE LOOPT AND COMPR
/ /READ NEXT IN SEQUENCE
01471 760000 LAW
01472 545012 SAD BLOC1 /NO BLOCK IF = LAW
01473 601476 JMP RBAK5 /READ BACKWARD
01474 707704 LEM
01475 625011 JMP* EXIT /C(EXIT) = BLKB5 OR BLKH5
/
.EJECT

```

/READ SAME BANK WITH WORD PATTERN FROM HI TO LO,  
/THEN RECHECK REST OF MEMORY

01476	205010	/		
01477	045000	RBAK5	LAC PATBNK	/CURRENT BANK WITH WORD PATTERN
01500	101744		DAC MEMADR	/EQUALS LAST LOC. TO TEST
01501	707702		JMS CKRAK	/SEE IF TESTED BANK HAS PROGRAM
01502	205036		FEM	
01503	045037		LAC CNTRL	/CNTRL = 036074 OR 741700
01504	777760		DAC COMPR	
01505	045006		LAW -2	
01506	777770	BAK5	DAC LOOPT	/COUNT 16 SHIFTS
01507	045004		LAW -1	
01510	205037		DAC RPETE	/READ 8 TIMES
01511	744010		LAC COMPR	
01512	045037		RCL	
01513	750400		DAC COMPR	
01514	740001		SNL!CLA	/READ 0'S IF LINK = 1
01515	045007		CMA	/READ 1'S
01516	224777		DAC PATR	/COMPARE WORD
01517	545007		LAC* LSTLOC	/READ ONE
01520	741000		SAD PATR	
01521	102070		SKP	/O.K.
01522	445004		JMS ERRORA	/PRINT INFO
01523	601516		ISZ RPETE	/SKIP AFTER 8 TIMES
01524	204777		JMP .-5	
01525	545000		LAC LSTLOC	/CURRENT ADDRESS
01526	601535		SAD MEMADR	/DONE IF LOWEST ADDRESS
01527	777777		JMP CKRD	
01530	344777		LAW -1	
01531	044777		TAD LSTLOC	/DECREMENT ADDRESS COUNT
01532	445006		DAC LSTLOC	
01533	601506		ISZ LOOPT	/16 SHIFTS IF 0
01534	601502		JMP BAK5	
			JMP RBAK5+4	/RESTORE COMPR AND LOOPT
				/READ NEXT IN DESCENDING ORDER
01535	760000	/		
01536	545012	CKRD	LAW	
01537	621431		SAD BLOC1	/NO BLOCK IF = LAW
01540	707704		JMP* REWD5	/EXIT AND CHECK REST OF MEMORY
01541	625011		LEM	
			JMP* EXIT	/C(EXIT) = BLK05 OR BLKJ5
		/		
			.EJECT	

```

/TEST 5A. WRITE IN SAME BANK FROM HI TO LO
/
01542 102035
01543 205010
01544 045000
01545 101744
01546 205035
01547 045036
01550 205036
01551 045037
01552 777760
01553 045006
01554 707702
01555 777754
01556 045004
01557 205037
01560 744010
01561 045037
01562 751400
01563 740001
01564 064777
01565 445004
01566 601564
01567 204777
01570 545000
01571 601600
01572 777777
01573 344777
01574 044777
01575 445006
01576 601555
01577 601550

/
01600 760000
01601 545012
01602 741000
01603 604326
01604 101402
01605 205035
01606 545036
01607 205034
01610 045036
01611 101431
01612 101402
01613 205010
01614 045000

TST5A JMS WRT1S /WRITE 1'S IN ALL OF MEMORY
LAC PATBNK /CURRENT BANK WITH PATTERN
DAC MEMADR /MEMADR = LAST LOC. TO TEST
JMS CKRAK /SEE IF TESTED BANK HAS PROGRAM
LAC PCWA /PCWA = 741700

LOP5A DAC CNTRL
LAC CNTRL
DAC COMPR
LAW -20
DAC LOOPT /COUNT 16 SHIFTS
FEM

BAK5A LAW -24
DAC RPETE /WRITE 8 TIMES
LAC COMPR
RCL
DAC COMPR
SZL:CLA /WRITE 0'S IF LINK = 0
CMA /WRITE 1'S
DAC* LSTLOC /WRITE
ISZ RPETE /SKIP AFTER 8 WRITES
JMP ,-2
LAC LSTLOC /CURRENT ADDRESS
SAD MEMADR /DONE 1 BANK IF EQUAL
JMP RST5
LAW -1
TAD LSTLOC /DECREMENT ADDRESS COUNT
DAC LSTLOC
ISZ LOOPT /16 SHIFTS IF 0
JMP BAK5A
JMP LOP5A /RESTORE

/
RST5 LAW
SAD RLOC1 /NO BLOCK IF = LAW
SKP
JMP BLKF5
JMS REST5 /SETUP TO CHECK REST OF MEMORY
LAC PCWA /= 741700 CONTROL WORD
SAD CNTRL /SEE IF IT'S THE PRESENT PATTERN
LAC PCW /YES. GET COMPLEMENT FOR READ FORWARD
DAC CNTRL
JMS RFWD5 /GO READ LO TO HI; HI TO LO
JMS REST5 /RECHECK REST OF MEMORY
LAC PATBNK
DAC MEMADR /RESTORE FIRST TO TEST

.EJECT

```

/WRITE COMPLEMENT PATTERN FROM HI TO LO

01615 205035  
01616 545036  
01617 601622  
01620 101744  
01621 601550

LAC PCWA  
SAD CNTRL  
JMP NXPT5 /SETUP FOR NEXT BANK  
JMS CK9AK /RESTORE ADDRESS COUNTERS  
JMP LOPSA /WRITE COMPLEMENT BACKWARDS

.EJECT

```

/SETUP TO WRITE NEXT BANK
/
01622      205010      NXPT5      LAC PATBNK      /CURRENT BANK
01623      345103      TAD K17S      /ADD 017777
01624      544775      SAD LAST1     /EQUALS LAST LOC. OF HIGHEST BANK
01625      600212      JMP EXAM5+4   /DONE ALL TESTS ON ALL MEMORY

01626      102035      JMS WRT1S     /GO SETUP TO RELOCATE
01627      205010      LAC PATBNK    /WRITE 1'S IN ALL OF MEMORY
01630      345073      TAD K20K     /CURRENT BANK
01631      045010      DAC PATBNK    /ADD 8K TO GET NEXT BANK
01632      045000      DAC MEMADR    /ADDRESS COUNT
01633      345103      TAD K17S     /ADD 017777
01634      044777      DAC LSTLOC    /LAST LOC. IN NEXT BANK
01635      601331      JMP LOP5-2    /START OVER WITH NEW BANK
/
/
/ROUTINE TO DETERMINE NEXT BANK TO TEST
/
01636      000000      NXTBNK      0
01637      204777      LAC LSTLOC    /LAST LOC. OF CURRENT BANK
01640      544776      SAD LAST2     /LAST LOC. OF HIGHEST BANK
01641      601655      JMP THRU      /DNF ALL BANKS
01642      205000      LAC MEMADR    /1ST OF NEXT TO TEST
01643      505072      AND K10K     /CLEAR ALL BUT BIT 5
01644      740200      SZA
01645      601663      JMP HIGH4     /IN HIGH 4K NOW
01646      205000      NOTIN      LAC MEMADR
01647      345103      TAD K17S
01650      044777      DAC LSTLOC
01651      750004      LAS
01652      741100      SPA
01653      102062      JMS HALT     /CHECK ACS0 FOR HALT
01654      621636      JMP* NXTBNK  /GO HALT
/                                     /EXIT

01655      750004      THRU      LAS
01656      741100      SPA
01657      102062      JMS HALT     /CHECK ACS 0 FOR HALT
01660      441636      ISZ NXTBNK   /GO HALT
01661      441636      ISZ NXTBNK   /NXTBNK+2
01662      621636      JMP* NXTBNK  /EXIT

/
/
HIGH4      LAC MEMADR
01664      505076      AND K60K     /CLEAR ALL BUT BITS 3,4
01665      544771      SAD FLOADA   /SEE IF TESTING THIS BANK
01666      741000      SKP          /YES..
01667      601646      JMP NOTIN
01670      205000      LAC MEMADR
01671      345072      TAD K10K
01672      045000      DAC MEMADR   /MEMADR NOW = 1ST LOC. OF NEXT BANK
01673      601646      JMP NOTIN
/
.EJECT

```

/ROUTINE TO DETERMINE IF BANK UNDER TEST CONTAINS  
/PROGRAM, USED FOR WRITING AN READING FORWARDS.

```

/
01674 000000 CBANK 0
01675 205000 LAC MEMADR /C(MEMADR) = BITS 3 AND 4 ONLY
01676 505076 AND K60K
01677 544771 SAD FLOADA /FLOADA = BANK WITH PROGRAM
01700 601707 JMP CFIELD /ADJUST ADDRESS CONSTANTS
01701 205000 LAC MEMADR
01702 345103 TAD K17S /ADD 017777
01703 044777 DAC LSTLOC /LAST ADDRESS OF TESTED BANK
01704 204775 LAC LAST1
01705 044776 DAC LAST2 /VERY LAST TO TEST
01706 621674 JMP* CRANK /EXIT

/
01707 101710 CFIELD JMS .+1 /STORE EPC
01710 000000 0
01711 201710 LAC .-1
01712 505072 AND K10K /MASK ALL BUT BIT 5
01713 740200 SZA /IN LOWER 4K IF 0
01714 601727 JMP HIADJ /ADJUST FOR UPPER 4K
01715 205000 LAC MEMADR
01716 245072 XOR K10K
01717 045000 DAC MEMADR /FIRST TO TEST IS NOW 010000
/INSTEAD OF 000000

01720 245103 XOR K17S
01721 045007 DAC PATR
01722 245072 XOR K10K /ADD 17777
01723 044777 DAC LSTLOC /LAST LOC. IN TESTED BANK
01724 204775 LAC LAST1
01725 044776 DAC LAST2 /VERY LAST TO TEST
01726 621674 JMP* CRANK /EXIT

/
/ADJUST CONSTANTS WHEN PROGRAM IS IN HI 4K
/
01727 205000 HIADJ LAC MEMADR /FIRST ADDRESS TO TEST
01730 245102 XOR K7S
01731 044777 DAC LSTLOC /LAST TO TEST = 007777
01732 204775 LAC LAST1 /VERY LAST IN LAST BANK
01733 505076 AND K60K /MASK BITS 3,4
01734 544771 SAD FLOADA /SEE IF PROGRAM IS IN HIGH BANK
01735 601741 JMP .+4 /YES
01736 204775 LAC LAST1
01737 044776 DAC LAST2 /VERY LAST TO TEST = 017777
01740 621674 JMP* CRANK /EXIT
01741 245102 XOR K7S
01742 044776 DAC LAST2 /VERY LAST = 007777 INSTEAD OF 017777
01743 621674 JMP* CRANK

```

.EJECT



/ROUTINE TO DETERMINE IF BANK UNDER TEST CONTAINS  
/PROGRAM. USED FOR WRITING AND READING BACKWARDS.

```

01744      000000      CKBAK      0
01745      205000      LAC MEMADR      /LAST LOC. TO TEST
01746      505076      AND K60K
01747      544771      SAD FLOADA      /FLOADA = BANK WITH PROGRAM
01750      601756      JMP HILO      /ADJUST ADDRESS CONSTANTS
01751      345103      TAD K17S      /ADD 017777
01752      044777      DAC LSTLOC      /1ST ADR. OF TESTED BANK
01753      205001      LAC FIRST1
01754      045002      DAC FIRST2      /VERY LAST TO TEST IN LOW BANK
01755      621744      JMP* CKBAK      /EXIT

/
01756      101757      /HILO      JMS .+1      /STORE EPC
01757      000000      0
01760      201757      LAC .-1
01761      505072      AND K10K      /MASK ALL BUT BIT 5
01762      740200      SZA      /NO SKIP IF IN HI 4K FIELD
01763      602002      JMP ADJHI      /ADJUST FOR UPPER 4K
01764      205000      LAC MEMADR
01765      045007      DAC PATR
01766      245072      XOR K10K
01767      045000      DAC MEMADR      /MEMADR = 010000
01770      245102      XOR K7S      /ADD 007777 TO GET 017777
01771      044777      DAC LSTLOC      /= 1ST LOC. TO TEST GOING BACKWARDS
01772      205001      LAC FIRST1      /FIRST LOC. OF LOWEST BANK
01773      544771      SAD FLOADA      /DOES LOWEST HAVE PROGRAM
01774      601777      JMP .+3      /YES
01775      045002      DAC FIRST2      /VERY LAST TO TEST EQUALS 1ST
/
01776      621744      JMP* CKBAK      /LOCATION OF LOWEST BANK
01777      245072      XOR K10K      /EXIT
02000      045002      DAC FIRST2      /ADD 010000
02001      621744      JMP* CKBAK      /VERY LAST TO TEST GOING BACKWARDS
/
/ADJUST WHEN PROGRAM IS IN HI 4K
/
02002      205000      ADJHI      LAC MEMADR
02003      245102      XOR K7S      /ADD 007777
02004      044777      DAC LSTLOC      /LAST LOC = 1ST TO TEST
02005      245103      XOR K17S
02006      045007      DAC PATR
02007      205001      LAC FIRST1      /FIRST LOC. OF LOWEST BANK
02010      045002      DAC FIRST2      /LAST TO TEST = 1ST LOC. IN LO BA
02011      621744      JMP* CKBAK      /EXIT

.EJECT

```

/SETUP FOR NEXT BANK IN DESCENDING ORDER

02012	000000	/		
02013	205000	NXBAK	0	
02014	545002		LAC MEMADR	/LAST LOC. OF CURRENT BANK
02015	602027		SAD FIRST2	/= LAST TO TEST IN LOWEST BANK
02016	505076		JMP NORAK	/ALL DONE
02017	345074		AND K60K	/MASK 3 AND 4
02020	045000		TAD M20K	/SUBTRACT BK
02021	245103		DAC MEMADR	/LAST TO TEST IN NEXT BANK
02022	044777		XOR K17S	/ADD 017777
02023	750004		DAC LSTLOC	/FIRST TO TEST IN NEXT BANK
02024	741100		LAS	
02025	102062		SPA	/CHECK ACS 0 FOR HALT
02026	622012		JMS HALT	/GO HALT
			JMP* NXBAK	/EXIT
		/		
02027	750004	NORAK	LAS	
02030	741100		SPA	/CHECK CS 0 FOR HALT
02031	102062		JMS HALT	/GO HALT
02032	442012		ISZ NXBAK	
02033	442012		ISZ NXBAK	/NXBAK+2
02034	622012		JMP* NXBAK	/EXIT TO NEXT ROUTINE

.EJECT

```

/ROUTINE TO WRITE 1'S INTO ALL OF MEMORY
/
02035 000000 WRT1S 0
02036 205001 LAC FIRST1
02037 045000 DAC MEMADR
02040 101674 JMS CBANK /SEE IF 1ST HAS PROGRAM
02041 707702 FEM /EXTFD ON
02042 777777 RITE LAW -1 /AC = 777777
02043 065000 DAC* MEMADR /WRITE ONE
02044 205000 LAC MEMADR
02045 544777 SAD LSTLOC /LSTLOC = LAST LOC. TO TFST
02046 602051 JMP .+3 /DONE ONE BANK
02047 445000 ISZ MEMADR /INCREMENT ADDRESS COUNT
02050 602042 JMP RITE /WRITE IN NEXT
02051 445000 JMS NXTRNK /SETUP FOR NEXT BANK
02052 101636 JMS CBANK /SEE IF NEXT HAS PROGRAM
02053 101674 JMP RITE-1
02054 602041 LAC FIRST1 /RESTORE 1ST BANK ADDRESSES
02055 205001 DAC MEMADR /FIRST TO TEST IN FIRST BANK
02056 045000 TAD K17S
02057 345103 DAC LSTLOC /LAST TO TEST IN 1ST BANK
02060 044777 JMP* WRT1S /EXIT
02061 622035

/
/
/ALL ACS0 HALTS OCCUR HERE. ACS MAY BE CHANGED.
/PRESS CONTINUE TO RESUME HALTED TFST IF NO ACS
/CHANGES. PRESS CONTINUE TO EXECUTE ANY ACS CHANGES.
/
02062 000000 HALT 0
02063 740040 HLT
02064 750004 LAS /READ ACS
02065 544773 SAD MCWA /SEE IF CHANGED FROM PREVIOUS
02066 622062 JMP* HALT /NO CHANGES, RESUME HALTED TEST
02067 600127 JMP STOVER /DECODE NEW ACS

.EJECT

```

/ERROR ROUTINE. ACSØ WILL CAUSE HALT AFTER  
/PRINT-OUT IF RAISED.

00070 000000  
00071 707704  
00072 044755  
00073 204777  
00074 045040  
00075 202070  
00076 042100  
00077 602105

/  
ERRORA 0  
LEM  
DAC BAD /SAVE BAD DATA  
LAC LSTLOC /OCTAL ADR.  
DAC OCADR /SAVE  
LAC ERRORA /C(ERRORA) = (PC) FROM TEST ROUTINE  
DAC ERROR  
JMP ERROR+5  
/  
.EJECT

```

02100      000000      /
02101      707704      ERROR      0
02102      044755      LEM
02103      205000      DAC BAD      /EXTEND OFF
02104      045040      LAC MEMADR   /SAVE BAD DATA
02105      205007      DAC OCADR    /OCTAL ADDRESS
02106      045041      LAC PATR
02107      777777      DAC GOOD     /GOOD DATA
02110      045004      LAW -1
02111      045005      DAC RPFTE    /LAW -1 PREVENTS SECOND TYPE-OUTS
02112      204757      DAC WRCNT    /OF SAME ADDRESS
02113      542311      LAC ERWRD
02114      741000      SAD ENFRR    /ERROR TABLE DONE IF EQUAL
02115      602121      SKP
02116      202310      JMP .+4
02117      044757      LAC ERTBL    /RESTORE POINTER
02120      602130      DAC ERWRD    /C(ERTBL)=ERWRD+1
02121      205040      JMP SW2      /CHECK ACS 2
02122      505067      LAC OCADR    /OCTAL ADR
02123      545022      AND K70K     /MASK 3,4 AND 5
02124      602130      SAD LAST     /SAME 4K AS LAST ERROR IF EQUAL
02125      045022      JMP .+4      /SAME
02126      064757      DAC LAST
02127      444757      DAC* ERWRD   /STORE IN TABLE
02130      750004      ISZ ERWRD    /INCREMENT POINTER
02131      742010      SW2          LAS
02132      740100      RTL
02133      602141      SMA
02134      760207      JMP SW1      /CHECK ACS 2 FOR BELL
02135      102254      LAW 207
02136      744000      JMS PRFRR    /RING BELL
02137      707702      EREXIT      CLL
02140      622100      FEM
02141      750004      SW1          JMP* ERROR   /RETURN TO CURRENT TEST
02142      740010      LAS
02143      741100      RAL
02144      602136      SPA
02145      760000      JMP EREXIT   /CHECK ACS 1 FOR INHIBIT PRINT
02146      562537      /INHIBIT
02147      602152      /
02150      102232      /SETUP TO PRINT
02151      602136      /
02152      104537      LAW
02153      205003      SAD* SUPTBL  /IF 1ST LOC. OF SUPTBL=LAW,
02154      102254      JMP .+3      /NO ADR. SUPPRESSION WANTED
02155      777767      JMS CSUP
02156      045006      JMP EREXIT   /SEE IF THIS ERROR IS SUPPRESSED
02157      102301      JMS CRLF    /YES. RETURN TO CURRENT TEST
02158      102301      LAC TNUM    /CR, LF
02159      102301      JMS PRFRR   /NOT SUPPRESSED
02160      102301      LAW -11     /PRINT TEST NO.
02161      102301      DAC LOOPT   /-9
02162      102301      JMS SPING   /COUNTS SPACES
02163      102301      /SPACE 9
02164      102301      .EJECT

```

02160	205040	LAC OCADR	/OCTAL ADDRESS
02161	044537	DAC CRIF	/SAVE TEMPORARILY
02162	102261	JMS PROCTL	/PRINT THE ADDRESS
02163	777771	LAW -7	
02164	045006	DAC LOOPT	/COUNTS SPACES
02165	102301	JMS SPING	/SPACE 7
02166	205041	LAC GOOD	/COMPARE WORD
02167	044537	DAC CRLF	/SAVE
02170	102261	JMS PROCTL	/PRINT THE GOOD
02171	777775	LAW -3	
02172	045006	DAC LOOPT	/SPACE COUNTER
02173	102301	JMS SPING	/SPACE 3
02174	204755	LAC BAD	/DATA AS READ
02175	044537	DAC CRLF	/SAVE
02176	102261	JMS PROCTL	/PRINT THE BAD
02177	777773	LAW -5	
02200	045006	DAC LOOPT	/SPACE COUNTER
02201	102301	JMS SPING	/SPACE 5
02202	205003	LAC TNUM	
02203	505064	AND K377	
02204	545055	SAD K261	
02205	741000	SKP	
02206	602213	JMP .+5	
02207	202307	LAC LAL	
02210	045032	DAC PRNT	
02211	103010	JMS PNXT	
02212	602222	JMP SW0	
02213	205010	LAC PATBNK	/RANK WITH PATTERN
02214	744010	RCL; RTL;	RTL; RAL
02215	742010		
02216	742010		
02217	740010		
02220	345054	TAD K260	
02221	102254	JMS PRERR	/PRINT BANK NO.
02222	750004	LAS	
02223	741100	SPA	/CHECKACS 0 FOR HALT
02224	102062	JMS HALT	/GO HALT
02225	750004	LAS	
02226	505051	AND K200	
02227	740200	SZA	/CHECK FOR KEYBOARD INPUT
02230	602312	JMP KYBRD	/BIT 10 A 1. ACCEPT INPUT
02231	602136	JMP EREXIT	/RETURN TO CURRENT TEST
		/	
		/ROUTINE TO CHECK FOR SUPPRESSED ADDRESS	
		/	
02232	000000	CSUP 0	
02233	222537	LAC* SUPTBL	
02234	545040	SAD OCADR	/COMPARE WITH CURRENT ADDRESS
02235	602245	JMP TOP	/SPPPRESSED.
02236	545016	SAD LSTSUP	/SEE IF DONE WITH LIST
02237	602250	JMP SPEXT	
02240	202537	LAC SUPTRL	
02241	542540	SAD ENTBL	/SEE IF DONE WITH TABLE
02242	602250	JMP SPEXT	/YES
02243	442537	ISZ SUPTBL	/INCREMENT POINTER
02244	602233	JMP CSUP+1	/COMPARE NEXT
02245	202536	LAC STRL	/RESTORE POINTER

02246 042537  
02247 622232

DAC SUPTBL  
JMP\* CSUP

/EXIT

.EJECT

```

/
02250 202536 SPFXT LAC STBL
02251 042537 DAC SUPTBL
02252 442232 ISZ CSUP
02253 622232 JMP* CSUP

/
02254 000000 PRERR 0 /PRINT TEST OR BANK#
02255 700406 TLS
02256 700401 TSF
02257 602256 JMP .-1
02260 622254 JMP* PHERR

/
/PRINT 6 DIGIT OCTAL NOS.
/
02261 000000 PROCTL 0
02262 777772 LAW -6
02263 045006 DAC LOOPT /DIGIT COUNTER
02264 204537 LAC CRLF
02265 744010 POSITN RCL; RTL
02266 742010
02267 044537 DAC CRLF
02270 740010 RAL
02271 505066 AND K7 /MASK AC 15-17
02272 345054 TAD K260 /MAKE ASCII
02273 102254 JMS PRERR /PRINT 1
02274 445006 ISZ LOOPT /DONE 6 WHEN SKIP
02275 741000 SKP
02276 622261 JMP* PROCTL /EXIT
02277 204537 LAC CRLF
02300 602265 JMP POSITN /POSITION NEXT NUMBER

/
02301 000000 SPING 0
02302 760240 LAW 240
02303 102254 JMS PRERR /PRINT SPACE
02304 445006 ISZ LOOPT /DONE SPACING IF SKIP
02305 602302 JMP SPING+1 /ONE MORE
02306 622301 JMP* SPING /EXIT

/
02307 004745 LAL ALL
02310 004760 ERTBL ERWRD+1
02311 004770 ENERR ERWRD+11

/
/XADR9 - TAPE 3
/
/ROUTINES TO ACCEPT KEYBOARD INPUT FOR TEST SELECTION;
/ADDRESS SUPPRESSION AND BLOCK TEST LIMITS. PLACE ACS10 DOWN
/BEFORE RE-INITIATING MAIN PROGRAM.
/
02312 703302 KYBRD CAF /CLEAR FLAGS
02313 707704 LEM
02314 145033 DZM RITN /TEMP. STORAGE FOR INPUT CHARS.

/
/TYPE "TEST#" AND WAIT FOR INPUT
/
02315 202541 TSTNO LAC TSNX
02316 045032 DAC PRNT
02317 104537 JMS CRLF /CR,IF

```



02320 103010  
 02321 102766  
 02322 045033  
 02323 545064  
 02324 602312  
 02325 545052  
 02326 602350  
 02327 777517  
 02330 345033  
 02331 740100  
 02332 602335  
 02333 103657  
 02334 602312  
 02335 205033  
 02336 740001  
 02337 345043  
 02340 345061  
 02341 740100  
 02342 602345  
 02343 103657  
 02344 602312  
 02345 205033  
 02346 045003  
 02347 602350

TSTN

JMS PNXT  
 JMS KEYIN  
 DAC BITN  
 SAD K377  
 JMP KYBRD  
 SAD K215  
 JMP SUPIN  
 LAW -261  
 TAD BITN  
 SMA  
 JMP .+3  
 JMS WOTIS  
 JMP KYBRD  
 LAC BITN  
 CMA  
 TAD K1  
 TAD K265  
 SMA  
 JMP .+3  
 JMS WOTIS  
 JMP KYBRD  
 LAC BITN  
 DAC TNUM  
 JMP SUPIN

/PRINT "TEST#"  
 /GOT WAIT FOR INPUT  
 /SAVE TTY CHAR.  
 /IS INPUT A RUBOUT  
 /YES. START OVER  
 /NO TEST WANTED IF A C.R.  
 /LAST TEST PATTERN WILL BE USED

/IF AC IS NEG., TEST # IS <1  
 /IT IS >1  
 /PRINT QUESTION MARK  
 /START OVER

/2'S COMPLEMENT TEST #  
 /  
 /IF AC IS NEG., TEST # IS >5

/THERE ISN'T MORE THAN 5 TESTS  
 /START OVER

/WAIT FOR C.R.

.EJECT

/DONE WITH TEST#. NOWDO ADR. SUPPRESSION

02350	202536	SUPIN	LAC STRL	/1ST LOCATION IN SUPPRESS TABLE
02351	042537		DAC SURTBL	/PTRNTR
02352	202530		LAC ROTB	
02353	042527		DAC ROTA	
02354	222527		LAC* ROTA	
02355	045017		DAC NROTA	/SHIFT COUNTER
02356	760000		LAW	
02357	062537		DAC* SUPTBL	/A LAW IN 1ST LOC. SAYS NO
02360	145021		DZM ADRCW	/ADDRESS TO BE SUPPRESSED
02361	104537		JMS CRLF	/USED TO STORE 15 BIT ADDRESS
02362	202542		LAC SUPSX	
02363	045032		DAC PRNT	
02364	103010		JMS PNXT	/PRINT "SUPPRESS"

/ACCEPT 1ST ADDRESS AND THEN WAIT FOR A COLON  
/OR A COMMA

02365	203577	NXSUP	LAC INSUP	/C(INSUP) = SUPIN
02366	045020		DAC OVER	
02367	203600		LAC SUPDN	/C(SUPDN) = DNSUP
02370	045011		DAC EXIT	
02371	102766		JMS KEYIN	/WAIT FOR INPUT
02372	102775		JMS LEGAL	/CHECK VALIDITY
02373	222527		LAC* ROTA	
02374	045017		DAC NROTA	/C(NROTA) = COUNT FOR LEFT SHIFTS
02375	205033		LAC RITN	
02376	505066		AND K7	/MASK 15-17
02377	045033		DAC RITN	/SAVE
02400	104416		JMS GENADR	/START ASSEMBLING 1ST ADDRESS
02401	602365		JMP NXSUP	/GET NEXT INPUT

/RETURN HERE FROM GENADR AFTER 5 CHARS. REC'D.

02402	205021		LAC ADRCW	/FIRST ADDRESS
02403	062537		DAC* SUPTBL	/STORE IN 1ST LOC. OF TABLE
02404	442537		ISZ SUPTBL	
02405	145021		DZM ADRCW	/CLEAR
02406	102766		JMS KEYIN	/WAIT FOR A: , OR C.R.
02407	545064		SAD K377	/CHECK FOR RUBOUT
02410	602312		JMP KYPRD	/START OVER WITH TEST #
02411	545052		SAD K215	/CHECK FOR C.R.
02412	602517		JMP DNSUP	
02413	545053		SAD K254	/CHECK FOR COMMA
02414	602451		JMP SUPBLK	/A COMMA = SUPPRESS A BLOCK
02415	545062	CKCLN	SAD K272	/CHECK FOR COLON
02416	741000		SKP	
02417	603006		JMP QUERY	/NONE OF THE ABOVE.

.EJECT

/ACCEPT INDIVIDUAL ADDRESSES

```

/
02420      102766      SUP1      JMS KEYIN          /WAIT FOR INPUT
02421      102775      JMS LEGAL         /CHECK VALIDITY
02422      222527      LAC* ROTA
02423      045017      DAC NROTA        /COUNTS LEFT SHIFTS MADE
02424      205033      LAC BITN
02425      505066      AND K7          /MASK ACS 15-17
02426      045033      DAC BITN
02427      104416      JMS GENADR      /ASSEMBLE ADDRESS
02430      602420      JMP SUP1        /WAIT FOR NEXT CHAR.
02431      205021      LAC ADCRW      /COMPLETE ADDRESS
02432      062537      DAC* SUPTBL    /STORE IN SUPPRESSION TABLE
02433      045016      DAC LSTSUP     /LSTSUP = LAST TO SUPPRESS
02434      202537      LAC SUPTBL
02435      542540      SAD ENTBL      /CHECK FOR 256 ADDRESSES
02436      602443      JMP .+5        /WAIT FOR C.R.
02437      442537      ISZ SUPTBL     /INCREMENT POINTER
02440      145021      DZM ADCRW
02441      102766      JMS KEYIN      /WAIT FOR COLON INPUT
02442      545052      SAD K215      /DONE IF C. R.
02443      602517      JMP DNSUP
02444      602415      JMP CKCLN     /IS IT REALLY A COLON

/
02445      102766      JMS KEYIN      /WAIT FOR C.R.
02446      545052      SAD K215
02447      602517      JMP DNSUP
02450      603006      JMP QUFY      /NOT A C.R.

```

/ACCEPT TWO INPUTS FOR A SUPPRESSED BLOCK

```

/
02451      102766      SUPBLK      JMS KEYIN          /WAIT FOR INPUT
02452      102775      JMS LEGAL         /CHECK VALIDITY
02453      222527      LAC* ROTA
02454      045017      DAC NROTA        /COUNT LEFT SHIFTS
02455      205033      LAC BITN
02456      505066      AND K7          /MASK ACS 15-17
02457      045033      DAC BITN
02460      104416      JMS GENADR      /ASSEMBLE ADDRESS
02461      602451      JMP SUPBLK     /WAIT FOR NEXT

```

.EJECT

02462	205021	LAC ADRCW	/COMPLETE ADDRESS
02463	045016	DAC LSTSUP	/SAVE
02464	202536	LAC STRL	
02465	042537	DAC SUPTBL	/SETUP TABLE POINTER
02466	222537	LAC* SUPTBL	
02467	740001	CMA	
02470	345043	TAD K1	/2'S COMP. 1ST ADDRESS
02471	345016	TAD LSTSUP	/SUBTRACT 2ND ADDRESS
02472	740100	SMA	/1ST IS > LAST IF SKIP
02473	602502	JMP SETUP-3	
02474	222537	LAC* SUPTBL	/REVERSE THE TWO ADDRESSES
02475	045006	DAC LOOP	/SAVE FIRST
02476	205016	LAC LSTSUP	
02477	062537	DAC* SUPTBL	/LAST IS NOW FIRST
02500	205006	LAC LOOP	
02501	045016	DAC LSTSUP	/FIRST IS NOW LAST
02502	222537	LAC* SUPTBL	
02503	545016	SAD LSTSUP	/ARE THEY EQUAL
02504	602517	JMP DNSUP	/YES
02505	202537	LAC SUPTBL	
02506	542540	SAD ENTBL	
02507	602517	JMP DNSUP	/256 CHARS STORED. IGNORE ANY MORE
02510	222537	LAC* SUPTBL	
02511	345043	TAD K1	
02512	442537	ISZ SUPTBL	/INCREMENT POINTER
02513	062537	DAC* SUPTBL	
02514	545016	SAD LSTSUP	/WAS LAST THE LAST TO SUPPRESS
02515	602517	JMP DNSUP	/YES
02516	602505	JMP SETUP	
/			
/RESTORE POINTERS BEFORE ENTERING NEXT LINE			
/			
02517	202536	DNSUP	
02520	042537	LAC STBL	
02521	202530	DAC SUPTBL	
02522	542527	LAC ROTB	/ROTR AND ROTA MUST BE EQUAL
02523	602543	SAD ROTA	
02524	042527	JMP BLK1	
02525	103657	DAC ROTA	/RESTORE SHIFT COUNT POINTER
02526	602350	JMS WOTIS	/LAST ADDRESS WAS <5 CHARS
		JMP SUPIN	/START OVER WITH SUPPRESS
/			
02527	002531	ROTA	/= LOC. OF 1ST LAW
02530	002531	ROTB	/= LOC. OF 1ST LAW
02531	777763	LAW -15	/ROTATE 12 LEFT FOR 1ST DIGIT
02532	777766	LAW -12	/9 LFFT FOR 2ND
02533	777771	LAW -7	/6 LFFT FOR 3RD
02534	777774	LAW -4	/3 LFFT FOR 4TH
02535	777777	LAW -1	/NONE FOR 5TH
/			
.EJECT			

```

02536      005111      STBL      KEND+2      /VALUE = LAST LOC. OF PROGRAM+2
02537      000000      SUPTRL     0
02540      005510      ENTBL      KEND+401    /TABLE = 256 LOCS. (DECIMAL)
02541      004605      TSNX      TSN
02542      004613      SUPSX     SUPS
/
/INPUT ROUTINE FOR ADDRESS LIMITS OF BLOCK #1.
/PRESS CR IF NO BLOCKS WANTED.
/
02543      703302      BLK1      CAF
02544      145021      DZM ADRCW
02545      760000      LAW
02546      045012      DAC BLOC1
02547      045013      DAC BLOC2
02550      222527      LAC* ROTA
02551      045017      DAC NROTA      /SHIFT COUNTER
02552      104537      JMS CRLF
/
/TYPE "BLOCK#1" AND WAIT FOR INPUT
/
02553      203605      BLKN1     LAC BLKSX
02554      045032      DAC PRNT
02555      103010      JMS PNXT      /PRINT BLOCK #1
02556      203603      LAC DONE2     /C(DONE2) = DBLK1
02557      045011      DAC EXIT
02560      203601      LAC NBLK      /C(NBLK) = BLK1
02561      045020      DAC OVER
02562      102766      BLK1A     JMS KEYIN     /WAIT FOR INPUT
02563      102775      JMS LEGAL     /CHECK FOR LEGAL CHAR.
02564      205033      LAC BITN     /INPUT CHAR.
02565      505066      AND K7       /MASK AC 15-17
02566      045033      DAC BITN
02567      104416      JMS GENADR    /ASSEMBLE ADDRESS
02570      602562      JMP BLK1A
/
/ENTER HERE AFTER FIRST ADDRESS RECEIVED
/
02571      205021      LAC ADRCW     /ASSEMBLED ADDRESS
02572      045012      DAC BLOC1     /FIRST ADR. OF FIRST BLOCK
02573      145021      DZM ADRCW     /CLEAR
02574      102766      JMS KEYIN     /WAIT FOR COMMA
02575      545053      SAD K254     /CHECK FOR COMMA
02576      741000      SKP          /O.K.
02577      603006      JMP QUERY     /PRINT QUERY MARK
02600      102766      BLK1R     JMS KEYIN     /WAIT FOR INPUT
02601      102775      JMS LEGAL     /SEE IF IT'S LEGAL
02602      205033      LAC BITN     /INPUT CHAR.
02603      505066      AND K7       /MASK AC 15-17
02604      045033      DAC BITN
02605      104416      JMS GENADR    /ASSEMBLE SECOND ADR.
02606      602600      JMP BLK1R
/
.EJECT

```

/  
 /ENTER HERE AFTER SECOND ADDRESS  
 /

02607	205021	LAC ADRCW	/ASSEMBLED ADDRESS
02610	045013	DAC BLOC2	/LAST ADR. OF 1ST BLOCK
02611	145021	DZM ADRCW	
/			
02612	202530	DBLK1 LAC ROTB	
02613	542527	SAD ROTA	/MUST BE EQUAL
02614	602617	JMP .+3	/O.K.
02615	042527	DAC ROTA	
02616	603006	JMP QUERY	/ADDRESS IS NOT 5 CHARS.
02617	760000	LAW	/LAW = NO INPUT
02620	545012	SAD RLOC1	
02621	602662	JMP BLK2	
02622	545013	SAD RLOC2	/CHECK FOR 2 INPUTS
02623	603006	JMP QUFRY	/PRINT QUERY AND START OVER
02624	205012	LAC BLOC1	
02625	505067	AND K70K	
02626	045033	DAC RITN	/TEMP. SAVE ADR. BITS 3,4 AND 5
02627	205013	LAC RLOC2	
02630	505067	AND K70K	
02631	044537	DAC CRLF	/DO SAME WITH UPPER LIMIT
02632	102645	JMS PROG	/SEE IF LIMITS OVERLAP PROGRAM
02633	602662	JMP BLK2	/O.K.
02634	104537	JMS CRLF	/LIMITS IN SAME 4K AS PROGRAM
02635	203605	LAC BLKSX	
02636	045032	DAC PRNT	
02637	103010	JMS PNXT	/PRINT BLOCK #1
02640	203606	LAC OVRLP	
02641	045032	DAC PRNT	
02642	103010	JMS PNXT	/PRINT BLOCK #1
02643	103610	JMS LOCAT	/PRINT AREA OF PROGRAM
02644	602543	JMP BLK1	/START OVER
/			
02645	000000	PROG 0	
02646	102647	JMS .+1	/STORE EPC
02647	000000	0	
02650	202647	LAC .-1	
02651	505067	AND K70K	/MASK PC 3,4 AND 5
02652	545033	SAD BITN	/CHECK IF SAME AS LOW LIMIT
02653	741000	SKP	/YES. ERROR
02654	602657	JMP .+3	/NO
02655	442645	ISZ PROG	
02656	622645	JMP* PROG	
02657	544537	SAD CRLF	/SEE IF SAME AS HIGH LIMIT
02660	442645	ISZ PROG	/YES
02661	622645	JMP* PROG	/EXIT

.EJECT

```

/INPUT ROUTINE FOR BLOCK #2
/
02662      703302      PLK2      CAF
02663      145021          DZM ADRCW
02664      760000          LAW
02665      045014          DAC BLOC3
02666      045015          DAC BLOC4
02667      104537          JMS CRLF
/
/TYPE BLOCK #2 AND WAIT FOR INPUT
/
02670      203607      BLKN2      LAC BLKTX
02671      045032          DAC PRNT
02672      103010          JMS PNXT          /PRINT BLOCK #2
02673      203604          LAC DONE3        /C(DONE3)=DBLK2
02674      045011          DAC EXIT
02675      203602          LAC TBLK          /C(TBLK) = BLK2
02676      045020          DAC OVER
02677      102766      BLK2A      JMS KEYIN        /WAIT FOR INPUT
02700      102775          JMS LEGAL        /CHECK FOR LEGAL CHAR.
02701      205033          LAC BITN
02702      505066          AND K7           /MASK AC15-17
02703      045033          DAC BITN
02704      104416          JMS GENADR       /ASSEMBLE ADDRESS
02705      602677          JMP BLK2A
/
/ENTER HERE AFTER FIRST ADDRESS
/
02706      205021          LAC ADRCW        /ASSEMBLED ADDRESS
02707      045014          DAC BLOC3        /1ST ADR. OF 2ND BLOCK
02710      145021          DZM ADRCW
02711      102766          JMS KEYIN        /WAIT FOR COMMA
02712      545053          SAD K254
02713      741000          SKP
02714      603006          JMP QUERY        /PRINT QUERY
02715      102766      BLK2B      JMS KEYIN        /WAIT FOR INPUT
02716      102775          JMS LEGAL        /SEE IF IT'S LEGAL
02717      205033          LAC BITN
02720      505066          AND K7
02721      045033          DAC BITN
02722      104416          JMS GENADR
02723      602715          JMP BLK2B
/
/RETURN HERE AFTER 2ND ADDRESS
/
02724      205021          LAC ADRCW        /ASSEMBLED ADDRESS
02725      045015          DAC BLOC4        /LAST ADR. OF 2ND BLOCK
02726      145021          DZM ADRCW
02727      102766          JMS KEYIN        /WAIT FOR C.R.
02730      545052          SAD K215
02731      741000          SKP              /DNF
02732      603006          JMP QUERY        /PRINT QUERY
/
.EJECT

```

```

/
DBLK2  LAC ROTB
        SAD ROTA           /MUST BE EQUAL
        JMP .+3           /O.K.
        DAC ROTA
        JMP QUFRY         /ADR. IS NOT 5 CHARS.
        LAW
        SAD BLOC3         /LAW=NO INPUT
        JMP STLOOP
        SAD BLOC4         /CHECK FOR 2 INPUTS
        JMP DBLK2-1       /START OVER
        LAC BLOC3
        AND K70K
        DAC BITN         /TEMP. SAVE MA 3, 4 AND 5
        SAD BLOC4
        AND K70K
        DAC CRLF         /TEMP. SAVE HIGH LIMIT
        JMS PRG           /SEE IF LIMITS OVERLAP PROGRAM
        JMP STLOOP        /SETUP TO START LOOPS
        JMS CRLF         /LIMITS IN SAME 4K AS PROGRAM
        LAC BLKTX
        DAC PRNT
        JMS PNXT         /PRINT BLOCK #2
        LAC OVRLP
        DAC PRNT
        JMS PNXT         /PRINT LIMITS OVERLAP PROGRAM
        JMS LOCAT        /PRINT AREA OF PROGRAM
        JMP BLK2         /START OVER

/
/CHARACTER INPUT ROUTINE
/
KEYIN  0
        KRB
        KSF               /INITIALIZE
        JMP .-1           /WAIT
        KRF
        DAC BITN         /TEMP. SAVE
        JMP* KEYIN       /EXIT

/
/SEE IF CHARACTER IS LEGAL
/
LEGAL  0
        LAC BITN         /TTY CHAR.
        SAD K377         /CHECK FOR RUBOUT
        JMP KYBRD        /START OVER WITH TEST #
        SAD K215         /CHECK FOR C.R.
        JMP* EXIT        /TERMINATE
        AND K370
        SAD K260
        JMP* LFGAL
        JMS WOTIS       /EXIT
        JMS WOTIS       /PRINT QUESTION MARK
        JMP* OVER       /START PRESENT LINE OVER

/
.EJECT

```

```

02733  202530
02734  542527
02735  602740
02736  042527
02737  603006
02740  760000
02741  545014
02742  603666
02743  545015
02744  602732
02745  205014
02746  505067
02747  045033
02750  205015
02751  505067
02752  044537
02753  102645
02754  603666
02755  104537
02756  203607
02757  045032
02760  103010
02761  203606
02762  045032
02763  103010
02764  103610
02765  602662

```

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02766  000000
02767  700312
02770  700301
02771  602770
02772  700312
02773  045033
02774  622766

```

```

02775  000000
02776  205033
02777  545064
03000  602312
03001  545052
03002  625011
03003  505063
03004  545054
03005  622775
03006  103657
03007  625020

```



```

03010 000000 PNXT 0
03011 445032 IS7 PRNT /INCREMENT POINTER
03012 225032 LAC* PRNT
03013 741200 SNA
03014 623010 JMP* PNXT /ALL DONE
03015 700406 TLS
03016 700401 TSF
03017 603016 JMP .-1
03020 742020 RTR; RTR; RTR
03021 742020
03022 742020
03023 742020 RTR; RAR
03024 740020
03025 700406 TLS
03026 700401 TSF
03027 603026 JMP .-1
03030 603011 JMP PNXT+1
/
/Routine TO SETUP FOR FIRST PROGRAM MOVE
/
03031 707704 CMOVE LEM
03032 202310 LAC ERTBL
03033 044757 DAC ERWRD
03034 204756 LAC FLAGS
03035 740020 RAR /PUT AC 17 IN LINK
03036 741400 SZL /FORCED MOVE MADE IF=1
03037 600156 JMP EXTST /KEEP RUNNING
03040 740010 RAL
03041 741100 SPA /NOT 1ST MOVE IF A 1
03042 603146 JMP NXTMV /SEE WHERE TO GO NEXT
03043 245100 XOR K400K
03044 044756 DAC FLAGS /SET BIT 0
03045 750004 LAS
03046 505044 AND K3 /MASK UPPER LIMIT
03047 044774 DAC ULBNK
03050 750004 LAS
03051 742020 RTR
03052 505044 AND K3 /MASK LOWER LIMIT
03053 544774 SAD ULBNK /ONLY ONE BANK IF EQUAL
03054 603241 JMP ONLY1 /MOVF TO OPPOSITE 4K
03055 203055 LAC . /CURRENT BANK
03056 505072 AND K10K /MASK BIT 5
03057 740200 SZA /DON'T MOVE IF NOW IN HI 4K
03060 600100 JMP BEGIN /CLEAR AND START OVER.
03061 204775 LAC LAST1 /LAST BANK TO TEST
03062 505076 AND K60K
03063 044771 DAC FLOADA /LAST BAN WILL=CURRENT BANK
03064 760000 LAW /-8K
03065 344771 TAD FLOADA
03066 045023 DAC NXLOC /NEXT DEST'N AFTER THIS ONE
03067 103552 JMS WHERE /SEE WHERE THE PROGRAM IS
03070 044770 DAC FLOAD
03071 544771 SAD FLOADA /IS PRESENT SAME AS NEXT
03072 603134 JMP SUR1 /YES. SETUP FOR NEXT 1 LOWER
/

```

/NOW CHECK FOR ERROR RECORDED IN DEST'N BANK

03073 760000  
 03074 564757  
 03075 603106

/  
 CKERR LAW  
 SAD\* ERWRD /NO ERRORS IF = LAW  
 JMP STMV

03076 224757  
 03077 544771  
 03100 603117  
 03101 444757  
 03102 204757  
 03103 542311  
 03104 741000  
 03105 603076

LAC\* ERWRD  
 SAD FLOADA /DOES ERROR ADR. = NEW BANK  
 JMP EQUAL /YES.  
 IS\* ERWRD  
 LAC ERWRD  
 SAD ENERR  
 SKP  
 JMP CKERR+3 /DONE TABLE AND NO ERRORS  
 /LOOK AT NEXT

03106 202310  
 03107 044757  
 03110 204771  
 03111 045025  
 03112 103552  
 03113 045024  
 03114 545025  
 03115 600105  
 03116 603473

/  
 STMV LAC ERTBL  
 DAC ERWRD /RESTORE POINTER  
 LAC FLOADA /NEXT BANK  
 DAC DESTN  
 JMS WHERE /CURRENT BANK AND FIELD  
 DAC SOURCE  
 SAD DESTN /ARE THEY EQUAL  
 JMP BEGIN+5 /YES, START OVER, DON'T MOVE  
 JMP MOVE /MOVE FROM HERE TO C(DESTN)

/ERROR IN NEW BANK. TRY NEXT BANK LOWER

03117 545001  
 03120 603131  
 03121 741200  
 03122 603126  
 03123 760000  
 03124 344771  
 03125 045023  
 03126 202310  
 03127 044757  
 03130 603134

/  
 EQUAL SAD FIRST1 /DOES ADR. = LOWEST BANK  
 JMP DNMVE /YES, DON'T MOVE  
 SNA /IS IT ADR. 0, BANK 0  
 JMP .+4 /YES  
 LAW /-8K  
 TAD FLOADA /SUBTRACT 8K FROM NEW BANK  
 DAC NXLOC /NEXT NEW BANK  
 LAC ERTBL  
 DAC ERWRD /RESTORE POINTER  
 JMP SUB1

03131 202310  
 03132 044757  
 03133 600105

/  
 DNMVE LAC ERTBL /RESTORE POINTER  
 DAC ERWRD  
 JMP BEGIN+5 /START OVER

/SUBTRACT 8K FROM NEW BANK

03134 205023  
 03135 544771  
 03136 603117  
 03137 044771  
 03140 545001  
 03141 603073  
 03142 760000  
 03143 344771  
 03144 045023  
 03145 603073

/  
 SUB1 LAC NXLOC /NEXT NEW BANK  
 SAD FLOADA /SEE IF IT'S = CURRENT NEW ONE  
 JMP EQUAL /YES, TRY NEXT LOWER  
 DAC FLOADA /NEW NEW ONE  
 SAD FIRST1 /IS IT LOWEST BANK  
 JMP CKERR /YES, CHECK FOR ERRORS  
 LAW /-8K  
 TAD FLOADA  
 DAC NXLOC /NEW BANK FOR NEXT PASS  
 JMP CKERR /CHECK FOR ERRORS.

.EJECT

/ROUTINE TO DETERINE PROGRAM MOVE AFTER ONE MOVE HAS BEEN MADE.

```

/
03146      204756      /
03147      740010      /
03150      741100      /MOVF WITHIN A BANK IF A 1
03151      603241      /
03152      103552      /WHERE IS THE PROGRAM NOW
03153      045024      /
03154      760000      /
03155      564757      /NO ERRORS IF 1ST = LAW
03156      603171      /
03157      202310      /
03160      044757      /
03161      224757      /GET AN ERROR ADDRESS
03162      545023      /
03163      603211      /ERROR IN NEXT BANK. TRY NEXT
03164      444757      /
03165      204757      /
03166      542311      /
03167      741000      /DONF TABLE AND NO ERRORS
03170      603161      /
/
03171      202310      /
03172      044757      /RESTORE POINTER
03173      205023      /NEW BANK
03174      544771      /DOES IT = CURRENT BANK
03175      603200      /YES
03176      545001      /DOES IT = LOWEST BANK
03177      603222      /YES. CLEAR FLAGS AND MOVE
03200      545001      /DOES THE CURRENT ALSO=
/                                     /THE LOWEST BANK
/                                     /YES. SETUP FOR HIGHEST BANK
03201      603234      /NEW CURRENT BANK
03202      044771      /-8K
03203      760000      /
03204      344771      /
03205      045023      /NEW NEXT BANK
03206      204771      /
03207      045025      /
03210      603473      /MOVE FROM HERE TO C(DESTN)
/
03211      205023      /
03212      741200      /IS NEXT = BANK 0
03213      603131      /YES. DON'T MOVE
03214      760000      /-8K
03215      345023      /NEW NEXT BANK
03216      045023      /
03217      544771      /DOES IT = CURRENT BANK
03220      603212      /YES
03221      603157      /SEE IF ERROR IN NEW BANK
/

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.EJECT

```

03222 205023 MVBK LAC NXLOC /
03223 044771 DAC FLOADA /NEW CURRENT BANK
03224 045025 DAC DESTN
03225 103552 JMS WHERE /WHERE IS PROGRAM
03226 045024 DAC SOURCE
03227 760000 LAW
03230 064757 DAC* ERWRD
03231 045022 DAC LAST
03232 144756 DZM FLAGS /CLEAR PROGRAM FLAGS
03233 603473 JMP MOVE /MOVE
/
03234 204775 NXTHI LAC LAST1
03235 505076 AND K60K
03236 045023 DAC NXLOC /HIGHEST BANK = NEXT BANK
03237 044770 DAC FLOAD
03240 603157 JMP CKNXT+3 /CHECK FOR ERROR
/
/Routine to move routine to opposite 4K field
/WITHIN ONE BANK
/
03241 205001 ONLY1 LAC FIRST1
03242 544771 SAD FLOADA /DON'T MOVE IF NOT EQUAL
03243 603246 JMP .+3
03244 144756 DZM FLAGS
03245 600127 JMP STOVER /START OVER
03246 103552 JMS WHERE /WHERE IS PROGRAM NOW
03247 045024 DAC SOURCE /MOVE FROM HERE -
03250 245072 XOR K10K
03251 045025 DAC DESTN /TO HERE
03252 205100 LAC K400K
03253 744020 RCR
03254 244756 XOR FLAGS /SET BIT 1
03255 044756 DAC FLAGS
/
/CHECK FOR ERROR IN 4K FIELD
/
03256 760000 CFLD LAW
03257 564757 SAD* ERWRD /NO ERRORS IF = LAW
03260 603273 JMP MV4K
03261 202310 LAC ERTBL
03262 044757 DAC ERWRD
03263 224757 LAC* ERWRD
03264 545025 SAD DESTN
03265 603131 JMP DNMVE /ERROR IN 4K FIELD
03266 444757 ISZ ERWRD
03267 204757 LAC ERWRD
03270 542311 SAD ENERR
03271 741000 SKP
/
.EJECT

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03272 603263
03273 202310
03274 044757
03275 205025
03276 045026
03277 770000
03300 045006
03301 165026
03302 445026
03303 445006
03304 603301
                                MV4K  JMP CFLD+5
                                LAC ERTBL
                                DAC ERWRD
                                CLR4K  LAC DESTN
                                DAC MOVED
                                LAW -10000
                                DAC LOOPT
                                OZM* MOVED
                                ISZ MOVED
                                ISZ LOOPT
                                JMP .-3
/
/STARTING MOVING TO OPPOSITE 4K
/
03305 205024
03306 045027
03307 205025
03310 045026
03311 445027
03312 225027
03313 044537
03314 505101
03315 245101
03316 741200
03317 603327
03320 204537
03321 245072
03322 445026
03323 065026
03324 544550
03325 603331
03326 603311
                                MVRTN  LAC SOURCE
                                DAC MOVES
                                LAC DESTN
                                DAC MOVED
                                ISZ MOVES
                                LAC* MOVES
                                DAC CRLF
                                AND K700K
                                XOR K700K
                                SNA
                                JMP OPRAT
                                LAC CRLF
                                XOR K10K
                                ISZ MOVED
                                DAC* MOVED
                                SAD DLMT
                                JMP MVCST
                                JMP MVRTN
/
03327 204537
03330 603322
                                OPRAT  LAC CRLF
                                JMP MVOPR
/
03331 445027
03332 225027
03333 445026
03334 065026
03335 544550
03336 741000
03337 603331
03340 204756
03341 505043
03342 740200
03343 103547
                                MVCST  ISZ MOVES
                                LAC* MOVES
                                ISZ MOVED
                                DAC* MOVED
                                SAD DLMT
                                SKP
                                JMP MVCST
                                LAC FLAGS
                                AND K1
                                SZA
                                JMS FCHLT
/
                                /RESTORE POINTER
                                /-4K
                                /CLEAR THE 4K FIELD FIRST
                                /DONE CLEARING WHEN = 0
                                /MOVF FROM HERE
                                /TO HERE
                                /GET 1 FROM THIS FIELD
                                /SAVE TEMPORARILY
                                /CLEAR ALL BUT OP CODE
                                /OP CODE IS A 7X IF 0
                                /DON'T MODIFY IT
                                /ADJUST ADR. BY 10000
                                /PUT IN OPPOSITE 4K FIELD
                                /C(DLMT) = 752525
                                /NOW MOVE CONSTANT TABLES
                                /MOVF ANOTHER
                                /DONE CONSTANTS IF = 752525
                                /HALT IF FORCED MOVE
                                /PRESS CONT. AFTER RESTORING ACS
                                .EJECT

```

03344	203344		LAC .	
03345	505072		AND K10K	
03346	741200		SNA	/SEE WHICH 4K CURRENTLY IN
03347	625031		JMP* RGNHI	/LOWER EXIT TO UPPER
03350	204756		LAC FLAGS	
03351	505043		AND K1	
03352	054756		DAC FLAGS+10000	
03353	625030		JMP* RGNLO	/UPPER EXIT TO LOWER
/				
/ROUTINE TO FORCE MOVE THE PROGRAM				
/				
03354	744002	FCDMV	STL	
03355	750010		GLK	
03356	044756		DAC FLAGS	/SET FLAG FOR FORCED MOVE
03357	202310		LAC ERTBL	
03360	044757		DAC ERWRD	
03361	707704		LEM	
03362	750004		LAS	
03363	742020		RTR;	RTR; PTR
03364	742020			
03365	742020			
03366	505076		AND K60K	
03367	045025		DAC DESTN	/SAVE NEW BANK
03370	103552		JMS WHERE	/WHERE IS PROGRAM NOW
03371	045024		DAC SOURCE	/CURRENT FIELD AND BANK
03372	750004		LAS	
03373	505050		AND K100	
03374	740200		SZA	/IF BIT 11 A 1 MOVE TO HI 4K
03375	603432		JMP MVFHI	
03376	760000	CKFCD	LAW	
03377	564757		SAD* ERWRD	/NO ERRORS IF 1ST = LAW
03400	603414		JMP WHWAY	/NONF. SEE WHICH WAY TO MOVE
03401	202310		LAC ERTBL	
03402	044757		DAC ERWRD	/RESTORE POINTER
03403	224757		LAC* ERWRD	
03404	545025		SAD DESTN	/SEE IF ERROR ADR. = NEW BANK
03405	603413		JMP XPRT	/YES. PRINT MESSAGE
03406	444757		ISZ ERWRD	
03407	204757		LAC ERWRD	
03410	542311		SAD ENERR	
03411	603414		JMP .+3	
/INCREMENT POINTER				
03412	603403		JMP CKFCD+5	
03413	103556	XPRT	JMS PRSEL	/PRINT ERROR IN SELECTED 4K
/				
03414	205025	WHWAY	LAC DESTN	/IS DESTN TO THIS BANK
03415	544771		SAD FLOADA	
03416	603273		JMP MV4K	/YES. MOVE TO OTHER 4K
03417	044771		DAC FLOADA	/NEW CURRENT BANK
03420	203420		LAC .	
03421	505072		AND K10K	
03422	741200		SNA	/MOVE FROM HI TO LO 4K IF A 1
03423	603473		JMP MOVE	/MOVE SAME 4K TO SAME 4K
03424	205030		LAC RGNLO	/RGNLO = JMP STOVER
03425	505102		AND K7S	
03426	245025		XOR DESTN	/PUT BANK # ON THE JMP

03427 045030  
03430 707702  
03431 603273

DAC RGNLO  
FEM  
JMP MV4K

.EJECT

```

/
/ FORCE MOVE TO AN UPPER 4K
/
03432 205025 MVEHI LAC DESTN
03433 245072 XOR K10K /SET RIT 5 FOR HI 4K
03434 045025 DAC DESTN
03435 760000 FCDCCK LAW
03436 564757 SAD* ERWRD /NO ERRORS IF EQUAL
03437 603453 JMP WCHWA /SEE WHICH WAY TO MOVE
03440 202310 LAC ERTBL
03441 044757 DAC ERWRD /RESTORE POINTER
03442 224757 LAC* ERWRD
03443 545025 SAD DESTN /DOFS ERROR ADR. = NEW BANK
03444 603452 JMP PRTSL /YES. PRINT MESSAGE
03445 444757 ISZ ERWRD
03446 204757 LAC ERWRD
03447 542311 SAD ENFRR
03450 603453 JMP .+3

/INCREMENT POINTER
03451 603442 JMP FCDCCK+5
03452 103556 PRTSL JMS PRSEL /PRINT ERROR IN SELECTED 4K

/
WCHWA LAC DESTN
03453 205025 AND K60K
03454 505076 SAD FLOADA /IS DESTN TO THIS BANK
03455 544771 JMP MV4K /YES. MOVE TO OTHER 4K
03456 603273 LAC DESTN
03457 205025 DAC FLOADA /NEW CURRENT BANK
03460 044771 LAC .
03461 203461 AND K10K
03462 505072 SZA /MOVE FROM HI TO HI IF A 1
03463 740200 JMP MOVE
03464 603473 LAC BGNHI /BGNHI = JMP STOVER IN HI 4K
03465 205031 AND K7S
03466 505102 XOR DESTN /PUT RIT 5 AND BANK # ON THE JMP
03467 245025 DAC BGNHI
03470 045031 EEM
03471 707702 JMP MV4K
03472 603273

/
/ MOVE ROUTINE TO MOVE ONE 4K TO SAME 4K
/
MOVE FEM
03473 707702 LAC DESTN
03474 205025 DAC MOVED
03475 045026 LAW -10000
03476 770000 DAC LOOPT
03477 045006 DZM* MOVED /CLEAR THE 4K FIELD
03500 165026 ISZ MOVED
03501 445026 ISZ LOOPT
03502 445006 JMP .-3
03503 603500

/
.EJECT

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03504	205025		LAC DESTN	
03505	045026		DAC MOVED	/MOVF FROM HERE
03506	205024		LAC SOURCE	
03507	045027		DAC MOVES	/TO HERE
03510	445027	MOVEI	ISZ MOVES	/INCREMENT SOURCE POINTER
03511	225027		LAC* MOVES	
03512	445026		ISZ MOVED	/INCR. DESTINATION POINTER
03513	065026		DAC* MOVED	
03514	544550		SAD DLMT	/DNF INSTRUCTIONS IF=752525
03515	741000		SKP	
03516	603510		JMP MOVEI	
03517	205030		LAC BGNLO	/JMP STOVER IN LO 4K
03520	505102		AND K7S	
03521	245025		XOR DESTN	/PUT BANK# ON STOVER ADR.
03522	045030		DAC BGNLO	
03523	205031		LAC RGNHI	/JMP STOVER IN HI 4K
03524	505103		AND K17S	
03525	245025		XOR DESTN	/PUT BANK# ON STOVER ADR.
03526	045031		DAC BGNHI	
03527	445027	MOVEC	ISZ MOVES	
03530	225027		LAC* MOVES	
03531	445026		ISZ MOVED	
03532	065026		DAC* MOVED	
03533	544550		SAD DLMT	/DNF CONSTANTS IF = 752525
03534	741000		SKP	
03535	603527		JMP MOVEC	
03536	204756		LAC FLAGS	
03537	505043		AND K1	
03540	740200		SZA	/GO HALT IF A FORCED MOVF
03541	103547		JMS FCHLT	
03542	203542		LAC .	
03543	505072		AND K10K	/SEE WHICH 4K CURRENTLY IN
03544	740200		SZA	/LOWFR IF SKIP
03545	625031		JMP* BGNHI	/JMP TO STOVER IN HI 4K
03546	625030		JMP* BGNLO	/JMP TO STOVER IN LO 4K
			.EJECT	

```

/
/ HALT HERE IN CURRENT BANK AFTER A FORCED MOVE.
/ RESTORE ACS AND PRESS CONTINUF TO START PROGRAM IN NEW LOCATION.
/
03547 000000 FCHLT 0
03550 740040 HLT /RESTORE ACS AND PRESS CONT.
03551 623547 JMP* FCHLT
/
03552 000000 WHERE 0
03553 203552 LAC .-1
03554 505067 AND K70K
03555 623552 JMP* WHERE
/
/ PRINT ERROR IN SELECTED 4K
/
03556 000000 PRSEL 0
03557 104537 JMS CRLF /CR,LF
03560 203566 LAC ERSEL
03561 045032 DAC PRNT
03562 103010 JMS PNXT /PRINT MESSAGE
03563 104537 JMS CRLF /CR,LF
03564 102062 JMS HALT /GO HALT
03565 623556 JMP* PRSEL /RETURN HERE IF NO ACS CHANGES
/
03566 004716 ERSEL SLTER
/
/ PRINT L.L. EXCEEDS U.L.
/
03567 203576 HEXED LAC XCEED
03570 045032 DAC PRNT
03571 104537 JMS CRLF /CR,LF
03572 103010 JMS PNXT /L.L. EXCEEDS U.L.
03573 104537 JMS CRLF
03574 102062 JMS HALT /GO HALT
03575 600127 JMP STOVER /START OVER
/
03576 004732 XCEED LMTUL
/
. EJECT

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03577	002350	/	INSUP	SUPIN	
03600	002517		SUPDN	DNSUP	
03601	002543		NBLK	BLK1	
03602	002662		TBLK	RLK2	
03603	002612		DONE2	DBLK1	
03604	002733		DONE3	DBLK2	
03605	004623		HLKSX	RLKS	
03606	004643		OVRLP	OVLAP	
03607	004633		BLKTX	RLKT	
		/			
		/	/PRINT AREA OF PROGRAM		
		/			
03610	000000	LOCAT	0		
03611	104537		JMS CRLF		
03612	203656		LAC PISIN		
03613	045032		DAC PRNT		
03614	103010		JMS PNXT		/PRINT "PROGRAM IS IN"
03615	103616		JMS .+1		
03616	000000	EPC	0		
03617	203616		LAC .-1		
03620	505067		AND K70K		/MASK PC 3, 4 AND 5
03621	744010		RCL; RTL; RTL		
03622	742010				
03623	742010				
03624	740010		RAL		
03625	345054		TAD K260		/RANK #
03626	044537		DAC CRLF		/SAVE FMPORARILY
03627	203616		LAC EPC		
03630	505072		AND K10K		/MASK BIT 5
03631	740200		SZA		/SEF IF IN LOW OR HIGH 4K
03632	603647		JMP HIGHR		
03633	203653		LAC LOW		
03634	045032		DAC PRNT		
03635	103010		JMS PNXT	/PRINT "LOWER"	
		/			
			.EJECT		

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03636 203655 OFBNK LAC FLDBNK
03637 045032 DAC PRNT
03640 103010 JMS PNXT /PRINT "4K FIELD OF BANK"
03641 204537 LAC CRLF
03642 700406 TLS /PRINT BANK NO.
03643 700401 TSF
03644 603643 JMP .-1
03645 104537 JMS CRLF
03646 623610 JMP* LOCAT

/
03647 203654 HIGHR LAC HIRE
03650 045032 DAC PRNT
03651 103010 JMS PNXT /PRINT "UPPER"
03652 603636 JMP OFBNK

/
03653 004671 LOW LOWER
03654 004676 HIRE HIGH /HIGHER
03655 004703 FLDBNK FILD /4K FIELD OF BANK
03656 004660 PISIN PLOC /PROGRAM IS IN

/
03657 000000 WOTIS 0
03660 104537 JMS CRLF /CR, LF
03661 760277 LAW 277 /QUESTION MARK
03662 700406 TLS
03663 700401 TSF
03664 603663 JMP .-1
03665 623657 JMP* WOTIS

/
/SETUP ROUTINES FOR TESTING BLOCKS OF CORE
/
03666 104537 STLOOP JMS CRLF
03667 760000 LAW /SEE IF ANY BLOCKS SELECTED
03670 545012 SAD BLOC1
03671 741000 SKP /LAW=NONE SELECTED
03672 603676 JMP SETU1
03673 545014 SAD BLOC3
03674 600127 JMP STOVER /NO BLOCKS SELECTED
03675 603731 JMP SETU2

/
03676 205003 SETU1 LAC TNUM /SEE IF A TEST WANTED
03677 741200 SNA
03700 760261 LAW 261 /NO, USE TEST 1
03701 045003 DAC TNUM

/
/XMADR9 - TAPE 5
/
03702 205012 LAC BLOC1
03703 740001 CMA
03704 345043 TAD K1 /2'S COMPLEMENT UPPER LIMIT
03705 345013 TAD BLOC2
03706 741100 SPA /REVERSE IF NEG.
03707 603760 JMP REVR1
03710 205012 SINGL LAC BLOC1
03711 505076 AND K60K
03712 045010 DAC PATBNK

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03713	205003		LAC TNUM	/DETERMINE PATTERN
03714	505064		AND K377	
03715	545055		SAD K261	
03716	603767		JMP SIMU1	/TEST 1 PATTERN
03717	545056		SAD K262	
03720	604056		JMP SIMU2	/TEST 2 PATTERN
03721	545057		SAD K263	
03722	604116		JMP SIMU3	/TEST 3 PATTERN
03723	545060		SAD K264	
03724	604200		JMP SIMU4	/TEST 4 PATTERN
03725	545061		SAD K265	
03726	604256		JMP SIMU5	/TEST 5 PATTERN
03727	205003		LAC TNUM	
03730	740040		HLT	/SHOULD NEVER GET HERE
/				
03731	205003	SETU2	LAC TNUM	
03732	741200		SNA	
03733	760261		LAW 261	
03734	045003		DAC TNUM	
03735	205014		LAC BLOC3	
03736	740001		CMA	
03737	345043		TAD K1	
03740	345015		TAD BLOC4	
03741	741100		SPA	/LAST ADR. IS >FIRST IF 0
03742	603753		JMP REVR2	/REVRSE CONTENTS
03743	205014		LAC BLOC3	
03744	045012		DAC BLOC1	/1ST TO TEST
03745	205015		LAC BLOC4	
03746	045013		DAC BLOC2	/LAST TO TEST
03747	760000		LAW	/A LAW = NO BLOCK SELECTED
03750	045014		DAC BLOC3	
03751	045015		DAC BLOC4	
03752	603710		JMP SINGL	
/				
03753	205015	REVR2	LAC BLOC4	
03754	045012		DAC BLOC1	
03755	205014		LAC BLOC3	
03756	045013		DAC BLOC2	
03757	603747		JMP REVR2-4	
/				
03760	205013	REVR1	LAC BLOC2	
03761	045033		DAC BITN	
03762	205012		LAC BLOC1	
03763	045013		DAC BLOC2	
03764	205033		LAC BITN	
03765	045012		DAC BLOC1	
03766	603710		JMP SINGL	
			.EJECT	

```

/
/SETUP FOR TEST 1
/
03767      102035      SIMU1      JMS WRT1S      /PUT 1'S INTO ALL OF CORE
03770      205012      LAC BLOC1     /1ST LOC. OF BLOCK 1
03771      045000      DAC MEMADR    /ADDRESS COUNT
03772      205013      LAC BLOC2     /LAST LOC. OF BLOCK 1
03773      044777      DAC LSTLOC    /LAST LOC. TO TEST
03774      600327      JMP WBLK1     /WRITE PATTERN IN BLOCK 1
/
/RETURN HERE AFTER WRITING BLOCK 1
/
03775      205000      BLKA1      LAC MEMADR     /LAST ADDRESS WRITTEN
03776      545015      SAD BLOC4     /SEE IF 2 BLOCKS WRITTEN
03777      604011      JMP T1RDF     /SETUP TO READ FORWARD
04000      760000      LAW          /SEE IF BLOCK 2 WANTED
04001      545014      SAD BLOC3     /NO. READ BLOCK 1 FORWARDS
04002      604011      JMP T1RDF
04003      104371      JMS CKADR
04004      205014      LAC BLOC3     /1ST LOC. OF BLOCK 2
04005      045000      DAC MEMADR    /ADDRESS COUNT
04006      205015      LAC BLOC4     /LAST LOC. OF BLOCK 2
04007      044777      DAC LSTLOC    /LAST TO TEST
04010      600327      JMP WBLK1     /WRITE PATTERN IN BLOCK 2
/
/SETUP TO READ FORWARD
/
04011      205012      T1RDF      LAC BLOC1     /1ST LOC., BLOCK 1
04012      045000      DAC MEMADR    /ADDRESS COUNT
04013      205013      LAC BLOC2     /LAST LOC., BLOCK 1
04014      044777      DAC LSTLOC    /LAST TO TEST
04015      600366      JMP RLOP1     /READ BLOCK 1 FORWARDS
/
/RETURN HERE AFTER READING A BLOCK FORWARD
/
04016      205000      BLKB1      LAC MEMADR     /LAST READ
04017      545015      SAD BLOC4     /DONE BOTH BLOCKS IF EQUAL
04020      604031      JMP T1RDA     /READ 2 BLOCKS BACKWARD
04021      760000      LAW
04022      545014      SAD BLOC3     /SEE IF 2 SELECTED
04023      604036      JMP T1RDB     /NO. READ 1 BACKWARD
04024      205014      LAC BLOC3     /1ST LOC. OF BLOCK 2
04025      045000      DAC MEMADR
04026      205015      LAC BLOC4     /LAST LOC. OF BLOCK 2
04027      044777      DAC LSTLOC
04030      600366      JMP RLOP1     /READ BLOCK 2 BACKWARD
/
04031      205014      T1RDA      LAC BLOC3     /1ST IN BLOCK 2 = LAST TO READ
04032      045000      DAC MEMADR
04033      205015      LAC BLOC4     /LAST IN BLOCK 2 = 1ST TO READ
04034      044777      DAC LSTLOC
04035      600430      JMP RAK1     /READ BLOCK 2 BACKWARD
/
.EJECT

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03713	205003		LAC TNUM	/DETERMINE PATTERN
03714	505064		AND K377	
03715	545055		SAD K261	
03716	603767		JMP SIMU1	/TEST 1 PATTERN
03717	545056		SAD K262	
03720	604056		JMP SIMU2	/TEST 2 PATTERN
03721	545057		SAD K263	
03722	604116		JMP SIMU3	/TEST 3 PATTERN
03723	545060		SAD K264	
03724	604200		JMP SIMU4	/TEST 4 PATTERN
03725	545061		SAD K265	
03726	604256		JMP SIMU5	/TEST 5 PATTERN
03727	205003		LAC TNUM	
03730	740040		HLT	/SHOULD NEVER GET HERE
/				
03731	205003	SETU2	LAC TNUM	
03732	741200		SNA	
03733	760261		LAW 261	
03734	045003		DAC TNUM	
03735	205014		LAC BLOC3	
03736	740001		CMA	
03737	345043		TAD K1	
03740	345015		TAD BLOC4	
03741	741100		SPA	/LAST ADR. IS >FIRST IF 0
03742	603753		JMP REVR2	/REVRSE CONTENTS
03743	205014		LAC BLOC3	
03744	045012		DAC BLOC1	/1ST TO TEST
03745	205015		LAC BLOC4	
03746	045013		DAC BLOC2	/LAST TO TEST
03747	760000		LAW	/A LAW = NO BLOCK SELECTED
03750	045014		DAC BLOC3	
03751	045015		DAC BLOC4	
03752	603710		JMP SINGL	
/				
03753	205015	REVR2	LAC BLOC4	
03754	045012		DAC BLOC1	
03755	205014		LAC BLOC3	
03756	045013		DAC BLOC2	
03757	603747		JMP REVR2-4	
/				
03760	205013	REVR1	LAC BLOC2	
03761	045033		DAC BITN	
03762	205012		LAC BLOC1	
03763	045013		DAC BLOC2	
03764	205033		LAC BITN	
03765	045012		DAC BLOC1	
03766	603710		JMP SINGL	
			.EJECT	

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/
/SETUP FOR TEST 1
/
03767 102035 SIMU1 JMS WRT1S /PUT 1'S INTO ALL OF CORE
03770 205012 LAC BLOC1 /1ST LOC. OF BLOCK 1
03771 045000 DAC MEMADR /ADDRESS COUNT
03772 205013 LAC BLOC2 /LAST LOC. OF BLOCK 1
03773 044777 DAC LSTLOC /LAST LOC. TO TEST
03774 600327 JMP WBLK1 /WRITE PATTERN IN BLOCK 1
/
/RETURN HERE AFTER WRITING BLOCK 1
/
03775 205000 BLKA1 LAC MEMADR /LAST ADDRESS WRITTEN
03776 545015 SAD BLOC4 /SEE IF 2 BLOCKS WRITTEN
03777 604011 JMP T1RDF /SETUP TO READ FORWARD
04000 760000 LAW /SEE IF BLOCK 2 WANTED
04001 545014 SAD BLOC3
04002 604011 JMP T1RDF /NO. READ BLOCK 1 FORWARDS
04003 104371 JMS CKADR
04004 205014 LAC BLOC3 /1ST LOC. OF BLOCK 2
04005 045000 DAC MEMADR /ADDRESS COUNT
04006 205015 LAC BLOC4 /LAST LOC. OF BLOCK 2
04007 044777 DAC LSTLOC /LAST TO TEST
04010 600327 JMP WBLK1 /WRITE PATTERN IN BLOCK 2
/
/SETUP TO READ FORWARD
/
04011 205012 T1RDF LAC BLOC1 /1ST LOC., BLOCK 1
04012 045000 DAC MEMADR
04013 205013 LAC BLOC2 /LAST LOC., BLOCK 1
04014 044777 DAC LSTLOC
04015 600366 JMP RLOP1 /READ BLOCK 1 FORWARDS
/
/RETURN HERE AFTER READING A BLOCK FORWARD
/
04016 205000 BLKB1 LAC MEMADR /LAST READ
04017 545015 SAD BLOC4 /DONF BOTH BLOCKS IF EQUAL
04020 604031 JMP T1RDA /READ 2 BLOCKS BACKWARD
04021 760000 LAW
04022 545014 SAD BLOC3 /SEE IF 2 SELECTED
04023 604036 JMP T1RDB /NO. READ 1 BACKWARD
04024 205014 LAC BLOC3 /1ST LOC. OF BLOCK 2
04025 045000 DAC MEMADR
04026 205015 LAC BLOC4 /LAST LOC. OF BLOCK 2
04027 044777 DAC LSTLOC
04030 600366 JMP RLOP1 /READ BLOCK 2 BACKWARD
/
04031 205014 T1RDA LAC BLOC3 /1ST IN BLOCK 2 = LAST TO READ
04032 045000 DAC MEMADR
04033 205015 LAC BLOC4 /LAST IN BLOCK 2 = 1ST TO READ
04034 044777 DAC LSTLOC
04035 600430 JMP RAK1 /READ BLOCK 2 BACKWARD
/
.EJECT

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04036 205012 /
04037 045000 T1RDR LAC BLOC1 /1ST IN BLOCK 1 = LAST TO READ
04040 205013 DAC MEMADR /LAST IN BLOCK 1 = 1ST TO READ
04041 044777 LAC BLOC2 /READ BLOCK 1 BACKWARD
04042 600430 DAC LSTLOC /RETURN HERE AFTER READING A BLOCK BACKWARD
JMP BAK1 /

04043 204777 /
04044 545012 BLKC1 LAC LSTLOC /LAST READ
04045 741000 SAD BLOC1 /ALL DONE IF EQUAL
04046 604036 SKP /SETUP FOR BLOCK 1
04047 750004 JMP T1RDR
04050 741100 LAS /CHECK ASC 0 FOR HALT
04051 102062 SPA /GO HALT
04052 505051 JMS HALT
04053 740200 AND K200 /CHECK ASC 10
04054 602312 SZA
04055 603767 JMP KYHRD /START ALL OVER
JMP SIMU1 /

/SETUP FOR TEST 2
/
04056 102035 SIMU2 JMS WRT1S /WRITE 1'S INTO ALL OF CORE
04057 205012 LAC BLOC1 /1ST LOC. IN BLOCK 1
04060 045000 DAC MEMADR /ADDRESS COUNT
04061 740001 CMA /COMPLEMENT
04062 505103 AND K17S /CLEAR BITS 0-4
04063 045007 DAC PATR
04064 205013 LAC BLOC2
04065 505076 AND K60K
04066 245007 XOR PATR /PUT BANK# ON PATR
04067 045007 DAC PATR /PATTERN TO WRITE IN BLOCK 1
04070 205013 LAC BLOC2 /LAST LOC. IN BLOCK 1
04071 044777 DAC LSTLOC /LAST TO TEST
04072 600470 JMP LOP2+2 /WRITE PATTERN IN BLOCK 1

/RETURN HERE AFTER READING BLOCK 1
/
04073 760000 BLKA2 LAW
04074 545014 SAD BLOC3 /SEE IF 2 BLOCKS SELECTED
04075 604107 JMP BLKB2 /NO. WRITE AND READ BLOCK 1
04076 104371 JMS CKADR
04077 205014 LAC BLOC3 /1ST LOC. IN BLOCK 2
04100 045000 DAC MEMADR
04101 205015 LAC BLOC4 /LAST LOC. IN BLOCK 2
04102 044777 DAC LSTLOC
04103 204106 LAC DONE4 /C(DONE4) = BLKB2
04104 045011 DAC EXIT
04105 600672 JMP REST2+2 /SEE IF BLOCK 2=ALL 1'S

/
04106 004107 DONE4 BLKB2

/RETURN HERE AFTER READING BLOCK 2
/
04107 750004 BLKB2 LAS
04110 741100 SPA /CHECK ASC 0 FOR HALT
04111 102062 JMS HALT /GO HALT

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04112      505051      AND K200      /MAST BIT 10
04113      740200      SZA
04114      602312      JMP KYRRD     /GO ACCEPT INPUT
04115      604056      JMP SIMU2     /START OVER WITH BLOCK 1

/
/SETUP FOR TFST 3
/
04116      102035      SIMU3      JMS WRT15     /WRITT 1'S INTO ALL OF CORE
04117      205012      LAC BLOC1   /1ST LOC. OF BLOCK 1
04120      045000      DAC MEMADR
04121      205013      LAC BLOC2   /LAST LOC. OF BLOCK 1
04122      044777      DAC LSTLOC
04123      600737      JMP LOP3+1  /WRITE BLOCK1

/
04124      760000      BLKA3     LAW
04125      545014      SAD BLOC3   /SEE IF 2 BLOCKS SELECTED
04126      604140      JMP BLKB3   /ONLY ONE
04127      104371      JMS CKADR
04130      205014      LAC BLOC3   /1ST LOC. OF BLOCK 2
04131      045000      DAC MEMADR
04132      205015      LAC BLOC4   /LAST LOC. OF BLOCK 2
04133      044777      DAC LSTLOC
04134      204137      LAC DONE5   /C(DONE5)=BLKB3
04135      045011      DAC EXIT
04136      600672      JMP REST2+2

/
04137      004140      DONE5     BLKB3

/
04140      205012      BLKB3     LAC BLOC1
04141      045000      DAC MEMADR
04142      205013      LAC BLOC2
04143      044777      DAC LSTLOC
04144      601031      JMP FWD3-1  /READ BLOCK1 FORWARD

/
04145      205012      BLKC3     LAC BLOC1
04146      045000      DAC MEMADR
04147      205013      LAC BLOC2
04150      044777      DAC LSTLOC
04151      601055      JMP BAK3-1  /READ BLOCK 1 BACKWARD

/
04152      760000      BLKD3     LAW
04153      545014      SAD BLOC3   /SEE IF 2 BLOCKS
04154      604165      JMP BLKE3   /ONLY ONE
04155      205014      LAC BLOC3   /1ST LOC. OF BLOCK2
04156      045000      DAC MEMADR
04157      205015      LAC BLOC4   /LAST LOC. OF BLOCK 2
04160      044777      DAC LSTLOC
04161      204164      LAC DONE6   /C(DONE6)=BLKE3
04162      045011      DAC EXIT
04163      600672      JMP REST2+2 /READ BLOCK 2

/
04164      004165      DONE6     BLKE3

/
.EJECT

```

04165	205012	BLKE3	LAC BLOC1	/RESTORE BLOCK 1 LIMITS
04166	045000		DAC MEMADR	
04167	205013		LAC BLOC2	
04170	044777		DAC LSTLOC	
04171	750004		LAS	
04172	741100		SPA	/CHECK ACS 0 FOR HALT
04173	102062		JMS HALT	/GO HALT
04174	505051		AND K200	
04175	740200		SZA	/CHECK BIT 10
04176	602312		JMP KYRRO	/GO ACCEPT INPUT
04177	601110		JMP BAK3A-1	
		/		
		/SETUP FOR TEST 4		
		/		
04200	102035	SIMU4	JMS WRT1S	/WRITE 1'S INTO ALL OF CORE
04201	205012		LAC BLOC1	
04202	045000		DAC MEMADR	
04203	205013		LAC BLOC2	
04204	044777		DAC LSTLOC	
04205	601162		JMP LOP4+1	/WRITE BLOCK 1
		/		
04206	204221	BLKA4	LAC DONE7	/C(DONE7)=BLKB4
04207	045011		DAC EXIT	
04210	760000		LAW	
04211	545014		SAD BLOC3	/SEE IF 2 BLOCKS
04212	604222		JMP BLKB4	
04213	104371		JMS CKADR	
04214	205014		LAC BLOC3	
04215	045000		DAC MEMADR	
04216	205015		LAC RLOC4	/C(DONE7) = BLKB4
04217	044777		DAC LSTLOC	
04220	600672		JMP REST2+2	/READ BLOCK 2
		/		
		.EJECT		

04221	004222	/	DONE7	BLKB4	
04222	205012	/	BLKB4	LAC BLOC1	
04223	045000			DAC MEMADR	
04224	205013			LAC BLOC2	
04225	044777			DAC LSTLOC	
04226	601246			JMP FWD4-4	/RFAD BLOCK 1 AGAIN
04227	204255	/	BLKC4	LAC DONE8	/C(DONE8)=BLKD4
04230	045011			DAC EXIT	
04231	760000			LAW	
04232	545014			SAD BLOC3	
04233	604235			JMP BLKD4	
04234	604214			JMP BLKA4+6	
04235	750004	/	BLKD4	LAS	
04236	741100			SPA	/CHECK ACS 0
04237	102062			JMS HALT	/GO HALT
04240	505051			AND K200	
04241	740200			SZA	/CHECK BIT 10
04242	602312			JMP KYRRD	/GO ACCEPT INPUT
04243	205033			LAC BITN	
04244	545100			SAD K400K	/DONE 18 BITS IF EQUAL
04245	604200			JMP SIMU4	/START OVER
04246	744010			RCL	
04247	045033			DAC BITN	
04250	205012			LAC BLOC1	
04251	045000			DAC MEMADR	
04252	205013			LAC BLOC2	
04253	044777			DAC LSTLOC	
04254	601164			JMP LOP4+3	/DO NEXT BIT POSITION
04255	004235	/	DONE8	BLKD4	
		/			
				.EJECT	

/SETUP FOR TEST 5

```

/
SIMU5   JMS WRT1S           /WRT1S INTO ALL OF CORE
        LAC PCW             /PCW=036074
        DAC CNTRL          /CNTP=PATTERN CONTROL WORD
        LAC BLOC1         /1ST LOC. OF BLOCK 1
        DAC MEMADR
        LAC BLOC2         /LAST LOC. OF BLOCK 1
        DAC LSTLOC
        JMP LOP5+1        /WRITE BLOCK 1 FORWARD

BLKA5   LAC DONE9         /C(DONE9)=BLKB5
        DAC EXIT
        LAC BLOC1         /RESTORE LIMITS
        DAC MEMADR
        JMP RBLK5        /READ BLOCK 1 FORWARD

/
BLKB5   LAC DON10        /C(DON10)=BLKC5
        DAC EXIT
        LAC BLOC1         /RESTORE LIMITS
        DAC MEMADR
        JMP RBAK5+3     /READ BLOCK 1 BACKWARD

/
BLKC5   LAC DON11        /C(DON11)=BLKD5
        DAC EXIT
        LAW
        SAD BLOC3         /SEE IF 2 BLOCKS
        JMP BLKD5        /NO. WRITE COMPLEMENT
        JMS CKADR
        LAC BLOC3         /1ST LOC. OF BLOCK 2
        DAC MEMADR
        LAC BLOC4         /LAST LOC. OF BLOCK 2
        DAC LSTLOC
        JMP REST2+2     /READ BLOCK 2

/
BLKD5   LAC PCWA         /PCWA=741700
        SAD CNTRL        /DONT COMPLEMENT IF EQUAL
        JMP BLKE5        /SETUP TO WRITE BACKWARD
        DAC CNTRL
        JMP SIMU5+3     /WRITE COMPLEMENT IN BLOCK 1

/
BLKE5   JMS WRT1S           /WRITE 1'S INTO ALL OF CORE
        LAC BLOC1         /1ST LOC. OF BLOCK 1
        DAC MEMADR
        LAC BLOC2         /LAST LOC. OF BLOCK 1
        DAC LSTLOC
        JMP LOP5A       /WRITE BLOCK 1 BACKWARD

/
BLKF5   LAC DON12        /C(DON12)=BLKG5
        DAC EXIT
        LAW
        SAD BLOC3         /SEE IF 2 BLOCKS
        JMP BLKG5
        JMP BLKC5+5     /READ BLOCK 2
    
```

.EJECT

04334	204413	/	BLKG5	LAC DON13	/C(DON13)=BLKH5
04335	045011			DAC EXIT	
04336	205013			LAC BLOC2	
04337	044777			DAC LSTLOC	
04340	205035			LAC PCWA	/PCWA=741700
04341	545036			SAD CNTRL	/SEE WHICH TO READ
04342	205034			LAC PCW	/PCW=036074
04343	045036			DAC CNTRL	
04344	604270			JMP BLKA5+2	/READ BLOCK 1 FORWARD
		/			
04345	204414		BLKH5	LAC DON14	/C(DON14)=BLKJ5
04346	045011			DAC EXIT	
04347	604275			JMP BLKB5+2	/READ BLOCK 1 BACKWARD
		/			
04350	204415		BLKJ5	LAC DON15	/C(DON15)=BLKL5
04351	045011			DAC EXIT	
04352	760000			LAW	
04353	545014			SAD BLOC3	/SEE IF 2 SELECTED
04354	604356			JMP BLKL5	
04355	604305			JMP BLKC5+5	/READ BLOCK 2
		/			
04356	205035		BLKL5	LAC PCWA	/PCWA=741700
04357	545036			SAD CNTRL	/ALL DONE IF EQUAL
04360	741000			SKP	
04361	604320			JMP BLKE5	/WRITE COMPLEMENT BACKWARD
04362	750004			LAS	
04363	741100			SPA	/CHECK ACS0 FOR HALT
04364	102062			JMS HALT	/GO HALT
04365	505051			AND K200	
04366	740200			SZA	/CHECK ACS 10
04367	602312			JMP KYBRD	/GO ACCEPT INPUT
04370	604256			JMP SIMU5	/LEEP LOOPING
		/			
04371	000000		CKADR	0	
04372	205014			LAC BLOC3	
04373	740001			CMA	
04374	345043			TAD K1	
04375	345015			TAD BLOC4	
04376	740100			SMA	/1ST ADR. IS > 2ND IF A 1
04377	624371			JMP* CKADR	/1ST IS < 2ND
04400	205014			LAC BLOC3	/REVERSE BLOC3 AND BLOC4
04401	044537			DAC CRLF	/SAVE BLOC3
04402	205015			LAC BLOC4	
04403	045014			DAC BLOC3	
04404	204537			LAC CRLF	
04405	045015			DAC BLOC4	
04406	624371			JMP* CKADR	/EXIT
		/			
				.EJECT	

04407	004273	/		
04410	004300	DONE9	BLKB5	
04411	004313	DON10	BLKC5	
04412	004334	DON11	BLKD5	
04413	004345	DON12	BLKG5	
04414	004350	DON13	BLKH5	
04415	004356	DON14	BLKJ5	
		DON15	BLKL5	
		/		
04416	000000	GENADR	0	/ROUTINE TO ASSEMBLE ADDRESSES
04417	707704		LEM	
04420	222527		LAC* ROTA	
04421	045017		DAC NROTA	
04422	205033		LAC BITN	/INPUT NUMBER
04423	445017	CNROT	ISZ NROTA	/INCREMENT SHIFT COUNT
04424	604442		JMP GOLEFT	/ROTATE 1 LEFT
04425	245021		XOR ADCRW	/XOR WITH PARTIAL ADDRESS
04426	045021		DAC ADCRW	
04427	777777		LAW -1	
04430	562527		SAD* ROTA	/REC'D 5 NUMBERS IF EQUAL
04431	604434		JMP .+3	
04432	442527		ISZ ROTA	/INCREMENT LAW POINTER
04433	624416		JMP* GFNADR	/GO WAIT FOR NEXT
04434	202530		LAC ROTB	
04435	042527		DAC ROTA	/RESTORE LAW POINTER
04436	222527		LAC* ROTA	
04437	045017		DAC NROTA	
04440	444416		ISZ GENADR	
04441	624416		JMP* GENADR	/EXIT
		/		
04442	744010	GOLEFT	RCL	
04443	604423		JMP CNROT	
		/		
			.EJECT	



```

/
/HEADER ROUTINE
/
04444 000000 PHDR 0
04445 104537 JMS CRLF /CR, LF
04446 204467 LAC TSTX /POINTER FOR "TEST"
04447 045032 DAC PRNT
04450 104500 JMS CLMN
04451 204470 LAC ADRX /OCTAL ADR.
04452 045032 DAC PRNT
04453 104500 JMS CLMN
04454 204471 LAC GDATX /GOOD
04455 045032 DAC PRNT
04456 104500 JMS CLMN
04457 204472 LAC BDATX /BAD
04460 045032 DAC PRNT
04461 104500 JMS CLMN
04462 204473 LAC BWPA /BANK WITH PAT.
04463 045032 DAC PRNT
04464 104500 JMS CLMN
04465 104537 HOONE JMS CRLF /CR,LF
04466 624444 JMP* PHDR /START PROGRAM

/
/
04467 004551 TSTX TST /POINTER FOR "TEST"
04470 004555 ADRX ADR /"OCTAL ADR."
04471 004564 GDATX GDAT /"GOOD"
04472 004570 BDATX RDAT /"BAD"
04473 004574 BWPA RWPAT /"BANK WITH PAT."
04474 004751 END PEND

/
04475 104444 HDR JMS PHDR
04476 740040 HLT
04477 600100 JMP BEGIN

/
.EJECT

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```

/
/PRINT
/
CLMN      0
04500     000000
04501     445032      ISZ PRNT      /INCREMENT CHAR. POINTER
04502     225032      LAC* PRNT   /GET 2 CHARS
04503     741200      SNA        /DONE 1 COLUMN HEADER IF 0
04504     604521      JMP SPAC5  /DO 5 SPACES
04505     700406      TLS       /PRINT 1ST
04506     700401      TSF
04507     604506      JMP .-1
04510     742020      RTR;      RTR;      RTR
04511     742020
04512     742020
04513     742020      RTR;      RAR
04514     740020
04515     700406      TLS       /PRINT 2ND
04516     700401      TSF
04517     604516      JMP .-1
04520     604501      JMP CLMN+1 /GET NEXT PAIR

/
SPAC5     LAC CLMN   /C(CLMN)=(PC) AFTER JMS
04521     204500
04522     505103      AND K17S
04523     544536      SAD DNHDR  /C(DNHDR)= LOCATION HOONE
04524     624500      JMP* CLMN /DONE WITH HEADER
04525     777773      LAW -5    /-5
04526     045006      DAC LOOPT
04527     760240      LAW 240  /SPACE
04530     700406      SPCE     TLS
04531     700401      TSF
04532     604531      JMP .-1
04533     445006      ISZ LOOPT /SKIP AFTER 5 SPACES
04534     604530      JMP SPCE

/
04535     624500      JMP* CLMN /GET NEXT COLUMN HEADER

/
DNHDR     HDONE

/
CRLF      0
04537     000000
04540     760215      LAW 215  /CR
04541     700406      TLS
04542     700401      TSF
04543     604542      JMP .-1
04544     544546      SAD .+2
04545     624537      JMP* CRLF
04546     760212      LAW 212  /LF
04547     604541      JMP CRLF+2
04550     752525      DLMT     752525

/
.EJECT

```

04551	004551	TST	.		/TEST
04552	305324		305324;	324323	
04553	324323				
04554	000000		0		
		/			
04555	004555	ADR	.		/OCTAL ADR.
04556	303317		303317;	301324;	240314; 304301
04557	301324				
04560	240314				
04561	304301				
04562	256322		256322;	0	
04563	000000				
		/			
04564	004564	GDAT	.		/GOOD
04565	317307		317307;	304317	
04566	304317				
04567	000000		0		
		/			
04570	004570	RDAT	.		/BAD
04571	301302		301302;	377304	
04572	377304				
04573	000000		0		
		/			
04574	004574	BWPAT	.		/BANK WITH PAT.
04575	301302		301302;	313316;	327240; 324311
04576	313316				
04577	327240				
04600	324311				
04601	240310		240310;	301320;	256324
04602	301320				
04603	256324				
04604	000000		0		
		/			
			.EJECT		

04605	004605	TSN				
04606	305324		305324;	324323;	240243;	240255
04607	324323					
04610	240243					
04611	240255					
04612	000000		0			
		/				
04613	004613	SUPS				
04614	325323		325323;	320320;	305322;	323323
04615	320320					
04616	305322					
04617	323323					
04620	255240		255240;	377240;	0	
04621	377240					
04622	000000					
		/				
04623	004623	BLKS				
04624	314302		314302;	303317;	240313;	261243
04625	303317					
04626	240313					
04627	261243					
04630	255240		255240;	377240;	0	
04631	377240					
04632	000000					
		/				
04633	004633	BLKT				
04634	314302		314302;	303317;	240313;	262243
04635	303317					
04636	240313					
04637	262243					
04640	255240		255240;	377240;	0	
04641	377240					
04642	000000					
		/				
04643	004643	OVLAP				
04644	311314		311314;	311315;	323324;	317240
04645	311315					
04646	323324					
04647	317240					
04650	305326		305326;	314322;	320301;	320240
04651	314322					
04652	320301					
04653	320240					
04654	317322		317322;	322307;	315301;	0
04655	322307					
04656	315301					
04657	000000					
		/				
04660	004660	PLOC				
04661	322320		322320;	307317;	301322;	240315
04662	307317					
04663	301322					
04664	240315					
04665	323311		323311;	311240;	240316;	0
04666	311240					
04667	240316					
04670	000000					

04671	004671	LOWER	.				
04672	317314		317314;	305327;	240322;	0	
04673	305327						
04674	240322						
04675	000000						
/							
04676	004676	HIGH	.				
04677	320325		320325;	305320;	240322;	0	
04700	305320						
04701	240322						
04702	000000						
/							
04703	004703	FIELD	.				
04704	313264		313264;	306240;	305311;	304314	
04705	306240						
04706	305311						
04707	304314						
04710	317240		317240;	240306;	301302;	313316	
04711	240306						
04712	301302						
04713	313316						
04714	377240		377240;	0			
04715	000000						
/							
04716	004716	SLTER	.				
04717	322305		322305;	317322;	240322;	316311	
04720	317322						
04721	240322						
04722	316311						
04723	323240		323240;	314305;	303305;	305324	
04724	314305						
04725	303305						
04726	305324						
04727	240304		240304;	313264;	0		
04730	313264						
04731	000000						
/							
04732	004732	LMTUL	.				
04733	256314		256314;	256314;	305240;	303330	
04734	256314						
04735	305240						
04736	303330						
04737	305305		305305;	323304;	325240;	314256	
04740	323304						
04741	325240						
04742	314256						
04743	377256		377256;	0			
04744	000000						
/							
04745	004745	ALL	.				
04746	314301		314301;	377314;	0		
04747	377314						
04750	000000						
04751	004751	PEND	.				
04752	316305		316305;	377304;	0		
04753	377304						
04754	000000						

.EJECT

04755	000000	/		
04756	000000	BAD	0	/SAVFS BAD DATA
04757	000000	FLAGS	0	/VARIOUS BITS = PROGRAM FLAGS
04760	000000	ERWRU	0	
04761	000000		0	/STORES ADR. OF 1ST ERROR IN EACH BANK
04762	000000		0	
04763	000000		0	
04764	000000		0	
04765	000000		0	
04766	000000		0	
04767	000000		0	
04770	000000	FLOAD	0	/BANK PROGRAM LOADED IN
04771	000000	FLOADA	0	/BANK CURRENTLY WITH PROGRAM
04772	000000	MCW	0	/VALUE OF ACS
04773	000000	MCWA	0	/SAME AS MCW
04774	000000	ULRNK	0	/HAS HIGHEST BANK TO TEST
04775	000000	LAST1	0	/VERY LAST LOC. TO TEST IN HIGH BANK
04776	000000	LAST2	0	/SAME AS LAST1, BUT CHANGES
04777	000000	LSTLOC	0	/LAST LOC. TO TEST IN CURRENT BANK
05000	000000	MEMADR	0	/ADDRESS COUNTER
05001	000000	FIRST1	0	/1ST LOC. TO TEST IN FIRST BANK
05002	000000	FIRST2	0	/SAME AS FIRST1, BUT CHANGES
05003	000000	TNUM	0	/CURRENT TEST# (ASCII)
05004	000000	RPETE	0	/LOOP COUNTER
05005	000000	WRCNT	0	/LOOP COUNTER
05006	000000	LOOP	0	/TEST LOOP COUNTER
05007	000000	PATR	0	/DATA FOR COMPARISON
05010	000000	PATBNK	0	/CURRENT BANK WITH PATTERN
05011	000000	EXIT	0	/EXITS TO READ FORWARD OR BACKWARD
05012	760000	BLOC1	LAW	/1ST ADR. IN BLOCK 1
05013	760000	BLOC2	LAW	/LAST ADR. IN BLOCK 1
05014	760000	BLOC3	LAW	/1ST ADR. IN BLOCK 2
05015	760000	BLOC4	LAW	/LAST ADR. IN BLOCK 2.
05016	000000	LSTSUP	0	/SAVFS LAST SUPPRESSED ADR.
05017	000000	NROTA	0	/SHIFT COUNTER
05020	000000	OVER	0	/SAVFS EXIT ADRS.
05021	000000	ADRCW	0	/CONTAINS PARTIALLY ASSEM. ADR.
05022	000000	LAST	0	/ADR. OF LAST READ ERROR
05023	000000	NXLOC	0	/NEXT BANK TO MOVE TO
05024	000000	SOURCE	0	/BANK TO MOVE FROM
05025	000000	DESTN	0	/BANK TO MOVE TO
05026	000000	MOVED	0	/ADR. COUNTERS FOR MOVING
05027	000000	MOVES	0	
05030	000127	BGNLO	STOVER	/EXIT ADR. TO LO 4K
05031	010127	BGNHI	STOVER +10000	/EXIT ADR. TO HI 4K
		/		

.EJECT

05032	000000	PRNT	0	/POINTER FOR PRINT ROUTINES
05033	000000	HITN	0	/HOLDS A SLIDING BIT PATTERN
05034	036074	PCW	036074	/PATTERN WORD FOR TEST 5
05035	741700	PCWA	741700	/COMPLEMENT OF PCW
05036	000000	CNTRL	0	/TEMP. STORAGE FOR PCW OR PCWA
05037	000000	COMPR		/TEMP. STORAGE FOR TST5 COMPARE WORD
05040	000000	OCADR	0	/FAILING OCTAL ADR.
05041	000000	GOOD	0	/GOOD DATA
05042	000000	K0	0	
05043	000001	K1	1	
05044	000003	K3	3	
05045	000014	K14	14	
05046	000017	K17	17	
05047	000040	K40	40	
05050	000100	K100	100	
05051	000200	K200	200	
05052	000215	K215	215	
05053	000254	K254	254	
05054	000260	K260	260	
05055	000261	K261	261	
05056	000262	K262	262	
05057	000263	K263	263	
05060	000264	K264	264	
05061	000265	K265	265	
05062	000272	K272	272	
05063	000370	K370	370	
05064	000377	K377	377	
05065	000400	K400	400	
05066	000007	K7	7	
05067	070000	K70K	070000	
05070	002000	K2K	2000	
05071	004000	K4K	4000	
05072	010000	K10K	10000	
05073	020000	K20K	20000	
05074	760000	M20K	760000	
05075	040000	K40K	40000	
05076	060000	K60K	60000	
05077	076000	K76K	76000	
05100	400000	K400K	400000	
05101	700000	K700K	700000	
05102	007777	K7S	7777	
05103	017777	K17S	17777	
05104	037777	K37S	37777	
05105	057777	K57S	57777	
05106	077777	K77S	77777	
05107	005111	KEND	+.2	
		/		
	004475		.END HDR	/PRINT HEADER AFTER LOADING
			NO ERROR LINES	

ADJHT	02002
ADK	04555
ADKQW	05021
ADKX	04470
ADK1	00245
ADK2	00251
ADK3	00253
ADK4	00255
ADK5	00257
ADK6	00263
ADK7	00265
ADK8	00267
ADK9	00273
ADK0	00275
ALI	04745
ALI1	00675
BAD	04755
BAK1	00430
BAK2	00616
BAK3	01056
BAK3A	01111
BAK5	01506
BAK5A	01555
BASE	00320
B DAT	04570
B DATX	04472
REGIN	00100
RGVHT	05031
RGVLO	05030
RITN	05033
BLKA1	03775
BLKA2	04073
BLKA3	04124
BLKA4	04206
BLKA5	04266
BLKB1	04016
BLKB2	04107
BLKB3	04140
BLKB4	04222
BLKB5	04273
BLKC1	04043
BLKC3	04145
BLKC4	04227
BLKC5	04300
BLKD3	04152
BLKD4	04235
BLKD5	04313
BLKE3	04165
BLKE5	04320
BLKF5	04326
BLKG5	04334
BLKH5	04345
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REST3	00772
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REST4A	01217
REST5	01402
REST5A	01410
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FWD3	01032
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FW5	01443
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NXTHT	03234
UNI Y1	03241
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MVRTN	03311

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CKE CD	03376
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XHWAY	03414
MVHI	03432
ECOCK	03435
PRISI	03452
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