DB8-E
interprocessor buffer
engineering drawings

digital equipment corporation · maynard. massachusetts

MASTER DRAWING LIST

MAINTENANCE MANUALS NO.	TITLE	UNIT VARIATIONS DB8-EA	DB8-EB
DB8-E	INTERPROCESSOR BUFFER	X	X

USED ON OPTIONS							
PDP8-E							

REVISIONS REV.	DATE	CHG. NO.	APP'D.
A	8/71	DB8E-1	
B	10/71	DB8E-2	
C	2/72	DB8E-3	
D	4/72	DB8E-4	

DRN. K. GULICK	DATE	5/26/71
CHK'D. K. GULICK	DATE	5/26/71
ENG.	DATE	5-26-71
PROJ. ENG.	DATE	5-26-71
PROD.	DATE	
FIRST USED ON	PDP8-E	
SCALE	NONE	
SHEET	1 OF 2	

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

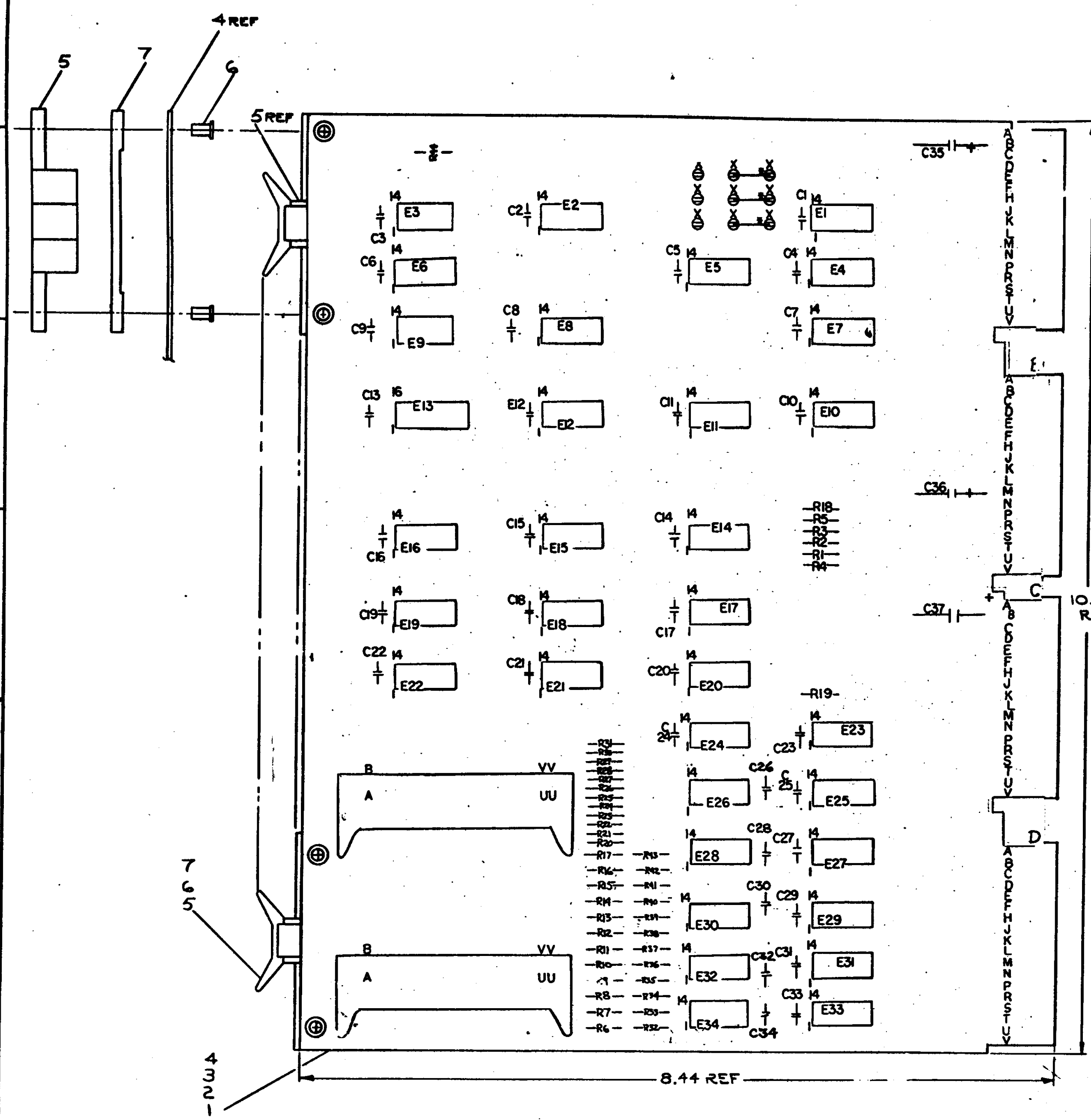
TITLE: INTERPROCESSOR BUFFER

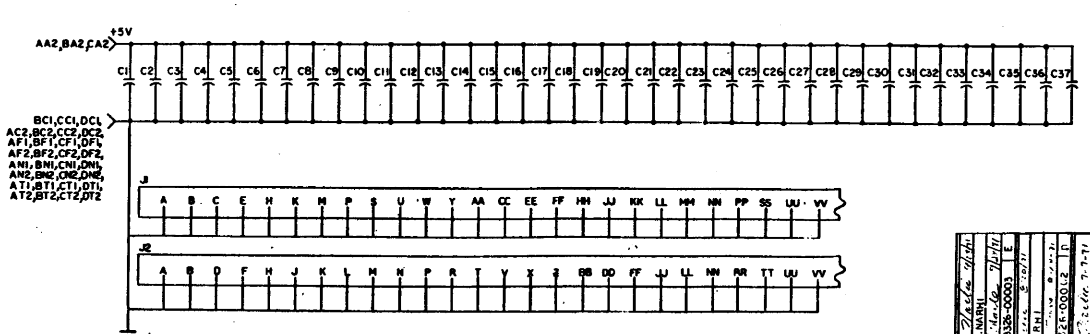
SIZE	CODE	NUMBER	REV.
A	ML	DB8-E	D

DIST.

PRINT SET DB8-E	DWG. NO.	REV. LET.	NO. OF SHEETS	TITLE	OPTION NO.
X	E-CS-M8326-Ø-1	D	2	INTERPROCESSOR BUFFER	
~~X~~	~~[illegible]~~	~~[illegible]~~	~~1~~	~~I/O CABLE (BCØ8R)~~	
X	C-UA-BCØ8R-Ø-Ø	E	1	I/O CABLE (BCØ8R)	
X	A-PL-DB8-E-0	B	1	INTERPROCESSOR BUFFER	
X	A-SP-DB8 E-1	A	4	ENGINEERING SPECIFICATION	
X	A-SP-DB8-E-2	A	3	TEST PROCEDURE	
X	A-SP-DB8-E-3	A	3	ACCEPTANCE PROCEDURE	
X	C-CS-549209_0-1		1	I/O CABLE ADAPTER	
—	LIBKIT-8E-DB8E	REF		PROGRAM LIBRARY KIT	
X	A-SP—DB8—E—4		1	ACCESSORY LIST	

TITLE	SHEET	SIZE	CODE	NUMBER	REV.
INTERPROCESSOR BUFFER	2 OF 2	A	ML	DB8-E	D





NOTES:

1. UNLESS OTHERWISE NOTED:
   RESISTORS = 1/4W 5%
   ~~DIODES = D664~~
   CAPACITORS = .01μf 100V 20%
2. ⊖ = INDICATES SPLIT LUG
3. JUMPERS B, D, F, ARE CUSTOMER OPTIONS PER TABLE - DO NOT INSERT. JUMPERS A, C, E ARE NOT OPTIONS - INSERT.

CODE	JUMPERS A	B	C	D	E	F
50	X		X		X	
51	X		X			X
52	X			X	X	
53	X			X		X
54		X	X		X	
55		X	X			X
56		X		X	X	
57		X		X		X

IC TYPE	GND	+5V
DEC 6314	1	8
DEC 6380	1	8
DEC 8251	8	16
DEC 384	1	8

GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTFULLY EXCEPTIONS ARE STATED ABOVE

IC PIN LOCATIONS

ITEM NO	AWG	FROM PT	TO PT
26	22	SEE NOTE 3	

JUMPER LIST

QTY.	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
12	R22-R31	RES 68K 1/4W 5%	13-01423	27
A/R		WIRE #22AWG SOLID BUS	9107560-01	26
8	E2, E11, E20, E23-E27	I.C. DEC 7474	1905547	25
2	E3, E10	I.C. DEC 7400	1905575	24
9	E6, E12, E28-E34	I.C. DEC 7440	1905579	23
1	E5	I.C. DEC 384	1909486	22
1	E13	I.C. DEC 8251	1909594	21
2	E7, E14	I.C. DEC 7404	1909686	20
5	E4, E16, E17, E19, E22	I.C. DEC 97401	1909973	19
5	E1, E9, E15, E18, E21	I.C. DEC 6380	1909971	18
1	E2	I.C. DEC 6314	1909972	17
12	R6-R17	RESISTOR 100 1/4W 5%	1300229	16
2	R1, R2	RESISTOR 150 1/4W 5%	1300250	15
18	R3, R4, R5, R19, R18, R32-R44	RESISTOR 220 1/4W 5%	1300271	14
3	C35, C36, C37	CAP. 6.8μf 35V 20% TANT	1000067	12
34	C1-C34	CAP. .01μf 100V 20% DISC	1001610	11
2		CONNECTOR, 40 PIN	1209941	10
9		SPLIT LUG	9006735	9
A/R				8
4		SPACER (CABLE CLAMP)	1202704	7
8		EYELET GS4-11 STIMPSON	9006750	6
4		HANDLE FLIP CHIP- MAGENTA	9008337-06	5
1		ETCHED CIRCUIT BOARD	5009511	4
REF		MODULE HISTORY LIST	B-MH-M8326-0-0	3
REF		ASSY/DRILLING HOLE LAYOUT	D-AH-M8326-0-5	2
REF		X-Y COORDINATE HOLE LOC.	K-CO-M8326-0-4	1

PARTS LIST

ETCH BOARD REV E

SEMICONDUCTOR CONVERSION CHART

DIGITAL EQUIPMENT CORPORATION

TITLE: INTERPROCESSOR BUFFER

DRN. DATE 4/5/71 · CHK'D. DATE 4-5-71 · ENG. · PROJ. ENG. · PROD.

FIRST USED ON A-ML-DB8-E

SCALE 2/1 · SHEET 1 OF 2

SIZE E · CODE CS · NUMBER M8326-Ø-1 · REV. E

MDB3 L API
MDB4 L BAI
MDB5 L BLI
MDB6 L BMI
MDB7 L BPI
MDB8 L BKI
I/O PAUSE L CBI
TP3 N CH2
MDB9 L DLI
MDB10 L DMI
MDB11 L DPI
INITIALIZE H CRI
CBI 7444
INT ENA
INT. I/O L
CSI SKIP L
C/PI INT RQST L
CEL CB L
CNI C/I L
J2E
J2C
+5V
-15V
DB2
J1RR
J1D
J1F
J1H
J1L
J1N
J1R
J1T
J1V
J1X
J1Z
J1BB
J1DD
J2SS
J2PP
J2MM
J2KK
J2HH
J2EE
J2CC
J2AA
J2Y
J2W
J2U
J2S
DATA 0 L API
DATA 1 L ASI
DATA 2 L BUI
DATA 3 L BVI
DATA 4 L BSI
DATA 5 L BUI
DATA 6 L BVI
DATA 7 L DRI
DATA 8 L DSI
DATA 9 L DUI
DATA 10 L DVI
DATA 11 L

QTY.	DESCRIPTION	PART NO.	ITEM NO.

PARTS LIST

UNLESS OTHERWISE SPECIFIED
DIMENSIONS IN INCHES
TOLERANCES
DECIMALS X .005 FRACTIONS ± 1/64 ANGLES ± 1/2°
FINAL SURFACE QUALITY
MACHINE FINISH AND BREAK SHARP CORNERS

	NAME	DATE
DRN		6-12-71
CHK		6-30-71
ENG		6-30-71
PROJ. ENG.		6-29-71
PROD.		DATE

MATERIAL
FINISH
FIRST USED ON
SCALE
SHEET 2 OF 2

digital EQUIPMENT CORPORATION
maynard, massachusetts

TITLE
INTERPROCESSOR BUFFER

SIZE/CODE E | NUMBER CS M8326-0-1 | REV. E
DIST.

D CS M8326-0-1

REVISIONS			
LTR	CHANGE NO.		INIT
E	M8326-00003	6-11-71	MMH

WIRE TABLE

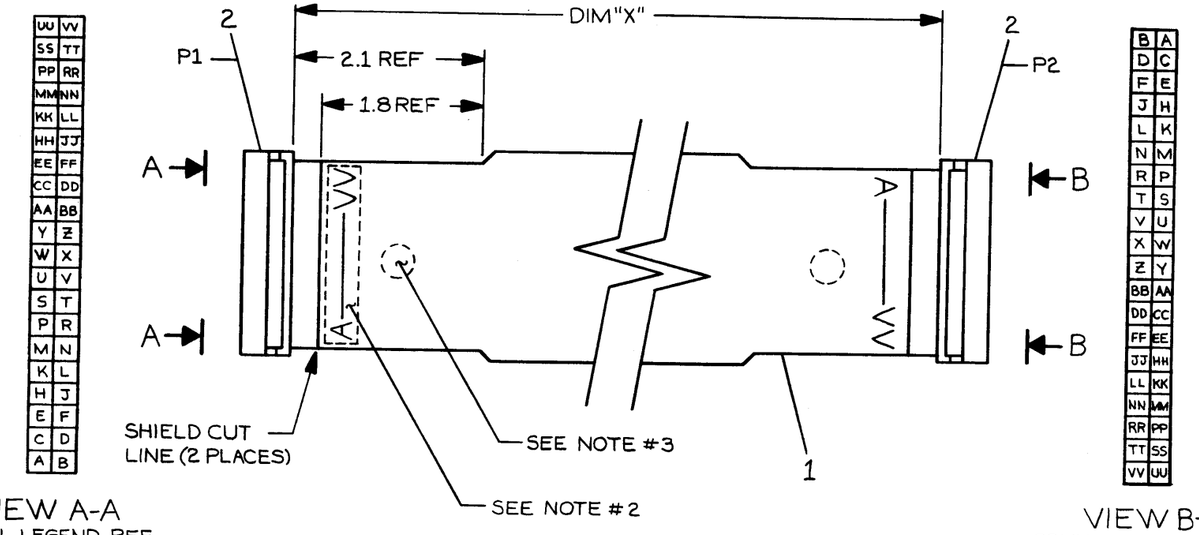
FROM	TO	FROM	TO
P1-A	P2-VV	P1-Y	P2-X
P1-B	P2-UU	P1-Z	P2-W
P1-C	P2-TT	P1-AA	P2-V
P1-D	P2-SS	P1-BB	P2-U
P1-E	P2-RR	P1-CC	P2-T
P1-F	P2-PP	P1-DD	P2-S
P1-H	P2-NN	P1-EE	P2-R
P1-J	P2-MM	P1-FF	P2-P
P1-K	P2-LL	P1-HH	P2-N
P1-L	P2-KK	P1-JJ	P2-M
P1-M	P2-JJ	P1-KK	P2-L
P1-N	P2-HH	P1-LL	P2-K
P1-P	P2-FF	P1-MM	P2-J
P1-R	P2-EE	P1-NN	P2-H
P1-S	P2-DD	P1-PP	P2-F
P1-T	P2-CC	P1-RR	P2-E
P1-U	P2-BB	P1-SS	P2-D
P1-V	P2-AA	P1-TT	P2-C
P1-W	P2-Z	P1-UU	P2-B
P1-X	P2-Y	P1-VV	P2-A

LEGEND

NUMBER	DIM "X"	PRECUT LENGTH
BCØ8R-Ø1	1FT	1FT 1.5 IN±1IN
BCØ8R-Ø2	2FT	2FT 1.5 IN±1IN
BCØ8R-Ø3	3FT	3FT 1.5 IN±1IN
BCØ8R-Ø4	4FT	4FT 1.5 IN±1IN
BCØ8R-Ø6	6FT	6FT 1.5 IN ±2IN
BCØ8R-Ø8	8FT	8FT 1.5 IN ±2IN
BCØ8R-1Ø	1ØFT	1ØFT 1.5 IN±2IN
BCØ8R-12	12FT	12FT 1.5 IN±3IN
BCØ8R-2Ø	2ØFT	2ØFT 1.5 IN±3IN
BCØ8R-25	25FT	25FT 1.5 IN ±3IN
BCØ8R-3Ø	3ØFT	3ØFT 1.5 IN ±.6FT
BCØ8R-5Ø	5ØFT	5ØFT 1.5 IN ± 1FT
BCØ8R-6Ø	6ØFT	6ØFT 1.5 IN±1.2FT
BCØ8R-75	75FT	75FT 15 IN±1.5FT
BCØ8R-AØ	1ØØFT	1ØØFT 1.5IN±2FT
BCØ8R-A3	13ØFT	13ØFT 1.5IN±2.6FT
BCØ8R-A6	16ØFT	16ØFT 1.5IN±3.2FT

NOTES:

1. ASSEMBLE THIS CABLE PER PROCESS SPEC #7606485-Ø-Ø.
2. CONNECTOR LEGEND IDENTIFICATION TO BE PLACED ON SHIELD SIDE OF CABLE IN THIS AREA AS SHOWN.
3. INSPECTION & TEST STAMPS TO BE PLACED AT EACH END OF THE CABLE ASSEMBLY.



VIEW A-A
CONN. LEGEND REF.

UU	VV
SS	TT
PP	RR
MM	NN
KK	LL
HH	JJ
EE	FF
CC	DD
AA	BB
Y	Z
W	X
U	V
S	T
P	R
M	N
K	L
H	J
E	F
C	D
A	B

VIEW B-B
CONN. LEGEND REF.

B	A
D	C
F	E
J	H
L	K
N	M
R	P
T	S
V	U
X	W
Z	Y
BB	AA
DD	CC
FF	EE
JJ	HH
LL	KK
NN	MM
RR	PP
TT	SS
VV	UU

REVISIONS			
CHK	CHANGE NO.	REV.	
PG	BCØ8R-00004	J	REVISED & REDRAWN 3-6-74 Steve Remond
			P. GARDNER 2/17/73

2	CONNECTOR, 4Ø SOCKET	1211ØØ6	2
A/R	CABLE, 4Ø COND. FLAT W/SHIELD	17ØØØØ4	1
	QTY. DESCRIPTION	PART NO.	ITEM NO.

PARTS LIST

FIRST USED ON OPTION/MODEL: —/—

UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES
TOLERANCES
DECIMALS: .xxx = .005, .xx = .02, .x = .1
ANGLES: ±0° 30'
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY √

MATERIAL —/—
FINISH —/—

DRN. D. FONTAINE	DATE 8-28-70	
CHK'D. J. FLEMING	DATE 8-28-70	
ENG. P. GARDNER	DATE 9-3-70	
PROJ. ENG. P. GARDNER	DATE 9-3-70	
PROD. DONALD	DATE 9-4-70	

NEXT HIGHER ASSY.

SCALE NONE
SHEET 1 OF 1

digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

TITLE
BCØ8R
I/O CABLE

SIZE	CODE	NUMBER	REV.
C	UA	BCØ8R-Ø-Ø	J

DIST.

SIZE C CODE UA NUMBER BCØ8R-Ø-Ø REV. J

DEC FORM NO. DRC 100-B

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

PARTS LIST

MADE BY KEN GULICK	CHECKED KEN GULICK	SECTION 1
DATE 5-26-71	DATE 5-26-71	
ENG Larry Marlie	PROD R. J. Olson	ISSUED SECT. 1
DATE 5-26-71	DATE 5-28-71	

ITEM NO.	DWG NO. / PART NO.	DESCRIPTION	QUANTITY / VARIATION DB8-EA	DB8-EB *
	E-CS-M8326-Ø-1	INTERPROCESSOR BUFFER	1	1
	~~D-UA-BCØ8J-Ø-10~~	~~I/O CABLE (BCØ8J)~~		~~2~~
	D-UA-BCØ8R-Ø-25	I/O CABLE (BCØ8R)	1	-
	~~D-UA-BCØ8~~R-0-6	I/O CABLE (BCØ8R) SEE NOTE	-	2
	D-UA-BCØ8R-Ø-1	I/Ø CABLE (BCØ8R) BELOW	-	2
	e-CS-5409209-0-1	I/O CABLE ADAPTER	2	2

* EITHER 6 OR 10 FOOT BCØ8R CABLES MAY BE SHIPPED IF CUSTOMER SPECIFIES LENGTH, IF NONE SPECIFIED, SHIP 10 FOOT CABLES.

TITLE	ASSY NO.	SIZE	CODE	NUMBER	REV.	ECO NO.
INTERPROCESSOR BUFFER	NONE	A	PL	DB8-E-Ø	B	DB8E-00002
	SHEET 1 OF 1	DIST.				

DEC FORM DRA 110 DEC 16-(325)-1031-N870

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 8/12/71

TITLE DB8-E INTERPROCESSOR BUFFER SPEC.

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
A	ECO CHANGE	DB8E-00004	NARHI	4/72	L. Narhi	4/25/72

ENG	APPD	SIZE	CODE	NUMBER	REV
Larry Narhi	Larry Narhi	A	SP	DB8-E-1	A

DEC FORM NO. DRA 107

ENGINEERING SPECIFICATION [redacted] CONTINUATION SHEET

TITLE DB8-E INTERPROCESSOR BUFFER

1. Overall Description

The DB8-E is an interprocessor buffer designed to plug directly into the PDP8/E OMNIBUS™. This option allows two 8/E's to transfer data between themselves, one 12 bit word at a time, at a maximum rate of approximately 5K Hz, allowing for software overhead.

Jumpers on the board allow the user to select any device code between 5Ø and 57 so up to 8 DB8-E's can be connected to one PDP8/E. The interconnecting cables allow the 8/E's to be up to 100 feet apart.

This option was designed primarily as an interprocessor buffer for the 8/E, however, this option may be used single ended as a data path between an 8/E and user designed logic.

2. General Specification

2.1 The basic option, DB8-EA, consists of one module, M8326, and one pair of BC08-R cables, up to 100' long. The DB8-EB is one M8326 module and a pair of BC08-R cables, and (2) cable adapters #5409209.

2.2 Option jumpers on the module allows the device code to be set to any value between 5Ø and 57.

2.3 The entire option is contained on one 8½" 8/E Quad module.

2.4 Temperature limits are: 32°F to 131°F
(Ø°C to 55°C)

Power required is: +5.Ø volts at 600 MA

3. Specification of Vendor Supplied Equipment

See applicable purchase specifications for board components.

4. Programming

	SIZE	CODE	NUMBER	REV
	A	SP	DB8-E-1	A

DEC FORM NO 16-1022

4.1 The IOT codes are:

Mnemonic	Octal Code	Function
DBRF	65X1	Skip if the receive set to a 1.
DBRD	65X2	Read incoming data into the AC, clear receive flag.
DBTF	65X3	Skip if the done flag is set to a 1.
DBTD	65X4	Load the AC into the transmit buffer, transmit and set the transmit flag.
DBEI	65X5	Enable the Interrupt Request line.
DBDI	65X6	Disable the Interrupt Request line.
DBCD	65X7	Clears the done flop.

NOTE: Initialize disables interrupts from occurring.

4.2 There are no maintenance instructions.

4.3 Data format is 12 parallel bits in and 12 parallel bits out.

4.4 There are no operator controls.

5. Interface Specification

Data is handled in the following manner for a transmit IOT.

a) First, the AC loaded into the transmit buffer in 8/E #1. Immediately, the data becomes true at the inputs to the receivers in 8/E #2.

b) At the trailing edge of the IOT, the done flag is set in 8/E #2 signifying that data is ready to be read; and if Interrupt Enable is true, Interrupt Request occurs.

For a receive IOT, the following takes place:

a) 8/E #2 issues IOT 65X7 to clear the "Done F/F"

b) When the read IOT occurs, the receivers are enabled in 8/E #2 and the data is loaded into the AC on a jam transfer.

c) At the trailing edge of the IOT the flag in #2 8/E is cleared readying it for another transfer.

The same sequence of events will occur when transferring data from 8/E #2 to 8/E #1. Each data output can sink 8 MA (5 TTL loads) and still maintain standard TTL noise immunity. Rise and fall times, without cable, are < 5Ø ns. Data is true at +3.Ø volts and false at Ø volts. Outputs are series terminated with 100 ohms. Each data input presents one unit load (1.6 MA) to a driver. Each input is clamped to -.6 volts. Data is true at +3.Ø volts and false at ØV.

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 6/2/71

TITLE DB8-EA/DB8-EB INTERPROCESSOR BUFFER TEST PROCEDURE

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
A	ECO CHANGE	DB8E-00002	L.NARHI	10/2-71	L Narhi	10/25/71

ENG	APPD	SIZE	CODE	NUMBER	REV
Larry Narhi	R X Allen	A	SP	DB8-E-2	A

DEC FORM NO. DRA 107

ENGINEERING SPECIFICATION [redacted] CONTINUATION SHEET

TITLE DB8-EA/DB8-EB INTERPROCESSOR BUFFER TEST PROCEDURE

1.0 EQUIPMENT

1.1 PDP8/E Standard (2 each)
1.2 Heat Box
1.3 453 Scope and voltage probes
1.4 Teletype (2 each)
1.5 Binary Loader Tape
1.6 MAINDEC-8E-DOPA-PB
DB8-E Interprocessor Buffer Test
1.7 M8326 tester module with one BC08R tester cable

2.0 PAPERWORK

2.1 Check paperwork in the envelope making sure it is complete as required by DEC Standard # 101.
2.1.1 Test and Inspection Record
2.1.2 Key sheet and ECO status sheet will contain both CS and etch revision.
2.1.3 Quality Control Inspection Report.
2.1.4 PDP8/E Progress Report.

3.0 TEST STATION SET UP (PART I)

3.1 Connect one BC08R cable from the output to input of the DB8E (M8326), to be tested.
3.2 Insert M8326 in PDP8-E # 1.

4.0 TEST STATION SET UP (PART II)

4.1 Insert M8326 tester module in PDP8-E # 2.
4.2 Insert M8326 module that is to be tested in PDP8-E # 1.
4.3 Take one BC08R cable and connect one end to the output of DB8-E in 8E # 1 and the other to the input of DB8-E in 8E # 2.
4.4 Take another BC08R and connect the same with 8E # 2 to 8E #1.

5.0 LOADING PROCEDURE

5.1 Load binary loader in 8E # 1 and # 2.
5.2 Load diagnostic in 8E # 1 and # 2.

6.0 DB8-E CHECKOUT

6.1 The following test programs are to be run 5 minutes each.
6.1.1 Part 1 (Test station set up Part 1).
6.1.2 Part 2 (Test station set up Part 2)

NOTE: The DB8-E being tested is the one in 8E # 1.

SIZE	CODE	NUMBER	REV
A	SP	DB8-E-2	A

DEC FORM NO 16-1022 DRA 108

7.0 HEAT TEST

7.1 Heat test is to be run after successful completion of all previously indicated tests.
7.2 Connect M8326 as per test station set up part 1.
7.3 Lower heat box over 8E # 1.
7.4 Turn on heat to box. Once indicator light goes off run part 1 test for 10 minutes.
7.5 Turn the heat switch off and open the two ports on the left side of the heat box.
7.6 Allow 15 minutes for the machine to cool before raising the heat box.
7.7 Terminate the test once the machine has run for 5 minutes of room temperature.

8.0 FINAL OPERATION AND INSPECTION

8.1 Remove the tested DB8E (M8326) from 8E # 1.
8.2 Disconnect cables from board.
8.3 If M8326 is to be used on a DB8-EA, attach one BC08R to the module. When used as a DB8-EB, two BC08R cables and two 54-09209 cable adapters will be required to go with the M8326.
8.4 Check that the following paperwork has been completed:
Envelope
ECO Status Sheet
QC Sheet
8E Progress Report

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 9/10/71

TITLE ACCEPTANCE PROCEDURE FOR THE DB8-E INTERPROCESSOR BUFFER

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
A	ECO CHANGE	DB8E-00002	L.NARHI	10-12-71	L. Narhi	10/15/71

ENG	APPD	SIZE	CODE	NUMBER	REV
Larry Narhi	C. Stevens	A	SP	DB8-E-3	A

DEC FORM NO. DRA 107

ENGINEERING SPECIFICATION [redacted] CONTINUATION SHEET

TITLE ACCEPTANCE PROCEDURE FOR THE DB8-E INTERPROCESSOR BUFFER

1.Ø Equipment Required

1.1 One or two M8326 modules
1.2 Two (2) BCØ8-R or two (2) BCØ8-J cables
1.3 MAINDEC-8E-DØPA, Interprocessor Buffer diagnostic
1.4 Two (2) PDP8/E computers with TTY

2.Ø Check the keysheet and Construction Requisition to see which of the following is required:

2.1 DB8-EA, which is one M8326 module and one BCØ8-R cable or

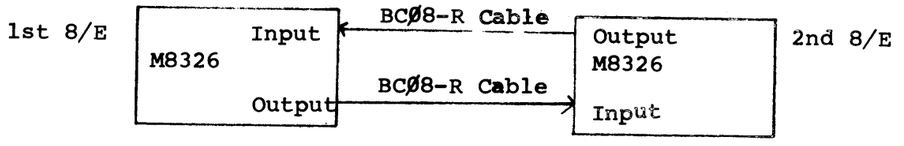
2.2 DB8-EB, which is one M8326 module and two BCØ8-J cables.

3.Ø Check the M8326(s) to see that the module has:

3.1 Proper circuit revision
3.2 Date code
3.3 Heat tested

4.Ø If the DB8-EB is required go to paragraph 5.Ø for acceptance testing. If the DB8-EA is required continue with this paragraph.

4.1 Insert one M8326 into each 8/E.
4.2 Connect one BCØ8-R cable to the "Input" connector on one of the M8326's, connect the other end of the cable to the "Output" connector of the other M8326. Reverse the procedure for the other cable.
The system would then look like this:



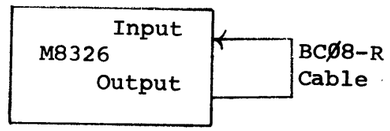
	SIZE	CODE	NUMBER	REV
	A	SP	DB8-E-3	A

DEC FORM NO 16-1022

4.3 Load the diagnostic into both 8/E's.
4.4 Start up one 8/E at location $2ØØØ_8$
4.5 Start the other 8/E at location $1ØØØ_8$
4.6 Minimum acceptance consists of running the diagnostic for 15 minutes.

5.Ø For the DB8-EB acceptance:

5.1 Insert the M8326 into the 8/E.
5.2 Connect one end of a BCØ8-R cable into the "Input" connector on the M8326. Connect the other end of the cable into the "Output" connector on the same M8326. The system would then look like:



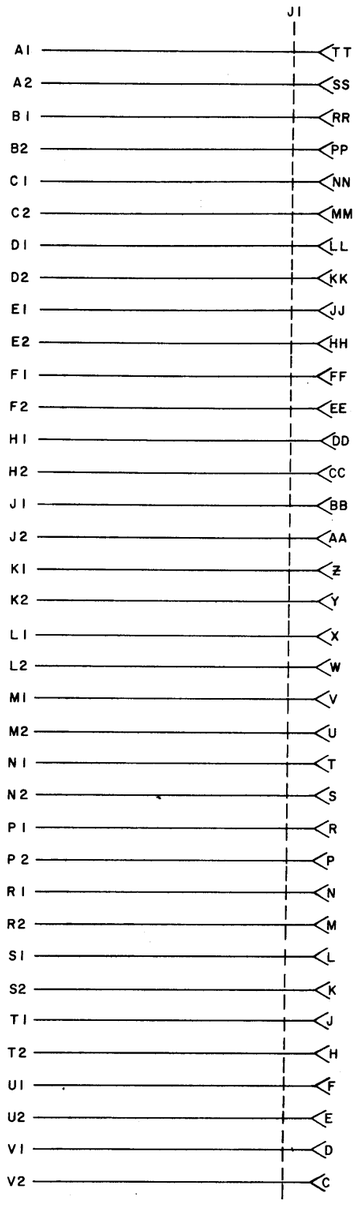
5.3 Load the diagnostic into the 8/E.
5.4 Load address $Ø2ØØ_8$, hit clear, set the SR = Ø, hit Continue.
5.5 Minimum acceptance consists of running the diagnostic for 15 minutes.

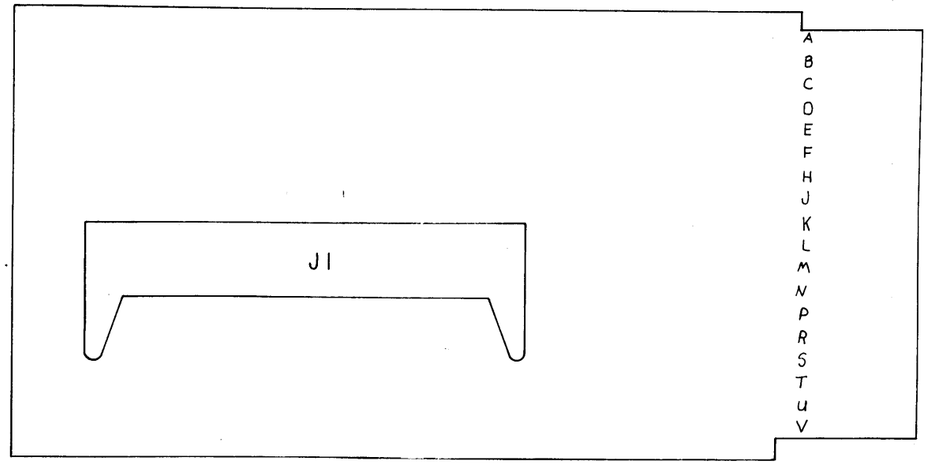
6.0 Shipping Hardware

6.1 M8326 Module 1 per
6.2 BC08R-25 1 per (DB8-EA only)
6.3 BC08 R 2 per (DB8-EB only)
54-09209 ADAPTERS 2 PER (DB8-EB ONLY)

7.0 Shipping software

7.1 LIBKIT-8E-DB8E
7.2 Print set as per A-ML-DB8-E including engineering specs.
7.3 8/E Maintenance Manual Vol II





QTY	REF. DESIGNATION	DESCRIPTION	DEC PART NO.	ITEM NO.
A/R		GRIPLETS	12102144-0	6
1	J1	RIGHT ANGLE HEADER	1209941	5
1		ETCHED CIRCUIT BOARD	5009208	4
		MODULE ECO HISTORY	B-MH-5409209-0-4	3
		ASSY/DRILLING HOLE LAYOUT	C-AH-5409209-0-5	2
		X-Y COORDINATE HOLE LOCATION	K-CO-5409209-0-4	1
		PARTS LIST		

REVISIONS			
REV.	CHG NO.	CHK	

DRN. S. Cooper	DATE 11/29/71	
CHK'D	DATE 11/29/71	
ENGR	DATE 1-12-72	
PROD.	DATE	

TRANSISTOR & DIODE CONVERSION CHART			
DEC	EIA	DEC	EIA

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE: I/O CABLE ADAPTER

SIZE	CODE	NUMBER	REV.
C	CS	5409209-0-1	A

PRINTED CIRCUIT REV. B

DEC FORM NO.
DRC 102

SIZE	CODE	NUMBER	REV.
C	CS	5409209-0-1	A

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ACCESSORY LIST

MADE BY J.Mc Cluskey DATE 1/30/72	CHECKED B Smith DATE 2-8-72	SECTION
ENG B. Smith DATE 2-8-72	PROD B Smith DATE 2-8-72	ISSUED SECT.

LEGEND

D DOCUMENT
DN DOCUMENT CHANGE NOTICE
PA PAPER TAPE ASCII
PB PAPER TAPE BINARY
PM PAPER TAPE READ-IN-MODE

ITEM NO.	DWG NO./PART NO.	DESCRIPTION	QUANTITY/VARIATION DB8-E-A	DB8-E-B
1	M8326	DB8/E Control Module	1	1
2	BC08R-25	DB8/E Cable	1	0
3	BC08R-10	DB8/E Cable	0	1
4	54-09209	DB8/E Adapters	0	2
5	Maindec-8E-DOS8-PB	DB8/E Interprocessor Buffer Test Paper Tape	1	1
6	Maindec-8E-DOS8-D	DB8/E Interprocessor Buffer Test Document	1	1
7	A-ML-DB8-E	DB8/E Print Set	1	1
8	DB8/E	DB8/E Interprocessor Buffer Maintenance Manual	1	1
		Note When Item 8 Is Temporarily Waived		
		Ship The Following		
	A-SP-DB8-E-1	DB8/E Engineering Specifications		
	A-SP-DB8-E-3	DB8/E Acceptance Procedure		

KIT CHECK BY ____ DATE ____

INSTALLATION CHECK BY ____ DATE ____

TITLE	ASSY. NO.	SIZE	CODE	NUMBER	REV.	ECO NO
DATA BUFFER	SHEET OF	A	AL	DB8-E-4		
		DIST.				

DEC FORM NO.
DRA 121