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.REPT 0

IDENTIFICATION

PRODUCT CODE: AC-F0848-MC
PRODUCT NAME: CXPLAB0 PCL11 MODULE
PRODUCT DATE: FEB 1979
MAINTAINER: DAVE WIENS, CSS KANATA

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1. ABSTRACT

PLA IS AN IOMODX ROUTINE THAT EXERCISES 1 (ONE) PCL11 ATTACHED TO THE PDP-11 SYSTEM UNITS. IT EXERCISES PCL11 BY CAUSING THE TRANSMITTER IN THE UNIT TO TRANSMIT A PULL SILO (64 WORDS) TO THE RECEIVER IN THE SAME UNIT. DATA IS OBTAINED FOR THE TRANSMISSION VIA INP AND READ MONITOR. ALL ERRORS ARE REPORTED ON THE CONSOLE PRINT DEVICE.

2. REQUIREMENTS

HARDWARE: PDP-11 WITH PCL11 ON THE UNTRUS TDM BUS CABLE DETACHED FROM UNIT UNDER TEST.

STORAGE: PLA REQUIRES:

- 1. DECIMAL WORDS: 810
- 2. DECIMAL WORDS: 1452
- 3. OCTAL BYTES: 3124

OTHER: THIS MODULE IS MEANT TO BE CONFIGURED AND RUN WITH DEC/X11 ONLY.

3. PASS DEFINITION

ONE PASS OF THE PLA MODULE CONSISTS OF 3072 CYCLES OF THE BASIC TEST SEQUENCE: TRANSMIT 64 WORDS TO THE RECEIVER, CHECK RCV'D WORDS FOR DATA ERRORS, TRAP ALL HARDWARE ERRORS. ONE PASS TAKES APPROXIMATELY 30 SECONDS.

4. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:
DEVICE ADDRESS: 164200
VECTOR: 170
PRIORITY (BRI) 5

REQUIRED PARAMETERS:
SRI: MUST CONTAIN PCL11 RCVR TDM-BUS ADDRESS.

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5. DEVICE / OPTION SETUP

SINCE PCL11 IS AN INTER-PROCESSOR COMMUNICATIONS DEVICE, BUS MAKE CERTAIN THAT THE UNIT IS DISCONNECTED FROM THE TDM BUS TO OTHER PROCESSORS. THAT THE RECEIVERS TDM-BUS ADDRESS BE ALSO IT IS VITAL. THAT THE RECEIVERS TDM-BUS ADDRESS BE KNOWN SO THAT IT MAY BE ENTERED INTO "SRI" AT CONFIGURE TIME.

7. MODULE OPERATION

TEST SEQUENCE:

- A. SET UP DEVICE REGISTER ADDRESSES AND MODULE VARIABLES
- B. PERFORM VALIDITY CHECK ON HARDWARE TO INSURE THAT THE MODULE WILL NOT GET "HUNG UP"
- C. GET WRITE AND READ INFORMATION FOR NPR
- D. ENABLE XMTR AND RCVR ERRORS - START TRANSMISSION.
- E. CHECK FOR XMTR & RCVR ERRORS - REPORT ANY AND RETRY UP TO RETRY LIMIT. GETS DROPPED IF TO OCTAL
- F. ERRORS OCCUR IN ONE CYCLE AND SRI IS POSITIVE (BIT 15 CLEAR). IF END OF PASS, REPORT AND GO TO C

7. OPERATION OPTIONS

- SRI BIT15 SET (1) RETRY LIMIT IS EXCEEDED, RESET RETRY LIMIT AND CONTINUE.
- SRI BIT15 CLEAR (0) IF ERROR RETRY LIMIT IS EXCEEDED A HARD ERROR IS ASSUMED AND THE MODULE IS DROPPED.

NOTE

THE RETRY COUNT IS CLEARED EVERY CYCLE UNLESS ERRORS OCCUR.

SRI BITS<7:0> MUST CONTAIN THE CORRECT RECEIVER TDM BUS ADDRESS. (COMMONLY CALLED RECEIVER NUMBER) THIS IS AN OCTAL NUMBER BETWEEN 1 AND 37.

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8. ERROR MESSAGES

- A. THE STANDARD DEC/X11 ERROR ROUTINES ARE USED IN THIS MODULE.
- B. FURTHER PRINTOUTS ARE SELF-EXPLANATORY.
- C. ERROR MESSAGES DUMP THE CONTENTS OF THE DEVICE REGISTERS IN THE FOLLOWING ORDER:
RECEIVER:
RCR RSR RDDB RDBC RDBA RDCRC
TRANSMITTER:
TCR TSR TSDB TSBC TSBA TMMR TSCRC

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150 ;PCL11 - ENDR
151 ;PCL11 - DEC/X11 SYSTEM EXERCISER MODULE
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153 000000*
154 000000*
155
156 IOMODX <PLAB>,164200,170,5,0,0,3072,0,BUFIN,64,64,
157 TITLE 150000,PLAB,164200,170,5,0,3072,0,BUFIN,64,64,
158 ; DDICOM VERSION 6 23-MAY-78
159 .LIST BIN
160 *****
161 BEGIN:
162 MODNAM: .ASCII /PLAB / ;MODULE NAME.
163 XFLDAG: .BVT OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
164 ADDR: 164200+0 ;1ST DEVICE ADDR.
165 VECTOR: 170+0 ;1ST DEVICE VECTOR.
166 BRT: .BVT PRTV5+0 ;1ST BR LEVEL.
167 DRT: .BVT PRTV0+0 ;2ND BR LEVEL.
168 DRTD1: 0+ ;DEVICE INDICATOR 1.
169 SRT: OPEN ;SWITCH REGISTER 1
170 SR1: OPEN ;SWITCH REGISTER 2
171 SR2: OPEN ;SWITCH REGISTER 3
172 SR3: OPEN ;SWITCH REGISTER 4
173 SR4: OPEN ;SWITCH REGISTER 4
174 *****
175 STAT: 150000 ;STATUS WORD
176 INIT: START ;MODULE START ADDR.
177 SPOINT: MODSP ;MODULE STACK POINTER.
178 PASCNT: 0 ;PASS COUNTER
179 ICONT: 3072. ;# OF ITERATIONS PER PASS=3072.
180 ITCOUNT: 0 ;LOC TO COUNT ITERATIONS
181 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
182 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
183 SOPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
184 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
185 SVSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
186 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
187 CONFIG: 0 ;RESERVED FOR MONITOR USE
188 RES1: 0 ;RESERVED FOR MONITOR USE
189 RES2: 0 ;RESERVED FOR MONITOR USE
190 SVR0: OPEN ;LOC TO SAVE R0.
191 SVR1: OPEN ;LOC TO SAVE R1.
192 SVR2: OPEN ;LOC TO SAVE R2.
193 SVR3: OPEN ;LOC TO SAVE R3.
194 SVR4: OPEN ;LOC TO SAVE R4.
195 SVR5: OPEN ;LOC TO SAVE R5.
196 SVR6: OPEN ;LOC TO SAVE R6.
197 CSRA: OPEN ;ADDR OF CURRENT CSR.
198 SBADR: OPEN ;ADDR OF GOOD DATA, OR
199 ACSR: OPEN ;CONTENTS OF CSR.
200 WASADR: OPEN ;ADDR OF BAD DATA, OR
201 ASTAT: OPEN ;STATUS REG CONTENTS.
202 ERRTP: OPEN ;TYPE OF ERROR.
203 ASB: OPEN ;EXPECTED DATA.
204 AWAS: OPEN ;ACTUAL DATA.
205 RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
206 WDMO: OPEN ;WORDS TO MEMORY PER ITERATION
207 WDMO: OPEN ;WORDS FROM MEMORY PER ITERATION
208 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
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206 IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
207 RBUFA: BUFIN ;READ BUFFER VIRTUAL ADDRESS
208 RBUFA: OPEN ;READ BUFFER PHYSICAL ADDRESS
209 RBUFA: OPEN ;READ BUFFER EA BITS
210 RBUFS2: 64 ;SIZE OF THE READ BUFFER
211 WBUFA: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
212 WBUFA: OPEN ;WRITE BUFFER EA BITS
213 WBUFA: OPEN ;WRITE BUFFER EA BITS
214 WBUFS: 64 ;WRITE BUFFER SIZE REQUESTED
215 WBUFS2: OPEN ;WRITE BUFFER SIZE AVAILABLE
216 CDERRCT: OPEN ;CDATA/DATCK ERROR COUNT
217 CDWDCT: OPEN ;CDATA/DATCK WORD COUNT
218 FREE: OPEN ;RESERVED FOR FUTURE USE
219 .REPT SPSIZ ;MODULE STACK STARTS HERE.
220 .NLST 0
221 .WORD 0
222 .LIST
223 .ENDR
224 *****
225 MODSP:
226 ;SOME EXTRA DEFINITIONS:
227 ;TRANSMIT START FUNCTION:
228 TXMSTR = 60101
229 ;RECEIVE START FUNCTION:
230 RCVSTR = 60001
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234 000252 004767 001126 START: JSR PC,SETUP ;GENERATE DEVICE ADDRESSES
235 000256 004767 001322 JSR PC,ISDRP ;SET MASTER, DROP MODULE?
236 000262 005767 001734 TSTB DEVICE ;IF SET, DROP MODULE
237 000266 001045 BNE FINI
238
239 000270 * RESTRT:
240 000270 * 104415 000000 * 000124 * GETPAS,BEGIN,RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
241 000272 * 016767 177630 001702 * MOV RBUF$Z,RCBC ;GET RCVR BUFF SIZE
242 000274 * 006367 001676 * ASL RCBC ;DOUBLE IT FOR BYTE COUNT
243 000310 * 005467 001672 * NEG RCBC ;NEGATE BYTE COUNT
244
245 000314 * BCGW:
246 000314 * 104414 000000 * GMBUFS, BEGIN ;GET WRITE BUFFER INFORMATION
247 000320 * 016767 177616 * MOV WBUF$Z,TXBC ;GET ALLOCATED BUFFER SIZE
248 000322 * 006367 001652 * ASL TXBC ;DOUBLE IT FOR BYTE COUNT
249 000332 * 005467 001652 * NEG TXBC ;NEGATE BYTE COUNT
250
251 000336 * BCHK:
252 000336 * 105067 001652 * CLR B ERLG ;CLEAR ERRORS FLAG
253 000342 * 004767 000000 * JSR PC,TRNSFR ;SEND SOME DATA & RCV IT
254 000348 * 105767 001652 * TSTB ERLG ;IF ERRORS, RETRY UP TO 10 TIMES
255 000352 * 001723 * BNE RETRY
256 000354 * 004767 001006 * JSR PC,ERSUB2 ;LOAD ERROR INFORMATION
257
258 000360 * 104412 000000 * 000126 * CDATA$,BEGIN,RBU$PA ; REQUEST FOR MONITOR TO CHECK DATA
259 000366 * 000370 * . * ; IF ERROR, CONTINUE
260
261 000372 * 105067 001627 * CLR B TRY ;REFRESH RETRY COUNT
262
263 000374 * 104413 000000 * PASS: ENDDITS,BEGIN ;SIGNAL END OF ITERATION.
264 ;MONITOR SHALL TEST END OF PASS
265
266 000400 * 009745 * BR BCGW
267
268 000402 * 052777 000002 002042 FINI: BIS #BIT1,@RCR ;CLEAR RCVR HDWARE
269 000410 * 052777 000002 002012 BIS #BIT1,@TCR ;CLEAR XMTR HDWARE
270 000416 * 104410 000000 * ENDS,BEGIN ;

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269 000422 * 105267 001575 RETRY: INCB TRY ;ERROR RETRY RTN ENTRY POINT
270 000426 * 122767 000010 * CMPB #10,TRY ;COUNT THE RETRYS
271 000434 * 001340 * BNE BCHK ;LIMIT EXCEEDED?
272 000436 * 104403 * GVC000 * 002474 * MSGNS,BEGIN,EXCED ;ASCII MESSAGE CALL WITH COMMON HEADER
273 000442 * 012777 000002 * MOV #BIT1,@RCR ;CLEAR RCVR HDWARE
274 000444 * 012777 000002 * MOV #BIT1,@TCR ;CLEAR XMTR HDWARE
275 000446 * 005767 000002 * TST SRI ;DROP THE MODULE?
276 000448 * 100724 * BMI BCHK ;NOT IF SRI BIT15=1
277 000466 * 000745 * BNE FINI ;YES, IF SRI BIT15=0
278
279 ;PCI DRIVE SUBROUTINE
280 ;CALLED BY: JSR PC,TRNSFR
281
282 ;TRANSFER 64 WORDS TO XMTR VIA NPR
283 ;TRANSMIT IT TO THE RECEIVER VIA TDM BUS
284 ;TRANSFER RCV DATA TO MODULE (BUBIN) VIA NPR
285 ;WAIT FOR COMPLETION FROM XMTR & RCVR
286
287
288 000470 * TRNSFR:
289 000470 * 012777 000002 001754 * MOV #BIT1,@RCR ;DRIVER SUBROUTINE ENTRY
290 000476 * 012777 000002 001724 * MOV #BIT1,@TCR ;CLR RCVR HDWARE
291 000504 * 016777 001476 001716 * MOV RCBC,@RDC ;CLR XMTR HDWARE
292 000512 * 016777 001472 001716 * MOV TXBC,@RDC ;LOAD RCVR BYTE COUNT
293 000520 * 016777 177462 001734 * MOV RBUF$A,@RDBA ;LOAD XMTR NPR DEST ADDRESS
294 000526 * 016767 177376 001462 * MOV RBUF$A,@RDBA ;AND GET EXT ADDR BITS
295 000534 * 016777 177374 001676 * MOV WBUF$A,@RDBA ;LOAD XMTR NPR SOURCE ADDRESS
296 000542 * 016767 177370 001450 * MOV WBUF$A,@RDBA ;AND GET EXT ADDR BITS
297 000550 * 116767 177242 001443 * MOV B SRI,@RDBA ;GET RCVR ADDRESS FOR XMTR
298 000556 * 052767 060001 001432 * BIS #RCVSTR,@RDBA ;LOAD RCV FUNCTION AND GO
299 000564 * 052767 060101 001426 * BIS #TXMSTR,@RDBA ;LOAD XMTR FUNCTION AND GO
300 000572 * 012777 000620 * MOV #TXINT,@XVECT ;SET XMTR INTR ENTRY POINT
301 000600 * 016777 001412 001644 * MOV RFUNCTION,@RCR ;CONNECT RCVR FIRST
302 000606 * 016777 001406 * MOV XFUNCTION,@TCR ;NOW CONNECT XMTR
303 000614 * 104400 000000 * EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

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304 ;TRANSMITTER INTERRUPT ENTRY POINT
305
306 TXINT:
307     BIC     #BIT6,@TCR          ;XMTN INTR ENTRY POINT
308     ;-----
309     ;IRQS,BEGIN,11$          ;CLEAR XMTN INTR ENABLE
310     ;-----
311     ;-----
312     ;-----
313     ;-----
314     ;-----
315     ;-----
316     ;-----
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319     ;-----
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326 ;CHECK FOR TRANSMITTER ERRORS AND REPORT, IF ANY
327
328 TXRS:
329     MOVW   #-1,TXMSRS          ;ENTRY POINT FOR XMTN ERR CHK
330     BIT    #BIT15,@TSR        ;SET XMTN ERROR FLAG
331     BEQ    10$                ;HARDWARE ERROR?
332     BEQ    10$                ;NO CHECK FURTHER
333     BEQ    #BIT11,@TSR       ;WAS MASTER DOWN?
334     BEQ    4$                ;NO, REPORT WHAT IT WAS
335     MSGNS, BEGIN,MSDWN       ;ASCII MESSAGE CALL WITH COMMON HEADER
336     JSR    PC,@STDRP         ;GO SEE IF WE CAN KEEP MODULE
337     TSTB   DEVICE            ;OK?
338     BEQ    TXRRTN            ;YES, TRY AGAIN
339     JMP    FINI              ;NO, DROP THE MODULE BECAUSE
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374 ;CHECK FOR RECEIVER ERRORS AND REPORT, IF ANY.
375
376 001166* 112767 177777 001031 RCERS: MOVB #1,RCVERS ;ENTRY POINT FOR RCVR ERR CHK
377 001166* 032777 100000 001252 BIT #BIT5,ARSR ;SET RCVR ERROR FLAG
378 001174* 001415 BRQ 105 ;HARDWARE ERROR?
379 001202* 001412 JSR PC,ERSUB2 ;NO CHECK FURTHER
380 001204* 004767 000156 176670 JSR PC,ERSUB2 ;LOAD ERROR INFORMATION
381 001210* 012767 000017 MOVB #17,ERRTYP ;UNKNOWN RECEIVER ERROR
382 *****
383 001216* 104405 000000* 002452* HRDRS,BEGIN,TABLE2 ;RCVR HARDWARE ERROR
384 *****
385 001224* 000167 000106 JMP RCRRTN ;ERROR RETURN
386 001230* 032777 000400 001216 10S: BIT #BIT5,ARSR ;DATA OUTPUT RDY SET?
387 001240* 001415 BRQ 3S ;NO, CONTINUE CHECK
388 001240* 104403 000000* 002522* MSGNS,BEGIN,ERDOPR ;ASCII MESSAGE CALL WITH COMMON HEADER
389 001246* 004767 000114 JSR PC,ERSUB2 ;LOAD ERROR INFORMATION
390 001252* 012767 000044 176626 MOVB #44,ERRTYP ;FLAG SHOULD NOT BE SET
391 *****
392 001260* 104406 000000* 002452* SOPSRS,BEGIN,TABLE2 ;DAT OUT RDY SET
393 *****
394 001266* 000167 000044 JMP RCRRTN ;ERROR RETURN
395 001272* 032777 000040 001154 3S: BIT #BIT5,ARSR ;REJECT COMPLETED INTERRUPT?
396 001300* 001415 BRQ 4S ;NO, CONTINUE CHECK
397 001302* 104403 000000* 002530* MSGNS,BEGIN,MRJTD ;ASCII MESSAGE CALL WITH COMMON HEADER
398 001310* 004767 000052 JSR PC,ERSUB2 ;LOAD ERROR INFORMATION
399 001314* 012767 000044 176564 MOVB #44,ERRTYP ;FLAG SHOULD NOT BE SET
400 *****
401 001322* 104406 000000* 002452* SOPSRS,BEGIN,TABLE2 ;REJ COMPL SET
402 *****
403 001330* 000167 000002 JMP RCRRTN ;ERROR RETURN
404 001336* 000207 4S: BR PCOK ;SHOULDN'T BE HERE, LEAVE.
405 001336* 000207 RCRRTN: RTS PC ;RETURN TO CALLER
406
407 001340* 105067 000661 RCOK: CLR RB RCVERS ;NO ERRORS, SKIP RETRY
408 001344* 000167 177766 JMP RCRRTN

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409 001350* 016767 001056 176522 ERSUB1: MOV TSR,CSRA ;RTN TO LD XMTR ERR INFO
410 001356* 017767 001050 176516 MOV #TSR,ACSR ;LOAD ADDR OF XMTR STATUS REG
411 001364* 000207 RTS PC ;LOAD CONTENTS OF XMTR STAT REG
412 ;RETURN
413
414 001366* 016767 001062 176504 ERSUB2: MOV RSR,CSRA ;ROUTINE TO LD RCVR ERR INFO
415 001366* 017767 001054 176500 MOV #RSR,ACSR ;LOAD ADDR OF RCVR STAT REG
416 001402* 000207 RTS PC ;LD CONTENTS OF RCVR STAT REG
417 ;RETURN
418
419 ;ROUTINE TO GENERATE DEVICE ADDRSSES AND VECTORS
420
421
422 001404* SETUP: MOV ADDR,R0 ;SETUP ROUTINE ENTRY POINT
423 001410* 016700 176376 MOV R0,TCR ;GET DEVICE BASIC ADDRESS
424 001416* 010667 001014 TST (R0)+ ;TCR ADDRESS
425 001418* 005720 MOV R0,TSR ;TSR ADDRESS
426 001418* 010667 TST (R0)+ ;TSR ADDRESS
427 001422* 005720 MOV R0,TSDB ;TSDB ADDRESS
428 001424* 010067 001004 TST (R0)+ ;TSDB ADDRESS
429 001430* 005720 MOV R0,TSBC ;TSBC ADDRESS
430 001432* 010067 001000 TST (R0)+ ;TSBC ADDRESS
431 001436* 005720 MOV R0,TSBA ;TSBA ADDRESS
432 001440* 010067 TST (R0)+ ;TSBA ADDRESS
433 001444* 005720 MOV R0,TMMR ;TMMR ADDRESS
434 001446* 010067 000770 INC R0 ;TMMR ADDRESS
435 001452* 005200 MOV R0,TMMRH ;TMMR HIGH BYTE
436 001454* 010067 000770 INC R0 ;TMMR HIGH BYTE
437 001460* 005200 MOV R0,TSCRC ;TSCRC ADDRESS
438 001464* 010667 000756 MOV VECTOR,R0 ;GET BASIC VECTOR
439 001466* 016700 176316 MOV R0,TVXECT ;SAVE IT
440 001472* 010667 000774 MOV #XINT,(R0)+ ;SET INTR POINTER IN CASE.
441 001476* 012720 000620 MOVB BR1,(R0) ;SET XMTR PRIORITY
442 001506* 005720 176304 TST (R0)+ ;SET XMTR PRIORITY
443 001510* 005720 MOV R0,RCVCTR ;SAVE RCVR VECTOR
444 001512* 010067 000754 ADD #2,R0 ;SAVE RCVR VECTOR
445 001514* 062700 000002 MOVB BR1,(R0)
446 001520* 116710 176266

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447	001524	016700	176256	MOV	ADDR, R0	;GET DEVICE BASIC ADDRESS
448	001530	062700	000020	ADD	#20, R0	;ADD OFFSET FOR RCVR ADDR
449	001534	010067	000712	MOV	R0, RCR	;RCR ADDRESS
450	001540	005720		TST	(R0)+	
451	001542	010067	000706	MOV	R0, RSR	;RSR ADDRESS
452	001546	005720		TST	(R0)+	
453	001550	010067	000702	MOV	R0, RDBB	;RDBB ADDRESS
454	001554	005720		TST	(R0)+	
455	001556	010067	000676	MOV	R0, RDDB	;RDDB ADDRESS
456	001562	005720		TST	(R0)+	
457	001564	010067	000672	MOV	R0, RDBA	;RDBA ADDRESS
458	001570	010067		CMP	(R0), (R0)+	
459	001572	010067	000666	MOV	R0, RDCRC	;RDCRC ADDRESS
460	001576	105067	000421	CLR	RY	;CLEAR RETRY COUNTER
461	001602	000207		RTS	PC	;RETURN

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;THIS ROUTINE TESTS IF THE DEVICE IS WORTHY OF RUNNING WITH
;DEC/X11 MODULE.
; IT CHECKS THAT MASTER IS SET, (IF NOT, THAT IT CAN BE SET)
; THEN DOES A SIMPLE TEST TO SEE IF THE DEVICE CAN INTERRUPT
; AND GO
; IF THIS TEST FAILS, THE MODULE WILL BE DROPPED.
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001694	052777	000002	000616	TSTDRP:	BIS	#BIT1, @TCR	;DROP TEST ROUTINE ENTRY
001692	052777	000002	000632		BIS	#BIT1, @RCR	;CLEAR XMTR HDWARE
001635	105067	000376			CLRB	DEVICE	;CLEAR RCVR HDWARE
001624	105077	000620			CLRB	@TMMRH	;CLEAR HARD ERROR FLAG
001630	112777	000021	000612		MOVB	#21, @TMMRH	;CLEAR MASTER, AUTO ADDR ETC.
001636	132777	000021	000604		RIB	#21, @TMMRH	;SET MASTER & AUTO ADDR
001644	001016				BNE		;ARE THEY SET?
001646	112777	177777	000346		MOVB	#-1, DEVICE	;YES, NEXT TEST
001654	104403	000000	002536		MSGNS, BEGIN, MSHDR		;NO, SET DEVICE TO DROP MODULE
001662	012767	000042	176216		MOV	#42, ERRTP	;ASCII MESSAGE CALL WITH COMMON HEADER
001670	104405	000000	002430		HRDRS, BEGIN, TABLE1		;ACTIVE BIT SHD BE SET.
001676	000167	000266					;MASTER OR AUTO ADDR CLR
001792	012777	120000	000520	1S:	JMP	TSTRN	;EXIT
001710	012777	177777	000516		MOV	#120000, @TCR	;SET RIB & SND WD
001716	012767	177600	000270		MOV	#-1, @SDB	;PUT A WORD INTO SILO
001724	104407	000000		2S:	MOV	#200, CLK	;SET UP TO WAIT A BIT
001734	032777	000020	000470		BREAKS, BEGIN		;TEMPORARY RETURN TO MONITOR...
001742	001016				BREAKS, BEGIN		;THEN CONTINUE AT NEXT INSTRUCTION.
001744	005267				BIT	#BIT4, @TSR	;TDM BUS BUSY SET?
001750	003265				BNE	3S	;YES, NEXT TEST
001752	112767	177777	000242		INC	CLK	;NO, WAIT A WHILE
001760	012767	000006	176120		BNE	2S	
001766	104405	000000	002430		MOVB	#-1, DEVICE	;SET DEVICE TO DROP MODULE
001774	000167	000170			MOV	#0, ERRTP	;DEVICE WON'T GO.
002006	012767	177600	000200	3S:	HRDRS, BEGIN, TABLE1		;XMTR WON'T GO
002014	104407	000000		4S:			;XMTR WON'T INTERRUPT
002024	032777	020000	000376		JMP	TSTRN	;EXIT
002032	001416				BIS	#BIT12, @TSR	;CAUSE FORCED TXM ERR
002034	005267	000154			MOV	#-200, CLK	;SET UP TO WAIT A BIT
002040	003265				BREAKS, BEGIN		;TEMPORARY RETURN TO MONITOR...
002050	012767	177777	000152		BREAKS, BEGIN		;THEN CONTINUE AT NEXT INSTRUCTION.
002056	104405	000000	002430		BIT	#BIT13, @TCR	;IS SND WD CLEAR NOW?
002064	000167	000100			REQ	5S	;YES, XMTR LOOKS OK
					INC	CLK	;WAIT & GIVE IT A CHANCE
					BNE	4S	
					MOVB	#-1, DEVICE	;SET DEVICE TO DROP MODULE
					MOV	#23, ERRTP	;DEVICE FAILED TO INTERRUPT
					HRDRS, BEGIN, TABLE1		;XMTR WON'T INTERRUPT
					JMP	TSTRN	;EXIT

```
515 002070 012777 000002 000332 5S: MOV #BIT1,@TCR ;CLEAR XMTR HDWARE
516 002076 052777 020000 000346 BIS #BIT1,@RCR ;SET RCV WD IN RCVR
517 002104 052777 010000 000342 BIS #BIT1,@RSR ;CAUSE FAKE TXN ERR IN RCVR
518 002112 012767 177600 000374 MOV #-100,CLK ;SET UP TO WAIT A BIT
519 002120 104407 000000 000000 6S: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
520 002124 104467 000000 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
521 002130 032777 020000 000314 BIT #BIT13,@RCR ;IS RCV WD CLEAR NOW?
522 002136 001414 000050 000050 BEQ TSTRN ;YES, RCVR LOOKS GOOD TOO
523 002140 005267 000050 000050 INC CLK ;NO, WAIT & GIVE IT A CHANCE
524 002144 001365 000000 000000 BNE 6S
525 002146 112767 177777 000046 MOVB #-1,DEVICE ;SET DEVICE TO DROP MODULE
526 002154 012767 000023 175724 MOV #23,ERRTYP ;DEVICE FAILED TO INTERRUPT
527 002162 104405 000000 002452 ;*****
528 002170 052777 000002 000254 HRDRS,BEGIN,TABLE2 ;RCVR WON'T INTERRUPT
529 002176 052777 000002 000224 ;*****
530 002204 000207 TSTRN: BIS #BIT1,@RCR ;CLEAR RCVR HDWARE
531 BIS #BIT1,@TCR ;CLEAR XMTR HDWARE
532 RTS PC ;RETURN
533
```

```
534 ;CONSTANT AND VARIABLE STORAGE
535 RCRC: .WORD 0
536 TXBC: .WORD 0
537 CNT: .WORD 0
538 CLK: .WORD 0
539 RFUNCT: .WORD 0
540 XFUNCT: .WORD 0
541 DEVICE: .BYTE 0
542 TRV: .BYTE 0
543 ERFLG: .BYTE 0
544 RCVERSS: .BYTE 0
545 TXMERS: .EVEN 0
546 002230 000100 BUFIN: .BLKW 64.
547 002430
548 002430
549 002434
550 002436
551 002440
552 002442
553 002444
554 002446 177777
555 002450
556 002452 000000 TMMRH: .WORD 0
557 002452
558 002454
559 002456
560 002460
561 002462
562 002464
563 002466 177777
564 002470
565 002472
566
567
568
569
570
571
```

```
TABLE1:
TCR: .WORD 0
TSR: .WORD 0
TSDB: .WORD 0
TSBC: .WORD 0
TSBA: .WORD 0
TMR: .WORD 0
TSCRC: .WORD 177777
TABLE2:
RCR: .WORD 0
RSR: .WORD 0
RDB: .WORD 0
RDBB: .WORD 0
RDBC: .WORD 0
RDBA: .WORD 0
RDCRC: .WORD 0
RCVECT: .WORD 177777
TXVECT: .WORD 0
```

```
572  
573  
574 002474* 002544*  
575 002476* 177777*  
576 002500* 002514*  
577 002504* 002644*  
578 002504* 002644*  
579 002506* 177777*  
580 002510* 002707*  
581 002514* 177777*  
582 002514* 003053*  
583 002520* 177777*  
584 002522* 003077*  
585 002524* 003012*  
586 002526* 177777*  
587 002530* 003077*  
588 002530* 003053*  
589 002534* 177777*  
590 002536* 003107*  
591 002540* 003042*  
592 002542* 177777*  
593
```

;ASCII MESSAGE STORAGE

```
EXCED: MSG1  
177777  
MSTDWN: MSG2  
177777  
WRCAD: MSG3  
177777  
NMSINT: MSG4  
177777  
MRJCT: MSG11  
MSG5  
177777  
ERDOPR: MSG12  
MSG7  
177777  
MRJTD: MSG12  
MSG5  
177777  
MSHDR: MSG13  
MSG8  
177777
```

```
594 002544* 042445 051122 051117 MSG1: .ASCIZ *ERROR RETRY FOR THIS CYCLE EXCEEDED*  
595 002552* 051040 052105 054522  
596 002560* 043040 051117 052043  
597 002574* 045110 021117 054533  
598 002574* 046103 020105 054105  
599 002602* 042503 042105 042105  
600 002610* 000045 000000 000000  
601 002614* 050045 046103 046440 MSG2: .ASCIZ *PCL MASTER WENT DOWN !!*  
602 002616* 042527 042524 020122  
603 002626* 042527 052116 042040  
604 002634* 053517 020116 020441  
605 002642* 000045 000000 000000  
606 002644* 051045 053103 020122 MSG3: .ASCIZ *RCVR BUSY, OR WRONG RCVR ADDRESS*  
607 002652* 052500 054523 020054  
608 002660* 051117 053440 047522  
609 002666* 043516 051040 053103  
610 002674* 020122 042101 051104  
611 002702* 051505 022523 000000  
612 002707* 000045 040515 052223 MSG4: .ASCIZ *MASTER HAS JUST SET ON THIS PCL!!*  
613 002714* 051105 044040 051061  
614 002722* 045040 051525 020124  
615 002730* 042523 020124 047117  
616 002736* 052040 044510 020123  
617 002744* 041520 030514 022461  
618 002752* 000000 000000 000000  
619 002752* 000040 042515 051523 MSG5: .ASCIZ * MESSAGE WAS REJECTED BY RCVR*  
620 002760* 043501 020105 040527  
621 002766* 022123 042522 042512  
622 002774* 052103 042105 041040  
623 003002* 022131 041522 051126  
624 003010* 000045 000000 000000  
625 003012* 042040 052101 020101 MSG7: .ASCIZ * DATA OUTPUT READY SET*  
626 003020* 052517 050124 052105  
627 003026* 051040 040505 054504  
628 003034* 051440 052105 000045  
629 003032* 040515 052123 051105 MSG8: .ASCIZ *MASTER WILL NOT SET*  
630 003056* 053440 042111 020114  
631 003056* 047516 020124 042523  
632 003064* 022524 000000 000000  
633 003057* 000045 054055 052115 MSG11: .ASCIZ *%-XMTR-*  
634 003074* 026522 000000 000000  
635 003107* 026522 051055 053103 MSG12: .ASCIZ *%-RCVR-*  
636 003107* 000045 000000 000000  
637 003107* 000045 040510 042122 MSG13: .ASCIZ *%HARD ERROR*  
638 003114* 042440 051122 051117  
639 003122* 000045 000000 000000  
640 000001 .END
```


RBUPPA	000126R	208#	257	293											
RBUPSZ	000132R	408#	241												
RBUPVA	000134R	240	240												
RCBC	002206R	241#	242*	243*	291	536#									
RCERS	001166R	315	376#												
RCOK	001340R	404	407#												
RCR	002452R	266#	274#	289*	301*	449*	471*	516*	522	531*	563#				
RCRRTN	001336R	389#	398	403	405#	408									
RCRECT	002470R	444#	356#												
RCVERS	002255R	316	320*	377*	407*	545#									
RCVSTR	= 060001	233#	298												
RDRA	002462R	297#	457*			567#									
RDBC	002460R	294	566#												
RDCRC	002464R	459#	455#												
RDDB	002456R	453#	568#												
RESTRT	000270R	202	239#												
RES1	000056R	185#													
RES2	000060R	186#													
RES3	000422R	154#													
RESTRY	002426R	194#	269#												
RFUNC	002425R	193#	398	301	540#										
RSH	002454R	378	386	395	415	416	451*	517*	564#						
RSTRT	000112R	202#													
SBADP	000102R	195#													
SETUP	001404R	134#	422#												
SFCNT	000042R	174#													
SDPER	= 104406	225#	352	368	392	401									
SDPAS	000046R	180#													
SPDINT	= 000032R	174#													
SPSIZ	= 000016R	161#	218	297											
SR1	000020R	168#	276												
SR2	000022R	169#													
SR3	000022R	169#													
SR4	000024R	170#													
START	000252R	173#	234#												
STAT	000278R	177#													
SVL	000062R	187#													
SVR1	000064R	188#													
SVR2	000066R	189#													
SVR3	000070R	190#													
SVR4	000072R	191#													
SVR5	000077R	193#													
SVR6	000078R	193#													
SVSCNT	000052R	183#													
TABLE1	002430R	343	352	368	481	497	512	550#							
TABLE2	002452R	383	392	401	529	562#									
TCCR	002434R	459#	290*	290*	302*	307#	424*	470*	484*	505	515*	532*	551#		
TCR	002434R	459#	475#												
TMMRH	002450R	355	357*	436*	473*	474*	475	560#							
TRNSFR	000470P	252	288#												
TRPDFD	= 000322	422#													
TRY	002448R	268#	270*				271	460*	543#						
TSA	002448R	268#	432#				555#								
TSC	002436R	269#	430*				554#								
TSCRC	002444R	438#	557#												
TSDR	002434R	428#	485*				553#								
TSR	002432R	330	332	346	360	364	410	411	426*	490	500*	552#			

TSDRP	001604R	235	335	469#											
TSTRTN	002179R	483	499	514	523	531#									
TXBC	002216R	247*	248*	249*	292	337#									
TXERS	000716R	312	328#												
TXINT	000526R	300	306#				441								
TXMERS	002226R	313	329*	329*	372*	546#									
TXMSTR	= 000101	239#	369												
TXOK	001156R	165	372#												
TXRRTN	001154P	337	345	354	359	363	370#	373							
TXVECT	002472R	300#	440*	571#											
VECTOR	000010R	163	439												
WASADR	000104R	197#													
WBUFEA	000136R	217#	296												
WBUFPA	000134P	211#	295												
WBUFRQ	000140P	213#													
WBUFSZ	000142R	214#	247												
WDFR	000116R	204#													
WDFD	000114R	203#													
WRCAD	002504R	348	578#												
XFLAG	000005R	161#					299*	302	541#						
XFUNCT	= 003124P	258#	297*	548#											

.ARS. 000000 000
 003124 001

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0
 XPLAB0,XPLABQ/SQL/CRF:SYM=DDXCOM,XPLAB0
 RUN-TIME: 1 3 SECONDS
 RUN-TIME RATIO: 19/4=4.1
 CORE USED: 8K (15 PAGES)