

.REM -

IDENTIFICATION

PRODUCT CODE: AC-E962R-MC
PRODUCT NAME: CXPCS80 PCS-11 MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT
PCS IS AN IOMOD THAT EXERCISES PCS CONTROLLER AND FILE BOX. IT WILL EXERCISE ALL BITS OF CSR ADDRESSES, CHECK FOR MAINTENANCE INTERRUPT CAPABILITIES AND CHECK ALL THE ADDRESSES FROM 171000 TO 171375 IN MAINTENANCE MODE.
2. REQUIREMENTS
HARDWARE: IOCM CONTROL MODULE WITH FILE BOX
STORAGE:: PCS REQUIREMENTS:
1. DECIMAL WORDS: 748
2. OCTAL WORDS: 1354
3. OCTAL BYTES: 2730
3. TEST DESCRIPTION
ONE PASS OF THE MODULE CONSISTS OF CSR CHECKS INTERRUPT TEST AND IAR TEST IN MAINTENANCE MODE DURING THE TEST AND IAR TEST SO THE TESTS WILL NOT EFFECT I/O MODULES
4. EXECUTION TIME
ONE PASS OF THE TEST TAKES LESS THEN 1 SEC
5. CONFIGURATION REQUIREMENTS
DEFAULT PARAMETERS
DEVADR: 171376
VECTOR: 234
DEVCHI: 1
6. MODULE OPERATION
TEST SEQUENCE
A. SET UP THE DEVICE ADDRESSES AND CLEAR IT
B. SET D BIT, READ IT BACK AND CLEAR IT
C. SET T BIT, READ IT BACK AND CLEAR IT
D. SET RIF BIT, READ IT BACK AND CLEAR IT
E. SET E BIT, READ IT BACK AND CLEAR IT
F. SET U BIT, READ IT BACK AND CLEAR IT
G. SET M BIT AND READ ALL ADDRESSES FROM 171000 TO 171375
H. SET MAINTENANCE INTERRUPT AND READ ALL ONES FROM IAR
I. SET MAINTENANCE INTERRUPT AND READ ALL ONES FROM IAR
J. RESET CSR.
7. OPERATOR OPTIONS:
NONE
8. PRINTOUTS:

PCSB DEC/X11 SYSTEM EXERCISER MODULE
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SEQ 0003

ALL PRINTOUTS ARE STANDARD

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104 000000: IONMOD <PCSB > 171376 234 0 0 0 10 147
105 000000: MODULE 140000 PCSB 171376 234 0 0 0 10 147
106 ; TITLE PCSB DEC/X11 SYSTEM EXERCISER MODULE
107 ; DDXCOM VERSION 6 23-MAY-78
108 ; LIST BIN
109 ;*****
110 ;*****
111 000000: REGIN:
112 000000: 041520 041123 040 MODNAM: -ASCII /PCSB / ;MODULE NAME
113 000000: XFLAG: 000 OPEN ;USED TO KEEP TRACK OF WBUF USAGE
114 000000: 171376 ADDR: 171376+0 ;1ST DEVICE ADDR
115 000010: 000234 VECTOR: 234+0 ;1ST DEVICE VECTOR.
116 000010: 000 BR1: -BYTE PRIV0+0 ;1ST BR LEVEL.
117 000010: 000 BR2: -BYTE PRIV0+0 ;2ND BR LEVEL.
118 000010: 000001 DVI1: 0 ;DEVICE INDICATOR 1.
119 000010: 000000 SR1: OPEN ;SWITCH REGISTER 1.
120 000020: 000000 SR2: OPEN ;SWITCH REGISTER 2.
121 000020: 000000 SR3: OPEN ;SWITCH REGISTER 3.
122 000020: 000000 SR4: OPEN ;SWITCH REGISTER 4.
123 ;*****
124 000026: 140000 STAT: 140000 ;STATUS WORD
125 000030: 000246 INIT: START ;MODULE START ADDR.
126 000030: 000224 SPOINT: MODSP ;MODULE STACK POINTER.
127 000030: 000000 PASCNT: 0 ;PASS COUNTER.
128 000030: 000010 ICONT: 0 ;# OF ITERATIONS PER PASS=10
129 000040: 000000 SOFCNT: 0 ;LOC TO COUNT ITERATIONS
130 000040: 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
131 000040: 000000 SOPPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS
132 000040: 000000 SYSCNT: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
133 000050: 000000 RANUM: 0 ;LOC TO SAVE HARD ERRORS PER PASS
134 000050: 000000 CONFIC: 0 ;# OF SYS ERRORS ACCUMULATED
135 000050: 000000 RES1: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
136 000050: 000000 RES2: 0 ;RESERVED FOR MONITOR USE
137 000050: 000000 SVR0: OPEN ;RESERVED FOR MONITOR USE
138 000050: 000000 SVR1: OPEN ;LOC TO SAVE R0.
139 000050: 000000 SVR2: OPEN ;LOC TO SAVE R1.
140 000050: 000000 SVR3: OPEN ;LOC TO SAVE R2.
141 000050: 000000 SVR4: OPEN ;LOC TO SAVE R3.
142 000050: 000000 SVR5: OPEN ;LOC TO SAVE R4.
143 000070: 000000 SVR6: OPEN ;LOC TO SAVE R5.
144 000070: 000000 CSRA: OPEN ;LOC TO SAVE R6.
145 000100: 000000 SBADR: ;ADDR OF CURRENT CSR.
146 000100: 000000 ACSR: OPEN ;ADDR OF GOOD DATA, OR
147 000100: 000000 WASADR: OPEN ;CONTENTS OF CSR.
148 000100: 000000 ASRAT: OPEN ;ADDR OF BAD DATA, OR
149 000100: 000000 ERRTYP: OPEN ;STATUS REG CONTENTS.
150 000100: 000000 ASB: OPEN ;TYPE OF ERROR
151 000100: 000000 AWAS: OPEN ;EXPECTED DATA.
152 000110: 000000 RSTRT: OPEN ;ACTUAL DATA.
153 000110: 000000 WDR: OPEN ;RESTART ADDRESS AFTER END OF PASS
154 000110: 000274 WDR: OPEN ;WORDS TO MEMORY PER ITERATION
155 000110: 000000 INTR: OPEN ;WORDS FROM MEMORY PER ITERATION
156 000120: 000000 IDNUM: 147 ;# OF INTERRUPTS PER ITERATION
157 000120: 000000 IDNUM: 147 ;MODULE IDENTIFICATION NUMBER=147
158 000122: 000040 ;MODULE STACK STARTS HERE.
159 .REPT SPSIZ

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160 ;.NLIST
161 ;.WORD 0
162 ;.LIST
163 ;.ENDR
164 000224: MODSP:
165 ;*****
166

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167 000224 000000 TEMP1: -WORD 0 ;TEMPORARY STORAGE
168 000226 000000 TEMP2: -WORD 0 ;TEMPORARY STORAGE
169 000230 000000 TEMP3: -WORD 0 ;TEMPORARY STORAGE
170 000232 000000 CSR: -WORD 0 ;ADDRESS OF CSR = 171377
171 000234 000000 IAR: -WORD 0 ;ADDRESS OF IAR = 17136
172 000240 000000 INTFLG: -WORD 0 ;INTERRUPT OCCURED FLAG
173 000242 000000 CNT: -WORD 0 ;PASS COUNT
174 000244 000000 BASE: -WORD 0 ;ADDRESS 171000
175 000244 000000 VECT2: -WORD 0 ;SECOND ADDR OF VECTOR
176 000246 012767 000001 177644 START: MOV #1,INTR ; ONE INTERRUPT/ITERATION
177 000252 012767 000010 177632 MOV #8,WDR0 ;8. WORDS TO MEM/ITERATION
178 000262 012767 000010 177626 MOV #8,WDR0 ;8. WORDS FROM MEM/ITERATION
179 000270 005067 177744 CLR CNT ;CLEAR PASS COUNT
180 000274 016767 177506 RESTRT: MOV R0,IAR ;SET ADDRESS OF IAR = 171376
181 000304 010967 177720 MOV R0,CSR ;SET ADDRESS OF CSR = 171377
182 000306 010967 177720 IMC R0,CSR ;SET ADDRESS OF THE LOWEST ID = 171000
183 000312 042700 000377 MOV #377,R0
184 000316 010967 177720 MOV R0,BASE
185 000322 012703 000033 177676 1$: MOV #9,*3,R3 ;SET UP WAIT LOOP
186 000326 012777 000002 177676 BISB #2,ACSR ;SET CBIT TO CLEAR CSR
187 000334 000240 000000 32$: NOP ;WAIT FOR CLEAR
188 000336 005303 DEC R3
189 000340 001375 BNE #32,32$
190 000342 012793 000044 177656 33$: MOV #2,*3,R3 ;ANOTHER WAIT LOOP
191 000344 000002 177656 34$: BISB #2,ACSR
192 000356 005303 NOP
193 000360 001375 DEC R3
194 000362 005477 177644 BNE #34,34$
195 000366 005477 177636 TSTB #CSR ;IS CSR CLEAR?
196 000370 177636 177512 BEQ #CSR,AWAS ;NO, SET BAD DATA FOR ERROR CALL
197 000376 005067 177504 CLR ASB ;SET GOOD DATA.
*****
000402 104404 000000 DATERS,BEGIN ;DATA ERROR!!!
*****
000406 152777 000001 177616 ;TEST GBIT
000414 132777 000001 177610 BISB #1,ACSR ;SET RIF BIT
000414 132777 000001 177610 BITB #1,ACSR ;IS IT SET
000432 016767 177602 177450 MOV #CSR,ACSR ;YES, GO TO 3$
000440 104403 177574 177440 MOV #CSR,CSRA ;LOAD CONTENTS OF CSR FOR ERROR
000440 104403 000000 002056 MSGNS,BEGIN,RIFNOT ;ASCII MESSAGE CALL WITH COMMON HEADER
000446 012767 000025 177432 MOV #25,ERRTYP ;BIT STUCK
*****
000454 104405 000000 000000 HRDRS,BEGIN,NULL ;RIF BIT IS NOT SETTING
*****
000462 132777 000001 177542 3$: BITB #1,ACSR ;NOW RIF BIT SHOULD BE CLEAR
000470 001417 177504 BEQ #4$ ;YES, GO TO 4$

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223 000472 117767 177534 177492 MOVR #CSR,ACSR
224 000500 016767 177526 177392 MOVR #CSR,CSRA
225 000506 104403 000000 002062 MSGNS,BEGIN,RIFCLR ;ASCII MESSAGE CALL WITH COMMON HEADER
226 000514 012767 000025 177364 MOV #25,ERRTYP ;BIT STUCK
*****
228 000522 104405 000000 000000 HRDRS,BEGIN,NULL ;RIF BIT IS NOT CLEARING
*****
230 000530 152777 000004 177474 4$: BISB #4,ACSR ;SET GBIT AT CSR
231 000534 132777 000004 177466 BITB #4,ACSR ;TEST IF SET
232 000544 001017 BNE #5$ ;YES, GO TO 5$
233 000546 117767 177460 177326 MOVR #CSR,ACSR
234 000554 016767 177452 177316 MOV #CSR,CSRA
235 000562 104403 000000 002066 MSGNS,BEGIN,DBITS ;ASCII MESSAGE CALL WITH COMMON HEADER
236 000570 012767 000025 177310 MOV #25,ERRTYP ;BIT STUCK
*****
238 000576 104405 000000 000000 HRDRS,BEGIN,NULL ;GENERIC BIT IS NOT SETTING
*****
240 000604 142777 000004 177470 5$: BICB #4,ACSR ;CLEAR GBIT AT CSR
241 000612 132777 000004 177412 BITB #4,ACSR ;TEST IF CLEAR
242 000620 001417 BNE #6$ ;YES, GO TO 6$
243 000622 117767 177404 177252 MOVR #CSR,ACSR
244 000630 016767 177376 177242 MOV #CSR,CSRA
245 000636 104403 000000 002072 MSGNS,BEGIN,TBITS ;ASCII MESSAGE CALL WITH COMMON HEADER
246 000644 012767 000025 177234 MOV #25,ERRTYP ;BIT STUCK
*****
248 000652 104405 000000 000000 HRDRS,BEGIN,NULL ;GENERIC BIT IS NOT CLEARING
*****
250 000660 152777 000020 177344 6$: BISB #20,ACSR ;SET DBIT AT CSR
251 000666 001017 000020 177336 BITB #20,ACSR ;TEST IF SET
252 000674 001017 BNE #7$ ;YES, GO TO 7$
253 000676 117767 177330 177176 MOVR #CSR,ACSR
254 000704 016767 177322 177166 MOV #CSR,CSRA
255 000712 104403 000000 002076 MSGNS,BEGIN,DBITS ;ASCII MESSAGE CALL WITH COMMON HEADER
256 000720 012767 000025 177160 MOV #25,ERRTYP ;BIT STUCK
*****
258 000726 104405 000000 000000 HRDRS,BEGIN,NULL ;DBIT IS NOT SETTING
*****
260 000734 152777 000010 177270 7$: BISB #10,ACSR ;SET TBIT ACSR
261 000742 132777 000010 177262 BITB #10,ACSR ;IS IT SET
262 000750 001017 BNE #8$ ;YES, CONT.
263 000752 117767 177254 177122 MOVR #CSR,ACSR
264 000760 016767 177246 177112 MOV #CSR,CSRA
265 000766 104403 000000 002102 MSGNS,BEGIN,TBITS ;ASCII MESSAGE CALL WITH COMMON HEADER
266 000774 012767 000025 177104 MOV #25,ERRTYP ;BIT STUCK
*****
268 001002 104405 000000 000000 HRDRS,BEGIN,NULL ;TBIT NOT SETTING
*****
270 001010 142777 000010 177214 8$: BICB #10,ACSR ;CLEAR TBIT
271 001016 132777 000010 177206 BITB #10,ACSR ;IS IT CLEAR
272 001024 001417 177200 177046 MOVR #CSR,ACSR

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379 001034 016767 177172 177036 MOV CSR,CSRA
380 001034 016767 000000 001034 MSGNS,BEGIN,EBITC ;ASCII MESSAGE CALL WITH COMMON HEADER
381 001056 012767 000000 177030 MOV #1,ERRTYP ;BIT STUCK
382 *****
383 001056 104405 000000 000000 HDRERS,BEGIN,NULL ;BIT NOT CLEARING
384 *****
385
386 001064 012703 000025 177134 9S: MOV #7,*3,R3 ;SET UP WAIT LOOP
387 001070 012777 000020 177134 BICB #20,@CSR ;CLEAR DBIT
388 001076 000240 000000 000000 50S: NOP ;WAIT FOR CSR
389 001100 005303 000000 000000 DEC R3
390 001104 001375 000000 177120 BNE #50,@CSR
391 001112 001420 000020 177120 BEQ #10,@CSR
392 001114 001417 000000 000000 BEQ #10,@CSR
393 001116 117767 177110 176756 MOVB @CSR,ACSR
394 001124 016767 177107 175745 MOV CSR,CSRA
395 001124 016767 000000 001124 MSGNS,BEGIN,DBITC ;ASCII MESSAGE CALL WITH COMMON HEADER
396 001146 012767 000025 176746 MOV #2,ERRTYP ;BIT STUCK
397 *****
398 001146 104405 000000 000000 HDRERS,BEGIN,NULL ;DBIT NOT CLEARING
399 *****
400
401 001154 152777 000040 177050 10S: BISB #40,@CSR ;SET MBIT
402 001162 132777 000040 177042 BITB #40,@CSR ;IS IT SET
403 001170 001417 000000 000000 BNE #13 ;NO
404 001172 117767 177034 176702 MOVB @CSR,ACSR
405 001200 016767 000026 176774 MOV CSR,CSRA
406 001214 012767 000025 176664 MSGNS,BEGIN,MBITC ;ASCII MESSAGE CALL WITH COMMON HEADER
407 001222 104405 000000 000000 MOV #25,ERRTYP ;BIT STUCK
408 *****
409 001222 104405 000000 000000 HDRERS,BEGIN,NULL ;MAINTENANCE BIT IS NOT SETTING
410 *****
411
412 001230 142777 000040 176774 11S: BICB #40,@CSR ;CLEAR MBIT
413 001236 132777 000040 176766 BITB #40,@CSR ;IS IT CLEAR
414 001244 001417 000000 000000 BEQ #12 ;NO
415 001246 117767 176760 176626 MOVB @CSR,ACSR
416 001262 016767 000000 001262 MOV CSR,CSRA
417 001276 012767 000025 176610 MSGNS,BEGIN,MBITC ;ASCII MESSAGE CALL WITH COMMON HEADER
418 001276 104405 000000 000000 MOV #25,ERRTYP ;BIT STUCK
419 *****
420 001276 104405 000000 000000 HDRERS,BEGIN,NULL ;MAINTENANCE BIT IS NOT CLEARING
421 *****
422
423 001304 152777 000100 176720 12S: BISB #100,@CSR ;SET EBIT
424 001312 132777 000100 176712 BITB #100,@CSR ;IS IT SET
425 001320 001017 000000 000000 BNE #13 ;NO
426 001322 117767 176704 176552 MOVB @CSR,ACSR
427 001330 016767 000000 001330 MOV CSR,CSRA
428 001344 012767 000025 176534 MSGNS,BEGIN,EBITC ;ASCII MESSAGE CALL WITH COMMON HEADER
429 001344 104405 000025 176534 MOV #25,ERRTYP ;BIT STUCK
430 *****
431 001352 104405 000000 000000 HDRERS,BEGIN,NULL ;INTERRUPT ENABLE BIT IS NOT SETTING
432 *****
433

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335 001360 142777 000100 176644 13S: BICB #100,@CSR ;CLEAR EBIT
336 001366 132777 000100 176636 BITB #100,@CSR
337 001374 001417 000000 000000 BEQ #4 ;NO
338 001376 116767 176630 176476 MOVB @CSR,ACSR
339 001412 016767 000000 001412 MOV CSR,CSRA
340 001412 014403 000000 001412 MSGNS,BEGIN,EBITC ;ASCII MESSAGE CALL WITH COMMON HEADER
341 001420 012767 000025 176460 MOV #25,ERRTYP ;BIT STUCK
342 *****
343 001426 104405 000000 000000 HDRERS,BEGIN,NULL ;INTERRUPT ENABLE BIT IS NOT CLEARING
344 *****
345
346 ;THIS TEST WILL CHECK ALL ADDRESSES WITH MBIT SET
347
348 001434 112777 000040 176570 14S: MOVB #40,@CSR ;SET MBIT
349 001442 016702 176574 MOV BASE,R2
350 001446 005000 000000 000000 CLR R0
351 001450 005001 000000 000000 CLR R1
352 001452 152777 000001 176552 15S: BISB #1,@CSR ;SET RIF BIT
353 001460 112201 000000 000000 MOVB (R2)+R1 ;READ ADDRESS 171000+R0 AND
354 001462 042701 177400 BIC #177400,R1 ;LOAD CONTENTS INTO R1
355
356 CMP R0,R1 ;R1 SHOULD BE EQUAL R2
357 BEQ #15 ;YES
358 001472 010067 176410 MOV R0,ASB ;NO, SAVE GOOD DATA
359 001476 010167 176406 MOV R1,AWAS ;SAVE BAD DATA
360 001500 016767 176334 176372 MOV BASE,ACSR ;SAVE ADDRESS
361 001510 150467 176366 BITB #0,@CSR
362 001514 016767 176312 176356 MOV CSR,CSRA
363 *****
364 001522 104404 000000 000000 HDRERS,BEGIN ;DATA ERROR!!!
365 *****
366
367 001526 005200 000376 16S: INC R0 ;GO TO NEXT ADDRESS
368 001530 122700 000376 CMPB #376,R0 ;IS IT LAST ONE
369 001534 001346 BNE #15 ;NO, DO IT AGAIN
370
371 ;THIS TEST CHECKS MAINTENANCE INTERRUPT
372 ;IF MBIT & EBIT ARE SET ICM GENERATES
373 ;INTERRUPT AT ADDRESS 234 AND IAR HAS
374 ;UPPER BYTE OF CSR ADDRESS (377)
375
376 001536 012777 001672 176244 17S: MOV #20,@VECTOR ;SET INTERRUPT VECTOR
377 001544 016767 000000 176472 VECTOR,VECT2 ;SET VECTOR + 2
378 001552 005067 176422 176464 ADD #2,VECT2
379 001560 116777 176226 176456 MOVB BR1,VECT2 ;SET PRIORITY LEVEL ON INTERRUPT
380 001566 005067 176444 176444 CLR INTFLG ;CLEAR INTERRUPT FLAG
381 001572 012767 000005 176424 MOV #5,TEMP1 ;SET COUNT FOR TIMEOUT
382 001600 152777 000140 176424 BITB #140,@CSR ;ENABLE INTERRUPT
383
384 001606 104407 000000 000000 18S: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
385 001612 104407 000000 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
386
387 001616 005767 176414 TST INTFLG ;INTERRUPT OCCUR?
388 001624 005367 176374 BNE #215 ;YES, IS
389 DEC TEMP1 ;NO, IS IT TIMEOUT
390

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391 001630 001366 BNE 18$ ;NO, LOOP TO BREAK
392
393 001632 117767 176374 176242 MOV# @CSR,ACSR
394 001640 016767 176366 176234 MOV CSR,CSRA
395 001646 104403 000000 002134 MSGNS,BEGIN,NOINT ;ASCII MESSAGE CALL WITH COMMON HEADER
396 001654 012767 000023 176224 MOV #1,ERRTYP ;NO INTERRUPT
397 *****
398 001662 104405 000000 000000 HDRERS,BEGIN,NULL ;NO INTERRUPT
399 *****
400
401 001670 000444 BR 22$
402
403 001672 142777 000100 176332 20$: BICB #100,@CSR ;CLEAR INTERRUPT ENABLE
404 001700 005267 176332 INC INTFLG ;GET INTERRUPT FLAG
405 001704 000002 RTI
406
407 001706 152777 000001 176316 21$: BISB #1,@CSR ;SET RIF BIT
408 001714 117700 176314 MOV# @IAR,R0 ;CHECK IF IAR = 377
409 001720 122700 000377 CMP# #377,R0
410 001724 001426 BEQ 22$ ;YES, GO TO END
411 001728 110967 176156 MOV# R0,@WAS ;NO, SAVE BAD DATA
412 001732 012767 000377 176146 MOV #377,ASB ; SAVE GOOD DATA
413 001740 012767 176270 176134 MOV# @IAR,ACSR ; SAVE ADDRESS
414 001748 016767 176262 176124 MOV IAR,CSRA
415 001754 104403 000000 002140 MSGNS,BEGIN,IARERR ;ASCII MESSAGE CALL WITH COMMON HEADER
416 001762 012767 000001 176118 MOV #1,ERRTYP ;DATA ERROR
417 *****
418 001770 104405 000000 000000 HDRERS,BEGIN,NULL ;WRONG DATA IN IAR AFTER INTERRUPT
419 *****
420 DATERS,BEGIN ;DATA ERROR!!!
421 *****
422 001776 104404 000000 22$: MOV #11,*3,R3 ;SET UP WAIT LOOP
423 002002 012703 000041 BICB #2,@CSR ;CLEAR CSR
424 002006 152777 000002 176216 60$: NOP ;WAIT
425 002014 000240 DEC R3
426 002018 005303 BNE 60$
427 002026 012767 000036 176176 65$: MOV #10,*3,R3 ;WAIT SOME MORE
428 002034 002040 BICB #2,@CSR ;SET UP ANOTHER LOOP
429 002038 005303 DEC R3 ;CLEAR AGAIN
430 002042 001375 BNE 65$
431 002046 104413 000000 23$: ENDITS,BEGIN ;SIGNAL END OF ITERATION.
432 002048 000167 176222 JMP RESTRT ;MONITOR SHALL TEST END OF PASS
433 002052 104410 000000 FINI: ENDS,BEGIN ;
434
435 .EVEN
436
437 002056 002144 RIFNOT: MES1
438 002060 177777 177777
439
440
441
442
443
444
445

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447 002062 002173 RIFCLR: MES2
448 002064 177777 177777
449
450 002066 002223 GBITS: MES3
451 002070 177777 177777
452
453 002072 002263 GBITC: MES4
454 002074 177777 177777
455
456 002076 002324 DBITS: MES5
457 002100 177777 177777
458
459 002102 002350 TBITS: MES6
460 002104 177777 177777
461
462 002106 002374 TBITC: MES7
463 002110 177777 177777
464
465 002112 002421 DBITC: MES8
466
467 002114 002446 MBITS: MES9
468 002116 177777 177777
469
470 002120 002472 MBITC: MES10
471 002122 177777 177777
472
473 002124 002517 EBITS: MES11
474 002128 177777 177777
475
476 002130 002563 EBITC: MES12
477 002132 177777 177777
478
479 002134 002630 NOINT: MES13
480 002136 177777 177777
481
482 002140 002666 IARERR: MES14
483 002142 177777 177777
484
485 002144 044522 020106 044502 MES1: .ASCIZ "RIF BIT IS NOT SETTING"
486 002152 020124 051511 047040
487 002160 052177 051440 052105
488 002166 044524 043516 000
489 002170 122 043111 041040 MES2: .ASCIZ "RIF BIT IS NOT CLEARING"
490 002200 052111 044440 020123
491 002206 047516 020124 046103
492 002214 040505 044522 043516
493 002244 000
494 002248 000
495 002250 041105 047105 051105 MES3: .ASCIZ "GENERIC CODE BIT IS NOT SETTING"
496 002236 020105 044502 020124
497 002244 051511 047040 052117
498 002252 051440 052105 044524
499 002260 043516 000
500 002264 000
501 002270 041511 047105 051105 MES4: .ASCIZ "GENERIC CODE BIT IS NOT CLEARING"
502 002276 020105 044502 020124

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GETPAS = 104415	166#																		
GMBUFS = 104414	166#																		
HRDCNT = 000044R	133#																		
HRDRS = 104405	388#	218	228	239	250	261	272	283	299	310	321	332	343						
HRDPAS = 000050R	133#																		
IAR = 000234R	172#																		
IARERR = 002140R	415#																		
ICONT = 000036R	128#																		
ICOUNT = 000129R	158#																		
IDNUM = 000129R	158#																		
INIT = 000030R	125#																		
INITLG = 000236R	173#	380*	388		404*														
INTR = 000129R	127#	178*																	
MBIT2 = 104415	166#																		
MBITC = 002120R	318#	470#																	
MBITS = 002114R	307#	467#																	
MS31 = 000144R	243#	485#																	
MS310 = 002477R	470#	546#																	
MS311 = 002517R	473#	540#																	
MS313 = 002567R	479#	537#																	
MS314 = 002630R	479#	544#																	
MS32 = 002666R	482#	549#																	
MS322 = 002173R	447#	489#																	
MS33 = 002223R	450#	504#																	
MS35 = 004424R	450#	500#																	
MS36 = 002350R	459#	510#																	
MS37 = 002374R	462#	514#																	
MS38 = 002421R	469#	518#																	
MS39 = 000000R	166#	522#																	
MDDMAH = 000000R	166#																		
MDDSP = 000224R	126#	164#																	
MSGNS = 104403	395#	415	225	236	247	258	269	280	296	307	318	329	340						
MSGSS = 104402	166#																		
MSGSI = 000000R	395#																		
MSGNT = 002134R	398#	479#																	
NULL = 000000	166#	218	228	239	250	261	272	283	299	310	321	332	343						
OPEN = 000000	398#	418																	
OTAS = 104420	166#	150	120	121	122	130	130	140	141	142	143	144	145	146					
PASCNT = 000034R	127#	152	153	153	155	156	157	157	166#										
PIRQS = 000004	166#																		
POPSD = 0005726	166#																		
POPS2 = 0226266	166#																		
PRTV = 000000	116#																		
PRTV0 = 000000	116#	117	166#																
PRTV1 = 000040	166#																		
PRTV2 = 000100	166#																		
PRTV3 = 000140	166#																		
PRTV4 = 000140	166#																		
PRTV5 = 000140	166#																		
PRTV6 = 000300	166#																		
PRTV7 = 000340	166#																		

PS = 177776	166#																		
PUSH = 005740	166#																		
PUSH2 = 024546	166#																		
RANDS = 104417	166#																		
RANNUM = 000054R	133#																		
RESTART = 000274R	137#	182#	437																
RS31 = 000060R	138#																		
RTFCLR = 002062R	225#	447#																	
RTFNDR = 002056R	214#	443#																	
RRSFR1 = 000184R	129#																		
SADR = 000040R	130#																		
SOPCNT = 000042R	130#																		
SOPERS = 104406	166#																		
SOPPAS = 000046R	132#																		
SPOINT = 000032R	126#																		
SRIZ = 000040R	119#	159																	
SR2 = 000020R	120#																		
SR3 = 000022R	121#																		
SR4 = 000024R	122#																		
START = 000246R	152#	178#																	
TAT = 000026R	139#																		
SVR0 = 000062R	140#																		
SVR1 = 000064R	141#																		
SVR2 = 000066R	141#																		
SVR3 = 000070R	142#																		
SVR4 = 000074R	142#																		
SVR5 = 000076R	145#																		
SVR6 = 000076R	145#																		
SYSCNT = 000052R	134#																		
TBITC = 002106R	280#	462#																	
TBITS = 002102R	268#	459#																	
TEMP1 = 000224R	168#	381*	390*																
TEMP2 = 000224R	168#																		
TEMP3 = 000230R	170#																		
TRPDFD = 000022	166#																		
VECTOR = 000010R	115#	376*	377	377*	379*														
VECT2 = 000244R	170#	377*																	
WASADR = 00010R	156#																		
WDR = 000116R	155#	180*																	
WDT0 = 000114R	155#	179*																	
XFLAG = 000005R	113#																		

. ABS. 000000 000
 002730 001

PCSB DEC/111 SYSTEM EXERCISER MODULE
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CORE USED: 7K (13 PAGES)

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CROSS REFERENCE TABLE -- USER SYMROLS

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