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IDENTIFICATION

PRODUCT CODE: AC-E866L-MC
PRODUCT NAME: CXDHALO DH11 16-LNE PROG
PRODUCT DATE: FEB 1979
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT:

DHA IS AN IOMOD THAT EXERCISES UP TO FOUR (CONSECUTIVELY ADDRESSED) DH11 ASYNCHRONOUS INTERFACES. IT USES MAINTENANCE MODE TO TRANSMIT AND RECEIVE A BINARY COUNT PATTERN OUTPUTTED AND RECEIVED IN 64 CHARACTER BURSTS. THE MAJOR PORTION OF THE ERROR CHECKING IS DEFERRED TO LEVEL 0. ALL LINES SELECTED FOR TEST ARE ACTIVATED AND RUN CONCURRENTLY.

2. REQUIREMENTS:

HARDWARE: AT LEAST ONE DH11 INTERFACE

STORAGE:: DHA REQUIRES:
1. DECIMAL WORDS: 1066
2. OCTAL WORDS: 02052
3. OCTAL BYTES: 4124

3. PASS DEFINITION:

ONE PASS OF THE DHA MODULE CONSISTS OF TRANSMITTING AND RECEIVING 425984. CHARACTERS FOR EACH DH11 SELECTED

4. EXECUTION TIME:

VARIES WITH BAUD RATE BUT SHOULD TAKE AN AVERAGE OF ONE MINUTE TO COMPLETE ONE PASS WHEN RUNNING ALONE.

5. CONFIGURATION PARAMETERS:

DEFAULT PARAMETERS:

DVA: 1, VCT: 1, BR1: 5, BR2: 5, UVC: 1, SRI: 0

REQUIRED PARAMETERS:

AT CONFIGURATION TIME THE USER MUST SPECIFY:

DVA: ADDRESS OF FIRST DH11 CSR REG.
VCT: VECTOR ADDRESS OF FIRST DH11
UVC: NO OF DH11'S IF GREATER THAN 1
SRI: BIT15 MUST BE SET TO A "1" FOR
8. WORDS BETWEEN ADJACENT VECTORS
(2040 CONFIGURATION)

6. DEVICF OPTION SETUP:-----

NONE REQUIRED

7. MODULE OPERATION:-----

START: DETERMINE IF ANY ERRORS ARE SELECTED. DO NOT RUN THE MODULE IF NO DEVICES ARE SELECTED. IF THERE ARE SELECTED DEVICES, INITIALIZE THE BINARY COUNT PATTERN AT 0. CONTINUE PROCESSING.

RESTR: INITIALIZE THE ITERATION COUNTER TO 6556. DETERMINE IF ANY DH11'S ARE SELECTED. LOAD THE INTERRUPT VECTORS TO POINT TO THE JSR LINKING TABLE.

SETUP2: INITIALIZE THE QUEUE POINTERS. CLEAR ALL THE BUFFERS AND QUEUES. CLEAR THE BUFFER ACCESS FLAG (LCKOUT) IN CASE IT WAS STILL SET BY A CONTROL C INTERRUPT OF THE PROGRAM.

ACTVATE: THIS BLOCK OF CODE GETS THE PHYSICAL ADDRESS OF THE TRANSMITTER TEXT BUFFER. IT INITIALIZES EACH DH11 SELECTED. IT SAVES THE EXTENDED ADDRESS INFORMATION IN THE APPROPRIATE SYSTEM CONTROL REGISTER BITS. EACH OF THE SIXTEEN LINES IS LOADED WITH THE PHYSICAL ADDRESS AND LENGTH OF THE TRANSMITTER BUFFER. ALSO, EIGHT BITS PER CHARACTER IS SELECTED. IF A RAUDRATE IS SELECTED, IT IS CALCULATED AND ASSIGNED. OTHERWISE THE DEFAULT RATE OF 9600 BAUD IS ASSUMED.

INITIAL: THE DATA PATTERN IS LOADED INTO THE TRANSMITTER BUFFER. IT IS A BINARY COUNT PATTERN WHICH, ON SUCCESSIVE ITERATIONS, BEGINS 0, 4, 10, 14, 20, STATUS THE SILO ALARM LEVEL IS PLACED IN INTERRUPTS ARE ENABLED. REGISTER TRANSMITTER AND ERROR SELECTED DH11 ARE ALL THE TRANSMITTERS FOR EACH SELECTED DH11 ARE ENABLED.

TMRSET: TMRCTL IS USED AS A MULTIPLYING FACTOR TO DETERMINE THE WAITING LENGTH FOR THE WATCHDOG TIMER. IT IS PRESENTLY SET AT 5 TO ALLOW SEVENTY-FIVE SECONDS TO ELAPSE BEFORE TAKING FURTHER ACTION.

TIMER: THIS IS THE WATCHDOG TIMER LOOP. IT IS CONTROLLED BY R4 AND TMRCNT. IF ALL DH11'S SELECTED GENERATED BOTH TRANSMIT AND RECEIVE INTERRUPTS, THE APPROPRIATE BIT IN DONPLG FOR THAT DH11 WILL BE CLEARED. IF THIS IN DOES NOT OCCUR IN THE GIVEN TIME PERIOD, THE NUMBER OF THE OFFENDING DH11 WILL BE CALCULATED, AND THIS WILL BE REPORTED IN A MODULE MESSAGE. THE

OFFENDING DEVICE IS DROPPED FROM THE EXERCISE. IF NO MORE DH11'S ARE SELECTED, THE MODULE ITSELF IS DROPPED FROM THE RUN. IF MORE ARE TO BE EXERCISED, HOWEVER, CONTROL IS TRANSFERRED TO "FINISH."

FINISH: CONTROL COMES HERE IF ALL SELECTED DH11'S WERE SUCCESSFULLY EXERCISED OR IF MORE DH11'S REMAIN AFTER ONE WAS HUNG. THE ITERATION COUNT IS DECREASED. IF THE COUNT DOES NOT REACH ZERO, CONTROL IS PASSED TO SETUP2 AND THE MODULE IS RUN AGAIN. WHEN THE COUNT REACHES ZERO, AN END OF PASS IS SIGNALLED.

XMTINT: THIS SEGMENT SAVES A POINTER TO THE CSR ADDRESS OF THE INTERRUPTING DH11 IN A FIRST-IN, FIRST-OUT WRAPAROUND BUFFER. THE TRANSMITTER QUEUE. THE ENTRY POINTER IS THEN UPDATED TO POINT TO THE NEXT ENTRY IN THE QUEUE.

XMTRV: THIS BLOCK FETCHES A POINTER TO A CSR ADDRESS FROM THE TRANSMITTER QUEUE, AND THE QUEUE IS UPDATED TO THE NEXT ENTRY. THE CSR IS TESTED TO DETERMINE WHAT KIND OF INTERRUPT OCCURRED. FALSE INTERRUPT AND NON-EXISTENT MEMORY INTERRUPTS ARE REPORTED. IF EVERYTHING IS CORRECT, THE RECEIVER FOR THIS DH11 IS ENABLED.

RCVINT: THIS SEGMENT SAVES A POINTER TO THE CSR ADDRESS OF THE INTERRUPTING DH11 IN THE RECEIVER QUEUE. IT UPDATES THE QUEUE ENTRY AND RESTORES THE VALUE OF R5 WHICH WAS SAVED BY THE JSR INSTRUCTION FROM THE LINKAGE TABLE.

RCVSRV: THE FIRST TASK IS TO PREVENT VOLATILE REGISTER INFORMATION FROM BEING DESTROYED. THIS IS DONE BY TESTING A SEMAPHORE, "LCKOUT." IF IT IS SET, CONTROL IS RETURNED TO THE MONITOR TO WAIT FOR THE AWHILE. IF IT IS CLEARED, ACCESS IS PERMITTED. THE FLAG IS SET TO DENY OTHER ACCESS TO THIS DEFERRED ROUTINE. A CSR ADDRESS IS OBTAINED FROM THE DEFERRED AND INTERRUPT ENABLED. THE RECEIVERS ARE SET UP TO RETRIEVE AS QUICKLY AS POSSIBLE THE DATA FROM THE DH11 SILO. EACH IF IS NOT TO SEE IF THE INFORMATION IS VALID. IF IT IS, THE REGISTER ARE SAVED AND A BREAK LOOP IS USED TO ALLOW MORE TIME FOR VALID INFORMATION TO BECOME AVAILABLE. IF AFTER THE ALLOWED TIME ALL THE CHARACTERS ARE STILL NOT RECEIVED, AN ERROR MESSAGE IS REPORTED. IN THE MESSAGE, NUMBERS FOLLOWING STAC IS THE OCTAL NUMBER OF CHARACTERS MISSING.

CKDATA: THIS SEGMENT INITIALIZES THE LINE CHECK BUFFER (LCKBUF) TO THE FIRST DATUM THAT WAS TRANSMITTED. THE DEVICE NUMBER IS SAVED FOR LATER USAGE. THE RECEIVED INFORMATION IS CHECKED FOR VALIDITY AND TRANSMISSION ERRORS. ERRORS ARE HANDLED BY THE

STATERR (STATUS ERROR) AND *DERRJP* (DATA ERROR) ROUTINES.

RCVDONE: THIS BLOCK CLEARS THE ACCESS SEMAPHORE TO ALLOW OTHER DEVICES TO USE THE LINE CHECK BUFFER. IT CARRIES A ONE BIT MASK USING RO AND THE WATCHDOG TIMER FLAG (DONFLG). WHEN THIS IS DONE, PROCESSING CONTROL IS RETURNED TO THE MONITOR.

SUBROUTINES

VCTLOAD: THIS ROUTINE IS CALLED IN 'SETUP1'. IT IS USED TO LOAD THE ADDRESS OF THE LINKING INSTRUCTION FOR INTERRUPT SERVICING INTO THE CORRESPONDING VECTOR SPACE. IT ALSO LOADS THE PRIORITY LEVEL AND THE DEVICE ADDRESS. THE LATTER IS LOADED INTO THE APPROPRIATE JSR TABLE ENTRY.

SAVREG: THIS ROUTINE SAVES THE FIVE VOLATILE INFORMATION REGISTERS IN A FIRST-IN, FIRST-OUT WRAPAROUND BUFFER, THE ERROR QUEUE.

GOTREG: THIS ROUTINE RETRIEVES THOSE SAME REGISTERS.

BAUDRTE: THIS ROUTINE CALCULATES THE BAUD RATE, ASSIGNS IT AND SELECTS 8 BITS/CHARACTER COMMUNICATION MODE. IF SRI=0, THE DEFAULT RATE OF 9600. BBAUD IS ASSIGNED. THE BAUD RATE SELECTED IS DETERMINED BY THE LEAST SIGNIFICANT (RIGHTMOST) SET BIT IN SRI.

STATERR: THIS ROUTINE DETERMINES WHETHER AN ERROR INDICATED IN THE RECEIVED CHARACTER INFORMATION WAS AN OVERRUN ERROR, A FRAMING ERROR, A PARITY ERROR, OR A DEVICE NUMBER OF THE ERRORING DEVICE IS REPORTED AS STATC. CSRA WILL BE CLEAR.

DERRJP: THIS ROUTINE REPORTS A DATA ERROR.

OPERATOR OPTIONS

8. -----
MODULE LOCATION DVID1 MAY BE MODIFIED (MOD. CMMD) TO EXERCISE ANY COMBINATION OF DH11'S.
MODULE LOCATION SRI MAY BE MODIFIED TO SELECT A DIFFERENT BAUD RATE.
THE FOLLOWING TABLE SHOULD BE USED:

FOR THE BAUD RATE	SRI=
9600	0
EXT. B	1
EXT. A	2

9600	4
4900	10
2400	20
1800	40
1200	100
600	200
300	400
200	1000
150	2000
134.5	4000
110	10000
75	20000
50	40000
2040 CONFIGURATION	100000

(8. WORDS BETWEEN VECTORS)

BIT15 MAY BE SET IN CONJUNCTION WITH ONE OF BITS
00 THRU 14 TO INDICATE SPECIFIC BAUD RATE OTHER THAN
DEFAULT OF 9600 BAUD. (SR<14:00> = 0)

THE DEFAULT RATE IS 9600 BAUD(SR1=0).

9. NON-STANDARD PRINTOUTS:

WHEN A STATUS ERROR IS DETECTED, DHA USES THE ERROR
CALL TO REPORT IT. THE LINE NUMBER IS
REPORTED IN STATC.

ALL OTHER PRINTOUT IS STANDARD.

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302          .LIST      SFQ,LOC,BIN
303          IOMDD <DHAL> 1,5,5,17000,25
304          000000*     140000,DHAL,1,5,5,17000,25
305          000000*     .TITLE  DHAL DEC/X11 SYSTEM EXERCISER MODULE
306          ;          .DDXCDW VERSION 6 23-MAY-78
307          .LIST      BIN
308          *****
309          000000*     BEGIN:
310          000000*     044104 046101 040  MODNAM: .ASCII /DHAL /;MODULE NAME
311          000005*     000          XFLAG: .RVTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
312          000006*     000001          ADDR: 1+0 ;1ST DEVICE
313          000010*     000001          VECTR: 1+0 ;1ST DEVICE VECTOR.
314          000012*     240          BR1: .RVTE PRTV5+0 ;1ST BR LEVEL.
315          000013*     240          BR2: .RVTE PRTV5+0 ;2ND BR LEVEL.
316          000014*     000001          DVID1: +1 ;DEVICE INDICATOR 1.
317          000016*     000000          SR1: OPEN ;SWITCH REGISTER 1
318          000020*     000000          SR2: OPEN ;SWITCH REGISTER 2
319          000022*     000000          SR3: OPEN ;SWITCH REGISTER 3
320          000024*     000000          SR4: OPEN ;SWITCH REGISTER 4
321          *****
322          000026*     140000          STAT: 140000 ;STATUS WORD
323          000030*     000224          INIT: START ;MODULE START ADDR.
324          000032*     000224          SPCINT: MODSP ;MODULE STACK POINTER.
325          000034*     000000          PASCNT: 0 ;PASS COUNTER.
326          000036*     017000          ICOUNT: 17000 ;# OF ITERATIONS PER PASS=17000
327          000040*     000000          ICDCNT: 0 ;LOC TO COUNT ITERATIONS
328          000042*     000000          SOPCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
329          000044*     000000          HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
330          000046*     000000          SOPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
331          000050*     000000          HRUPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
332          000052*     000000          SVSCNT: 0 ;# OF SVS ERRORS ACCUMULATED
333          000054*     000000          RANNUM: 0 ;HOLDS RANDOM # WHEN RAND
334          000056*     000000          CONFIG: ;RESERVED FOR MONITOR USE
335          000058*     000000          RES1: 0 ;RESERVED FOR MONITOR USE
336          000060*     000000          RES2: 0 ;RESERVED FOR MONITOR USE
337          000062*     000000          SVR0: OPEN ;LOC TO SAVE R0.
338          000064*     000000          SVR1: OPEN ;LOC TO SAVE R1.
339          000066*     000000          SVR2: OPEN ;LOC TO SAVE R2.
340          000070*     000000          SVR3: OPEN ;LOC TO SAVE R3.
341          000072*     000000          SVR4: OPEN ;LOC TO SAVE R4.
342          000074*     000000          SVR5: OPEN ;LOC TO SAVE R5.
343          000076*     000000          SVR6: OPEN ;LOC TO SAVE R6.
344          000100*     000000          CSRA: OPEN ;ADDR OF CURRENT CSR.
345          000102*     000000          SBADR: ;ADDR OF GOOD DATA, OR
346          000104*     000000          ACSR: OPEN ;CONTENTS OF CSR.
347          000106*     000000          WBSADR: ;ADDR OF BAD DATA, OR
348          000108*     000000          ASTAT: OPEN ;STATUS REG CONTENTS.
349          000110*     000000          ERRTP: ;TYPE OF ERROR.
350          000112*     000000          ASR: OPEN ;EXPECTED DATA.
351          000114*     000000          AWAS: OPEN ;ACTUAL DATA.
352          000116*     000244          RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
353          000118*     000000          WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
354          000120*     000000          WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
355          000122*     000000          INTR: OPEN ;# OF INTERRUPTS PER ITERATION
356          000124*     000025          IDNUM: 25 ;MODULE IDENTIFICATION NUMBER=25
357          .REPT      SPSIZ ;MODULE STACK STARTS HERE.
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358          .NLIST      0
359          .WORD
360          .LIST
361          .ENDR
362          000224*     MODSP:
363          *****
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364 ;START ROUTINE. DETERMINE IF ANY DH'S ARE SELECTED
365 ;IF SO, BEGIN MODULE PROCESSING... IF NOT, DROP THE MODULE FROM THE RUN
366
367 000224* 105067 003616 003566 START: CLR B DATA ;INITIALIZE THE DATA PATTERN WORD
368 000230* 016767 177560 003566 MOV DVID1,SELECT ;COPY THE DEVICE SELECTION PARAMETER. ARE
369 ;ANY DEVICES SELECTED?
370 000236* 001002 DROP: BNE REXSTR ;IF SO, BEGIN PROCESSING. IF NOT, DROP THE MODULE
371 000240* 104410 000000* ENDS,REGIN ;
372
373 ;INITIALIZE THE NUMBER OF PASSES TO BE MADE. SET UP THE DH11 INTERRUPT
374 ;VECTORS TO INTERRUPT IN THE SUBROUTINE LINKING TABLE. CALL SUBROUTINE
375 ;VCTLOAD FOR THIS PURPOSE.
376
377
378 000244* 005000 REXSTR: CLR R0 ;INIT POINTER
379 000246* 016701 177544 MOV SRI,R1 ;GET BAND RATE
380 000252* 042701 100000 BIC #100000,R1 ;THROW 2040 CNF BIT AWAY
381 000256* 001405 BEQ ZS ;LEAVE IF SRI IS 0
382 000260* 062700 000002 1S: ADD #2,R0 ;ELSE BUMP POINTER
383 000264* 006001 ROR R1 ;LOOK FOR THE SRI BIT
384 000266* 103401 BCS ZS ;LEAVE IF WE FOUND IT
385 000270* 000773 BR IS ;ELSE DO IT AGAIN
386 000272* 016067 002602* 177536 2S: MOV CNTTBL(R0),ICONT ;SET ITERATION COUNT
387 000300* 042767 177760 003516 BIC #177760,SELECT ;REMOVE UNWANTED BITS FROM THE DEVICE SELECTION
388 ;PARAMETER
389 000306* 016701 003512 SETUP1: MOV SELECT,R1 ;COPY THE DEVICE SELECTION PARAMETER INTO R1
390 000312* 001752 BEQ ;IF NO DEVICES SELECTED (ALL FLAGS CLEAR) DROP THE
391 ;MODULE
392 000314* 016700 177470 MOV VECTOR,R0 ;LOAD THE VECTOR ADDRESS IN R0
393 000320* 016702 177462 MOV ADDR,R2 ;LOAD R2 WITH ADDRESS OF FIRST DH11
394 000324* 012703 002502* 1S: MOV #LNKTAB,R3 ;POINT R3 TO THE BEGINNING OF JSR LINKING TABLE
395 ;ISOLATE A SELECTION FLAG IN THE CARRY BIT
396 000332* 103415 BCS S ;IF THE FLAG IS SET, GO SET UP THE VECTORS
397 000334* 001430 BEQ SETUP2 ;IF NO FLAGS ARE LEFT, GO SET UP THE BUFFERS
398 000336* 062700 000010 ADD #10,R0 ;IF MORE FLAGS ARE SET, ADJUST POINTERS. THE
399 ;VECTOR POINTER...
400 000342* 005767 177450 TST SRI ;EIGHT WORDS BETWEEN VECTORS ?
401 000346* 100002 BPL 4S ;BR IF NOT
402 000350* 062700 000010 ADD #10,R0 ;FIX VECTOR POINTER
403 000354* 062703 000020 4S: ADD #20,R3 ;THE ADDRESS POINTER...
404 000360* 062702 000020 3S: ADD #20,R2 ;AND THE LINKAGE TABLE POINTER
405 000364* 000761 BR ;GO SET UP THE NEXT DH11 ADDRESSES
406 000366* 004567 001426 2S: JSR R5,VCTLOAD ;CALL THE VECTOR SETUP ROUTINE FOR RECEIVER
407 000372* 000013 BR2 ;VECTOR, PASSING THE RECEIVER BR LEVEL AS THE
408 ;ARGUMENT
409 000374* 004567 001420 JSR R5,VCTLOAD ;SET UP THE TRANSMITTER VECTOR PASSING THE
410 000400* 000012 BR1 ;TRANSMITTER BR LEVEL AS THE ARGUMENT
411 000402* 005767 177410 TST SRI ;EIGHT WORDS BETWEEN VECTORS ?
412 000406* 100002 BPL 5S ;BR IF NOT
413 000410* 062700 000010 5S: ADD #10,R0 ;FIX VCTR POINTER
414 000414* 000761 BR 3S ;GO SETUP THE NEXT DH11
415
416 ;THIS BLOCK RESETS ALL THE QUEUE POINTERS, CLEARS THE TRANSMITTER TEXT BUFFER, THE
417 ;RECEIVER BUFFERS, AND THE QUEUES. THIS IS THE BEGINNING OF THE ITERATIVE PART OF THE
418 ;PROGRAM.
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420 000416* 012767 002642* 003364 SETUP2: MOV #XMTQUE,XMTQPI ;POINT THE TRANSMIT QUEUE ENTRY (IN) POINTER
421 ;TO THE BEGINNING OF THE QUEUE
422 000424* 012767 002642* 003360 MOV #XMTQUE,XMTQPO ;POINT THE TRANSMIT QUEUE RETRIEVAL (OUT)
423 ;POINTER TO THE BEGINNING OF THE QUEUE
424 000432* 012767 002662* 003354 MOV #RCVQUE,RCVQPI ;SET UP THE RECEIVER QUEUE POINTERS
425 000440* 012767 002662* 003350 MOV #RCVQUE,RCVQPO ;
426 000446* 012767 002703* 003344 MOV #ERRQUE,ERRQPI ;
427 000454* 012767 002703* 003340 MOV #ERRQUE,ERRQPO ;SETUP THE ERROR QUEUE POINTERS
428 000462* 012703 002642* 003340 MOV #XMTQUE,R3 ;POINT R3 TO THE BEGINNING OF THE QUEUE AREA
429 000466* 012704 000446 MOV #294,R4 ;USING R4 AS A COUNTER CLEAR -
430 000472* 005023 1S: CLR (R3)* ;TRANSMITTER, RECEIVERS, AND PRPOP QUEUES....
431 000474* 005304 DEC R4 ;TRANSMITTER TEXT BUFFER...
432 000476* 000761 BNE ;RECEIVER'S CIB BUFFERS
433 000500* 105067 003345 CLR R5 ;CLEAR THE BUFFER ACCESS FLAG
434
435 ;THIS BLOCK SETS THE WATCHDOG TIMER FLAG, INITIALIZES THE DH11 CONDITIONS, SETS THE CHAR
436 ;AND BAUD RATE, AND SETS THE EXTENDED ADDRESSING BITS FOR THE ACTIVE DH11S
437
438 000504* 016700 003314 ACTVATE: MOV SELECT,R0 ;COPY THE DEVICE SELECTION PARAMETER
439 000510* 110067 003333 MOV R0,DONFLG ;SET THE WATCHDOG TIMER DEVICE COMPLETION FLAG
440 000514* 016701 177266 MOV ADDR,R1 ;LOAD THE ADDRESS OF THE FIRST DH11
441 000520* 012767 002764* 003304 MOV #XMTBUF,VA ;GET THE PHYSICAL ADDRESS OF THE TRANSMITTER
442 ;BUFFER
443 000526* 104415 000000* 004032* GETPAS,BEGIN, VA ;GET PHYSICAL ADDRESS FROM 16-BIT VA
444 000534* 006200 1S: ASR R0 ;ISOLATE A SELECTION FLAG IN THE CARRY BIT
445 000536* 103404 BCS 3S ;IF SELECTED, GO SET UP THE DEVICE REGISTERS
446 000540* 001431 BEQ INITIAL ;IF NO MORE SELECTED, GO SET UP THE DATA
447 000542* 062701 000020 2S: ADD #20,R1 ;POINT R1 TO THE NEXT DH11
448 000546* 000772 BR IS ;PROCESS NEXT DH11
449 000550* 012711 004000 3S: MOV #RIT11,(R1) ;MASTER CLEAR THIS DH11 AND SELECT LINE 0
450
451 000554* 052711 001000 BIS #RIT9,(R1) ;ENABLE MAINTENANCE MODE OPERATION
452 000560* 042711 000060 BIC #60,(R1) ;ERASE INVALID INFORMATION FROM THE EA BITS IN THE CSR
453 000564* 056711 003246 EA,(R1) ;SET THE EXTENDED ADDRESS BITS OF THE TEXT
454 000570* 012702 000020 MOV #20,R2 ;USE R2 TO COUNT 16 LINES BEING SET UP
455 000574* 016761 003234 4S: MOV PA,(R1) ;LOAD THE PHYSICAL ADDRESS OF THE TRANSMITTER
456 ;BUFFER INTO THE CURRENT ADDRESS REGISTER
457 000602* 012761 177774 000010 MOV #-4,10(R1) ;LOAD THE BYTE COUNT REGISTER WITH THE NUMBER
458 ;OF CHARACTERS TO BE TRANSMITTED
459 000610* 004767 JSR PC,BAURTE ;GO CALCULATE THE BAUD RATE TO BE USED
460 000614* 005302 DEC R2 ;REDUCE THE COUNT. ARE ALL 16 LINES SET?
461 000616* 001751 BFC ZS ;IF YES, GO PROCESS NEXT DH11
462 000620* 005211 INCR (R1) ;IF NO, INCREMENT THE LINE SELECTION PARAMETER
463 000622* 000764 BR 4S ;GO SET UP THE NEXT LINE
464
465 ;THIS BLOCK SETS UP THE TRANSMITTER TEXT WITH THE TEST DATA. IT INSURES THAT THE
466 ;EXTENDED ADDRESS BITS ARE SET, THEN STARTS EACH RECEIVER AND EACH TRANSMITTER SELECTED
467
468 000624* 012701 002764* 003204 INITIAL: MOV #XMTBUF,R1 ;POINT R1 TO THE START OF THE BUFFER AREA
469 000630* 116767 003212* 003204 MOV R1,RCNDATA ;COPY THE FIRST DATUM BEING TRANSMITTED IN THIS
470 000636* 116767 003204 003177 MOV R1,RCNDATA+1 ;GROUP RCNDATA WILL BE USED TO SET
471 ;UP LMCB (THE LINE CHECK BUFFER) IN THE CKDATA ROUTINE
472 000644* 012702 000004 MOV #4,R2 ;USE R2 TO COUNT THE LOOP ITERATIONS
473 000650* 116721 003172 1S: MOV DATA,(R1)+ ;PUT A CHARACTER IN THE BUFFER, BUILDING THEM
474 000654* 105267 003166 INCR DATA ;IN DATA, WHICH WILL CREATE A BINARY INCREMENT PATTERN.
475 000660* 005302 DEC R2 ;REDUCE COUNT. HAVE ALL CHARACTERS BEEN MADE?

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476 000662 001372 BNE 1$ ;IF NO, GO LOAD THE NEXT CHARACTER
477 000664 016700 003134 MOV 1$ SELECT,R0 ;COPY THE DEVICE SELECTION PARAMETER
478 000670 016701 177117 MOV 1$ DR,R1 ;LOAD THE ADDRESS OF THE FIRST DH11
479 000674 006200 2S: ASR R0 ;ISOLATE A SELECTION FLAG IN THE CARRY BIT
480 000676 103404 BCS 4$ ;IF SELECTED, GO START THE DEVICE
481 000700 001414 BEQ TMRSET ;IF NO MORE SELECTED, GO START WATCHDOG TIMER
482 000702 062701 000020 ADD #20,R1 ;POINT R1 TO THE NEXT DH11
483 000706 000772 BR 1$ ;GO PROCESS THE NEXT DH11
484 000710 012761 000040 000015 4S: MOV #32,,16(R1) ;LOAD THE SILO ALARM LEVEL INTO THE SILO STATUS REGISTER
485 000716 052711 020000 BIS #R113,(R1) ;ENABLE TRANSMITTER AND NON-EXISTENT MEMORY INTERRUPTS
486 000722 012761 177777 000012 MOV #-1,12(R1) ;ENABLE THE TRANSMITTERS FOR ALL 16. LINES
487 000730 000764 BR 1$ ;PROCESS THE NEXT DH11
488
489 ;THIS IS THE WATCHDOG TIMER. WHEN A DEVICE HAS SUCCESSFULLY TRANSMITTED AND
490 ;RECEIVED ALL DATA, IT CLEARS ITS CORRESPONDING BIT IN DONFLG. IF THIS
491 ;DOES NOT OCCUR, AN ERROR MESSAGE IS REPORTED. STATC IN THE
492 ;ERROR MESSAGE TELLS WHICH DEVICE WAS SLOW OR HUNG. CSRA WILL BE ZERO.
493 ;A MODULE MESSAGE IS REPORTED. IF MORE DEVICES ARE TO BE PROCESSED, THIS
494 ;ITERATION IS CONSIDERED COMPLETE. IF NO DEVICES REMAIN TO BE PROCESSED,
495 ;THE MODULE IS DROPPED.
496
497 000732 012767 000005 003070 TMRSET: MOV #5,TMRCNT ;SET THE TIMER COUNT FOR THE BREAK LOOP
498 000740 005004 TIMER: CLR R4 ;USING R4, RETURN TO MONITOR 65535 TIMES
499 1S:
500 000742 104407 000000 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
501 000744 104407 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
502 000746 105767 003071 TSTR DONFLG ;IF DONFLG IS CLEAR, EACH SELECTED DEVICE WAS
503 ;SUCCESSFUL
504 000752 001454 BEQ FINISH ;IF SO, PERFORM ENDPASS PROCESSING
505 000758 005104 DEC R4 ;IF NOT, REDUCE COUNT AND BREAK AGAIN
506 000762 001367 BNE 1$ ;BREAK IF COUNT NOT EXCEEDED
507 000764 005367 003040 DEC TMRCNT ;REDUCE THE OVERALL TIME. IS IT EXCEEDED?
508 000770 001363 BNE TIMER ;IF NO, START ANOTHER BREAK LOOP
509
510 000772 116703 003051 MOV#R DONFLG,R3 ;IF TIMEOUT OCCURRED, SAVE THE REMAINING FLAG
511 ;IN R3
512 000776 040367 003022 BIC R3,SELECT ;CLEAR THE SELECTION FLAG FOR THIS DEVICE
513 001002 005067 177077 CLR CSRA ;SETUP TO REPORT CSR
514 001006 006003 2S: RDR R3 ;DETERMINE WHICH DEVICE WAS READ FOR REPORTING
515 ;PURPOSES
516 001010 103405 BCS 3$ ;IF THIS IS THE DEVICE, R4 CONTAINS THE CORRECT
517 ;LINE NUMBER... GO REPORT IT
518 001012 005204 INC R4 ;IF NOT, INCREMENT R4, WHICH WAS INITIALLY 0
519 001014 062767 000020 177056 ADD #20,CSRA ;FROM THE PREVIOUS LOOP
520 001022 000771 BR 1$ ;ADD DEVICE OFFSET FOR CORRECT DH11
521 001024 010467 003022 3S: MOV R4,NUMBA1 ;GO SEE IF THIS IS THE DEVICE
522 ;*****
523 ;THE DEVICE NUMBER WILL BE REPORTED AS STATC
524 ;CONVERT NUMBA1 TO ASCII AND
525 ;STORE AT M2
526 001030 104420 000000 004052 OTOAS,BEGIN,NUMBA1,M2
527 001036 004067 JSR PC,SAVREG ;SAVE THE REGISTERS BEFORE LEAVING THE MODULE
528
529 001040 004767 000772 JSR PC,SAVREG ;SAVE THE REGISTERS BEFORE LEAVING THE MODULE
530
531
```

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532 001044 066767 176736 177026 ADD ADDR,CSRA ;ADD BASE ADDRESS TO OFFSET FOR ERROR CALL
533 001052 005067 177030 CLR ERR1 ;UNKNOWN
534 *****
535 001056 104405 000000 000000 000000 000000 000000 000000 000000 000000
536 *****
537 001064 104403 000000 004054 MSGNS,BEGIN,HUNG ;ASCII MESSAGE CALL WITH COMMON HEADER
538 001072 004767 000776 JSR PC,GETREG ;RESTORE THE PREVIOUS REGISTER VALUES
539 001076 005766 002722 TST SELECT ;ARE THERE ANY DEVICES STILL ACTIVE?
540 001102 001002 BNE FINISH ;IF YES, REDUCE COUNT AND CONTINUE
541 ;IF NO, DROP THE MODULE
542 ENDS,BEGIN
543
544 ;THIS BLOCK REDUCES THE ITERATION COUNT AND GOES TO SETUP2 IF THE COUNT IS NOT EXCEEDED
545
546 001110 104413 000000 FINISH: ENDTIS,BEGIN ;SIGNAL END OF ITERATION.
547 001114 000167 177276 JMP SETUP2 ;MONITOR SHALL TEST END OF PASS
548
549 ;TRANSMITTER INTERRUPT SERVICE ROUTINE
550 ;THIS ROUTINE STORES THE ADDRESS OF THE CSR POINTER (OFFSET IN THE JSR LINKAGE
551 ;TABLE) IN THE TRANSMITTER QUEUE. IT DETERMINES THIS A NON-EXISTENT MEMORY ERROR
552 ;FOR A DATA INTERRUPT. IF IT IS THE FORMER IT IS SETUP FOR REPORTING. IF IT
553 ;IS THE LATTER, FURTHER SERVICE IS DEFERRED TO PRIORITY AND BY A PROGRAM
554 ;INTERRUPT REQUEST, THE ROUTINE THEN ENABLES INTERRUPTS FOR THE CORRESPONDING RECEIVER
555 ;AND RETURNS CONTROL TO THE MONITOR.
556
557 001120 010577 002664 XMTINT: MOV R5,XMTQPI ;LOAD THE OFFSET TO THE CSR INTO TRANSMITTER QUEUE
558 001124 062767 000002 002656 ADD #20,XMTQPI ;UPDATE THE QUEUE ENTRY POINTER
559 001132 022767 002660 002650 CMP #XMTQPI+16,XMTQPI ;HAS THE QUEUE BOUNDARY BEEN EXCEEDED?
560 BHS 1$ ;IF NO, CONTINUE PROCESSING
561 001140 103003 MOV #XMTQPI,XMTQPI ;IF YES, RESET THE POINTER TO QUEUE BEGINNING
562 001144 012767 002642 002640 1S: MOV R5,R5 ;RESTORE THE PREVIOUS R5 VALUE
563 001150 012605 MOV R0,(SP)
564 001152 010046 MOV R1,-(SP)
565 001154 010146 MOV R2,-(SP)
566 001156 010246 MOV R3,-(SP)
567 001160 010346 MOV R4,-(SP)
568 001162 010446 MOV #XMTQPO,R1 ;FETCH THE OFFSET FROM THE QUEUE
569 001164 017701 002622 XMTSRV: ADD #2,XMTQPO ;UPDATE THE QUEUE RETRIEVAL POINTER
570 001170 062767 000002 002614 CMP #XMTQPI+16,XMTQPO ;HAS THE QUEUE BOUNDARY BEEN EXCEEDED?
571 001176 022767 002660 002605 BHS 1$ ;IF NO, CONTINUE PROCESSING
572 001204 103003 MOV #XMTQPO,XMTQPO ;IF YES, RESET POINTER TO BEGINNING OF THE QUEUE
573 001208 012767 002642 002576 1S: MOV (R1),R0 ;LOAD CSR ADDRESS INTO R0
574 001214 011100 TST (R0) ;IS THIS A VALID INTERRUPT?
575 001216 005710 BMI 3$ ;IF YES, GO ENABLE RECEIVER INTERRUPT
576 001220 100467 002000 BIT #BIT10,(R0) ;IF NO, DETERMINE THE TYPE OF ERROR
577 001222 032710 BNE 2$ ;IF NON-EXISTENT MEMORY LEVEL 0 BY A PROGRAM
578 ;THAT TYPE OF ERROR, OTHERWISE IT IS A FALSE INTERRUPT
579 001226 001032 MOV R0,CSRA ;LOAD THE DEVICE ADDRESS
580 001230 010067 176644 MOV (R0),ACSR ;SAVE THE CSR CONTENTS
581 001234 011067 176642 JSR PC,SAVREG ;SAVE THE REGISTERS BEFORE ERROR MESSAGE
582 001240 004767 000577 MOV #R4,(SP)
583 001244 012604 MOV #R4,(SP)
584 001246 012603 MOV #R3,(SP)
585 001250 012602 MOV #R2,(SP)
586 001252 012601 MOV #R1,(SP)
```

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588 001254* 012600      MOV      (SP)+,R0
589 001256* 000004 000000* 001264*  PIRQS,BEGIN,10$      ; QUEUE UP TO CONTINUE AT 10$ AND RTI
590 001264* 012767 000011 176614 10$:  MOV      #11,ERRRTP    ;ILLEGAL INTERRUPT
591 001272* 104405 000000* 000000  HRDRS,BEGIN,NULL      ;FALSE INTERRUPT REQUEST FROM TRANSMITTER
592 001300* 004767 000570      JSR      PC,GETREG     ;RESTORE THE REGISTERS
593 001304* 042710 120000      BIC      #120000,(R0) ;DELETE THE FALSE INTERRUPT AND THE INTERRUPT
594 001310* 104400 000000*      EXITS,BEGIN          ;ENABLE
595 001310* 104400 000000*      MOV      R0,CSRA     ;LOAD THE DEVICE ADDRESS. MODULE WAIT FOR INTERRUPT.
596 001320* 011067 176560      MOV      (R0),ACSR   ;SAVE THE CSR CONTENTS
600 001324* 004767 000506      JSR      PC,SAVREG   ;SAVE THE REGISTER VALUE BEFORE ERROR MESS.
601 001330* 012604      MOV      (SP)+,R4
602 001330* 012604      MOV      (SP)+,R3
603 001330* 012604      MOV      (SP)+,R2
604 001330* 012604      MOV      (SP)+,R1
605 001330* 012604      MOV      (SP)+,R0
606 001340* 012600
607 001342* 000004 000000* 001350*  PIRQS,BEGIN,11$      ; QUEUE UP TO CONTINUE AT 11$ AND RTI
608 001350* 012767 000010 176530 11$:  MOV      #10,ERRRTP    ;BAD ADDRESS
609 001356* 104405 000000* 000000  HRDRS,BEGIN,NULL      ;NON-EXISTENT MEMORY ADDRESSING FAILURE
610 001364* 004767 000504      JSR      PC,GETREG     ;RESTORE THE REGISTERS
611 001370* 042710 122000      BIC      #122000,(R0) ;CLEAR INTERRUPT ENABLE AND INTERRUPT BITS
612 001374* 104400 000000*      EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
613 001400* 052710 000100      BIC      #BIT6,(R0)  ;ENABLE RECEIVER INTERRUPTS
614 001400* 052710 000100      MOV      (SP)+,R4
615 001400* 052710 000100      MOV      (SP)+,R3
616 001400* 052710 000100      MOV      (SP)+,R2
617 001400* 052710 000100      MOV      (SP)+,R1
618 001400* 052710 000100      MOV      (SP)+,R0
619 001410* 012602
620 001412* 012601
621 001414* 012600
622 001416* 000002
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637 001420* 010577 002370      RCVINT:  MOV      R5,RCVQPI    ;STORE THE OFFSET IN THE RETRIEVAL QUEUE
638 001430* 062767 000002* 002362  ADD      #2,RCVQPI    ;UPDATE THE QUEUE ENTRY POINTER
639 001440* 103003 002700* 002354  CMP      #RCVQVE+16,RCVQPI ;HAS THE QUEUE BOUNDARY BEEN EXCEEDED?
640 001442* 012767 002662* 002344  BHS     1$           ;IF NOT CONTINUE PROCESSING
641 001450* 012605      MOV      #RCVQVE,RCVQPI ;RESET THE POINTER TO QUEUE BEGINNING
642
643

```

RECEIVER INTERRUPT SERVICE ROUTINE
THIS ROUTINE STORES A POINTER TO THE CSR ADDRESS OF THE INTERRUPTING DEVICE IN THE
RECEIVER QUEUE. SERVICE IS DEFERRED TO PRIORITY LEVEL 0. AT DEFERRED SERVICE (RCVSRV),
THE OFFSET IS FETCHED FROM THE QUEUE. THE INTERRUPT AND INTERRUPT ENABLE BITS ARE
CLEARED AND THE SILO IS EMPTIED INTO THE SOFTWARE BUFFER. IF ALL CHARACTERS
WERE NOT RECEIVED, THIS IS REPORTED. CHECK THE BUFFER AND REPORT DATA ERRORS. IF
ANOTHER RECEIVER IS ALREADY USING THE LINE CHECK BUFFER (LNCKBF), SUBSEQUENT
RECEIVERS WILL NOT BE PERMITTED ACCESS UNTIL THE FIRST IS COMPLETE. IF THERE
ARE ANY CHARACTER REPORTS THAT DATA ARE CHECKED, IT RELEASES
THE LINE CHECK BUFFER AND CLEAR THE CORRESPONDING BIT IN DONFLG.

```

644 001452* 000004 000000* 001460*  PIRQS,BEGIN,RCVSRV   ; QUEUE UP TO CONTINUE AT RCVSRV AND RTI
645 001460* 105767 002365      RCVSRV:  ESTB     LCKOUT        ;TEST THE BUFFER LOCK FLAG. IS ACCESS PERMITTED?
646 001464* 001405 000000*      BR      1$           ;IF YES, GO SET THE LOCK AND CHECK THE DATA
647 001466* 104407 000000*      BREAKS,BEGIN        ;TEMPORARY RETURN TO MONITOR.
648 001472* 104407 000000*      BREAKS,BEGIN        ;THEN CONTINUE AT NEXT INSTRUCTION.
649 001476* 000770 002345      BR      RCVSRV      ;TRY TO ACCESS AGAIN
650 001500* 017700 002305 11$:  DECB     LCKOUT        ;DENY OTHER ACCESSSES TO THE BUFFER
651 001504* 017700 002305      MOV      RCVQPO,R0   ;FETCH THE OFFSET FROM THE QUEUE
652 001510* 062767 000002* 002300  ADD      #2,RCVQPI    ;UPDATE THE QUEUE RETRIEVAL POINTER
653 001516* 022767 002700* 002272  CMP      #RCVQVE+16,RCVQPI ;HAS THE QUEUE BOUNDARY BEEN EXCEEDED?
654 001524* 103003 002662* 002262  BHS     1$           ;IF NO, CONTINUE PROCESSING
655 001530* 012607 000000*      MOV      #RCVQVE,RCVQPI ;RESET THE POINTER TO THE QUEUE BEGINNING
656 001534* 012607 000000*      MOV      R0,R3       ;LOAD THE CSR ADDRESS INTO R1
657 001536* 042721 000300 1$:  BIC      #300,(R1)+  ;DISABLE RECEIVER INTERRUPT AND INTERRUPT ENABLE
658 001542* 011002 077000 002272      MOV      (R0),R2     ;LOAD THE SOFTWARE SILO BUFFER ADDRESS
659 001552* 012703 000100      MOV      #77000,RCVTHR ;SET THE RECEIVER BREAK LOOP TIMER
660 001556* 011122 000000*      MOV      (R1),(R2)+ ;STORE A WORD IN THE SOFTWARE SILO
661 001560* 002003 000000*      BGE     3$           ;IF THE DATA WASN'T VALID, GO ALLOW A LITTLE TIME FOR IT
662 001562* 005303 000000*      DEC     R2          ;IF IT WAS VALID, REDUCE THE COUNT.
663 001564* 001374 000000*      BNE     2$           ;IF NOT ZERO, GO STORE THE NEXT WORD
664 001568* 000434 000000*      BR      CKDATA      ;IF ALL CHARACTERS RECEIVED, GO CHECK THEM
665 001570* 005742 000000*      TST     (R2)        ;RESET R2 TO THE PROPER BUFFER LOCATION
666 001572* 004767 000240 3$:  JSR      PC,SAVREG   ;SAVE THE REGISTER VALUES
667 001576* 104407 000000*      BREAKS,BEGIN        ;TEMPORARY RETURN TO MONITOR.
668 001602* 104407 000000*      BREAKS,BEGIN        ;THEN CONTINUE AT NEXT INSTRUCTION.
669 001606* 004767 000262      JSR      PC,GETREG     ;RESTORE THE REGISTER VALUES
670 001612* 005367 002226      DEC     RCVTHR       ;HAS ENOUGH TIME BEEN PERMITTED FOR A CHARACTER?
671 001616* 100357 000017      BPL     2$           ;TRY TO GET ONE.
672 001620* 010367 176260      MOV      R3,ASTAT    ;STATC WILL SHOW HOW MANY CHARACTERS
673 001624* 014067 176250      MOV      -(R0),CSRA  ;WERE NOT RECEIVED
674 001630* 013067 176246      MOV      #0,(R0)+,ACSR ;LOAD THE DEVICE ADDRESS
675 001634* 004767 000176      JSR      PC,SAVREG   ;LOAD THE CSR CONTENTS
676 001640* 012767 000017 176240      JSR      PC,SAVREG   ;SAVE THE REGISTERS
677 001642* 012767 000017      MOV      #17,ERRRTP  ;UNKNOWN RCVR ERROR
678 001646* 104405 000000*      HRDRS,BEGIN,NULL    ;DID NOT RECEIVE ALL 64. CHARACTERS=NO. MISSING
679 001654* 004767 000214      JSR      PC,GETREG
680
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689
690 001660* 012703 000010      CKDATA:  MOV      #R3         ;SET UP A COUNT FOR CLEARING THE BUFFER
691 001664* 012705 003770*      MOV      LNCKBF,(R5) ;POINT TO THE BEGINNING OF THE BUFFER
692 001670* 016725 002146 2$:  MOV      #GNDATA,(R5)+ ;SET UP TWO LINE CHECK BYTES
693 001674* 005303 000000*      DEC     R3          ;REDUCE THE COUNT. HAVE 16. BYTES BEEN CLEARED?
694 001676* 001374 000000*      BNE     3$           ;IF NO, GO CLEAR THE REMAINING
695 001700* 011002 000010 002130      MOV      (R0),R2     ;LOAD THE SOFTWARE SILO ADDRESS IN R2
696 001702* 016067 000100      MOV      #64,(R0)+,DVCNMBR ;LOAD THE NUMBER THIS DEVICE
697 001710* 012703 000100 3$:  MOV      (R2)+,R0    ;LOAD COUNT TO COMPARE 64. CHARACTERS
698 001714* 012200      MOV      (R2)+,R0    ;FETCH A WORD FROM THE SILO INTO R0
699 001716* 002024      BGE     2$           ;IF IT'S INVALID, GO CLEAN UP BUFFERS

```

THIS ROUTINE PERMITS ONLY ONE DEVICE TO HAVE ITS DATA CHECKED AT A TIME. PENDING
DEVICES RETURN TO THE MONITOR IN A WAIT LOOP. WHEN ACCESS IS PERMITTED, THE DATA IS CHE
FOR EACH LINE OF THE DEVICE. DATA ERRORS ARE REPORTED.

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700 001720* 032700 070000 BIT #70000,R0 ;ARE THERE ANY TRANSMISSION ERRORS?
701 001724* 001402 BEQ 4S ;IF NO, GO DETERMINE THE LINE NUMBER
702 001726* 004767 000304 JSR PC,STATERR ;IF YES, GO DETERMINE THE ERROR TYPE
703 001732* 110067 002112 MOV# R0,RCVDATA ;SAVE THE RECEIVED CHARACTER
704 001736* 000300 SWAB R0 ;PUT THE LINE NUMBER IN R0'S LOWER BYTE
705 001744* 126067 177760 BIC #177760,R0 ;ISOLATE THE LINE NUMBER IN R0
706 001752* 001402 CMP# R0,LNCKBF(R0),RCVDATA ;IS THE DATA CORRECT?
707 001754* 004767 BEQ 5S ;IF YES, GO CHECK THE NEXT CHARACTER
708 001760* 105260 JSR PC,ERRROR ;IF NO, GO REPORT A DATA ERROR
709 001764* 003770 INCB LNCKBF(R0) ;SET UP THE NEXT CHARACTER FOR THIS LINE
710 001766* 001352 DEC R3 ;REDUCE THE COUNT, ARE ALL 64 CHARACTERS CHECKED?
711 BNE 3S ;IF NO, GO CHECK THE NEXT CHARACTER.
712 ;THIS ROUTINE UNLOCKS THE LINE CHECK BUFFER TO PERMIT ACCESS BY OTHER DEVICES.
713 ;IT THEN COMPUTES THE LINE NUMBER AND CLEARS THE APPROPRIATE FLAG FROM THE WATCHDOG
714 ;TIMER BYTE.
715
716 001770* 105067 002055 RCVNONE:CLR# LCKOUT ;RESET THE BUFFER ACCESS FLAG
717 001774* 005000 R0 ;THE FLAG WILL PROPAGATE IN R0
718 001776* 000261 SEC ;USE THE CARRY BIT TO BUILD THE DELETION FLAG
719 002000* 006100 ROL R0 ;MOVE THE FLAG TO THE NEXT DEVICE
720 002002* 005367 002032 DEC DVCNMBR ;WHEN THIS NUMBER IS NEGATIVE, THE CORRECT
721 002006* 002374 BGE 1S ;DEVICE IS POINTED TO.
722 002010* 140067 002033 BIC# R0,DONFLG ;CLEAR THIS FLAG
723 002014* 104400 000000# EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
724
725 ;SUBROUTINES
726
727 ;THE FIRST IS THE VECTOR LOADING SUBROUTINE. THE VECTOR ADDRESS IS PASSED IN R0,
728 ;THE DEVICE ADDRESS IS PASSED IN R2. THE LINKING TABLE ADDRESS IS PASSED IN R3.
729 ;THE RUSS REQUEST PRIORITY LEVEL IS A PARAMETER TAKEN INDIRECTLY THROUGH R5.
730
731 002020* 010320 VCTLOAD:MOV R3,(R0)+ ;LOAD THE ADDRESS OF THE LINKING INSTRUCTION
732 002022* 113520 MOV# R0,(R5)+,(R0)+ ;AND THE PRIORITY LEVEL INTO THE VECTOR
733 002024* 005200 INC R0 ;POINT R0 TO THE NEXT VECTOR ADDRESS
734 002030* 010223 CMP# R3,(R3)+ ;POINT R3 TO THE CSR INSET LOCATION
735 002032* 005723 TST R3 ;LOAD THE DEVICE ADDRESS IN THE LINK TABLE
736 002034* 000205 RTS ;ALIGN R3 FOR THE NEXT DEVICE
737 ;RETURN TO CALLING BLOCK
738
739 ;THIS ROUTINE SAVES THE REGISTER VALUES IN THE ERROR QUEUE, A FIRST-IN,
740 ;FIRST-OUT DISCIPLINE WRAPAROUND BUFFER.
741
742 002036* 016705 001756 SAVREG:MOV ERRQPI,R5 ;USE R5 FOR INDEXING CAPABILITY
743 002042* 010025 R0,(R5)+ ;SAVE R0...
744 002044* 012125 MOV R1,(R5)+ ;SAVE R1...
745 002046* 010225 MOV R2,(R5)+ ;SAVE R2...
746 002050* 010325 MOV R3,(R5)+ ;SAVE R3...
747 002052* 010425 MOV R4,(R5)+ ;AND R4
748 002054* 022705 CMP# ERRQUE+60,R5 ;HAS THE QUEUE BOUNDARY BEEN EXCEEDED?
749 002056* 103003 BHS 1S ;IF NO, GO RELOAD THE POINTER
750 002062* 012705 002702# MOV# ERRQUE,R5 ;RESET R5 TO THE QUEUE BEGINNING
751 002066* 010567 001726 1S: MOV R5,ERRQPI ;SET THE ENTRY POINTER TO NEXT ENTRY POINT
752 002072* 000207 RTS ;RETURN TO THE CALLING BLOCK
753
754 ;THIS ROUTINE RETRIEVES REGISTER VALUES FROM THE ERROR QUEUE
755

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756 002074* 016705 001722 GETREG:MOV ERRQPI,R5 ;USE R5 FOR INDEXING CAPABILITY
757 002100* 012125 R0,(R5)+60 ;RETRIEVE R0...
758 002102* 012501 MOV R1,(R5)+,R1 ; R1...
759 002104* 012502 MOV R2,(R5)+,R2 ; R2...
760 002106* 012503 MOV R3,(R5)+,R3 ; R3...
761 002110* 012504 MOV R4,(R5)+,R4 ; AND R4
762 002112* 012504 CMP# ERRQUE+60,R5 ;HAS THE QUEUE BOUNDARY BEEN EXCEEDED?
763 002116* 103002 BHS 1S ;IF NO, GO RELOAD THE POINTER
764 002120* 012705 002702# MOV# ERRQUE,R5 ;RESET R5 TO THE QUEUE BEGINNING
765 002124* 010567 001672 1S: MOV R5,ERRQPI ;SET THE RETRIEVAL POINTER TO THE NEXT FETCH POINT.
766 002130* 000207 RTS ;RETURN TO THE CALLING BLOCK
767
768 ;THIS ROUTINE CALCULATES THE BAUD RATE FROM THE SRI OPTION. IF SRI IS 0,THE DEFAULT
769 ;RATES OF 9600 ARE ASSIGNED. THE LEAST SIGNIFICANT BIT IS THE ONLY ONE CONSIDERED.
770 ;THE BAUD RATES ARE THEN LOADED INTO THE PROPER LINE PARAMETER REGISTER.
771
772 002132* 005767 175660 BAUDRTE:TST SRI ;IS A SPECIFIC RATE SELECTED?
773 002136* 001433 BEQ 3S ;IF NO, GO ASSIGN THE DEFAULT RATE
774 002140* 016704 175652 MOV SRI,R4 ;IF YES COPY SRI
775 002144* 100003 BPL 4S ;BR IF NOT R4, WORDS BETWEEN VECTORS
776 002146* 042704 BIC #BIT15,R4 ;CLEAR VECTOR SELECT BIT
777 002152* 010425 BEQ 3S ;BR IF DEFAULT, USE 9600 BAUD
778 002154* 012703 000017 4S: MOV #17,R3 ;SET UP THE BIT CONFIGURATION FOR A BAUD RATE SELECTION
779 002160* 006004 ROR R4 ;ISOLATE THE NEXT BIT IN THE CARRY BIT
780 002162* 103402 BCS 2S ;IF THIS IS THE BIT PUT TOGETHER THE BAUD RATE
781 002164* 005303 DEC R3 ;IF NOT, CALCULATE THE NEXT BIT CONFIGURATION
782 002166* 000774 BR 1S ;GO CHECK THE NEXT ALTERNATIVE
783 002170* 000257 2S: CCC ;ELIMINATE EXTRANEOUS FLAGS BEFORE ROTATING
784 002172* 010305 MOV R3,R5 ;MAKE A DUPLICATE COPY OF THE BAUD RATE
785 002174* 006105 ROL R5 ;BUILD THE TRANSMITTER RATE IN R5
786 002176* 006105 ROL R5
787 002200* 006105 ROL R5
788 002202* 006105 ROL R5
789 002204* 006305 ADD R3,R5 ;ADD THE RECEIVER RATE TO THE TRANSMITTER RATE
790 002206* 000305 SWAB R5 ;ALIGN THE RATE TO BIT POSITIONS 6-13
791 002210* 006005 ROR R5
792 002212* 006005 ROR R5
793 002214* 052705 000003 BIS #3,R5
794 002220* 050561 000004 BIS #5,4(R1) ;PLACE THE BAUD RATES IN THE LINE PARAMETER REGISTER
795 002224* 000207 RTS ;RETURN TO THE CALLING PROCEDURE
796 002226* 052761 033503 000004 3S: BIS #33503,4(R1) ;SET THE DEFAULT RATE(9600, BAUD)
797 002234* 000207 RTS ;RETURN TO CALLING PROCEDURE
798
799 ;THIS ROUTINE DETERMINES WHAT TYPE OF ERROR WAS INDICATED BY THE SILO AND REPORTS EACH
800
801 002236* 004767 177574 STATERR:JSR PC,SAVREG ;SAVE THE REGISTERS
802 002242* 010167 175634 MOV R1,ACSR ;REPORT THE CSR CONTENTS
803 002246* 005004 CLR R4 ;SETUP TO CALCULATE
804 002250* 016703 001564 DVCNMBR,R3 ;THE CSR
805 002254* 001404 BEQ 2S ;DEVICE 0 JUST ADD ADDRESS
806 002256* 062704 1S: ADD #0,R4 ;POINT NEXT DH1
807 002262* 005303 DEC R3 ;DECREMENT DH NUMBER
808 002264* 001374 BNE 1S ;RIGHT DH1?
809 002266* 066704 175514 2S: ADD ADDR,R4 ;ADD BASE ADDRESS TO OFFSET
810 002272* 010467 175602 MOV R4,CSRA ;REPORT CSR ADDRESS
811 002276* 010001 MOV R0,R1 ;CALC LINE #

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812 002300 000301
813 002302 043701 177770 SWAR R1 ;GET CORRECT HALF
814 002306 010167 175572 BIC R1,77770,R1 ;LEAVE ONLY LINE #
815 002311 006100 MOV R1,ASTAT ;PUT IT IN A STAT
816 002314 006100 ROL R0 ;PLACE THE OVERRUN BIT IN THE CARRY BIT
817 002316 103006 ROL R0
818 002320 012767 BCC 35 ;IF NO ERROR, GO CHECK THE NEXT BIT
819 002326 104406 000021 175560 MOV #21,ERRTYP ;OVERRUN ERROR
820 002326 104406 000000 000000 S0FERS,BEGIN,NULL ;OVERRUN ERROR-LINE NUMBER IN STATC
821 002334 006100 3S: ROL R0 ;PLACE THE FRAME BIT IN THE CARRY BIT
822 002336 103006 RGC 45 ;IF NO ERROR, GO CHECK THE NEXT BIT
823 002340 012767 MOV #22,ERRTYP ;FRAMING ERROR
824 002346 104406 000000 000000 S0FERS,BEGIN,NULL ;FRAMING ERROR-LINE NUMBER IN STATC
825 002354 006100 4S: ROL R0 ;CHECK FOR A PARITY ERROR
826 002356 103006 BCC 55 ;IF NO ERROR, GO RESTORE THE REGISTERS
827 002360 012767 000023 175520 MOV #23,ERRTYP ;PARITY ERROR
828 002366 104406 000000 000000 S0FERS,BEGIN,NULL ;PARITY ERROR-LINE NUMBER IN STATC
829 002374 004767 177474 5S: JSR PC,GETREG ;RESTORE THE REGISTERS
830 002400 000207 RTS PC ;RETURN TO CALLING PROCEDURE
831
832 ;THIS ROUTINE CALCULATES THE BUFFER ADDRESSES OF THE INCORRECT DATA AND REPORTS THE ERRO
833
834 DERROR: JSR PC,SAVREG ;SAVE THE PRESENT REGISTER VALUES
835 CLR R4 ;/SETUP
836 MOV DVCNMR,R3 ;/TO CALCULATE
837 DEC #20,R4 ;/THE CSR
838 ADD #20,R4 ;/PUT NEXT DH11
839 DEC R3 ;/DECREMENT DH NUMBER
840 BNE 1S ;/RIGHT DH11 ?
841 ADD ADDR,R4 ;/ADD BASE ADDRESS TO OFFSET
842 MOV R4,CSR ;/REPORT CSR ADDRESS
843 MOV RCVR(R0),ASR ;LOAD THE CORRECT DATA VALUE
844 MOVB RCVRDATA,AMAS ;LOAD THE ACTUAL DATA VALUE
845 TST -(R2) ;GET THE SILO ADDRESS OF THE DATA
846 MOV R2,WASADR ;LOAD IT FOR REPORTING
847 ADD #NCKRF,R0 ;CALCULATE THE CORRECT VALUE ADDRESS
848 MOV R0,SBADR ;REPORT IT
849 *****
850 DATERS,BEGIN ;DATA ERROR!!!
851 *****
852 JSR PC,GETREG ;RESTORE THE REGISTERS
853 RTS PC ;RETURN TO THE CALLING PROCEDURE
854
855 ;LINKAGE TABLE
856 ;EACH ENTRY CONSISTS OF A JSR INSTRUCTION FOLLOWED BY A LOCATION INTO WHICH THE CSR IS
857 ;LOADED, FOLLOWED BY THE SILO BUFFER ADDRESS, FOLLOWED BY A JSR FOR THE TRANSMITTER
858 ;INTERRUPT, THE CSR ADDRESS WORD, AND THE NUMBER OF THE INTERRUPTING DEVICE
859
860 LNKTAB: JSR R5,RCVINT ;LINK FOR RECEIVER 0
861 0
862 SILO0
863
864
865 002502 004567 176712
866 002510 002770-
867

```

```

868 002512 004567 176402 JSR R5,XMTINT ;LINK FOR TRANSMITTER 0
869 002520 000000 0
870 002522 004567 176672 JSR R5,RCVINT ;LINK FOR RECEIVER 1
871 002526 000000 0
872 002530 003170- SILO1
873 002532 000000 JSR R5,XMTINT ;LINK FOR TRANSMITTER 1
874 002536 000000 0
875 002540 000001 1
876 002542 004567 176652 JSR R5,RCVINT ;LINK FOR RECEIVER 2
877 002546 000000 0
878 002548 003170- SILO2
879 002552 004567 176342 JSR R5,XMTINT ;LINK FOR TRANSMITTER 2
880 002556 000000 0
881 002560 000002 2
882 002562 004567 176632 JSR R5,RCVINT ;LINK FOR RECEIVER 3
883 002566 000000 0
884 002570 003570- SILO3
885 002572 004567 176322 JSR R5,XMTINT ;LINK FOR TRANSMITTER 3
886 002576 000000 0
887 002580 000003 3
888
889
890 002602 017000 CNTTBL: 17000 ;DIFFERENT ITERATION COUNTS FOR
891 002604 017000 17000 ;DIFFERENT BAUD RATES
892 002606 017000 17000
893 002610 017000 17000
894 002612 011700 11700
895 002614 006000 6000
896 002616 005100 5100
897 002620 003000 3000
898 002622 001600 1600
899 002624 000750 750
900 002626 000500 500
901 002630 000350 350
902 002632 000300 300
903 002634 000260 260
904 002636 000165 165
905 002640 000110 110
906
907 ;THESE ARE THE QUEUES.
908
909 002642 000010 XMTQUE: -BLKW 8. ;TRANSMITTER SERVICE QUEUE
910 002662 000010 RCVQUE: -BLKW 8. ;RECEIVER SERVICE QUEUE
911 002702 000031 ERRQUE: -BLKW 25. ;ERROR SERVICE QUEUE
912
913 ;THESE ARE THE BUFFERS
914
915 002764 000004 XMTBUF: -BLKB 4. ;FOUR CHARACTER TRANSMITTER BUFFER
916 002770 000100 SILO0: -BLKW 64. ;RECEIVER BUFFER FOR DEVICE 0
917 003170 000100 SILO1: -BLKW 64. ;RECEIVER BUFFER FOR DEVICE 1
918 003770 000100 SILO2: -BLKW 64. ;RECEIVER BUFFER FOR DEVICE 2
919 003770 000100 SILO3: -BLKW 64. ;RECEIVER BUFFER FOR DEVICE 3
920 003770 000020 LNCKBF: -BLKB 16. ;BUFFER FOR CHECKING INDIVIDUAL LINE DATA
921
922 ;THESE ARE THE QUEUE POINTERS
923

```


924 004010 000000
925 004012 000000
926 004014 000000
927 004016 000000
928 004020 000000
929 004022 000000
930
931
932
933 004024 000000
934 004026 000000
935 004030 000000
936 004032 000000
937 004034 000000
938 004036 000000
939 004040 000000
940 004042 000000
941 004044 000000
942 004046 000
943 004047 000
944 004050 000
945 004051 000
946 004052 000000
947
948
949
950
951 004054 004060
952 004056 177777
953
954 004060 042504 044526 042503
955 004066 040
956 004067 000006
957 004075 040 052510 043515
958 004102 044445 020124 040527
959 004108 020173 05104 050117
960 004116 042520 022504 000
961
962 000001

XMTQPI: OPEN ; TRANSMITTER QUEUE ENTRY (IN) POINTER
XMTQPD: OPEN ; TRANSMITTER QUEUE RETRIEVAL POINTER (OUT)
RCVQPI: OPEN ; RECEIVER QUEUE POINTERS
RCVQPD: OPEN
ERRQPI: OPEN ; ERROR QUE POINTERS
ERRQPD: OPEN
; THESE ARE THE PROGRAM PARAMETERS
SELECT: OPEN ; ACTIVE DEVICE SELECTION PARAMETER
COUNT: OPEN ; COUNTER FOR THE NUMBER OF ITERATIONS
TMCNT: OPEN ; COUNTER FOR WATCHDOG TIMER
VA: OPEN ; LOCATION FOR VIRTUAL ADDRESS(USED BY GETPA)
PA: OPEN ; ARGUMENT RETURNED BY GETPA(PHYSICAL ADDRESS)
EA: OPEN ; ARGUMENT RETURNED BY GETPA(EXTENDED ADDRESS BITS)
DVCNMR: OPEN ; NUMBER OF DEVICE BEING PROCESSED
RGNDATA: OPEN ; TWO COPIES OF THE FIRST CHARACTER IN TRANSMIT PATTERN
RCVTMR: 0 ; RECEIVER BREAK LOOP TIMER
DATA: -BYTE OPEN ; CHARACTER BUILDING BYTE
DONFLG: -BYTE OPEN ; WATCHDOG FLAG FOR BUSY DEVICES
RCVDATA: -BYTE OPEN ; BUFFER FOR CHARACTER CHECKING
LCKOUT: -BYTE OPEN ; BUFFER ACCESS FLAG
NUMBAL: OPEN
; EVEN
; THIS IS THE ASCII MESSAGE
HUNG: M1
-1
M1: -ASCII "DEVICE"
M2: -BLKB 6
M3: -ASCIZ " HUNG&IT WAS DROPPED&"
; EVEN
; FND

ACSR 000102R 346# 582*
ACTVAT 000504R 438#
ADDRB 000006R 312#
ADDR22= 001000 364#
ASB 000106R 350# 848*
ASTAT 000104R 348# 814*
AWAS 000110R 425# 849*
BAURRT 000107 453# 772#
BEGIN 000000R 309# 372#
859# 613#
RGNDAT 004042R 859# 470*
BIT0 = 000001 469#
BIT1 = 000002 364#
BIT10 = 002000 364# 578
BIT11 = 004000 364# 449
BIT12 = 010000 364#
BIT13 = 020000 364# 486
BIT14 = 040000 364#
BIT15 = 100000 364# 776
BIT2 = 000004 364#
BIT3 = 000010 364#
BIT4 = 000020 364#
BIT5 = 000040 364#
BIT6 = 000100 364# 618
BIT7 = 000200 364#
BIT8 = 000400 364#
BIT9 = 001000 364#
BREAKS= 104407 364# 502
BRI 000012R 314# 410
BR2 000013R 315# 407
RTODS = 104411 364#
CDATA= 104412 364#
CKDATA 001660R 667# 690#
CNTTBL 002602R 386# 890#
CONFIG 000056R 334#
COUNT 004026R 934#
CSR 000100R 344# 515*
DATA 004046R 367# 470*
DATCK= 104411 364# 473*
DATERS= 104404 364# 474*
DERROR 002402R 709# 839#
DONFLG 004047R 439# 512
DROP 000240R 371# 390
DVCNMR 004040R 696# 721*
DVTD1 000014R 316# 804
EA 004036R 453# 841
ENDIT= 104413 364# 939#
ENDS = 104410 364# 547
ERRQPI 004020R 364# 542
ERRQPD 004022R 426# 372
ERRQUE 002702R 425# 751*
ERRTYP 000106R 349# 742
EXITS = 104400 364# 756
FINISH 001110R 506# 748
GFTPAS= 194415 364# 765*
748
750
762
764
772
780
818*
830*
847*
848*
855
839#
504
390
721*
368
939#
547
372
742
756
427
533*
599
540
546#

601* 678* 802*
440 478 532 809 846
814*
443 502 503 528 535 537 542 547 590 594 599
617 644 648 649 670 671 682 724 820 826 832
692 940#
503 648 649 670 671
522* 532* 581* 600* 677* 810* 847*
470 473 474* 942#
512 723* 943#
804 841 939#
542 542* 928#
751* 929#
765* 750
748 750
762 764 911#
680* 818* 824* 830*
724

. ARS. 000000 000
004124 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

XDHALO, XDHALO/SQL/CRF:SYM=DDXCOM, XDHALO
RUN-TIME: 1 2 3 SECONDS
RUN-TIME RATIO: 24/5=4.8
CORE USED: 7K (13 PAGES)