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IDENTIFICATION

PRODUCT CODE: AC-E836E-MC
PRODUCT NAME: CXCBBF0 C811 DISTRIBUTE MOD
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

CB8 IS A BKMOD THAT EXERCISES UP TO
"NN CB11 DISTRIBUTOR MODULES HAVING CONTIGUOUS
UNIBUS ADDRESSES. THE MAXIMUM VALUE IF "NN"
IS THE SAME AS THE MAXIMUM VALUE OF DISTRIBUTORS
MODULES ALLOWED FOR A SINGLE CB11. NON-
CONTIGUOUS GROUPS OF DISTRIBUTOR MODULES MAY BE
EXERCISED BY CONFIGURING THE CB8 MODULE SIMPLY THE
FOR EACH GROUP. THE MODULE SIMPLY TESTS THE
ABILITY TO SET AND CLEAR ALL BITS IN ALL
AVAILABLE DISTRIBUTOR REGISTERS SELECTED FOR
TEST. IF ANY BIT FAILS TO SET OR CLEAR PROPERLY
THE ERROR IS REPORTED ON THE CONSOLE ITTY.

2. REQUIREMENTS:

HARDWARE: A CB11 INTERFACE WITH AT LEAST
ONE DISTRIBUTOR MODULE.

STORAGE:: CB8 REQUIRES:

1. DECIMAL WORDS: 126
2. OCTAL WORDS: 0176
3. OCTAL BYTES: 374

3. PASS DEFINITION

ONE PASS OF THE CB8 MODULE RESULTS IN
100. ITERATIONS OF THE BASIC TEST SEQUENCE
WHICH CLEARS AND SETS ALL DISTRIBUTOR
REGISTERS SELECTED FOR TEST

4. EXECUTION TIME

CB8 RUNNING ALONE ON A PDP 11/05 SYSTEM
WITH ONE DISTRIBUTOR MODULE SELECTED TAKES
APPROXIMATELY 10 SECONDS.

5. CONFIGURATION PARAMETERS

DEFAULT PARAMETERS:

DVADR: 0, VECTOR: 0, BR1: 0, BR2: 0, DVECNT: 1, SR1: 0

REQUIRED PARAMETERS:

FOR EACH COPY OF CBB CONFIGURED THE
USER MUST SPECIFY THE FOLLOWING PARAMETERS:

DEVADR: EQUAL TO THE FIRST ADDRESS IN A
CONTIGUOUS GROUP
SRI: NUMBER OF MODULES (SEE "OPERATION OPTIONS")

6. -----
DEVICE OPTION SETUP:

NONE REQUIRED

7. -----
MODULE OPERATION

TEST SEQUENCE.

- A. SET UP THE PASS COUNTER FOR 100 ITERATIONS
- B. GET THE CONTENTS OF SRI TO FIND OUT HOW MANY REGISTERS TO TEST
- C. CLEAR A DISTRIBUTE REGISTER
- D. COUNT IT
- E. REPORT ANY ERROR
- F. GENERATE NEXT ADDRESS
- G. REPEAT B-F UNTIL ALL REGISTERS TESTED FOR ALL ZEROS
- H. REPEAT B-G BUT SET ALL REGISTERS TO ALL ONES AND TEST
- I. COUNT ONE ITERATION
- J. IF NOT 100 REPEAT B-I
- K. REPEAT END PASS, RESTART AT A.

8. -----
OPERATION OPTIONS

- A. USER CAN MODIFY "ADDR" AND "SRI" TO SELECT ANY GROUP OF DISTRIBUTE MODULES

SRI:

THE NUMBER (OCTAL) OF DISTRIBUTE MODULES TO BE TESTED (MUST BE CONTIGUOUS). THIS NUMBER MUST BE A STRAIGHT OCTAL NUMBER, NOT A BIT MAP AS IS NORMALLY USED IN DEVCNT. XXIMUN DEVCNT WAS NOT USED BECAUSE IT CAN HOLD A MAXIMUM OF 16 DEVICES AND CBB CAN RUN UP TO 256 DEVICES. IF SRI IS LEFT AT ZERO OR OTHERWISE IMPROPERLY SET UP, THE SYSTEM WILL EVENTUALLY CRASH.

9. NON-STANDARD PRINTOUTS

NONE-ALL PRINTOUTS HAVE THE STANDARD DEC/X11 FORMATS.

JCR11 DISTRIBUTE MODULE - DEC/X11 EXERCISER MODULE

000000- BKMOD > CBBE 100-34
000000- MODULE 40020, CBBE 100-34
; TITLE CBBE DEC/X11 SYSTEM EXERCISER MODULE
; DDXCOM VERSION 6 23-OCT-78
***** BIN *****
000000- BEGIN:
000000- 041103 042502 040 MODNAM: - ASCII / CBBE / ;MODULE NAME
000005- 000 XPLAC: + BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE
000006- 000000 ADDR: +0 ;1ST DEVICE ADDR
000010- 000000 VECTOR: +0 ;1ST DEVICE VECTOR.
000012- 000 BR1: - BYTE PRTY+0 ;2ND BR LEVEL
000013- 000 BR2: - BYTE PRTY+0 ;1ST BR LEVEL
000014- 000001 DIVD1: +1 ;DEVICE INDICATOR 1.
000017- 000000 SR1: OPEN ;SWITCH REGISTER 1
000020- 000000 SR2: OPEN ;SWITCH REGISTER 2
000022- 000000 SR3: OPEN ;SWITCH REGISTER 3
000024- 000000 SR4: OPEN ;SWITCH REGISTER 4

000026- 040020 STAR: 4020 ;STATUS WORD
000030- 000224 INTR: START ;MODULE START ADDR
000032- 000224 SPOINT: MODSP ;MODULE STACK POINTER.
000034- 000000 PASCNT: 0 ;PASS COUNTER.
000036- 000144 ICOUNT: 00. ;# OF ITERATIONS PER PASS=100.
000042- 000000 SOFCNT: 0 ;LOC TO COUNT ITERATIONS
000044- 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000046- 000000 HRDPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000050- 000000 SYSCNT: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000052- 000000 RANNUM: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000056- 000000 CONFIC: 0 ;# OF SVS ERRORS ACCUMULATED
000058- 000000 RES1: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000060- 000000 RES2: 0 ;RESERVED FOR MONITOR USE
000062- 000000 SVR0: OPEN ;RESERVED FOR MONITOR USE
000064- 000000 SVR1: OPEN ;LOC TO SAVE R0.
000066- 000000 SVR2: OPEN ;LOC TO SAVE R1.
000070- 000000 SVR3: OPEN ;LOC TO SAVE R2.
000072- 000000 SVR4: OPEN ;LOC TO SAVE R3.
000074- 000000 SVR5: OPEN ;LOC TO SAVE R4.
000076- 000000 SVR6: OPEN ;LOC TO SAVE R5.
000100- 000000 CSRA: OPEN ;LOC TO SAVE R6.
000102- 000000 SBADR: OPEN ;ADDR OF GOOD DATA, OR
000104- 000000 WASADR: OPEN ;ADDR OF BAD DATA, OR
000106- 000000 ASTAT: OPEN ;CONTENTS OF CSR.
000110- 000000 ASRTYP: OPEN ;ADDR OF BAD DATA, OR
000112- 000232 ASRTYP: OPEN ;STATUS REG CONTENTS.
000114- 000000 AWAS: OPEN ;TYPE OF ERROR
000116- 000000 RSTRT: RSTRT ;EXPECTED DATA.
000118- 000000 WDRTO: OPEN ;RSTRT ADDRESS AFTER END OF PASS
000120- 000000 WDRF: OPEN ;WORDS TO MEMORY PER ITERATION
000122- 000000 INTR: OPEN ;WORDS FROM MEMORY PER ITERATION
; # OF INTERRUPTS PER ITERATION

000122- 000034 IDNUM: 34 ;MODULE IDENTIFICATION NUMBER=34
000040 ;MODULE STACK STARTS HERE.
;REPT SPSIZ
;WLIST
;WORD 0
;LIST
;ENDR
000224- MODSP:

223 000224- 012767 000002 177664 START: MOV #2, WDR ;2 WORDS FROM MEM/ITERATION
224 000232- 018709 177550 RSTRT: MOV ADDR, R0 ;GET THE 1ST REG. ADDRESS
225 000232- 018709 177554 MOV SR1, R2 ;GET THE DISTR. MODULE COUNT
226 000244- 006302 1S: MOV (R0) ;MAKE IT A REG. COUNT
227 000244- 005010 ASL R2 ;CLEAR THE REGISTER
228 000248- 001923 DEC R2 ;RR IF IT DIDN'T CLEAR ALL BITS
229 000250- 001303 BNE CS ;COUNT ONE REG.
230 000252- 001402 4S: BRQ CS ;CLEAR ONE REG.
231 000254- 005729 TST (R0)+ ;RR IF ALL REGS CLEARED
232 000256- 007700 BR IS ;GENERATE NXT ADDRESS
233 000264- 016702 177522 3S: MOV ADDR, R0 ;GO CLEAR THE NEXT ONE
234 000270- 006302 MOV SR1, R2 ;GET THE 1ST REG. ADDRESS
235 000274- 002110 ASL R2 ;GET THE DISTR. MODULE COUNT
236 000274- 002110 8S: MOV (R0) ;MAKE IT A REG. COUNT
237 000300- 001022 177777 CMP #1, 177777, (R0) ;SET ALL ONES
238 000302- 005302 7S: BNE CS ;DID ALL BITS SET ?
239 000306- 001402 DEC R2 ;RR IF NOT
240 000310- 000770 BRQ CS ;COUNT ONE REG.
241 000312- 000770 TST (R0)+ ;RR IF ALL REGS SET
242 000314- 000770 BR CS ;GENERATE THE NXT ADDRESS
243 000316- 104413 000000- 2S: ;GO DO ANOTHER ONE
244 000316- 000745 ENDTIS, BEGIN ;SIGNAL END OF ITERATION.
245 000320- 010067 ;MONITOR SHALL TEST END OF PASS
246 000320- 177554 BR RSTRT ;RR TIL 100. TIMES THROUGH
247 000324- 177552 MOV (R0), ACSR ;SAVE THE ADDRESS OF THE REG.
248 000330- 000025 177550 MOV #25, ERRTP ;SAVE THE CONTENTS
249 000336- 104405 000000- ;BIT STUCK IN REG.
250 000344- 000741 ;*****
251 000346- 010067 177526 6S: BR 4S ;FAILED TO CLEAR ALL BITS
252 000352- 011067 177524 MOV (R0), ACSR ;GO TRY THE NEXT GUY
253 000356- 012767 000025 177522 MOV #25, ERRTP ;SAVE THE ADDRESS OF THE REG
254 000364- 104405 000000- 000000 MOV #25, ERRTP ;SAVE THE CONTENTS
255 000372- 000743 ;*****
256 000372- 000743 ;BIT STUCK IN REG.
257 000372- 000743 ;*****
258 000372- 000743 ;*****
259 000372- 000743 ;*****
260 000372- 000743 ;*****
261 000372- 000743 ;*****
262 000372- 000743 ;*****
263 000372- 000743 ;*****
264 000001 ;*****
;END

CBRE DEC/X11 SYSTEM EXERCISER MODULE	MACY11 30A(1052)	12-OCT-78	16:23	PAGE 8
XCBRE0.P11 12-OCT-78 11:54	CROSS REFERENCE TABLE -- USER SYMBOLS			
ACSR = 000102R	205#	249*	256*	
ADDR = 000006R	213#	225	234	
ADDR22= 001000	223#			
ASB = 000106R	209#			
ASBAT = 000104R	210#			
ANAS = 000110R	210#			
BECTN = 000000R	168#	245	252	259
BIT0 = 000001	223#			
BIT1 = 000002	223#			
BIT10 = 02000	223#			
BIT11 = 004000	223#			
BIT12 = 010000	223#			
BIT13 = 000000	223#			
BIT14 = 040000	223#			
BIT15 = 100000	223#			
BIT2 = 000004	223#			
BIT3 = 000010	223#			
BIT4 = 000020	223#			
BIT5 = 000040	223#			
BIT6 = 000100	223#			
BIT7 = 000200	223#			
BIT8 = 000400	223#			
BIT9 = 001000	223#			
BREAKS = 104407	223#			
BRA = 000102R	174#			
BR = 000013R	174#			
BTODS = 104421	223#			
CDATAS = 104412	223#			
CONFIG = 000010R	223#			
CSRA = 000100R	203#	248*	255*	
DATCKS = 104411	223#			
DATERS = 104404	223#			
DVIDL = 000010R	223#			
ENDI = 00013	223#	245		
ENDS = 104410	223#			
ERRTYP = 000106R	208#	250*	257*	
EXITS = 104400	223#			
GETPAS = 104415	223#			
GWBUFS = 104414	223#			
HRDCNT = 000044R	186#	252	259	
HRDRS = 10446	223#			
HRDPAS = 000050R	186#			
ICONT = 000036R	185#			
ICOUNT = 000040R	186#			
IDNUM = 000122R	215#			
INTR = 000120R	214#			
MAP22S = 104416	223#			
MODM4 = 000000R	169#			
MODSR = 00024R	183#	221#		
MSCNS = 104403	223#			
MSGSS = 104402	223#			
MSGS = 104401	223#			
NULL = 000000	170#	252	259	
OPEN = 000000	205	207	209	178 179 196 197 198 199 200 201 202 203

CBRE DEC/X11 SYSTEM EXERCISER MODULE	MACY11 30A(1052)	12-OCT-78	16:23	PAGE 9
XCBRE0.P11 12-OCT-78 11:54	CROSS REFERENCE TABLE -- USER SYMBOLS			
OTQAS = 104420	223#			
PASCNT = 000034R	184#			
PIRGS = 000004	223#			
POPSP = 002426	223#			
POPSP2 = 002426	223#	174	223#	
PRTV = 000000	173#			
PRTV0 = 000000	223#			
PRTV1 = 000040	223#			
PRTV2 = 000100	223#			
PRTV3 = 000140	223#			
PRTV4 = 000200	223#			
PRTV5 = 000240	223#			
PRTV6 = 000300	223#			
PRTV7 = 000340	223#			
PS = 177776	223#			
PSH = 177770	223#			
PUSH = 005740	223#			
PUSH2 = 024646	223#			
RAMDS = 104417	223#			
RAMNUM = 000054R	211#	225#	247	
RESTRT = 000232R	211#			
RES1 = 000056R	194#			
RES2 = 000060R	195#			
RSTRT = 000112R	211#			
SRADR = 000102R	204#			
SOPCNT = 000042R	187#			
SOPERS = 104406	223#			
SOPPAS = 000048R	183#			
SPINT = 000032R	183#			
SPSTZ = 000040	183#	216	235	
SR1 = 000016R	176#	226		
SR2 = 000020R	177#			
SR3 = 000022R	178#			
SR4 = 000024R	179#			
START = 000224R	182#	224#		
STAT = 000026R	181#			
SVRO = 000062R	196#			
SVR1 = 000064R	197#			
SVR2 = 000066R	198#			
SVR3 = 000070R	199#			
SVR4 = 000072R	200#			
SVRS = 000074R	201#			
SVRO = 000076R	202#			
SVSCNT = 000052R	191#			
TRPDF = 000022	191#			
VECTOR = 000010R	172#			
WASADR = 000104R	206#	224*		
WDFR = 000116R	213#			
WDTI = 000118R	213#			
XPLAG = 000005R	170#			
. ABS. 000000	000			
000374	001			

CBBE DEC/111 SYSTEM EXERCISER MODULE MACV11 30A(1052) 12-OCT-78 16:23 PAGE 10
XCBBEO.P11 12-OCT-78 11:54 CROSS REFERENCE TABLE -- USER SYMBOLS

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

SEQ 0008

XCBBEO, XCBBEO/SOL, CRF:SYM=DDXCON, XCBBEO
RUN-TIME: 11.2 SECONDS
RUN-TIME RATIO: 9/2=3.3
CORE USED: 7K (13 PAGES)