

1

.REM -

IDENTIFICATION

PRODUCT CODE: AC-E667J-MC
PRODUCT NAME: CXCPBJ0 EIS EXER MOD
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT CORPORATION. DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR ANY ERRORS THAT MAY APPEAR IN THIS MANUAL.

THE SOFTWARE DESCRIBED IN THIS DOCUMENT IS FURNISHED TO THE PURCHASER UNDER A LICENSE FOR USE ON A SINGLE COMPUTER SYSTEM AND CAN BE COPIED (WITH INCLUSION OF DIGITALS COPYRIGHT NOTICE) ONLY FOR USE IN SUCH SYSTEM, EXCEPT AS MAY OTHERWISE BE PROVIDED IN WRITING BY DIGITAL.

DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS SOFTWARE ON EQUIPMENT THAT IS NOT SUPPLIED BY DIGITAL.

COPYRIGHT (C) 1973,1978 DIGITAL EQUIPMENT CORPORATION

1. ABSTRACT:

CPB IS A BKMOD THAT EXERCISES THE EIS IN THE PDP11/45
AND THE PDP11/40.

2. REQUIREMENTS:

HARDWARE: ANY PDP11/45 OR PDP11/40 WITH EIS OPTION.
STORAGE: CPB REQUIRES:

1. DECIMAL WORDS: 471
2. OCTAL WORDS: 0727
3. OCTAL BYTES: 1656

3. PASS DEFINITION:

ONE PASS OF THE CPB MODULE CONSISTS OF EXECUTING EACH
INSTRUCTION 12500 TIMES.

4. EXECUTION TIME:

CPB RUNNING ALONE ON A PDP11/45 TAKES APPROXIMATELY
30 SECONDS.

5. CONFIGURATION REQUIREMENTS:

11/40 OR LSI WITHOUT EIS OPTION, SEE SECTION 8.

6. DEVICE/OPTION SETUP:

MAKE SURE EIS IS INSTALLED.

7. MODULE OPERATION:

- A. SETUP CYCLE COUNTER.
- B. TEST ALL EIS INSTRUCTIONS UNLESS SRI BIT#1 = 1.
- C. IF SRI BIT#1 = 1, CHECK ONLY KOR, SOB, MARK, SXT, AND RTT.
- D. IF NOT EOP, GO TO B. ELSE DO EOP AND GO TO A.

8. OPERATING OPTIONS:

SRI

BIT #0=0
BIT #0=1
BIT #2=0
BIT #2=1
I.F.
SRI=0
SRI=1
SRI=4
SRI=5

DO ALL EIS INSTRUCTIONS
11/40. CSI WITHOUT EIS (11V03)
DO ONLY KOR, SOB, MARK, SXT, AND RTT.
ACCESS PROCESSOR STATUS DIRECTLY.
USE MPPS INSTRUCTION.

ALL EIS, PSW DIRECT.
NO EIS, PSW DIRECT.
ALL EIS, USE MPPS.
NO EIS, USE MPPS.

9. NON-STANDARD PRINTOUTS:

NONE

```

000000 -          -          BKMOD <CPBJ > / 12500,2
000000 -          MODULF 40020,CPBJ / 12500,2
          ;          TITLE CPBJ DEC/X11 SYSTEM EXERCISER MODULE
          ;          DDSCOM VERSION 6 23-NOV-78
          ;          .LIST BIN
          ;*****
000000 -          BEGIN:
000000 - 050103 045102 040 MODNAM: .ASCIT /CPBJ / ;MODULE NAME
000005 - 000          XFLAG: .RYTE OPEN          ;USPD TO KEEP TRACK OF WBUFF USAGE
000010 - 000000          ADDR: +0          ;1ST DEVICE ADDR.
000012 - 000          VECTOR: +0          ;1ST DEVICE VECTOR.
000013 - 000          BR1: .RYTE PRTY+0          ;1ST BR LEVEL.
000014 - 000001          BR2: .RYTE PRTY+0          ;2ND BR LEVEL.
000015 - 000000          DRD1: +1          ;DEVICE INDICATOR 1.
000016 - 000000          SR1: OPEN          ;SWITCH REGISTER 1
000017 - 000000          SR2: OPEN          ;SWITCH REGISTER 2
000018 - 000000          SR3: OPEN          ;SWITCH REGISTER 3
000019 - 000000          SR4: OPEN          ;SWITCH REGISTER 4
          ;*****
000026 - 040020          STAT: 40020          ;STATUS WORD.
000030 - 000224          IMIT: START          ;MODULE START ADDR.
000032 - 000224          SPOINT: MODSP          ;MODULE STACK POINTER.
000034 - 000000          PASCNT: 0          ;PASS COUNTER.
000036 - 012500          ICOUNT: 12500          ;# OF ITERATIONS PER PASS=12500
000040 - 000000          ICOUNT: 0          ;LOC TO COUNT ITERATIONS
000042 - 000000          SOFCNT: 0          ;LOC TO SAVE TOTAL SOFT ERRORS
000044 - 000000          HRDCNT: 0          ;LOC TO SAVE TOTAL HARD ERRORS
000046 - 000000          SOPPAS: 0          ;LOC TO SAVE SOFT ERRORS PER PASS
000050 - 000000          HRDPAS: 0          ;LOC TO SAVE HARD ERRORS PER PASS
000052 - 000000          SYSCNT: 0          ;# OF SYS ERRORS ACCUMULATED
000054 - 000000          RANUM: 0          ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
          ;*****
000056 - 000000          CONFIG:
000058 - 000000          RES1: 0          ;RESERVED FOR MONITOR USE
000060 - 000000          RES2: 0          ;RESERVED FOR MONITOR USE
000062 - 000000          SVR0: OPEN          ;LOC TO SAVE R0.
000064 - 000000          SVR1: OPEN          ;LOC TO SAVE R1.
000066 - 000000          SVR2: OPEN          ;LOC TO SAVE R2.
000068 - 000000          SVR3: OPEN          ;LOC TO SAVE R3.
000070 - 000000          SVR4: OPEN          ;LOC TO SAVE R4.
000072 - 000000          SVR5: OPEN          ;LOC TO SAVE R5.
000074 - 000000          SVR6: OPEN          ;LOC TO SAVE R6.
000076 - 000000          CSRA: OPEN          ;ADDR OF CURRENT CSR.
000100 - 000000          SRA: OPEN          ;ADDR OF GOOD DATA, OP
000102 - 000000          ACSR: OPEN          ;CONTENTS OF CSR.
000104 - 000000          WASADR: OPEN          ;ADDR OF RAD DATA, OR
000106 - 000000          ASTAT: OPEN          ;STATUS REG CONTENTS.
000108 - 000000          ERRTYP: OPEN          ;TYPE OF ERROR
000110 - 000000          ASB: OPEN          ;EXPECTED DATA.
000112 - 000224          AWAS: OPEN          ;ACTUAL DATA.
000114 - 000000          RSTRT: RESTRT          ;RESTART ADDRFS AFTER END OF PASS
000116 - 000000          WDT0: OPEN          ;WORDS TO MEMORY PER ITERATION
000118 - 000000          WDFR: OPEN          ;WORDS FROM MEMORY PER ITERATION
000120 - 000000          INTR: OPEN          ;# OF INTERRUPTS PER ITERATION
000122 - 000002          IDNUM: 2          ;MODULE IDENTIFICATION NUMBER=2
          ;*****
          .REPT SPSIZ          ;MODULE STACK STARTS HERE.
  
```

```

          .NLIST
          .WORD 0
          .LIST
          .ENDR
000224 -          MODSP:
          ;*****
  
```

```

173 000224-
174 000224-
175 000224-
176 000224-
177 000224- 104413 000000-
178
179
180 000230- 012702 000010
181 000234- 012701 052525
182 000240- 012767 177777 001342
183 000246- 074162 001600-
184 000252- 022767 125252 001330
185 000254- 001403
186 000262-
187
188
189 000262- 104405 000000- 000000
190
191
192 000
193
194
195
196
197 000270- 012703 125252
198 000274- 012704 000010
199 000300- 012767 001624- 001330
200 000306- 012767 177777 001310
201 000342- 074378 001626-
202 000330- 022767 052525 001276
203 000326- 001403
204 000330-
205
206
207 000330- 104405 000000- 000000
208
209
210 000
211
212
213
214
215 000336- 000401
216 000340- 000406
217 000342- 012701 000010
218 000346- 077104
219 000350-
220
221
222 000350- 104405 000000- 000000
223
224
225 000
226
227
228

```

```

START:
RFSTRT:
ENDIT
ENDITS,BEGIN ;SIGNAL END OF ITERATION.
;MONITOR SHALL TEST END OF PASS

XOR1: MOV #10,R2 ;SET UP INDEX REGISTER
MOV #52525,R1 ;LOAD SOURCE OPERAND
MOV #-1,TEMP ;LOAD DESTINATION OPERAND
XOR R1,C(R2) ;XOR SOURCE,DFST.
CMP #125252,TEMP
BEQ IS
HRDR ***** <XOR INSTRUCTION FAILED> *****
-IF B <>
HRDRS,BEGIN,NULL ;XOR INSTRUCTION FAILED
-IF
HRDRS,BEGIN, ;XOR INSTRUCTION FAILED
.ENDC
*****

;SBTTL TEST XOR USING INDEX DEFERRFD
IS: MOV #125252,R3
MOV #10,R4
MOV #Y,X+10
MOV #-1,Y
XOR R3,8X(R4)
CMP #52525,Y
BEQ ZS
HRDR ***** <XOR -1,125252 FAILED> *****
-IF B <>
HRDRS,BEGIN,NULL ;XOR -1,125252 FAILED
-IF
HRDRS,BEGIN, ;XOR -1,125252 FAILED
.ENDC
*****

;SBTTL TEST SOB FOR BRANCH
ZS: BR SOB1
SOB2: BR SOB3
SOB1: MOV #10,R1
SOB R1,SOR2
HRDR ***** <SOB SHOULD HAVE BRANCHED> *****
-IF B <>
HRDRS,BEGIN,NULL ;SOB SHOULD HAVE BRANCHED
-IF
HRDRS,BEGIN, ;SOB SHOULD HAVE BRANCHED
.ENDC
*****

;SBTTL TEST SOB FOR NO BRANCH

```

```

229
230 000356- 000404
231 000360-
232
233 000360- 104405 000000- 000000
234
235
236 000
237
238
239 000366- 000403
240 000370- 012701 000001
241 000374- 077107
242
243
244
245
246 000376- 016706 177430
247 000402- 005001
248 000404- 012746 000007
249 000410- 012746 000001
250 000414- 012746 006401
251 000420- 010605
252 000422- 004767 000002
253 000426- 000403
254 000430- 016501 000002
255 000434- 000205
256 000436- 022701 000001
257 000442- 001403
258 000444-
259
260
261 000444- 104405 000000- 000000
262
263
264 000
265
266 000452- 026706 177354
267 000456- 001403
268 000460-
269
270
271 000460- 104405 000000- 000000
272
273
274 000
275
276 000466- 022705 000007
277 000472- 001403
278 000474-
279
280
281 000474- 104405 000000- 000000
282
283
284 000

```

```

SOB3: BR SOB4
SOB5:
;*****
-IF B <>
HRDRS,BEGIN,NULL ;SOB SHOULD NOT HAVE BRANCHED
-IF
HRDRS,BEGIN, ;SOB SHOULD NOT HAVE BRANCHED
.ENDC
*****

SOB4: MOV BR MA
SOB #1,R1
SOB R1,SORS

;SBTTL TEST MARK INSTRUCTION
MA: MOV SPOINT,SP ;INITIALIZE STACK POINTER
CLR R1
MOV #1,-(SP)
MOV #1,-(SP) ;PUSH A PARAMETER ON THE STACK
MOV #MARK1,-(SP) ;PUSH MARK 1 ON THE STACK
MOV SP,R5 ;LOAD PARAMETER POINTER
JSR PC,MARK0 ;GO TO SUBROUTINE
BR MARK0A
MARK0: MOV 2(R5),R1 ;GET THE PARAMETER
RTS R5 ;EXIT SUBROUTINE
MARK0A: CMP #1,R1 ;DID SUBROUTINE GET THE PARAMETER
BEQ IS
HRDR ***** <SUBROUTINE DID NOT GET PUSHED PARAMETER> *****
-IF B <>
HRDRS,BEGIN,NULL ;SUBROUTINE DID NOT GET PUSHED PARAMETER
-IF
HRDRS,BEGIN, ;SUBROUTINE DID NOT GET PUSHED PARAMETER
.ENDC
*****

IS: CMP SPOINT,SP ;IS STACK POINTER CORRECT?
BEQ ZS
HRDR ***** <STACK POINTER INCORRECT FOR MARK> *****
-IF B <>
HRDRS,BEGIN,NULL ;STACK POINTER INCORRECT FOR MARK
-IF
HRDRS,BEGIN, ;STACK POINTER INCORRECT FOR MARK
.ENDC
*****

ZS: CMP #7,R5 ;IS R5 CORRECT?
BEQ ZS
HRDR ***** <MARK DID NOT LOAD R5> *****
-IF B <>
HRDRS,BEGIN,NULL ;MARK DID NOT LOAD R5
-IF
HRDRS,BEGIN, ;MARK DID NOT LOAD R5
.ENDC

```

285
286
287
288
289 000502 012702 000010 001074
290 000506 012767 177777
291 000514 000257
292 000516 006762 001600
293 000522 005767 001062
294 000526 001403
295 000530
296
297
298 000530 104405 000000 000000
299
300
301 000
302
303
304 000536 005067 001046
305 000542 000277
306 000544 006767 001040 001032
307 000550 022767 177777
308 000556 001403
309 000560
310
311 000
312 000560 104405 000000 000000
313
314
315 000
316
317
318
319
320 000566 016706 177240
321 000572 032767 000004 177216
322 000600 001403
323 000602 000257
324 000604 106746
325 000606 000493
326 000610 000257
327 000612 016746 177776
328 000616 012746 000642
329 000622 000277
330 000624 000006
331 000626
332
333 000
334 000626 104405 000000 000000
335
336 000
337
338
339 000634 005726
340 000636 005726

```

;*****
;SBTTL TEST THE SXT INSTRUCTION
3S:  MOV    #10,R2          ;SET UP INDFX REGISTER
      MOV    #-1,TEMP      ;CLEAR ALL CONDITION CODES
      CCR    C(R2)         ;EXTEND 0'S INTO TEMP [C(R2)]
      TST   TEMP
      BRQ   4S
      HRDPR <0'S DOID NOT EXTEND INTO TEMP>
      .IF B <>
      HRDPR,BEGIN,NULL    ;0'S DOID NOT EXTEND INTO TEMP
      .IFF
      HRDPR,BEGIN,      ;0'S DOID NOT EXTEND INTO TEMP
      .ENDC
;*****
4S:  CLR    TEMP
      SCC   #1,TEMP        ;SET ALL CONDITION CODES
      SXT   TEMP           ;EXTEND 1'S INTO TEMP
      CMP   #-1,TEMP
      BRQ   5S
      HRDPR <-1 DID NOT EXTEND INTO TEMP>
      .IF B <>
      HRDPR,BEGIN,NULL    ;-1 DID NOT EXTEND INTO TEMP
      .IFF
      HRDPR,BEGIN,      ;-1 DID NOT EXTEND INTO TEMP
      .ENDC
;*****
;SBTTL TEST THE RTT INSTRUCTION
5S:  MOV    SPOINT,SP      ;RESET THE STACK POINTER
      BIT   #4,SRI        ;ACCESS P5W DIRFCT?
      BEQ   6S
      BEQ   YES
      CCC   #1,TEMP       ;CLEAR STATUS
      MPPS  -(SP)         ;PUSH ON STACK FOR RTT
      BPT   7S            ;SET UP PC FOR RTT
      CCR   C(R2)         ;CLR COND CODES
      MOV   PS,-(SP)      ;PUSH IT ON STACK AS PS FOR RTT
      MOV   #RTTA,-(SP)   ;PUSH RETURN PC ONTO STACK
      SCC   #1,TEMP       ;INSURE N=Z=V=C=1 BEFORE RTT
      RTT
      HRDPR <RTT FAILED>
      .IF B <>
      HRDPR,BEGIN,NULL    ;RTT FAILED
      .IFF
      HRDPR,BEGIN,      ;RTT FAILED
      .ENDC
;*****
      TST   (SP)+         ;POP STACK
      TST   (SP)+         ;POP STACK

```

341 000640 000407
342 000642
343 000642 100403
344 000644 101402
345 000646 102401
346 000650 000403
347 000652
348 000652
349
350 000
351 000652 104405 000000 000000
352
353 000
354
355
356 000660 032767 000001 177130
357 000666 001402 177330
358 000670 000167
359
360
361 000674 005003
362 000676 012702 000005
363 000702 005067 000746
364 000706 012767 000002 000740
365 000714 000277
366 000716 070267 000732
367 000722 100402
368 000724 101401
369 000726 102003
370 000730
371 000730
372
373 000
374 000730 104405 000000 000000
375
376 000
377
378
379 000736 022703 000012
380 000742 001403
381 000744
382
383 000
384 000744 104405 000000 000000
385
386 000
387
388
389 000752 005702
390 000754 001403
391 000756
392
393 000
394 000756 104405 000000 000000
395
396

```

RTTA: BR    RTTR          ;GO ON WITH TEST
      BMI   10S           ;IF N SET,REPORT ERROR
      BLOS  10S           ;IF C OR Z SET,REPORT ERROR
      BVS   10S           ;IF V SET,REPORT ERROR
      BR    RTTR          ;IF YES GO ON WITH TEST
10S:  HRDPR <INCORRECT STATUS>
      .IF B <>
      HRDPR,BEGIN,NULL    ;INCORRECT STATUS
      .IFF
      HRDPR,BEGIN,      ;INCORRECT STATUS
      .ENDC
;*****
RTTB: BIT   #1,SRI
      BEQ   MUL1
      JMP   START
MUL1: CLR   R3
      MOV   #5,R2         ;LOAD MULTIPLICAND
      CLR   PLIER
      MOV   #2,PLIER     ;LOAD MULTIPLIER
      SCC   #1,TEMP       ;PRESET CONDITION CODES
      MUL   PLIER,R2     ;MULTIPLY 2X5 RESULT IN R2 (MSH);R3 (LSH)
      BMI   10S           ;IF N SET,REPORT ERROR
      BLOS  10S           ;IF C OR Z SET,REPORT ERROR
      BVC   1S           ;IF V = 0,GO ON WITH TEST
10S:  HRDPR <CC'S NOT = TO 0'S>
      .IF R <>
      HRDPR,BEGIN,NULL    ;CC'S NOT = TO 0'S
      .IFF
      HRDPR,BEGIN,      ;CC'S NOT = TO 0'S
      .ENDC
;*****
1S:  CMP   #12,R3         ;RESULT =12?
      BEQ   2S           ;BRANCH IF THE RESULT (LSH) IS CORRECT
      HRDPR <INCORRECT RESULT (LSH)>
      .IF B <>
      HRDPR,BEGIN,NULL    ;INCORRECT RESULT (LSH)
      .IFF
      HRDPR,BEGIN,      ;INCORRECT RESULT (LSH)
      .ENDC
;*****
2S:  TST   R2
      BEQ   MUL2
      HRDPR <INCORRECT RESULT (MSH)>
      .IF B <>
      HRDPR,BEGIN,NULL    ;INCORRECT RESULT (MSH)
      .IFF
      HRDPR,BEGIN,      ;INCORRECT RESULT (MSH)

```

```

397          000          ;FNDC
398          ;*****
399
400
401 000764- 005003      MUL2: CLR      R3
402 000765- 005067      CLR      PLIER
403 000770- 012702      MOV      #125252,R2 ;LOAD MULTIPICAND
404 000772- 012767      MOV      #2,PLIER
405 001004- 070267      MUL      PLIER,R2 ;MULTIPLY 2X125252 RESULT=-1(R2)
406                                     ;52524(R3),N=1,Z=0,V=0,C=1
407 001010- 103003      JCC     10$ ;IF C = 0,REPORT ERROR
408 001012- 100002      BPL    10$ ;IF N = 0,REPORT ERROR
409 001014- 102401      BVS    10$ ;IF V SET,REPORT ERROR
410 001016- 001003      BNE    1$ ;IF Z = 0,GO ON WITH TEST
411 001020-
412 001020- 10$: HRDR , <INCORRECT CONDITION CODES>
413 ;*****
414 ;IF B <>
415 001020- 104405 000000- 000000 HRDRS,BEGIN,NULL ;INCORRECT CONDITON CODES
416 ;IF
417 HRDRS,BEGIN, ;INCORRECT CONDITION CODES
418 ;FNDC
419 ;*****
420 001025- 022702 177777 1$: CMP     #-1,R? ;CORRECT MSH RESULT?
421 001032- 001403      BQO     2$
422 001034-
423 HRDR , <INCORRECT MSH RESULT>
424 ;*****
425 001034- 104405 000000- 000000 ;IF B <>
426 HRDRS,BEGIN,NULL ;INCORRECT MSH RESULT
427 ;IF
428 HRDRS,BEGIN, ;INCORRECT MSH RESULT
429 ;FNDC
430 ;*****
431 001042- 022703 052524 2$: CMP     #52524,R3 ;CORRECT LSH RESULT?
432 001046- 001403      BQO     ASH1
433 001050-
434 HRDR , <INCORRECT LSH RESULT>
435 ;*****
436 001050- 104405 000000- 000000 ;IF B <>
437 HRDRS,BEGIN,NULL ;INCORRECT LSH RESULT
438 ;IF
439 HRDRS,BEGIN, ;INCORRECT LSH RESULT
440 ;FNDC
441 ;*****
442 ;SBTTL TEST ASH INSTRUCTION
443 ASH1: CLR     PLIER
444 001062- 005067 000572 ;LOAD SHIFT VALUE (+1 OR 1 PLACE LEFT)
445 001062- 012767 000001 000564 ;GET VALUE TO BE SHIFTED (#125252)
446 001070- 012702 125252 ;PRE-CLEAR ALL CC BITS
447 001074- 000257      CCC
448 001076- 000264      SPZ
449 001100- 000270      SEN
450 001102- 072267 000546 ASH     PLIER,R2 ;PRE-SET N BIT
451                                     ;SHIFT 1 PLACE LEFT RESULT = 52524
452 001106- 100403      BMI    10$ ;IF N SET,REPORT ERROR
453 001110- 001402      BQI    10$ ;IF Z SET,REPORT ERROR

```

```

453 001112- 102001      10$: BVC     10$ ;IF V = 0,REPORT ERROR
454 001114- 103403      BCS     1$ ;IF C SET,GO ON WITH TEST
455 001116-
456 001116- HRDR , <INCORRECT CONDITION CODES>
457 ;*****
458 ;IF B <>
459 001116- 104405 000000- 000000 HRDRS,BEGIN,NULL ;INCORRECT CONDITION CODES
460 ;IF
461 HRDRS,BEGIN, ;INCORRECT CONDITION CODES
462 ;FNDC
463 ;*****
464 001124- 022702 052524 1$: CMP     #52524,R2 ;RESULT CORRECT?
465 001130- 001403      BQO     ASH2
466 001132-
467 HRDR , <INCORRECT RESULT>
468 ;*****
469 001132- 104405 000000- 000000 ;IF B <>
470 HRDRS,BEGIN,NULL ;INCORRECT RESULT
471 ;IF
472 HRDRS,BEGIN, ;INCORRECT RESULT
473 ;FNDC
474 ;*****
475 001140- 005067 000540 ASH2: CLR     PLIER
476 001144- 012767 177777 ;LOAD SHIFT VALUE (-1 OR 1 PLACE RIGHT)
477 001152- 012701 052525 ;GET VALUE TO BE SHIFTED (#52525)
478 001156- 072167 000472 ;SHIFT 1 PLACE RIGHT RESULT = #25252
479 001162- 020127 025252 ;RESULT CORRECT?
480 001166- 001403      BQO     ASHC1
481 001170-
482 HRDR , <INCORRECT RESULT>
483 ;*****
484 001170- 104405 000000- 000000 ;IF B <>
485 HRDRS,BEGIN,NULL ;INCORRECT RESULT
486 ;IF
487 HRDRS,BEGIN, ;INCORRECT RESULT
488 ;FNDC
489 ;*****
490 ;SBTTL TEST ASHC INSTRUCTION
491 ASHC1: CLR     R? ;CLEAR MSH RESULT REGISTER
492 001176- 005002      CLR     PLIER
493 001200- 005067 000450 ;LOAD SHIFT COUNT (16 PLACES LEFT)
494 001204- 012767 000020 000442 ;GET VALUE TO BE SHIFTED (#125252)
495 001212- 012703 125252 ;PRE-CLEAR ALL CC BITS
496 001216- 000257      CCC
497 001220- 000264      SPZ
498 001222- 000261      SEC
499 001224- 073267 000424 ASHC   PLIER,R2 ;PRE-SET C BIT
500                                     ;SHIFT # IN R? TO R2
501 001230- 100003      BPL    10$ ;IF N = 0,REPORT ERROR
502 001232- 001402      BQO    10$ ;IF Z SET,REPORT ERROR
503 001234- 102001      BVC    10$ ;IF V = 0,REPORT ERROR
504 001238- 103003      BCC    1$ ;IF C = 0,GO ON WITH TEST
505 001240-
506 001240- 10$: HRDR , <INCORRECT CONDITION CODES>
507 ;*****
508 001          ;IF B <>

```

```
509 001240 104405 000000 000000 HDRS,REGIN,NULL ;INCORRECT CONDITION CODES
510 -IFF
511 HDRS,REGIN, ;INCORRECT CONDITION CODES
512 -FNDC
513 *****
514 001246 022702 125252 15: CMP #125252,R2 ;R2=#125252?
515 001252 001403 BEQ 2S ;BRANCH IF CORRECT RESULT
516 001254 HDRS,REGIN,NULL ;INCORRECT RESULT FOR ASHC LEFT SHIFT
517 *****
518 ;IF B <>
519 001254 104405 000000 000000 HDRS,REGIN,NULL ;INCORRECT RESULT FOR ASHC LEFT SHIFT
520 -IFF
521 HDRS,REGIN, ;INCORRECT RESULT FOR ASHC LEFT SHIFT
522 -FNDC
523 *****
524 001262 005703 25: TST R3 ;WAS # SHIFTED OUT OF R3?
525 001264 001403 BEQ ASHC2
526 001266 HDRS,REGIN,NULL ;# WAS NOT SHIFTED OUT OF R3 PROPERLY
527 *****
528 ;IF B <>
529 001266 104405 000000 000000 HDRS,REGIN,NULL ;# WAS NOT SHIFTED OUT OF R3 PROPERLY
530 -IFF
531 HDRS,REGIN, ;# WAS NOT SHIFTED OUT OF R3 PROPERLY
532 -FNDC
533 *****
534 ASHC2: CLR R3 ;CLEAR RESULT REGISTER
535 001276 005067 000352 CLR PLIER
536 001302 012767 177760 000344 MOV #16,PLIER ;LOAD SHIFT COUNT (16 PLACES RIGHT)
537 001310 012702 125252 MOV #125252,R2 ;LOAD # TO BE SHIFTED (#125252)
538 001314 000250 CLN ;PRE-CLEAR N BIT
539 001316 000264 STZ ;PRE-SET Z BIT
540 001320 000262 SEV ;" V "
541 001322 000261 SEC ;" C "
542 001324 073267 000324 ASHC PLIER,R2 ;SHIFT R2 16 PLACES RIGHT INTO R3
543 ;CC: N=1,Z=C=0
544 001330 100002 BPL 10S ;IF N = 0,REPORT ERROR
545 001332 101401 BLOS 10S ;IF C OR Z SET,REPORT ERROR
546 001334 102003 BVC 1S ;IF V = 0,GO ON WITH TEST
547 10$: HDRS,REGIN,NULL ;INCORRECT CONDITION CODES
548 *****
549 ;IF B <>
550 001336 104405 000000 000000 HDRS,REGIN,NULL ;INCORRECT CONDITION CODES
551 -IFF
552 HDRS,REGIN, ;INCORRECT CONDITION CODES
553 -FNDC
554 *****
555 001344 022702 177777 15: CMP #1,R2 ;DID SIGN EXTEND IN R2?
556 001350 001403 BEQ 2S
557 001352 HDRS,REGIN,NULL ;SIGN FAILED TO EXTEND
558 *****
559 ;IF B <>
560 001352 104405 000000 000000 HDRS,REGIN,NULL ;SIGN FAILED TO EXTEND
561 -IFF
562 HDRS,REGIN, ;SIGN FAILED TO EXTEND
563 -FNDC
564 *****
```

```
565 000000 -FNDC
566 *****
567 001360 022703 125252 25: CMP #125252,R3 ;DID R2 SHIFT TO R3
568 001366 001403 BEQ DIV1
569 HDRS,REGIN,NULL ;R2 DID NOT SHIFT INTO R3
570 *****
571 ;IF B <>
572 001366 104405 000000 000000 HDRS,REGIN,NULL ;R2 DID NOT SHIFT INTO R3
573 -IFF
574 HDRS,REGIN, ;R2 DID NOT SHIFT INTO R3
575 -FNDC
576 *****
577 ;SBTTL TEST THE DIVIDE INSTRUCTION
578
579 DIV1: MOV #4,R1 ;LOAD INDEX REGISTER
580 001374 012701 000004 CLR R2 ;CLEAR QUOTIENT REGISTER
581 001400 005002 MOV #52525,R3 ;LOAD LSH DIVIDEND
582 001402 012703 052525 DIV 8(R1),R2 ;PRE SET THE CONDITION CODES
583 001406 000277 001556 SEC ;DIVIDE #52525 BY 8(R1) (#52525)
584 001410 071261 BMI 10S ;QUOTIENT=1,REM=0,C=N=V=2=0
585 001414 100402 BLOS 10S ;IF N SET,REPORT ERROR
586 001416 101401 BLOS 10S ;IF C OR Z SET,REPORT ERROR
587 001420 102003 BVC 1S ;IF V = 0,GO ON WITH TEST
588 10$: HDRS,REGIN,NULL ;INCORRECT CONDITION CODES
589 *****
590 ;IF B <>
591 001422 104405 000000 000000 HDRS,REGIN,NULL ;INCORRECT CONDITION CODES
592 -IFF
593 HDRS,REGIN, ;INCORRECT CONDITION CODES
594 -FNDC
595 *****
596 001430 020227 000001 15: CMP R2,#1 ;QUOTIENT CORRECT?
597 001434 001403 BEQ 2S ;BRANCH IF THE QUOTIENT IS CORRECT
598 001436 HDRS,REGIN,NULL ;INCORRECT QUOTIENT
599 *****
600 ;IF B <>
601 001436 104405 000000 000000 HDRS,REGIN,NULL ;INCORRECT QUOTIENT
602 -IFF
603 HDRS,REGIN, ;INCORRECT QUOTIENT
604 -FNDC
605 *****
606 001444 005703 25: TST R3 ;REMAINDER CORRECT?
607 001446 001403 BEQ DIV2 ;BRANCH IF THE REMAINDER IS CORRECT
608 001450 HDRS,REGIN,NULL ;INCORRECT REMAINDER
609 *****
610 ;IF B <>
611 001450 104405 000000 000000 HDRS,REGIN,NULL ;INCORRECT REMAINDER
612 -IFF
613 HDRS,REGIN, ;INCORRECT REMAINDER
614 -FNDC
615 *****
616 001456 005067 000172 DIV2: CLR PLIER
617 001462 012704 157777 MOV #157777,R4 ;LOAD MSH DIVIDEND
```



```

621 001466* 012705 100001
622 001472* 012767 100000 000154
623 001500* 000277
624 001502* 071467 000146
625
626 001506* 100402
627 001510* 101401
628 001512* 102003
629 001514*
630 001514*
631
632
633 001514* 001 000000* 000000
634
635
636 000
637
638 001522* 020427 040000
639 001526* 001403
640 001530*
641
642
643 001530* 001 000000* 000000
644
645
646 000
647
648 001536* 020527 100001
649 001542* 001403
650 001544*
651
652
653 001544* 001 000000* 000000
654
655
656 000
657
658
659
660 001552* 000167 176446
661 001556* 125252
662 001560* 001556*
663 001562* 052525
664
665
666 001566* 001566*
667 001570* 001572*
668
669
670 001572* 001572*
671 001574* 125252*
672 001576* 052525*
673 001600* 000000*
674 001602* 001600*
675
676 001610*

```

```

MOV #100001,R5 ;LOAD LSH DIVIDEND
MOV #100000,PLIER ;LOAD DIVISOR INTO TEMP
SEC ;PRE-SET ALL CC BITS
DIV PLIER,R4 ;DIVIDE #157777 100001 BY 100000
;QUOTIENT=30000,RFM=100001,C=N=V=Z=0
BWI 10S ;IF N SET, REPORT ERROR
BLOS 10S ;IF C OR Z SET, REPORT ERROR
BVC 1S ;IF V = 0, GO ON WITH TEST

```

```

10S:
HRDR ***** <INCORRECT CONDITION CODES>
;IF B <>
HRDRS,BEGIN,NULL ;INCORRECT CONDITION CODES
;IFP
HRDRS,BEGIN, ;INCORRECT CONDITION CODES
;FNOC
*****
CMP R4,#40000 ;QUOTIENT CORRECT?
REQ 2S ;BRANCH IF THE QUOTIENT IS CORRECT
HRDR ***** <INCORRECT QUOTIENT>
;IF B <>
HRDRS,BEGIN,NULL ;INCORRECT QUOTIENT
;IFP
HRDRS,BEGIN, ;INCORRECT QUOTIENT
;ENOC
*****
CMP R5,#100001 ;REMAINDER CORRECT?
REQ 3S ;BRANCH IF THE REMAINDER IS CORRECT
HRDR ***** <INCORRECT REMAINDER>
;IF B <>
HRDRS,BEGIN,NULL ;INCORRECT REMAINDER
;IFP
HRDRS,BEGIN, ;INCORRECT REMAINDER
;ENOC
*****

```

```

3S:
JMP START
B:
125252
B ;ADDRESS OF B
052525

```

```

;=B+10
A:
-1
A+4

```

```

;=A+4
125252
A+10 ;ADDRESS OF A+10
052525

```

```

C:
0
C ;ADDRESS OF C

```

```

;=C+10

```

```

677 001610* 000000
678 001612* 001610*
679
680
681 001616* 001616*
682 001620* 000000
683 006401
684 001622* 000000
685 140000
686
687
688 001624* 177776
689 001626* 000000
690 001654* 000013
691 000001

```

```

TEMP: 0
TEMP ;ADDRESS OF TEMP

```

```

;=TEMP+5
TEMP+10 ;ADDRESS OF TEMP+10 OR "D"
D:0
MARK1= 6401
CNT: 0
UM=140000

```

```

PS=177776 ;PROCESSOR STATUS WORD
V:0
;PLKW 11-
PLIER:0
.END

```


SDB5	000360R	231#	241																
SDFCNT	000042R	137#																	
SDFERS	104406	173#																	
SDFPAS	000048R	139#																	
SPDINT	000032R	133#	246	266	370														
SPSTZ	= 000040	166																	
SR1	000016R	125#																	
SR2	000020R	127#																	
SR3	000022R	128#																	
SR4	000024R	129#																	
START	000224R	132	174#	358	660														
STAT	000026R	131#																	
SVR0	000062R	146#																	
SVR1	000064R	147#																	
SVR2	000066R	148#																	
SVR3	000070R	149#																	
SVR4	000072R	150#																	
SVR5	000074R	151#																	
SVR6	000076R	152#																	
SVSCNT	000052R	141#																	
TEMP	001610R	182#	184	290*	293	304*	306*	307	677#	678	680	681							
TRPDFD	= 000022	173#																	
UM	= 140000	685#																	
VECTDR	000010R	122#																	
WASADR	000104R	156#																	
WDFR	000116R	163#																	
WDT0	000114R	162#																	
X	001626R	199#	201*	689#															
XPLAG	000005R	120#																	
XOR1	000230R	180#																	
Y	001624R	199#	200*	202	688#														
.	= 001656R	665#	669#	676#	680#	689#													

. ABS. 000000 000
 001656 001

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

XCPBJO,XCPBJO/SOL/CRF:SYM=DDXCON,XCPBJO
 RUN-TIME: 1 2 3 SECONDS
 RUN-TIME RATIO: 12/4=3.0
 CORE USED: 7K (13 PAGES)