

.REM 8

IDENTIFICATION

PRODUCT CODE: AC-E451A-MC
PRODUCT NAME: CXTSAAO DEC/X11 TS11/TS04 MOD
PRODUCT DATE: FEB 1979
MAINTAINER: DEC/X11 SUPPORT GROUP

THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE AND SHOULD NOT BE CONSIDERED AS A COMMITMENT BY DIGITAL EQUIPMENT CORPORATION. DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR ANY ERRORS THAT MAY APPEAR IN THIS MANUAL.

THE SOFTWARE DESCRIBED IN THIS DOCUMENT IS FURNISHED TO THE PURCHASER UNDER A LICENSE FOR USE ON A SINGLE COMPUTER SYSTEM AND CAN BE COPIED (WITH INCLUSION OF DIGITALS COPYRIGHT NOTICE) ONLY FOR USE IN SUCH SYSTEM, EXCEPT AS MAY OTHERWISE BE PROVIDED IN WRITING BY DIGITAL.

DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS SOFTWARE ON EQUIPMENT THAT IS NOT SUPPLIED BY DIGITAL.

COPYRIGHT (C) 1978,1979 DIGITAL EQUIPMENT CORPORATION

1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36

37
38
39
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56
57
58
59
60
61
62
63
64
65
66
67
68
69
70
71
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
91
92

1.0 ABSTRACT

TSA IS AN IOMDX MODULE THAT CAN EXERCISE UP TO 4 TS11/TS04
MAGNETIC TAPE SUBSYSTEMS. IT EXERCISES THE DRIVES BY DOING A
WRITE, READ REVERSE, IN-CORE COMPARE, READ FORWARD, IN CORE
COMPARE. THIS SEQUENCE OF FUNCTIONS WILL BE DEFINED AS A
CYCLE. AN "END OF PASS" WILL BE REACHED AFTER 512 CYCLES. ALL
ERRORS DETECTED ARE REPORTED ON THE CONSOLE ITV.

2.0 REQUIREMENTS

HARDWARE: 1 TO 4 TS11/TS04 TAPE SUBSYSTEMS.
STORAGE: TSA REQUIRES 1345 WORDS OF STORAGE.

3.0 PASS DEFINITION

ONE PASS OF THE TSA MODULE CONSISTS OF 512 CYCLES OF THE BASIC
TEST SEQUENCE (WRITE, READ REVERSE, DATA COMPARE, READ FORWARD,
DATA COMPARE). THE WRITE AND READ DATA LENGTHS ARE 256 WORDS.
EXECUTION TIME

4.0

ONE PASS OF TSA RUNNING ALONE ON A PDP-11/34 TAKES APPROXIMATELY
1 MINUTE.

5.0 CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:
DEVADR: 172520 VECTOR: 224 BR1: 5 DEVCNT: 1
REQUIRED PARAMETERS:
(REQUIRED FOR MULTIPLE DRIVE OPERATION ONLY)
SR2: VECTOR FOR 2ND DRIVE
SR3: VECTOR FOR 3RD DRIVE
SR4: VECTOR FOR 4TH DRIVE
DEVICE/OPTION SETUP

MAKE CERTAIN THAT ALL DRIVES ARE POWERED UP, WRITE ENABLED AND
ON LINE. HAVING ALL DRIVES AT LOAD POINT IS NOT ESSENTIAL TO
THE OPERATION OF THE MODULE BUT WILL ENSURE THAT THE RECORD COUNT
ACCURATELY REFLECTS THE NUMBER OF RECORDS FROM LOAD POINT ON
THE 1ST PASS OF TAPE. (THE RECORD COUNT ACCOMPANIES ALL
PRINTOUTS AND CAN BE USED TO IDENTIFY BAD SPOTS ON TAPE).

93
94
95
96
97
98
99
100
101
102
103
104
105
106
107
108
109
110
111
112
113
114
115
116
117
118
119
120
121
122
123
124
125
126
127
128
129
130
131
132
133
134
135
136
137
138
139
140
141
142
143
144
145
146
147
148

7.0 MODULE OPERATION

TEST SEQUENCE:

- A. SET UP DEVICE REGISTER ADDRESSES AND MODULE VARIABLES.
- B. INITIALIZE ALL DEVICES.
- C. GET A DEVICE ADDRESS.
- D. ISSUE SET CHARACTERISTIC AND ERASE COMMANDS TO THE DEVICE.
- E. IF NOT END OF DEVICES, GO TO C.
- F. GET NEXT DEVICE ADDRESS.
- G. DO A WRITE - IF ERRORS, REPORT AND RETRY TO THE RETRY LIMIT.
- H. DO A READ REVERSE - IF ERRORS, REPORT AND RETRY TO THE RETRY LIMIT.
- I. DO A DATA CHECK - IF ERRORS, REPORT AND CONTINUE.
- J. DO A READ FORWARD - IF ERRORS, REPORT AND RETRY TO THE RETRY LIMIT.
- K. DO A DATA CHECK - IF ERRORS, REPORT AND CONTINUE.
- L. IF AT END OF TAPE - DO A REWIND.
- M. IF END OF PASS, REPORT AND GO TO C, ELSE GO TO F.

ERROR RECOVERY SEQUENCE:

```

IF RETRY LIMIT HAS BEEN REACHED, THEN:
: PRINT UNRECOVERABLE ERROR
: IF BIT 0 IS SET IN SRA, THEN:
: : DROP THIS DEVICE FROM THE TEST SEQUENCE.
ELSE:
: PRINT RECOVERABLE ERROR.
: INCREMENT THE RETRY COUNTER.
: IF THE FUNCTION IS A READ, THEN:
: : IF THE RETRY COUNT IS HALF THE LIMIT OR MORE, THEN:
: : SET THE OPPOSITE DIRECTION BIT IN THE RETRY COMMAND.
: : ISSUE THE RETRY COMMAND TO THE DEVICE.

```

RETRY LIMITS:

```

FOR READ/WRITE COMMANDS = 16 RETRIES.
FOR ALL OTHER COMMANDS = 9 RETRIES.

```

149
150
151
152
153
154
155
156
157
158
159
160
161
162
163
164
165
166
167
168
169
170
171
172
173
174
175
176
177
178
179
180
181
182
183
184
185
186
187
188
189
190
191
192
193
194
195
196
197
198
199
200
201
202

```

8.0  OPERATION OPTIONS
-----
SRI BIT0 CLEAR (0):
      IF AN UNRECOVERABLE ERROR OCCURS ON ANY FUNCTION, THE FUNC-
      TION IS ABORTED AND TESTING CONTINUES.

SRI BIT0 SET (1):
      IF AN UNRECOVERABLE ERROR OCCURS ON ANY FUNCTION, THE DEVICE
      IS DROPPED FROM THE TEST CYCLE.

SRI BIT1 CLEAR (0):
      ALL RECOVERABLE ERRORS ARE REPORTED.

SRI BIT1 SET (1):
      RECOVERABLE ERRORS ARE NOT REPORTED.

9.0  NON-STANDARD PRINTOUTS
-----
      ALL PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE DEC/X11
      DOCUMENTS.

      ERROR MESSAGES DUMP THE CONTENTS OF 10 LOCATIONS IN THE FOLLOWING
      ORDER:
      DCNT MTYPE MLEN RPC XSTAT0 XSTAT1 XSTAT2 XSTAT3
      RECORD RIRYC
      DCNT = DEVICE NUMBER (0 - 3).
      MTYPE = MESSAGE PACKET 1ST WORD - MESSAGE TYPE.
      MLEN = MESSAGE PACKET 2ND WORD - MESSAGE LENGTH.
      RPC = MESSAGE PACKET 3RD WORD - RESidual FRAME COUNT.
      XSTAT0 = MESSAGE PACKET 4TH WORD - EXTENDED STATUS REG 0.
      XSTAT1 = MESSAGE PACKET 5TH WORD - EXTENDED STATUS REG 1.
      XSTAT2 = MESSAGE PACKET 6TH WORD - EXTENDED STATUS REG 2.
      XSTAT3 = MESSAGE PACKET 7TH WORD - EXTENDED STATUS REG 3.
      RECORD = RECORD COUNT - NUMBER OF RECORDS FROM BOT.
      RIRYC = RETRY COUNT (WILL BE 0 ON ORIGINAL ERROR).

10.0  DEFINITION OF ERRORS
-----
      RECOVERABLE ERROR - TERMINATION CLASS CODE OF 4 OR 5 AND RETRY
      LIMIT NOT EXCEEDED.

      UNRECOVERABLE ERROR - TERMINATION CLASS CODE OF 6 OR RETRY LIMIT EXCEEDED.
      CLASS CODE OF 4 OR 5 AND RETRY LIMIT EXCEEDED.

```

```
203 000000- IOMODX <TSAA > 172520,224,5,0,0,512,,163,BUFIN,256,,256-
204 000000- MODULE 150000,TSAA 172520,224,5,0,0,512,,163,BUFIN,256,,256.
205 ; TITLE TSAA DEC/X11 SYSTEM EXERCISER MODULE
206 ; DDACRM VERSION 6 23-NOV-78
207 ;
208 ;*****
209 000000- BEGIN:
210 000000- 051524 040501 040 MODNAM: -ASCII /TSAA / ;MODULE NAME
211 000005- 172520 XPLAG: -BYTE OPEN ;USED TO KEEP TRACK OF WBOFF USAGE
212 000010- 000224 VECTOR: 224+0 ;1ST DEVICE ADDR
213 000012- 240 BRI: -BYTE PRIY5+0 ;1ST DEVICE VECTOR.
214 000013- 000 BR2: -BYTE PRTY0+0 ;1ST BR LEVEL-
215 000014- 000000 DVID1: 0+1 ;2ND BR LEVEL
216 000016- 000000 SR1: OPEN ;DEVICE INDICATOR 1.
217 000020- 000000 SR2: OPEN ;SWITCH REGISTER 1
218 000022- 000000 SR3: OPEN ;SWITCH REGISTER 3
219 000024- 000000 SR4: OPEN ;SWITCH REGISTER 4
220 ;*****
221 000026- 150000 STAT: 15000 ;STATUS WORD
222 000030- 001682- INIT: START ;MODULE START ADDR.
223 000032- 000252- SPOINT: MODSP ;MODULE STACK POINTER.
224 000034- 000000 PASCNT: 0 ;PASS COUNTER.
225 000036- 001000 ICONT: 512- ;# OF ITERATIONS PER PASS=512.
226 000040- 000000 SDFCNT: 0 ;LOC TO COUNT ITERATIONS
227 000042- 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
228 000044- 000000 SDFPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS
229 000046- 000000 HRDPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
230 000050- 000000 SYSCNT: 0 ;LOC TO SAVE HARD ERRORS PER PASS
231 000052- 000000 RANMON: 0 ;# OF SYS ERRORS ACCUMULATED
232 000054- 000000 CONFIG: 0 ;RESERVED FOR MONITOR USE
233 000056- 000000 RES1: 0 ;RESERVED FOR MONITOR USE
234 000060- 000000 RES2: 0 ;RESERVED FOR MONITOR USE
235 000062- 000000 SVR0: OPEN ;LOC TO SAVE R0.
236 000064- 000000 SVR1: OPEN ;LOC TO SAVE R1.
237 000066- 000000 SVR2: OPEN ;LOC TO SAVE R2.
238 000070- 000000 SVR3: OPEN ;LOC TO SAVE R3.
239 000072- 000000 SVR4: OPEN ;LOC TO SAVE R4.
240 000074- 000000 SVR5: OPEN ;LOC TO SAVE R5.
241 000076- 000000 SVR6: OPEN ;LOC TO SAVE R6.
242 000100- 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
243 000102- 000000 CSADR: OPEN ;ADDR OF GOOD DATA, OR
244 000104- 000000 ACSR: OPEN ;CONTENTS OF CSR.
245 000106- 000000 WASADR: OPEN ;ADDR OF BAD DATA, OR
246 000108- 000000 ASADR: OPEN ;STATUS REG CONTENTS.
247 000110- 000000 ERRTYP: OPEN ;TYPE OF ERROR
248 000112- 000000 ASB: OPEN ;EXPECTED DATA.
249 000114- 002064- RSTRT: RSTRT ;ACTUAL DATA.
250 000116- 000000 WDOT: OPEN ;RESTART ADDRESS AFTER END OF PASS
251 000118- 000000 WDFR: OPEN ;WORDS TO MEMORY PER ITERATION
252 000120- 000000 INTR: OPEN ;WORDS FROM MEMORY PER ITERATION
253 000122- 000163 IDNUM: 163 ;# OF INTERRUPTS PER ITERATION
254 000124- 000662- RBUFFA: BUFIN ;MODULE IDENTIFICATION NUMBER=163
255 000126- 000000 RBUFFP: OPEN ;READ BUFFER VIRTUAL ADDRESS
256 ;READ BUFFER PHYSICAL ADDRESS
```

```
259 000130- 000000 RBUFEA: OPEN ;READ BUFFER EA BITS
260 000132- 000400 RBUFSZ: 256 ;SIZE OF THE READ BUFFER
261 000134- 000000 WBUFEA: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
262 000136- 000000 WBUFSZ: 256 ;WRITE BUFFER EA BITS
263 000140- 000400 WBUFRQ: 256 ;WRITE BUFFER SIZE REQUESTED
264 000142- 000000 WBUFSZ: 256 ;WRITE BUFFER SIZE AVAILABLE
265 000144- 000000 CDEACT: OPEN ;DATA/DATCK ERROR COUNT
266 000146- 000000 CDMDC1: OPEN ;DATA/DATCK WORD COUNT
267 000150- 000000 FREE: OPEN ;RESERVED FOR FUTURE USE
268 ;*****
269 -REPT SPSIZ ;MODULE STACK STARTS HERE.
270 -NLIST
271 -WORD 0
272 -LIST
273 -ENDR
274 000252- MODSP:
275 ;*****
```

275
276
277
278
279
280 000252
281 000252
282 000001
283 000001
284
285
286
287
288
289 140204
290 100611
291 100205
292 100603
293 100201
294 102210
295 101205
296 101601
297 101201
298
299
300 020000
301
302
303
304 000200
305 100000
306
307
308
309 000001
310
311
312
313
314 000020
315 000020
316 000010
317 000016
318 000004
319 000040
320 177777

```
.LIST MC
.NLIST ME
.MCALL STRUCT
STRUCT
.PRINT ; INITIALIZE STRUCTURED MACROS
;SMAC: VERSION 1.1 ; LIST STRUCTURED MACRO INSTRUCTIONS.
;LSTTAG=1 ; LIST STRUCTURED MACRO TAGS.
-----
; THE FOLLOWING ARE TS04 COMMAND DEFINITIONS.
SCH=140204 ;SET CHARACTERISTIC.
ERS=100611 ;ERASE.
WFR=100205 ;WRITE.
RDR=100603 ;READ REVERSE.
RDF=100201 ;READ FORWARD.
RWD=102210 ;REWIND.
WTR=101205 ;WRITE RETRY.
RRR=101601 ;READ REVERSE RETRY.
RFR=101201 ;READ FORWARD RETRY.
; THE FOLLOWING ARE COMMAND WORD BIT DEFINITIONS.
CM.OPP=20000 ;OPPOSITE DIRECTION BIT.
; THE FOLLOWING ARE TSSR(STATUS REGISTER) BIT DEFINITIONS.
TS.SSR=200 ;SUBSYSTEM READY.
TS.SC=100000 ;SPECIAL CONDITION.
; THE FOLLOWING ARE EXTENDED STATUS REGISTER 0 BIT DEFINITIONS.
XS0.EOT=1 ;END OF TAPE.
; MISCELLANEOUS DEFINITIONS.
WRETRY=16. ;WRITE COMMAND RETRY LIMIT.
RRETRY=16. ;READ COMMAND RETRY LIMIT.
CRETRY=8. ;RETRY LIMIT FOR NON-READ/WRITE CMDS.
MSGCNT=14. ;MESSAGE PACKET LENGTH IN BYTES.
SCBLEN=4. ;SET CHARACTERISTIC BLOCK LENGTH IN WORDS.
CHARCDEF=4. ;CHARACTERISTIC CODE DEFAULT.
TERM=177777 ;TABLE TERMINATION INDICATOR.
-----
```

321
322
323 000252 000622
324 000254 000006
325 000256 000000
326 000260 000644
327 000262 000000
328 000264 000000
329 000266 000634
330 000270 000000
331 000272 000000
332
333
334
335 000274 000000
336 000276 000000
337 000300 000000
338 000302 000000
339
340
341
342 000304 000000
343 000306 000000
344 000310 000000
345
346
347
348 000312 000000
349 000314 000000
350 000316 000000
351 000320 000000
352
353
354
355 000322 000000
356 000324 000000
357 000326 000000
358 000330 000000
359 000332 000000
360 000334 000000
361 000336 000000
362 000340 000000
363 000342 000000
364 000344 000000
365 000346 000000
366 000350 000000
367 000352 000000
368 000354 000000
369 000356 000000
370 000358 000000
371 000360 000000

```
; THE FOLLOWING LOCATIONS ARE USED TO REQUEST BUFFERS FROM THE MONITOR.
CBUFFA: CNDPKT ;COMMAND PACKET VIRTUAL ADDRESS.
CBUFFA: 0 ;COMMAND PACKET PHYSICAL ADDRESS.
CRUPEA: 0 ;COMMAND PACKET EA BITS.
MBUFFA: MSGPKT ;MESSAGE PACKET VIRTUAL ADDRESS.
MBUFFA: 0 ;MESSAGE PACKET PHYSICAL ADDRESS.
MBUFFA: 0 ;MESSAGE PACKET EA BITS.
SBUFFA: MSGBLK ;MESSAGE BLOCK VIRTUAL ADDRESS.
SBUFFA: 0 ;MESSAGE BLOCK PHYSICAL ADDRESS.
SBUFFA: 0 ;MESSAGE BLOCK EA BITS.
; THE FOLLOWING LOCATIONS ARE THE DEVICE POINTERS.
DCNT: 0 ;DEVICE COUNTER.
DIND: 0 ;DEVICE INDEX.
DIND: 0 ;DEVICE INDICATOR.
VDIND: 0 ;VARIABLE DEVICE INDICATOR.
; THE FOLLOWING LOCATIONS ARE DEVICE VARIABLES.
TSSR: 0 ;STATUS REGISTER ADDRESS OF CURRENT DEVICE.
VECT: 0 ;VECTOR ADDRESS OF CURRENT DEVICE.
RECORD: 0 ;RECORD COUNT OF CURRENT DEVICE.
; THE FOLLOWING LOCATIONS ARE COMMAND VARIABLES.
CMD: 0 ;CURRENT TS04 COMMAND WORD.
RCMD: 0 ;RETRY COMMAND WORD.
RTRYL: 0 ;RETRY LIMIT.
RTRVC: 0 ;RETRY COUNT.
; THE FOLLOWING TABLES ARE USED TO SET THE DEVICE VARIABLES.
THESE TABLES ARE INITIALIZED DURING MODULE START INITIALIZATION.
TTSDB: 0 ;TTSDB ADR FOR DEVICE 0.
0 ;TTSDB ADR FOR DEVICE 1.
0 ;TTSDB ADR FOR DEVICE 2.
0 ;TTSDB ADR FOR DEVICE 3.
TSSR: 0 ;TSSR ADR FOR DEVICE 0.
0 ;TSSR ADR FOR DEVICE 1.
0 ;TSSR ADR FOR DEVICE 2.
0 ;TSSR ADR FOR DEVICE 3.
TVECT: 0 ;VECTOR ADR FOR DEVICE 0.
0 ;VECTOR ADR FOR DEVICE 1.
0 ;VECTOR ADR FOR DEVICE 2.
0 ;VECTOR ADR FOR DEVICE 3.
TREC: 0 ;RECORD COUNT FOR DEVICE 0.
0 ;RECORD COUNT FOR DEVICE 1.
0 ;RECORD COUNT FOR DEVICE 2.
0 ;RECORD COUNT FOR DEVICE 3.
```

```

372
373
374 000362 140204
375 000364 140204
376 000366 000010
377 000370 005053
378 000372 000270
379 000374 000270
380 000376 000270
381 000400 100611
382 000402 100611
383 000404 000010
384 000406 005064
385 000408 000560
386 000412 000560
387 000414 000560
388 000416 100205
389 000420 101205
390 000422 000570
391 000424 005072
392 000426 000134
393 000430 000136
394 000432 000142
395 000434 100601
396 000436 101601
397 000440 000020
398 000442 005100
399 000444 000126
400 000446 000130
401 000448 000132
402 000452 100201
403 000454 101201
404 000456 000020
405 000460 005111
406 000464 000126
407 000466 000132
408 000468 000132
409 000470 102210
410 000472 102210
411 000474 000010
412 000476 005172
413 000500 000520
414 000502 000560
415 000504 000560
416
417
418
419 000506 000
420 000507 000
421 000510 000
422 000511 000
423

```

```

) THE FOLLOWING TABLES ARE USED TO SET UP COMMAND VARIABLES.
SCHAR: SCH ;SET CHARACTERISTIC COMMAND.
SCH ;RETRY COMMAND
CRETRY ;RETRY LIMIT
SCHAS ;ADDRESS OF CMD ASCII.
SBUFFA ;ADDRESS OF BUFFER PHYSICAL ADR.
SBUFFA ;ADDRESS OF BUFFER EXTENDED ADR.
SBUFFS ;ADDRESS OF BUFFER SIZE.
ERASE: ERS ;ERASE COMMAND.
ERS ;RETRY COMMAND
CRETRY ;RETRY LIMIT
ERSAS ;ADDRESS OF CMD ASCII.
BCON ;ADDRESS OF BUFFER PHYSICAL ADR.
BCON ;ADDRESS OF BUFFER EXTENDED ADR.
BCON ;ADDRESS OF BUFFER SIZE.
WRITE: WRT ;WRITE COMMAND.
WRT ;RETRY COMMAND
WRTRY ;RETRY LIMIT
WRAS ;ADDRESS OF CMD ASCII.
WBUFFA ;ADDRESS OF BUFFER PHYSICAL ADR.
WBUFFA ;ADDRESS OF BUFFER EXTENDED ADR.
WBUFFS ;ADDRESS OF BUFFER SIZE.
RREV: RRR ;READ REVERSE COMMAND.
RRR ;RETRY COMMAND
RRETRY ;RETRY LIMIT
RDRAS ;ADDRESS OF CMD ASCII.
RBUFFA ;ADDRESS OF BUFFER PHYSICAL ADR.
RBUFFA ;ADDRESS OF BUFFER EXTENDED ADR.
RBUFFS ;ADDRESS OF BUFFER SIZE.
RFWD: RFR ;READ FORWARD COMMAND.
RFR ;RETRY COMMAND
RRETRY ;RETRY LIMIT
RDFAS ;ADDRESS OF CMD ASCII.
RBUFFA ;ADDRESS OF BUFFER PHYSICAL ADR.
RBUFFA ;ADDRESS OF BUFFER EXTENDED ADR.
RBUFFS ;ADDRESS OF BUFFER SIZE.
REWIND: RWD ;REWIND COMMAND.
RWD ;RETRY COMMAND
CRETRY ;RETRY LIMIT
WRAS ;ADDRESS OF CMD ASCII.
BCON ;ADDRESS OF BUFFER PHYSICAL ADR.
BCON ;ADDRESS OF BUFFER EXTENDED ADR.
BCON ;ADDRESS OF BUFFER SIZE.
) THE FOLLOWING BYTES ARE USED AS PROGRAM CONTROL FLAGS.
EOD: ;END OF DEVICES.
DROF: ;CURRENT DEVICE HAS BEEN DROPPED.
DMOD: ;MODULE IS TO BE DROPPED.
EREC: ;ERROR RECOVERY IS REQUIRED.
.EVEN

```

```

424
425
426
427 000512 000
428 000513 000
429 000514 000
430 000515 000
431
432
433
434
435 000516 003610
436 000520 003660
437 000522 003732
438 000524 004010
439 000526 004062
440 000528 004176
441 000530 004226
442 000532 004234
443 000534 004234
444
445
446
447
448 000536 004454
449 000540 004464
450 000542 004474
451 000544 004504
452
453
454
455
456 000546 000000
457
458 000550 000000
459 000552 000000
460 000554 000000
461 000556 000000
462
463
464
465 000560 000001
466 000562 000002
467 000564 000004
468 000566 000010
469
470
471
472
473 000570 000274
474 000572 000644
475 000574 000646
476 000576 000650
477 000600 000652
478 000602 000654
479 000604 000656

```

```

) THE FOLLOWING BYTES ARE UNEXPECTED INTERRUPT FLAGS AND MUST BE
ASSEMBLED IN ORDER.
UIFLO: ;DEVICE 0.
UIFL1: ;DEVICE 1.
UIFL2: ;DEVICE 2.
UIFL3: ;DEVICE 3.
.EVEN
) THE FOLLOWING LOCATIONS CONTAIN ADDRESSES OF THE TERMINATION CLASS CODE
HANDLING ROUTINES AND MUST BE ASSEMBLED IN ORDER.
TCCRA: TCC0 ;TERMINATION CLASS CODE 0.
TCC1 ;TERMINATION CLASS CODE 1.
TCC2 ;TERMINATION CLASS CODE 2.
TCC3 ;TERMINATION CLASS CODE 3.
TCC4 ;TERMINATION CLASS CODE 4.
TCC5 ;TERMINATION CLASS CODE 5.
TCC6 ;TERMINATION CLASS CODE 6.
TCC7 ;TERMINATION CLASS CODE 7.
) THE FOLLOWING LOCATIONS CONTAIN ADDRESSES OF UNEXPECTED INTERRUPT
ROUTINES AND MUST BE ASSEMBLED IN ORDER.
UIADR: UID0 ;DEVICE 0.
UID1 ;DEVICE 1.
UID2 ;DEVICE 2.
UID3 ;DEVICE 3.
) THE FOLLOWING LOCATIONS CONTAIN ADDRESSES OF THE COMMAND PACKET WORDS.
THESE ADDRESSES ARE CALCULATED AND STORED DURING INITIALIZATION.
CMDADR: 0 ;COMBINED PHYSICAL COMMAND PACKET
CMDPK1: 0 ;ADDRESS WITH EXTENDED BITS IN BITS 0+1.
CMDPK2: 0 ;ADDRESS OF COMMAND PACKET 1ST WORD.
CMDPK3: 0 ;ADDRESS OF COMMAND PACKET 2ND WORD.
CMDPK4: 0 ;ADDRESS OF COMMAND PACKET 3RD WORD.
) THE FOLLOWING CONSTANTS MUST BE ASSEMBLED IN ORDER.
BCON: 1
2
4
10
) THE FOLLOWING TABLE CONTAINS ADDRESSES OF VARIABLES TO BE PRINTED
IN THE EXTENDED ERROR REPORTS.
TABLE: DCNT ;DEVICE NUMBER
MSGPKT ;STATUS MESSAGE TYPE.
MSGPKT+2 ;STATUS MESSAGE LENGTH IN BYTES.
MSGPKT+4 ;RESIDUAL FRAME COUNT.
MSGPKT+6 ;EXTENDED STATUS REGISTER 0.
MSGPKT+10 ;EXTENDED STATUS REGISTER 1.
MSGPKT+12 ;EXTENDED STATUS REGISTER 2.

```

```

480 000606 000660 MSGPKT+14 ;EXTENDED STATUS REGISTER 3.
481 000610 000310 RECORD ;RECORD COUNT.
482 000612 000320 RTVC ;RETRY COUNT.
483 000614 177777 TERM ;TABLE TERMINATOR.
; MISCELLANEOUS PROGRAM VARIABLES.
SBUFSZ: SCHCNT ;SET CHARACTERISTIC BLOCK LENGTH IN WORDS.
TIME: 0 ;TIMEOUT COUNTER.
; THIS IS THE COMMAND PACKET - IT MUST BE ON A MODULO 4 BOUNDRY.
CHDPKT: 0 ;TS04 COMMAND WORD.
0 ;BUFFER PHYSICAL ADDRESS.
0 ;BUFFER EXTENDED ADR IN BITS 0+1.
0 ;BUFFER LENGTH IN BYTES.
0 ;EXTENSION TO INSURE MODULO 4 BOUNDRY
;(1ST 4 WORDS CAN BE PUSHED DOWN 1 WORD).
; THIS IS THE SET CHARACTERISTIC BLOCK.
MSGBLK: 0 ;MESSAGE PACKET PHYSICAL ADDRESS.
0 ;MESSAGE PACKET EXTENDED ADR IN BITS 0+1.
MSGCNT ;MESSAGE PACKET LENGTH IN BYTES.
SCDFLT ;DEFAULT CHARACTERISTIC CODE.
; THIS IS THE MESSAGE PACKET.
MSGPKT: 0 ;MESSAGE TYPE.
0 ;MESSAGE LENGTH IN BYTES.
RFC: 0 ;RESIDUAL FRAME COUNT.
XSTAT0: 0 ;EXTENDED STATUS REGISTER 0.
XSTAT1: 0 ;EXTENDED STATUS REGISTER 1.
XSTAT2: 0 ;EXTENDED STATUS REGISTER 2.
XSTAT3: 0 ;EXTENDED STATUS REGISTER 3.
; THIS IS THE READ BUFFER.
BUFIN: .BLKW 256. ;512 BYTES.

```

```

520 001662 001662 START: LET WOTO := RBUFSZ SHFT 1 ;LET # OF READ WDS PER CYCLE = BUFFER SIZE X2.
521 001662 016767 176244 176224 ;MOV RBUFSZ,WOTO
522 001670 006367 176220 ;LET # OF WRITE WDS PER CYCLE = BUFFER SIZE.
523 001674 016767 176242 176214 ;LET # OF INTERRUPTS PER CYCLE = 3.
524 001675 016767 176242 176214 ;LET DEVICE INDICATOR AND SAVE IT.
525 001702 012767 000003 176210 ;CLEAR MODULE DROP FLAG.
526 001710 016767 176100 176362 ;CALL IDP-INITIALIZE DEVICE POINTERS.
527 001718 105067 176566 ;WHILE THERE ARE MORE DEVICES:
528 001722 004767 001046 ;50000S:
529 001726 105767 176554 ;TSTR EOD
530 001729 001053 ;BNE 50001S
531 001734 ;LET R1 := DCNT ;SAVE DEVICE COUNTER IN REGISTER.
532 001734 016701 176334 ;MOV DCNT,R1
533 001740 016702 176332 ;LET R2 := DINK ;SAVE DEVICE INDEX IN REGISTER.
534 001744 010203 ;MOV DINK,R2
535 001746 006303 ;LET R3 := R2 SHFT 1 ;SAVE DEVICE ADDRESS INCREMENT IN REGISTER.
536 001750 ;LET TTSDR(R2) := ADDR + R3 ;GENERATE AND STORE TSDBR ADR IN TSDBR TABLE.
537 001750 016762 176032 000322 ;MOV ADDR,TTSDR(R2)
538 001756 060362 000322 ;ADD R3,TTSDR(R2)
539 001762 016262 000322 000332 ;LET TTSSR(R2) := TTSDR(R2) + #2 ;GENERATE AND STORE TSSR ADR IN TSSR TABLE.
540 001770 062762 000002 000332 ;MOV TTSDR(R2),TTSSR(R2)
541 001776 005702 ;IF R2 EQ #0 THEN ;IF THIS IS THE FIRST DEVICE THEN:
542 001778 001004 ;TST R2
543 002002 016767 176002 176332 ;LET TVECT := VECTOR ;STORE VECTOR DEFAULT IN VECTOR TABLE.
544 002010 000403 ;ELSE ;ELSE - FOR ALL OTHER DEVICES:
545 002010 016262 000016 000342 ;LET TVECT(R2) := SR1(R2) ;STORE SR2-SR4 CONTENTS IN VECTOR TABLE.
546 002020 ;ENDIF ;50003S:
547 002020 005062 000352 ;LET TREC(R2) := #0 ;CLEAR THIS DEVICE'S RECORD COUNT.
548 002024 105061 000512 ;LET UIFLO(R1) := #0 ;CLEAR UNEXPECTED INTERRUPT FLAG.
549 002030 ;CALL DVSET ;CALL DVSET-SET UP DEVICE VARIABLES.
550 002030 004767 000672 ;LET R4 := VECT ;SAVE VECTOR ADDRESS IN REGISTER.
551 002034 016704 176246 ;LET (R4)+ := UIADR(R2) ;POINT VECTOR TO UNEXPECTED INTERRUPT ROUTINE.
552 002040 016224 000536 ;LET (R4) := BR1 ;STORE INTERRUPT PRIORITY IN VECTOR TABLE.
553 002044 116714 175742 ;LET @TSSR := #0 ;ISSUE SUBSYSTEM INITIALIZATION TO TS04.
554 002050 005077 176230 ;CLR @TSSR

```

```

576 002054 004767 000744 CALL UPDP ;CALL UPDP-UPDATE DEVICE POINTER TO NEXT DEV.
577 002054 004767 000744 ENDDO ;END OF DEVICE INITIALIZATION LOOP.
578 002060 000722 ;50005:
579 002060 000722 BR ;50005:
580 002062 000404 ;BYPASS DT03 BUS SWITCH CHECK WHEN STARTING.
581 002062 000404 ;
582 ;
583 ;
584 ;
585 002064 005767 175744 RESTR: IF PASCNT EQ #0 THEN ;IF THE PASS COUNT IS CLEARED THEN:
586 002070 001001 ;TST PASCNT
587 002072 000673 ;BNE 50004S
588 002074 ;
589 ;
590 ;
591 ;
592 ;
593 ;
594 ;
595 ;
596 ;
597 ;
598 ;
599 ;
600 002110 032767 000003 176136 ;IF CMDPKT ADR IS NOT ON A MODULO 4 BOUNDARY THEN
601 002110 001406 ;BIT #BIT0IBIT1,CBUPF
602 002120 ;
603 002120 062767 000002 176124 LET CBUFVA := CBUFVA + #2 ;INCREASE CMDPKT VIRTUAL ADR BY 2,
604 ; CBUFVA
605 ;
606 ;
607 002126 104415 000000 000252 GETPAS,BEGIN, CBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT CBUFVA
608 ;
609 ;
610 ;
611 ;
612 ;
613 002142 016767 176116 176404 LET CMDADR := CMDADR + CBUPFA ;ADD PHYSICAL AND EXTENDED ADDRESSES
614 002146 006267 176374 ;AND STORE AS CMD PACKET ADDRESS.
615 002152 006267 176370 ;SAVE ADDRESS OF COMMAND PACKET
616 002156 006267 176364 ;
617 ;
618 002162 066767 176066 176356 LET CMDPK1 := CBUFVA ;SAVE ADDRESS OF COMMAND PACKET
619 ;
620 002170 016767 176056 176352 LET CMDPK2 := CBUFVA + #2 ;SAVE ADDRESS OF COMMAND PACKET
621 ;
622 002176 016767 176050 176346 LET CMDPK3 := CBUFVA + #4 ;SAVE ADDRESS OF COMMAND PACKET
623 ;
624 002204 062767 000002 176340 LET CMDPK4 := CBUFVA + #6 ;SAVE ADDRESS OF COMMAND PACKET
625 ;
626 002212 016767 176034 176334 ;
627 002220 062767 000004 176326 ;
628 ;
629 002226 016767 176020 176322 ;
630 002234 062767 000006 176314 ;
631 ;

```

```

632 ;
633 002242 104415 000000 000260 GETPAS,BEGIN, MBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT MBUFVA
634 002250 104415 000000 000266 GETPAS,BEGIN, SBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT SBUFVA
635 ;
636 ;
637 ;
638 ;
639 002256 016767 176000 176350 LET MSGBLK := MBUFVA ;PHYSICAL ADDRESS FROM MONITOR.
640 ;
641 002264 016767 175774 176344 LET MSGBLK+2 := MBUFVA SHIFT -4 ;SHIFT EXTENDED ADDRESS TO BITS 0+1.
642 ;
643 002272 006267 176340 ;MOV MSGBLK+2
644 002280 006267 176334 ;ASR MSGBLK+2
645 002286 006267 176330 ;ASR MSGBLK+2
646 002306 006267 176324 ;ASR MSGBLK+2
647 ;
648 ;
649 002312 004767 000456 CALL IDP ;AND STORE IN CHARACTERISTIC BLOCK 2ND WD.
650 002312 004767 000456 WHILEB EOD EQ #0 DD ;CALL IDP-INITIALIZE DEVICE POINTERS.
651 ;
652 ;
653 002316 105767 176164 ;50006S:
654 002322 001022 ;TSTB EOD
655 ;
656 002330 004767 000376 CALL DVSET ;CALL DVSET-SET UP DEVICE VARIABLES.
657 002334 000362 ;BNE ICE VARIABLES.
658 002336 004767 000742 JSR R5,CVSET ;CALL CVSET-SET UP COMMAND VARIABLES.
659 ;
660 ;
661 002342 0105767 176141 ;ADR OF SET CHAR CMD VARIABLE TABLE.
662 002346 001005 ;CALL ICCS-ISSUE SET CHARACTERISTICS
663 ;
664 002350 004567 000252 ;IFB DROP EQ #0 THEN ;COMMAND AND CHECK STATUS
665 002354 000400 ;IF CURRENT DEVICE NOT DROPPED THEN:
666 ;
667 ;
668 ;
669 ;
670 002362 004767 000436 JSR R5,CVSET ;CALL CVSET-SET UP CMD VARIABLES.
671 002366 000753 ;WORD ERASE ;ADR OF ERASE CMD VARIABLE TABLE.
672 ;
673 ;
674 ;
675 002370 ;CALL ICCS-ISSUE ERASE COMMAND
676 ;

```

```

677 002370- BEGIN MAIN ;BEGINNING OF MAIN MODULE.
678 002370- ;SMAIN:
679 002370- WHILE ICOUNT LT ICONT DO ;WHILE CYCLE COUNT IS LESS THEN THE
680 002370- ; 500015:
681 002370- 026767 175444 175440 CMP ICOUNT,ICONT
682 002376- 002105 BGE 500025
683 ;NUMBER OF CYCLES PER PASS:
684 002400- IFB DMOD NE #0 THEN ;IF DROP MODULE FLAG IS SET THEN:
685 002400- 105767 176104 TSTB DMOD
686 002404- 001402 BEQ 500035
687 ;LEAVE MODULE MAIN FLOW-DROP MODULE.
688 002406- 000167 000204 JMP FINIT
689 002412- ENDIF ;END MODULE DROP CHECK.
690 002412- 500035:
691 ;
692 ;
693 002412- 104414 000000- .NLIST MC
694 GMBUFF,BEGIN ;GET WRITE BUFFER INFORMATION
695 ;
696 002416- .NLIST MC
697 002416- 016702 175654 LET R2 := DINK ;SAVE DEVICE INDEX IN REGISTER
698 002422- 005262 000352- LET TREC(R2) := TREC(R2) + #1 ;INCREMENT DEVICE RECORD COUNT
699 ;
700 002426- CALL DVSET ;CALL DVSET-SET UP DEVICE VARIABLES,
701 002426- 004767 000274 JSR PC,DVSET
702 002432- 004567 000170 JSR R5,CVSET ;CALL CVSET-SET UP COMMAND VARIABLES,
703 002436- 000416- .WORD WRITE ;ADDRESS OF WRITE CMD VARIABLE TABLE,
704 ;CALL ICCS-ISSUE WRITE CMD + CHECK STATUS,
705 002440- 004767 000640 JSR PC,ICCS
706 ;IF CURRENT DEVICE HAS NOT BEEN DROPPED THEN:
707 002444- 105767 176037 TSTB DROP
708 002450- 001053 BNE 500045
709 ;CALL CVSET-SET UP COMMAND VARIABLES,
710 002456- 000434- .WORD READ ;ADDRESS OF READ CMD VARIABLE TABLE,
711 ;CALL ICCS-ISSUE READ REV + CHECK STATUS,
712 002460- 004767 000620 JSR PC,ICCS
713 ;IF THIS DEVICE HAS NOT BEEN DROPPED THEN:
714 002464- 105767 176017 TSTB DROP
715 002470- 001043 BNE 500055
716 002472- 012767 000001 175406 LET ERRTP := #1 ;LET ERROR TYPE EQUAL DATA COMPARE,
717 ; #1,ERRTP
718 ;
719 ;
720 002500- 104412 000000- 000126- .NLIST MC
721 CDATAS,BEGIN,RBUPPA ; REQUEST FOR MONITOR TO CHECK DATA
722 ;+2 ; IF ERROR, CONTINUE
723 ;
724 002510- 004567- 000112 .NLIST MC
725 002516- 000452- .JSR R5,CVSET ;CALL DVSET-SET UP COMMAND VARIABLES,
726 ;ADDRESS OF READ REV CMD VARIABLE TABLE,
727 002516- 004767 000562 .WORD RFW ;CALL ICCS-ISSUE READ REV + STATUS CHECK,
728 ;IF DEVICE HAS NOT BEEN DROPPED THEN:
729 002522- 105767 175761 JSR PC,ICCS
730 002528- 001024 TSTB DROP
731 ;LET ERROR TYPE EQUAL DATA COMPARE,
732 002530- 012767 000001 175350 LET ERRTP := #1 ; #1,ERRTP

```

```

733 ;
734 ;
735 002536- 104412 000000- 000126- .NLIST MC
736 002544- 002546- CDATAS,BEGIN,RBUPPA ; REQUEST FOR MONITOR TO CHECK DATA
737 ;+2 ; IF ERROR, CONTINUE
738 ;
739 002546- .NLIST MC
740 002546- 032767 000001 176076 IF #XSO.EOT, XSTATO THEN ;IF AT END OF TAPE THEN:
741 002554- 001411 BIT #XSO.EOT,XSTATO
742 002556- 004567 000044 BREQ 500075
743 002562- 000470- .JSR R5,CVSET ;CALL CVSET-SET UP COMMAND VARIABLES,
744 ;ADDRESS OF REWIND CMD VARIABLE TABLE,
745 002564- 004767 000514 CALL ICCS ;CALL ICCS-ISSUE REWIND, CHECK STATUS,
746 ;
747 002570- 016702 175502 JSR PC,ICCS
748 002574- 005062 000352- LET R2 := DINK ;SAVE DEVICE INDEX IN REGISTER,
749 ;CLEAR DEVICE RECORD COUNT,
750 002600- ENDIF ;END OF END OF TAPE CHECK,
751 002600- 500075:
752 002600- ENDIF ;END DEVICE DROP CHECK,
753 002600- 500085:
754 002600- ENDIF ;END DEVICE DROP CHECK,
755 002600- 500095:
756 002600- ENDIF ;END DEVICE DROP CHECK
757 002600- 500045:
758 002600- 004767 000220 CALL UPDP ;CALL UPDP-UPDATE DEVICE POINTERS,
759 ;
760 ;
761 002604- 104413 000000- .NLIST MC
762 ENDS,BEGIN ;SIGNAL END OF ITERATION.
763 ;MONITOR SHALL TEST END OF PASS
764 ;
765 002610- .NLIST MC
766 002610- 000667 ENDDO ;END MODULE CYCLE LOOP. BR 500015
767 ;
768 002612- END MAIN ;END MAIN MODULE.
769 500025:
770 ;
771 ;
772 002616- BEGIN FINI ;BEGINNING OF FINI MODULE.
773 002616- FINIT:
774 002616- ;
775 ;
776 002616- 104410 000000- .NLIST MC
777 ENDS,BEGIN ;
778 ;
779 002622- .NLIST MC
780 ;
781 ;
782 ;

```



```

849
850
851
852
853
854
855
856
857
858
859
860
861
862
863
864
865 002726*
866 002726* 010246
867 002730*
868 002730* 016702 175342
869 002734*
870 002734* 016267 000322* 175136
871 002742*
872 002742* 016267 000332* 175334
873 002750*
874 002750* 016267 000342* 175330
875 002756*
876 002756* 016267 000352* 175324
877 002764*
878 002764* 105067 175516
879 002770*
880 002770* 012602
881 002772* 000207
882
883

```

```

*****
*SUBROUTINE NAME: DVSET
*FUNCTION: SETS UP ALL DEVICE VARIABLES.
*CALLING SEQUENCE: CALL DVSET.
*PARAMETERS PASSED: NONE
*REGISTERS USED: R2.
*SUBORDINATE ROUTINES: NONE
*****
DVSET: PUSH R2 ;SAVE TABLE INDEX REGISTER ON STACK.
;LET INDEX REGISTER EQUAL DEVICE INDEX.
LET R2 := DINX ;LET INDEX REGISTER EQUAL DEVICE INDEX.
;GET TSDB ADR FROM TSDB TABLE AND SAVE IT.
LET CSRA := TTSSR(R2) ;GET TSDB ADR FROM TSDB TABLE AND SAVE IT.
;GET TSSR ADR FROM TSSR TABLE AND SAVE IT.
LET TSSR := TTSSR(R2) ;GET TSSR ADR FROM TSSR TABLE AND SAVE IT.
;GET VECTOR FROM VECTOR TABLE AND SAVE IT.
LET VECT := TVECT(R2) ;GET VECTOR FROM VECTOR TABLE AND SAVE IT.
;GET RECORD COUNT FROM TABLE AND SAVE IT.
LET RECORD := TREC(R2) ;GET RECORD COUNT FROM TABLE AND SAVE IT.
LET EOD :B= #0 ;CLEAR END OF DEVICE FLAG.
POP R2 ;RESTORE THE TABLE INDEX REGISTER.
RTS PC ;RETURN.

```

```

884
885
886
887
888
889
890
891
892
893
894
895
896
897
898
899
900
901
902
903
904
905
906
907
908
909
910
911
912
913
914
915
916
917 002774*
918 002774* 005067 175274
919 003000*
920 003000* 005067 175272
921 003004*
922 003004* 012767 000001 175270
923 003012*
924 003012* 105067 175470
925 003016*
926 003016* 105067 175465
927 003022* 000207
928
929

```

```

*****
*SUBROUTINE NAME: IDP
*FUNCTION: INITIALIZES DEVICE POINTERS.
DEFINITION OF DEVICE POINTERS:
1. DEVICE COUNTER-COUNTS 0-3.
2. VARIABLE INDEX-COUNTS 0-6 BY 2'S
3. VARIABLE DEVICE INDICATOR-CONTAINS ONLY THE BIT OF THE CURRENT DEVICE FROM THE DEVICE INDICATOR IN THE HEADER.
*****
      DEVICE  | 0 | 1 | 2 | 3 |
      COUNTER | 0 | 1 | 2 | 3 |
      INDEX   | 0 | 2 | 4 | 6 |
      VARIABLE IND. | 1 | 1 | 2 | 4 | 8 |
*****
*CALLING SEQUENCE: CALL IDP
*PARAMETERS PASSED: NONE
*REGISTERS USED: NONE
*SUBORDINATE ROUTINES: NONE
*****
IDP: LET DCNT := #0 ;CLEAR DEVICE COUNTER. CLR DCNT
LET DINX := #0 ;CLEAR DEVICE INDEX. CLR DINX
LET VDIND := #1 ;LET VARIABLE DEVICE INDICATOR EQUAL 1. MOV #1,VDIND
LET EOD :B= #0 ;CLEAR END OF DEVICES FLAG. CLR EOD
LET DROP :B= #0 ;CLEAR DEVICE DROPPED FLAG. CLR DROP
RTS PC ;RETURN.

```



```

1114 003402 004767 177630 JSR PC,DROPD
1115 003406 000476 ENDIF ;BRANCH TO ICCRTN-RETURN
1119 003410 005367 175204 UNTIL %TS.SSR SETIN %TSSR ;END OF READY WAIT/REPEAT LOOP.
1120 003414 032777 000200 174662 LET @VECT := #1 ;UPDATE THE TIMEOUT COUNTER,
1121 003414 032777 001736 ;GIVE TS04 ONE INSTUCTION TIME TO RESPOND.
1122 003422 012777 003530 174654 LET @VECT := #ICCS1 ;POINT VECTOR TO PIRQ CALL.
1123 003432 016777 175110 174440 LET @CSRA := CMDADR ;ISSUE THE CMD PACKET TO THE TS04.
1124 003442 032777 000200 174634 NOP ;GIVE TS04 ONE INSTUCTION TIME TO RESPOND.
1125 003442 032777 001425 IF %TS.SSR SETIN %TSSR THEN ;IF READY DID NOT DROP THEN:
1126 003452 012767 000003 174426 LET ERRTP := #3 ;LET ERROR TYPE EQUAL
1127 003460 017767 174620 174416 LET ASTAT := @TSSR ;SAVE CONTENTS OF TSSR FOR PRINTOUT.
1128 003466 017767 174406 174406 LET ACSR := @CSRA ;SAVE CONTENTS OF TSSB(CSR) FOR PRINTOUT.
1129 003474 104403 000000 005135 ;LIST MC
1130 003502 104403 000000 005206 MSGNS,BEGIN,READ ;ASCII MESSAGE CALL WITH COMMON HEADER
1131 003510 104405 000000 000570 MSGNS,BEGIN,CMDA ;ASCII MESSAGE CALL WITH COMMON HEADER
1132 003516 004767 177514 BRDERS,BEGIN,TABLE ;READY DID NOT DROP
1133 003524 000430 BR ICCRTN ;BRANCH TO ICCRTN-RETURN
1134 003524 000430 ENDIF ;END OF READY DROP PROCESSING,
1135 003524 000430 ;*****
1136 003524 104400 000000 ICCS1: ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
1137 003530 000004 000000 003536 PIRQ$,BEGIN,ICCS2 ;QUEUE UP TO CONTINUE AT ICCS2 AND RTI
1138 003536 016702 174534 ICCS2: ;LIST MC
1139 003542 016277 000536 174536 LET R2 := DINX ;LET INDEX REGISTER EQUAL DEVICE INDEX.
1140 003542 032777 100000 174526 LET @VECT := UIADR(R2) ;POINT VECTOR TO UNEXPECTED INTERRUPT ROUTINE.
1141 003550 001406 IF %TS.SC SETIN %TSSR THEN ;IF SPECIAL CONDITION IS SET IN TSSR THEN:
1142 003560 017702 174520 LET R2 := @TSSR CLR.BY #177761 ;MASK TERMINATION CODE OUT OF TSSR.
1143 003564 042702 177761 ;RETURN.

```

```

1170 003570 004772 000516 JSR PC,@TCCRA(R2) ;GO TO APPROPRIATE TCC HANDLING ROUTINE.
1171 003574 005767 174711 ENDIF ;END OF SPECIAL CONDITION STATUS CHECK.
1172 003574 005767 000641 IFB EREC NE #0 THEN ;IF ERROR RECOVERY REQUIRED FLAG IS SET THEN:
1173 003574 105767 001401 BR ICCSR ;GO DO ERROR RECOVERY.
1174 003600 000641 ENDIF ;END OF ERROR RECOVERY REQUIREMENT CHECK.
1175 003604 012602 ICCRTN: POP R2 ;RESTORE TABLE INDEX REGISTER.
1176 003604 000207 RTS PC ;RETURN.
1177 003604 000207 ;
1178 003606 000207 ;
1179 003606 000207 ;
1180 003606 000207 ;
1181 003606 000207 ;
1182 003606 000207 ;
1183 003606 000207 ;

```

```

1184
1185
1186
1187
1188
1189
1190
1191
1192
1193
1194
1195
1196
1197
1198
1199
1200
1201
1202
1203
1204 003610*
1205 003610* 005067 174272
1206 003614*
1207 003614* 017767 174464 174262
1208 003622*
1209 003622* 017767 174252 174252
1210
1211
1212
1213 003630* 104403 000000* 005145*
1214 003636* 104403 000000* 005208*
1215
1216 003644* 104406 000000* 000570*
1217
1218
1219
1220 003652*
1221 003652* 004767 177360
1222 003656* 000207

```

```

*****
*
*SUBROUTINE NAME: TCC0
*
*FUNCTION: PROCESSES TERMINATION CLASS CODE 0
(UNDEFINED SPECIAL CONDITION STATUS).
*
*CALLING SEQUENCE: CALL TCC0
*
*PARAMETERS PASSES: NONE
*
*REGISTERS USED: NONE
*
*SUBORDINATE ROUTINES: DROPD
*
*ENTRY PREREQUISITES: - COMMAND HAS BEEN ISSUED TO TS04.
- INTERRUPT HAS BEEN RECEIVED.
*****
TCC0: LET ERRTP := #0 ;LET ERROR TYPE EQUAL 0,
;LET ERROR TYPE EQUAL 0, CLR ERRTP
LET ASTAT := @TSSR ;SAVE CONTENTS OF TSSR FOR PRINTOUT
MOV TSSR,ASTAT
LET ACSR := @CSRA ;SAVE CONTENTS OF TSDB(CSR) FOR PRINTOUT
MOV @CSRA,ACSR
-NLIST MC
-NLIST MC
MSGNS,BEGIN,SPEC ;ASCII MESSAGE CALL WITH COMMON HEADER
MSGNS,BEGIN,CMDA ;ASCII MESSAGE CALL WITH COMMON HEADER
*****
SDERS,BEGIN,TABLE ;SPECIAL CONDITION STATUS
*****
-NLIST MC
-NLIST MC
CALL DROPD ;CALL DROPD-DROP THE DEVICE. JSR PC,DROPD
RTS PC ;RETURN.
-----

```

```

1223
1224
1225
1226
1227
1228
1229
1230
1231
1232
1233
1234
1235
1236
1237
1238
1239
1240
1241
1242 003660*
1243 003660* 012767 000006 174220
1244 003666*
1245 003666* 017767 174412 174210
1246 003674*
1247 003674* 017767 174200 174200
1248
1249
1250 003702* 104403 000000* 005152*
1251 003710* 104403 000000* 005208*
1252
1253 003716* 104405 000000* 000570*
1254
1255
1256
1257 003724*
1258 003724* 004767 177360
1259 003730* 000207
1260
1261

```

```

*****
*
*SUBROUTINE NAME: TCC1
*
*FUNCTION: PROCESSES TCC1 (ATTENTION CONDITION).
*
*CALLING SEQUENCE: CALL TCC1
*
*PARAMETERS PASSES: NONE
*
*REGISTERS USED: NONE
*
*SUBORDINATE ROUTINES: DROPD
*
*ENTRY PREREQUISITES: - COMMAND HAS BEEN ISSUED TO TS04.
- INTERRUPT HAS BEEN RECEIVED.
*****
TCC1: LET ERRTP := #6 ;SET ERROR TYPE EQUAL 6,
;SET ERROR TYPE EQUAL 6, MOV #6,ERRTP
LET ASTAT := @TSSR ;SAVE CONTENTS OF TSSR FOR PRINTOUT
MOV TSSR,ASTAT
LET ACSR := @CSRA ;SAVE CONTENTS OF TSDB(CSR) FOR PRINTOUT
MOV @CSRA,ACSR
-NLIST MC
-NLIST MC
MSGNS,BEGIN,DEVI ;ASCII MESSAGE CALL WITH COMMON HEADER
MSGNS,BEGIN,CMDA ;ASCII MESSAGE CALL WITH COMMON HEADER
*****
HDRS,BEGIN,TABLE ;DEVICE OFF LINE
*****
-NLIST MC
-NLIST MC
CALL DROPD ;CALL DROPD-DROP THE DEVICE. JSR PC,DROPD
RTS PC ;RETURN.
-----

```

```

1262
1263
1264
1265
1266
1267
1268
1269
1270
1271
1272
1273
1274
1275
1276
1277
1278
1279
1280
1281 003732-
1282 003732- 032767 000001 174712
1283 003740- 001022
1284 003742-
1285 003744- 012767 000051 174136
1286 003750-
1287 003750- 017767 174330 174126
1288 003756-
1289 003756- 017767 174116 174116
1290
1291
1292 003764- 104403 000000- 005156-
1293 003772- 104403 000000- 005206-
1294
1295 004000- 104406 000000- 000570-
1296
1297
1298
1299 004006-
1300 004006-
1301 004006- 000207
1302
1303

```

```

*****
*
*SUBROUTINE NAME: TCC2
*
*FUNCTION: PROCESSES TCC2 (TAPE STATUS ALERT).
*
*CALLING SEQUENCE: CALL TCC2
*
*PARAMETERS PASSES: NONE
*
*REGISTERS USED: NONE
*
*SUBORDINATE ROUTINES: NONE
*
*ENTRY PREREQUISITES: - COMMAND HAS BEEN ISSUED TO TS04.
- INTERRUPT HAS BEEN RECEIVED.
*****
TCC2: IF #XSO.EOT NOTSETIN XSTATO THEN ; IF NOT AT END OF TAPE THEN:
BIT #XSO.EOT,XSTATO
BNE 500245
LET ERRTP := #51 ;LET ERROR TYPE EQUAL 51
MOV #51,ERRTP
LET ASTAT := @TSSR ;SAVE CONTENTS OF TSSR FOR PRINTOUT.
MOV @TSSR,ASTAT
LET ACSRA := @CSRA ;SAVE CONTENTS OF TSDB(CSR) FOR PRINTOUT.
MOV @CSRA,ACSRA
-NLIST MC
-LIST MC
MSG#S,BEGIN,TAPE ;ASCII MESSAGE CALL WITH COMMON HEADER
MSG#S,BEGIN,CMDA ;ASCII MESSAGE CALL WITH COMMON HEADER
;*****
SDPER,BEGIN,TAPE ;TAPE STATUS ALERT
;*****
-NLIST MC
-LIST MC
ENDIF ;END TAPE STATUS ALERT PROCESSING.
500245:
RTS PC ;RETURN
-----

```

```

1304
1305
1306
1307
1308
1309
1310
1311
1312
1313
1314
1315
1316
1317
1318
1319
1320
1321
1322 004010-
1323 004010- 012767 000052 174070
1324 004016-
1325 004016- 017767 174262 174060
1326 004024-
1327 004024- 017767 174050 174050
1328
1329
1330 004032- 104403 000000- 005162-
1331 004040- 104403 000000- 005206-
1332
1333 004046- 104405 000000- 000570-
1334
1335
1336
1337 004054-
1338 004054- 004767 177156
1339 004060- 000207
1340
1341
1342

```

```

*****
*
*SUBROUTINE NAME: TCC3
*
*FUNCTION: PROCESSES TCC3 (FUNCTION REJECT).
*
*CALLING SEQUENCE: CALL TCC3
*
*PARAMETERS PASSES: NONE
*
*REGISTERS USED: NONE
*
*SUBORDINATE ROUTINES: DROPD
*
*ENTRY PREREQUISITES: - COMMAND HAS BEEN ISSUED TO TS04.
- INTERRUPT HAS BEEN RECEIVED
*****
TCC3: LET ERRTP := #52 ;LET ERROR TYPE EQUAL 52.
MOV #52,ERRTP
LET ASTAT := @TSSR ;SAVE CONTENTS OF TSSR FOR PRINTOUT.
MOV @TSSR,ASTAT
LET ACSRA := @CSRA ;SAVE CONTENTS OF TSDB(CSR) FOR PRINTOUT.
MOV @CSRA,ACSRA
-NLIST MC
-LIST MC
MSG#S,BEGIN,FUNC ;ASCII MESSAGE CALL WITH COMMON HEADER
MSG#S,BEGIN,CMDA ;ASCII MESSAGE CALL WITH COMMON HEADER
;*****
HDRS,BEGIN,TAPE ;FUNCTION REJECTED
;*****
-NLIST MC
-LIST MC
CALL DROPD ;CALL DROPD-DROP THE DEVICE.
JSR PC,DROPD
RTS PC ;RETURN
-----

```



```

1465
1466
1467
1468
1469
1470
1471
1472
1473
1474
1475
1476
1477
1478
1479
1480
1481
1482
1483
1484
1485
1486
1487
1488
1489
1490
1491
1492
1493
1494
1495
1496
1497
1498
1499
1500
1501
1502
1503

```

```

*****
*
*SUBROUTINE NAME:      TCC7
*
*FUNCTION:             PROCESSES TCC7 (FATAL SUBSYSTEM ERROR).
*
*CALLING SEQUENCE:    CALL TCC7
*
*PARAMETERS PASSED:   NONE
*
*REGISTERS USED:      NONE
*
*SUBORDINATE ROUTINES: DROPD
*
*ENTRY PREREQUISITES: - COMMAND HAS BEEN ISSUED TO TS04.
*                     - INTERRUPT HAS BEEN RECEIVED.
*
*****
TCC7:  LET ERRTP := #55                ;LET ERROR TYPE EQUAL 55.
      LET ASTAT := @TSSR              ;SAVE CONTENTS OF TSSR FOR PRINTOUT.
      LET ACSR := @CSRA               ;SAVE CONTENTS OF TSDB(CSR) FOR PRINTOUT.
      -LIST MC
      -LIST ME
      MSGM,BEGIN,RECO                ;ASCII MESSAGE CALL WITH COMMON HEADER
      MSGM,BEGIN,CMDA                ;ASCII MESSAGE CALL WITH COMMON HEADER
      *****
      SOPS,BEGIN,TABLE                ;FATAL SUBSYSTEM ERROR
      *****
      -LIST MC
      CALL DROPD                      ;CALL DROPD-DROP THE DEVICE.
      -LIST ME
      RTS PC                          ;RETURN
      -----

```

```

004234 012767 000055 173644
004234 017767 174036 173634
004250 017767 173624 173624
004256 104403 000000 005176
004264 104403 000000 005208
004272 104405 000000 000570
004300
004300 004767 176732
004304 000207

```

```

1504
1505
1506
1507
1508
1509
1510
1511
1512
1513
1514
1515
1516
1517
1518
1519
1520
1521
1522
1523
1524
1525
1526
1527
1528
1529
1530
1531
1532
1533
1534
1535
1536
1537
1538
1539
1540
1541
1542
1543
1544
1545
1546
1547

```

```

*****
*
*SUBROUTINE NAME:      RECER
*
*FUNCTION:             PRINT RECOVERABLE ERROR MESSAGE.
*
*CALLING SEQUENCE:    CALL RECER
*
*PARAMETERS PASSED:   NONE
*
*REGISTERS USED:      NONE
*
*SUBORDINATE ROUTINES: NONE
*
*****
RECER: IF #BIT1 NOTSET IN SR1 THEN      ;IF BIT 1 NOT SET IN SR1 THEN:
      BIT #BIT1,SR1
      BNE 50034$
      LET ERRTP := #53                ;LET ERROR TYPE EQUAL 53.
      LET ASTAT := @TSSR              ;SAVE CONTENTS OF TSSR FOR PRINTOUT.
      LET ACSR := @CSRA               ;SAVE CONTENTS OF TSDB(CSR) FOR PRINTOUT.
      -LIST MC
      -LIST ME
      MSGM,BEGIN,RECO                ;ASCII MESSAGE CALL WITH COMMON HEADER
      MSGM,BEGIN,CMDA                ;ASCII MESSAGE CALL WITH COMMON HEADER
      *****
      SOPS,BEGIN,TABLE                ;RECOVERABLE ERROR
      *****
      -LIST MC
      ELSE
      ELSE-IF BIT 1 SET IN SR1:
      BR 50035$
      LET SOPCNT := SOPCNT + #1        ;UPDATE SOFT ERROR COUNT.
      INTC
      ENDIF                            ;END SWITCH REGISTER CHECK.
      -LIST ME
      RTS PC                          ;RETURN
      -----

```

```

004306 032767 000002 173502
004314 001023
004316 012767 000053 173562
004324 017767 173754 173552
004332 017767 173542 173542
004340 104403 000000 005166
004346 104403 000000 005206
004354 104406 000000 000570
004362 000402
004364
004364 005267 173452
004370
004370 000207

```

```

1548 ;*****
1549 ;*
1550 ;* SUBROUTINE NAME: UNRER
1551 ;*
1552 ;* FUNCTIONS: - PRINT RECOVERABLE ERROR MESSAGE.
1553 ;* - DROP DEVICE IF BIT0 SET IN SRI.
1554 ;*
1555 ;* CALLING SEQUENCE: CALL UNRER
1556 ;*
1557 ;* PARAMETERS PASSED: NONE
1558 ;*
1559 ;* REGISTERS USED: NONE
1560 ;*
1561 ;* SUBORDINATE ROUTINES: DROPD
1562 ;*
1563 ;*****
1564
1565 004372 UNRER: LET ERRTP := #54 ;LET ERROR TYPE EQUAL 54.
1566 004373 ;LIST MC ;
1567 004400 ;LIST MC ;
1568 004400 ;LIST MC ;
1569 004406 ;LIST MC ;
1570 004406 ;LIST MC ;
1571 ;LIST MC ;
1572 004414 ;LIST MC ;
1573 004422 ;LIST MC ;
1574 004422 ;LIST MC ;
1575 004430 ;LIST MC ;
1576 004430 ;LIST MC ;
1577 ;LIST MC ;
1578 ;LIST MC ;
1579 ;LIST MC ;
1580 004436 ;LIST MC ;
1581 004436 ;LIST MC ;
1582 004444 ;LIST MC ;
1583 004446 ;LIST MC ;
1584 004446 ;LIST MC ;
1585 004452 ;LIST MC ;
1586 004452 ;LIST MC ;
1587 004452 ;LIST MC ;
1588 ;LIST MC ;
1589 ;LIST MC ;

```

```

1590 ;*****
1591 ;*
1592 ;* ROUTINES TO SERVICE UNEXPECTED INTERRUPTS
1593 ;*
1594 ;*
1595 ;*****
1596
1597 004454 UID0: LET UIPL0 := #1 ;SET DEVICE 0 UNEXPECTED INTERRUPT FLAG.
1598 004462 ;LIST MC ;
1599 ;LIST MC ;
1600 ;LIST MC ;
1601 004464 UID1: LET UIPL1 := #1 ;SET DEVICE 1 UNEXPECTED INTERRUPT FLAG.
1602 004472 ;LIST MC ;
1603 ;LIST MC ;
1604 ;LIST MC ;
1605 ;LIST MC ;
1606 004474 UID2: LET UIPL2 := #1 ;SET DEVICE 2 UNEXPECTED INTERRUPT FLAG.
1607 004502 ;LIST MC ;
1608 ;LIST MC ;
1609 ;LIST MC ;
1610 ;LIST MC ;
1611 004504 UID3: LET UIPL3 := #1 ;SET DEVICE 3 UNEXPECTED INTERRUPT FLAG.
1612 004512 ;LIST MC ;
1613 ;LIST MC ;
1614 ;LIST MC ;
1615 ;LIST MC ;

```


1136#	1170#	1147#	1161#	1162#	1163#	1164#	1168#	1169#	1170#	1205#	1206#	1207#
1208#	1209#	1210#	1220#	1224#	1244#	1245#	1246#	1247#	1248#	1258#	1285#	1286#
1287#	1288#	1289#	1290#	1294#	1324#	1325#	1326#	1327#	1328#	1339#	1366#	1367#
1369#	1370#	1371#	1378#	1379#	1380#	1381#	1382#	1383#	1384#	1392#	1426#	1427#
1429#	1430#	1431#	1433#	1434#	1435#	1436#	1437#	1438#	1439#	1440#	1450#	1451#
1452#	1453#	1454#	1455#	1456#	1457#	1458#	1459#	1460#	1461#	1462#	1463#	1464#
1465#	1466#	1467#	1468#	1469#	1470#	1471#	1472#	1473#	1474#	1475#	1476#	1477#
1478#	1479#	1480#	1481#	1482#	1483#	1484#	1485#	1486#	1487#	1488#	1489#	1490#
1491#	1492#	1493#	1494#	1495#	1496#	1497#	1498#	1499#	1500#	1501#	1502#	1503#
1504#	1505#	1506#	1507#	1508#	1509#	1510#	1511#	1512#	1513#	1514#	1515#	1516#
1517#	1518#	1519#	1520#	1521#	1522#	1523#	1524#	1525#	1526#	1527#	1528#	1529#
1530#	1531#	1532#	1533#	1534#	1535#	1536#	1537#	1538#	1539#	1540#	1541#	1542#
1543#	1544#	1545#	1546#	1547#	1548#	1549#	1550#	1551#	1552#	1553#	1554#	1555#
1556#	1557#	1558#	1559#	1560#	1561#	1562#	1563#	1564#	1565#	1566#	1567#	1568#
1569#	1570#	1571#	1572#	1573#	1574#	1575#	1576#	1577#	1578#	1579#	1580#	1581#
1582#	1583#	1584#	1585#	1586#	1587#	1588#	1589#	1590#	1591#	1592#	1593#	1594#
1595#	1596#	1597#	1598#	1599#	1600#	1601#	1602#	1603#	1604#	1605#	1606#	1607#
1608#	1609#	1610#	1611#	1612#	1613#	1614#	1615#	1616#	1617#	1618#	1619#	1620#
1621#	1622#	1623#	1624#	1625#	1626#	1627#	1628#	1629#	1630#	1631#	1632#	1633#
1634#	1635#	1636#	1637#	1638#	1639#	1640#	1641#	1642#	1643#	1644#	1645#	1646#
1647#	1648#	1649#	1650#	1651#	1652#	1653#	1654#	1655#	1656#	1657#	1658#	1659#
1660#	1661#	1662#	1663#	1664#	1665#	1666#	1667#	1668#	1669#	1670#	1671#	1672#
1673#	1674#	1675#	1676#	1677#	1678#	1679#	1680#	1681#	1682#	1683#	1684#	1685#
1686#	1687#	1688#	1689#	1690#	1691#	1692#	1693#	1694#	1695#	1696#	1697#	1698#
1699#	1700#	1701#	1702#	1703#	1704#	1705#	1706#	1707#	1708#	1709#	1710#	1711#
1712#	1713#	1714#	1715#	1716#	1717#	1718#	1719#	1720#	1721#	1722#	1723#	1724#
1725#	1726#	1727#	1728#	1729#	1730#	1731#	1732#	1733#	1734#	1735#	1736#	1737#
1738#	1739#	1740#	1741#	1742#	1743#	1744#	1745#	1746#	1747#	1748#	1749#	1750#
1751#	1752#	1753#	1754#	1755#	1756#	1757#	1758#	1759#	1760#	1761#	1762#	1763#
1764#	1765#	1766#	1767#	1768#	1769#	1770#	1771#	1772#	1773#	1774#	1775#	1776#
1777#	1778#	1779#	1780#	1781#	1782#	1783#	1784#	1785#	1786#	1787#	1788#	1789#
1790#	1791#	1792#	1793#	1794#	1795#	1796#	1797#	1798#	1799#	1800#	1801#	1802#
1803#	1804#	1805#	1806#	1807#	1808#	1809#	1810#	1811#	1812#	1813#	1814#	1815#
1816#	1817#	1818#	1819#	1820#	1821#	1822#	1823#	1824#	1825#	1826#	1827#	1828#
1829#	1830#	1831#	1832#	1833#	1834#	1835#	1836#	1837#	1838#	1839#	1840#	1841#
1842#	1843#	1844#	1845#	1846#	1847#	1848#	1849#	1850#	1851#	1852#	1853#	1854#
1855#	1856#	1857#	1858#	1859#	1860#	1861#	1862#	1863#	1864#	1865#	1866#	1867#
1868#	1869#	1870#	1871#	1872#	1873#	1874#	1875#	1876#	1877#	1878#	1879#	1880#
1881#	1882#	1883#	1884#	1885#	1886#	1887#	1888#	1889#	1890#	1891#	1892#	1893#
1894#	1895#	1896#	1897#	1898#	1899#	1900#	1901#	1902#	1903#	1904#	1905#	1906#
1907#	1908#	1909#	1910#	1911#	1912#	1913#	1914#	1915#	1916#	1917#	1918#	1919#
1920#	1921#	1922#	1923#	1924#	1925#	1926#	1927#	1928#	1929#	1930#	1931#	1932#
1933#	1934#	1935#	1936#	1937#	1938#	1939#	1940#	1941#	1942#	1943#	1944#	1945#
1946#	1947#	1948#	1949#	1950#	1951#	1952#	1953#	1954#	1955#	1956#	1957#	1958#
1959#	1960#	1961#	1962#	1963#	1964#	1965#	1966#	1967#	1968#	1969#	1970#	1971#
1972#	1973#	1974#	1975#	1976#	1977#	1978#	1979#	1980#	1981#	1982#	1983#	1984#
1985#	1986#	1987#	1988#	1989#	1990#	1991#	1992#	1993#	1994#	1995#	1996#	1997#
1998#	1999#	2000#	2001#	2002#	2003#	2004#	2005#	2006#	2007#	2008#	2009#	2010#
2011#	2012#	2013#	2014#	2015#	2016#	2017#	2018#	2019#	2020#	2021#	2022#	2023#
2024#	2025#	2026#	2027#	2028#	2029#	2030#	2031#	2032#	2033#	2034#	2035#	2036#
2037#	2038#	2039#	2040#	2041#	2042#	2043#	2044#	2045#	2046#	2047#	2048#	2049#
2050#	2051#	2052#	2053#	2054#	2055#	2056#	2057#	2058#	2059#	2060#	2061#	2062#
2063#	2064#	2065#	2066#	2067#	2068#	2069#	2070#	2071#	2072#	2073#	2074#	2075#
2076#	2077#	2078#	2079#	2080#	2081#	2082#	2083#	2084#	2085#	2086#	2087#	2088#
2089#	2090#	2091#	2092#	2093#	2094#	2095#	2096#	2097#	2098#	2099#	2100#	2101#
2102#	2103#	2104#	2105#	2106#	2107#	2108#	2109#	2110#	2111#	2112#	2113#	2114#
2115#	2116#	2117#	2118#	2119#	2120#	2121#	2122#	2123#	2124#	2125#	2126#	2127#
2128#	2129#	2130#	2131#	2132#	2133#	2134#	2135#	2136#	2137#	2138#	2139#	2140#
2141#	2142#	2143#	2144#	2145#	2146#	2147#	2148#	2149#	2150#	2151#	2152#	2153#
2154#	2155#	2156#	2157#	2158#	2159#	2160#	2161#	2162#	2163#	2164#	2165#	2166#
2167#	2168#	2169#	2170#	2171#	2172#	2173#	2174#	2175#	2176#	2177#	2178#	2179#
2180#	2181#	2182#	2183#	2184#	2185#	2186#	2187#	2188#	2189#	2190#	2191#	2192#
2193#	2194#	2195#	2196#	2197#	2198#	2199#	2200#	2201#	2202#	2203#	2204#	2205#
2206#	2207#	2208#	2209#	2210#	2211#	2212#	2213#	2214#	2215#	2216#	2217#	2218#
2219#	2220#	2221#	2222#	2223#	2224#	2225#	2226#	2227#	2228#	2229#	2230#	2231#
2232#	2233#	2234#	2235#	2236#	2237#	2238#	2239#	2240#	2241#	2242#	2243#	2244#
2245#	2246#	2247#	2248#	2249#	2250#	2251#	2252#	2253#	2254#	2255#	2256#	2257#
2258#	2259#	2260#	2261#	2262#	2263#	2264#	2265#	2266#	2267#	2268#	2269#	2270#
2271#	2272#	2273#	2274#	2275#	2276#	2277#	2278#	2279#	2280#	2281#	2282#	2283#
2284#	2285#	2286#	2287#	2288#	2289#	2290#	2291#	2292#	2293#	2294#	2295#	2296#
2297#	2298#	2299#	2300#	2301#	2302#	2303#	2304#	2305#	2306#	2307#	2308#	2309#
2310#	2311#	2312#	2313#	2314#	2315#	2316#	2317#	2318#	2319#	2320#	2321#	2322#
2323#	2324#	2325#	2326#	2327#	2328#	2329#	2330#	2331#	2332#	2333#	2334#	2335#
2336#	2337#	2338#	2339#	2340#	2341#	2342#	2343#	2344#	2345#	2346#	2347#	2348#
2349#	2350#	2351#	2352#	2353#	2354#	2355#	2356#	2357#	2358#	2359#	2360#	2361#
2362#	2363#	2364#	2365#	2366#	2367#	2368#	2369#	2370#	2371#	2372#	2373#	2374#
2375#	2376#	2377#	2378#	2379#	2380#	2381#	2382#	2383#	2384#	2385#	2386#	2387#
2388#	2389#	2390#	2391#	2392#	2393#	2394#	2395#	2396#	2397#	2398#	2399#	2400#
2401#	2402#	2403#	2404#	2405#	2406#	2407#	2408#	2409#	2410#	2411#	2412#	2413#
2414#	2415#	2416#	2417#	2418#	2419#	2420#	2421#	2422#	2423#	2424#	2425#	2426#
2427#	2428#	2429#	2430#	2431#	2432#	2433#	2434#	2435#	2436#	2437#	2438#	2439#
2440#	2441#	2442#	2443#	2444#	2445#	2446						

