

IDENTIFICATION

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1.0 GENERAL INFORMATION

1.1 PROGRAM ABSTRACT

1.1.1 STRUCTURE OF PROGRAM

THIS DIAGNOSTIC OCCUPIES 14.5K WORDS OF MEMORY AND IS COMPATIBLE WITH BOTH XXDP AND ACT. IT CAN BE RUN STANDALONE UNDER XXDP AND CAN BE CHAINED UNDER XXDP ACT AND ACT IN CHAINING MODE (SEE "CORE IMAGE" COMMAND BELOW FOR DETAILS OF THE CHAINING PROCEDURE). IT IS A SINGLE PROGRAM FROM THE STANDPOINT OF THE DIAGNOSTIC USER, BUT WE HAVE INCORPORATED INTO IT A CONTROL MODULE WHICH WILL BE RELEASED INDEPENDENTLY AS A DIAGNOSTIC SUPERVISOR.

WHEN THIS DIAGNOSTIC IS STARTED AT ADDRESS 200, CONTROL GOES FIRST TO THE SUPERVISOR PORTION WHICH WILL ASK CERTAIN "HARD CORE" QUESTIONS ABOUT THE ENVIRONMENT. THEN IT WILL ENTER COMMAND MODE INDICATED BY A PROMPT CHARACTER. (DS B) AT COMMAND MODE THE OPERATOR MAY ENTER ANY OF SEVERAL COMMANDS AS DESCRIBED BELOW.

THE SUPERVISOR CODING FOLLOWS IMMEDIATELY THE DIAGNOSTIC TEST CODING, BUT THE SUPERVISOR LISTING HAS BEEN SUPPRESSED FOR GENERAL DISTRIBUTION. A LIMITED DISTRIBUTION HAS BEEN MADE TO FIELD SERVICE OF THE SUPERVISOR ASSEMBLY LISTING, AND IT MAY BE CONSULTED IN EVENT OF A SOFTWARE PROBLEM.

1.1.2 DIAGNOSTIC INFORMATION

THE RL11/RLV11 CONTROLLER TEST (PART 2) IS A PDP-11 (LSI-11) BASED PROGRAM THAT WILL TEST THE CONTROLLER. IT COMPLEMENTS PART 1 BY EXTENDING THE TEST COVERAGE TO INCLUDE WRITE DATA, AT READ DATA, WRITE CHECKING AND READ DATA WITHOUT HEADS COMPARE. IT IS AIMED AT FULLY TESTING THE CONTROLLER IN THESE AREAS, BUT BY DEFAULT ALSO EXERCISES THE DRIVE. THE TEST COVERAGE OF THE PROGRAM IS EXTREMELY HIGH.

1.2 SYSTEM REQUIREMENTS

1.2.1 HARDWARE REQUIREMENTS

PDP-11/LSI-11 PROCESSOR WITH 16K OR MORE OF MEMORY
CONSOLE DEVICE (LA306LA36, VI50, ETC.)
RL11/RLV11 CONTROLLER(S)
1 - 8 RLOI DRIVES
1 - 8 RLOIK CARTRIDGES WITH BAD SECTOR FILE
KW11P KW11L (OPTIONAL)
LINEPRINTER (OPTIONAL)

1.2.2 SOFTWARE REQUIREMENTS

CZRLBB RL11/RLV11 CTRL 2

(FORMERLY MD-11-DZRLB-A)

- 1.3 RELATED DOCUMENTS AND STANDARDS
 - RL01 USERS MANUAL (EK-RL01-UG-PRE)
 - XXDP USERS MANUAL
 - 1.4 DIAGNOSTIC HIERARCHY PREREQUISITES
 - THE RL01 SUBSYSTEM SHOULD HAVE SUCCESSFULLY RUN THE FOLLOWING PROGRAMS:
 - CZRLAB0 RL11/RLV11 RL01 CONTROLLER TEST (PART 1)
 - 1.5 ASSUMPTIONS
 - THE HARDWARE OTHER THAN THE RL01 SUBSYSTEM IS ASSUMED TO WORK PROPERLY. FALSE ERRORS MAY BE REPORTED IF THE PROCESSOR, ETC., DO NOT FUNCTION PROPERLY.
 - 2.0 OPERATING INSTRUCTIONS
 - 2.1 HOW TO RUN THIS DIAGNOSTIC
 - 2.1.1 THE SIX STEPS OF EXECUTION
 - THIS DIAGNOSTIC SHOULD BE LOADED AND STARTED USING NORMAL XXDP PROCEDURES. THE START COMMAND SHOULD NOT SPECIFY AN ADDRESS, BECAUSE THE DIAGNOSTIC HAS THE PROPER TRANSFER ADDRESS CODED INTO IT.
- WHEN THIS DIAGNOSTIC IS STARTED, THE FOLLOWING STEPS WILL OCCUR:

 * STEP 1 *

A SHORT SERIES OF "HARDCORE QUESTIONS" WILL BE ASKED:

QUESTION	MEANING
L-CLK (L) N ?	IS THERE AN L-CLOCK?
P-CLK (L) N ?	IS THERE AN P-CLOCK?
50HZ (L) N ?	IS THE POWER 50 CYCLES (AS IN EUROPE)?
LSI (L) N ?	IS MACHINE AN LSI?
LPT (L) N ?	IS THERE A LINE PRINTER?
MEM (K) (D) 16 ?	HOW MANY K OF MEMORY ARE THERE?

THE DEFAULTS (SHOWN AFTER EACH QUESTION) CAN BE SELECTED BY HITTING CARRIAGE RETURN. IT IS POSSIBLE THAT NOT ALL OF THE QUESTIONS WILL BE ASKED. FOR EXAMPLE, IF YOU SAY "YES" TO THE L-CLOCK QUESTION, THE P-CLOCK QUESTION WILL NOT BE ASKED.

IF NEITHER P OR L CLOCK ARE ANSWERED YES THE OPERATOR WILL BE ASKED TO TYPE TWO CHARACTERS 4 SECONDS APART.

* STEP 2 *

WHEN YOU HAVE ANSWERED ALL THE HARDCORE QUESTIONS, THE DIAGNOSTIC WILL ISSUE THE PROMPT "DS-B>". FROM THIS POINT UNTIL THE TIME WHEN YOU RESTART XXDP, YOU WILL BE TALKING TO THE DIAGNOSTIC, NOT XXDP. WE WILL REFER TO THE PRESENCE OF THIS PROMPT AS BEING IN DIAGNOSTIC COMMAND MODE, AS OPPOSED TO XXDP COMMAND MODE.

AT THIS POINT YOU WILL ENTER A "START" COMMAND. THIS IS NOT THE SAME AS THE XXDP "START" COMMAND, WHICH YOU ALREADY ISSUED IN RESPONSE TO THE XXDP DOP PROMPT. THIS "START" COMMAND CAN TAKE A NUMBER OF SWITCHES AND FLAGS (ALL OPTIONAL) AND THE DETAILS OF THESE ARE SET FORTH IN "2.3 DETAILS OF COMMANDS AND SYNTAX". HOWEVER, IN ORDER TO USE THE PROGRAM, ALL YOU NEED TO SAY IS SOMETHING LIKE THIS:

STA/PASS:1/FLAGS:HOE

THINGS TO NOTE HERE:

1. ONLY THE FIRST THREE CHARACTERS OF THIS OR ANY COMMAND AT THE "DS-B>" LEVEL NEED TO BE TYPED.
2. THE "PASS" SWITCH SPECIFIES HOW MANY PASSES YOU DESIRE. A PASS CONSISTS OF RUNNING THE FULL DIAGNOSTIC AGAINST ALL UNITS BEING TESTED (THIS WILL BE EXPLAINED SHORTLY). PASS IS SPECIFIED IN THE ABOVE EXAMPLE.
3. THE "FLAGS" SWITCH MAY SPECIFY ANY OF A NUMBER OF FLAGS, BUT THE MAIN USEFUL ONES ARE:
 LOE LOOP ONE ERROR
 HOE HALT ON ERROR
 IER INHIBIT ERROR PRINTOUT
 THE HOE FLAG IS SPECIFIED IN THE ABOVE EXAMPLE (WE'LL SEE WHY SHORTLY).

* STEP 3 *

WHEN YOU HAVE TYPED IN A "START" COMMAND, THE DIAGNOSTIC WILL COME BACK WITH THE QUESTION "# UNITS" TO WHICH YOU SHOULD RESPOND BY TYPING IN THE NUMBER OF DEVICES YOU WISH TO TEST.

A WORD OF WARNING HERE: THE NUMBER OF UNITS DEPENDS ON THE TARGET DEVICE OF THE DIAGNOSTIC. FOR EXAMPLE, IF THE DIAGNOSTIC IS DIRECTED

AT A DISK DRIVE, THEN THE NUMBER OF UNITS WOULD BE THE NUMBER OF DRIVES TO BE TESTED. WHEREAS IF THE DIAGNOSTIC WERE DIRECTED AT THE DISK CONTROLLER THEN THE NUMBER OF UNITS WOULD BE THE NUMBER OF DISK CONTROLLERS. THE TARGET OF A DIAGNOSTIC CAN ALWAYS BE DETERMINED BY INSPECTING THE "HEADER" STATEMENT NEAR THE BEGINNING OF THE SOURCE CODE. ONE OF THE OPERANDS OF THIS "HEADER" STATEMENT SHOULD BE THE DEVICE TYPE OF THE DIAGNOSTIC.

* STEP 4 *

WHEN YOU HAVE TYPED IN THE NUMBER OF UNITS TO BE TESTED, THE DIAGNOSTIC WILL ASK YOU THE "HARDWARE QUESTIONS". THE ANSWERS TO THESE QUESTIONS ARE USED TO BUILD TABLES IN CORE, CALLED "HARDWARE TABLES". ONE HARDWARE P-TABLE WILL BE BUILT FOR EACH UNIT TO BE TESTED.

THERE ARE SEVERAL HARDWARE QUESTIONS AND THE ENTIRE SERIES WILL BE REPOSED N TIMES, WHERE N IS THE NUMBER OF UNITS.

THIS REPRESENTS A NEW PHILOSOPHY IN DIAGNOSTIC ENGINEERING. DIAGNOSTICS IN THE FUTURE WILL NOT BE WRITTEN TO AUTOSIZE OR ASSUME STANDARD ADDRESSES. INSTEAD, THEY WILL ASK THE OPERATOR FOR ALL THE INFORMATION THEY NEED TO TEST THE DEVICE.

* STEP 5 *

AFTER YOU HAVE ANSWERED ALL THE HARDWARE QUESTIONS (SEC 2.5) FOR ALL THE UNITS YOU WILL BE ASKED "CHANGE SW?" IF YOU WANT TO BE ASKED TYPE SOFTWARE QUESTIONS THAT DETERMINE THE BEHAVIOR OF THESE PROGRAMS, TYPE "Y". IF YOU WANT TO TAKE ALL THE DEFAULTS TO THESE QUESTIONS, TYPE "N". IF YOU TYPE "Y" YOU WILL BE ASKED THE SOFTWARE QUESTIONS (SEC 2.6), AND THE ANSWERS WILL BE PUT INTO THE SOFTWARE P-TABLE IN THE PROGRAM AND THE SERIES OF QUESTIONS WILL BE ASKED JUST ONCE, REGARDLESS OF THE NUMBER OF UNITS TO BE TESTED.

* STEP 6 *

AFTER YOU HAVE ANSWERED THE SOFTWARE QUESTIONS, THE DIAGNOSTIC WILL BEGIN TO EXECUTE THE HARDWARE TEST CODE. THERE ARE SEVERAL THINGS THAT CAN HAPPEN NEXT, DEPENDING ON WHETHER A HARDWARE ERROR IS ENCOUNTERED AND ALSO ON WHAT SWITCH VALUES YOU SELECTED ON THE START COMMAND. CONSIDER THE POSSIBILITIES:

1. IF NO ERROR IS ENCOUNTERED, THEN THE DIAGNOSTIC WILL SIMPLY EXECUTE THE DESIRED NUMBER OF PASSES AND RETURN TO COMMAND MODE (PROMPT DS-B>).

2. IF AN ERROR IS ENCOUNTERED, THEN ONE OF THREE THINGS HAPPENS, DEPENDING ON THE SETTINGS OF THE HOE AND LOE FLAGS.

HOE SET: THE ERROR WILL BE REPORTED ON THE CONSOLE AND THE DIAGNOSTIC WILL RETURN TO COMMAND MODE.
LOE SET: THE DIAGNOSTIC WILL LOOP ENDLESSLY ON THE BLOCK OF CODE THAT DETECTED THE ERROR.
NEITHER HOE NOR LOE SET: THE ERROR WILL BE REPORTED ON THE CONSOLE AND NORMAL EXECUTION WILL RESUME AS IF NO ERROR HAD OCCURED.

2.1.2 SAMPLE RUN-THROUGH

LET'S SEE HOW ALL THIS WORKS IN A REAL SITUATION. RECALL THAT WE ENTERED THE COMMAND "STA/PASS:1/FLAGS:HOE". THIS WOULD BE A VERY TYPICAL WAY TO RUN THE DIAGNOSTIC. IF NO ERRORS ARE ENCOUNTERED THE SINGLE REQUESTED PASS WILL BE EXECUTED AND THE PROMPT WILL BE REISSUED.

IF AN ERROR IS ENCOUNTERED, THE ERROR WILL BE REPORTED AND THE PROMPT WILL BE REISSUED (BECAUSE THE HOE FLAG IS SET). AT THIS POINT THERE ARE FOUR DIFFERENT WAYS YOU CAN GET THE PROGRAM GOING AGAIN:

1. ISSUE ANOTHER "START" COMMAND (THUS GOING THRU ALL OF STEPS 2, 3, 4, 5, AND 6 AGAIN)
2. ISSUE A "RESTART" COMMAND (SAME AS START COMMAND EXCEPT THAT THE HARDWARE QUESTIONS ARE NOT ASKED)
3. ISSUE A "CONTINUE" COMMAND (EXECUTION WILL RESUME AT THE BEGINNING OF THE PARTICULAR HARDWARE TEST (MOST DIAGNOSTICS CONSIST OF A NUMBER OF THESE) THAT IT WAS IN WHEN THE ERROR HALT OCCURED. NO QUESTIONS ASKED.
4. ISSUE A "PROCEED" COMMAND: EXECUTION WILL RESUME AT THE INSTRUCTION FOLLOWING THE ERROR REPORT (THIS IS A SPECIAL COMMAND AND CAN BE ISSUED ONLY AT A HALT ON ERROR).

THE MOST TYPICAL THING TO DO HERE IS TO ISSUE THE PROCEED, BUT WITH DIFFERENT FLAG SETTINGS. PROBABLY YOU WOULD WANT TO SAY

PRO/FLAGS:IER:LOE:HOE=0

THIS WILL DO THE FOLLOWING:

- 1. TURN ON THE IER (INHIBIT ERROR PRINTOUT) FLAG
- 2. TURN ON THE LOE FLAG
- 3. TURN OFF THE HOE FLAG
- 4. RESUME EXECUTION AT INSTRUCTION AFTER ERROR REPORT

THE DIAGNOSTIC WILL NOW LOOP ON THE BLOCK OF CODE THAT DETECTED AND REPORTED THE ERROR, BUT NO ERROR PRINTOUT WILL OCCUR. THUS YOU CAN STUDY THE ERROR OR SCOPE IT OR WHATEVER.

WHEN YOU'VE SEEN ENOUGH, YOU MAY HIT CONTROL/C. THIS WILL TAKE YOU OUT OF THE LOOP AND PUT YOU BACK INTO COMMAND MODE. YOU NOW HAVE THREE CHOICES:

- 1. START
- 2. RESTART
- 3. CONTINUE

LET'S SAY YOU'VE REPAIRED THE DEFECT FOUND ABOVE AND WANT TO FINISH RUNNING THE DIAGNOSTIC. YOU WOULD TYPE

CON/FLAGS:HOE:IER=0:LOE=0

THIS WILL RESTORE THE FLAGS TO THEIR ORIGINAL VALUES AND RESUME EXECUTION AT THE BEGINNING OF THE HARDWARE TEST YOU WERE IN. IF THE ERROR DOES NOT RECUR, THE EXECUTION WILL FLOW RIGHT ON THRU TO THE NEXT ERROR OR TO END OF PASS.

IF AT END OF PASS YOU WANT TO RUN THE DIAGNOSTIC AGAIN, YOU HAVE TWO CHOICES:

- 1. START
- 2. RESTART

YOU WOULD CHOOSE ONE, DEPENDING ON WHETHER YOU WANTED TO ANSWER THE HARDWARE QUESTIONS AGAIN.

2.2 HOW TO CREATE A CHAINABLE FILE

THE DIAGNOSTIC AS RECEIVED FROM RELEASE ENGINEERING CANNOT BE RUN IN CHAIN MODE. THAT IS WHY IT BEARS THE EXTENSION "BIN" INSTEAD OF "BIC". THERE IS A WAY, HOWEVER, TO CREATE A CHAINABLE PROGRAM FROM WHAT YOU'VE GOT.

IT CONSISTS OF RUNNING THE PROGRAM WITH THE SPECIAL COMMAND "CCCI" ISSUED WHERE YOU WOULD NORMALLY ISSUE A START COMMAND (TO THE PROMPT DS-B>). THIS COMMAND CAUSES THE DIAGNOSTIC TO GO THRU ALL THE QUESTIONS AND ANSWERS AND THEN TO HALT, JUST WHERE IT WOULD ORDINARILY BEGIN EXECUTION OF THE HARDWARE TEST CODE. AT THIS POINT YOU CAN DUMP THE PROGRAM AS IT SITS IN CORE TO THE LOAD MEDIUM, WITH THE NEW EXTENSION "BIC".

HERE IS A SAMPLE DIALOGUE TO ACCOMPLISH THIS:

```
R UPD2
RESTART: XXXXXX
*CLR
*LOAD DIAG.BIN
XFER:200 CORE:0,60602
L-CLK (L) N ?
-----
DS-B>CCI
# UNITS (D) ? 4
-----
CHANGE SW (L) ? N
PTAB END: 60632
*****
*AT THIS POINT THE MACHINE HALTS AND*
*YOU MUST RESTART AT ADDRESS XXXXXXX*
*****
*HCORE 60632
*CORE 0,60632
*DUMP DR0: DIAG.BIC
```

THE RESULT OF DOING THIS IS THAT YOU CAN NOW BUILD AN XXDP CHAIN FILE CONTAINING THE XXDP COMMAND

.R DIAG.BIC

AND THE DIAGNOSTIC WILL EXECUTE WITHOUT MANUAL INTERVENTION, USING THE ANSWERS THAT YOU GAVE IT WHEN YOU DID THE CCI COMMAND.

2.3 DETAILS OF COMMANDS AND SYNTAX

2.3.1 TABLE OF COMMAND VALIDITY

THERE ARE FOUR WAYS OF ENTERING DIAGNOSTIC COMMAND MODE, AND DIFFERENT SUBSETS OF THE DIAG COMMAND SET ARE AVAILABLE WITH EACH:

HOW ENTERED

1. OPERATOR ENTERED 'RUN DIAG'

LEGAL COMMANDS
 START
 PRINT
 DISPLAY
 FLAGS
 ZFLAGS

2. DIAGNOSTIC HAS FINISHED ALL ITS REQUESTED PASSED

START
 RESTART
 PRINT
 DISPLAY
 FLAGS
 ZFLAGS

3. OPERATOR INTERRUPTED THE DIAGNOSTIC WITH CTRL/C

START
 RESTART
 CONTINUE
 PRINT
 DISPLAY
 FLAGS
 ZFLAGS

4. AN ERROR WAS ENCOUNTERED WITH THE HOE FLAG SET SET

START
 RESTART
 CONTINUE
 PROCEED
 PRINT
 DISPLAY
 FLAGS
 ZFLAGS

2.3.2 COMMAND SYNTAX

 STA(RT)/TESTS:TEST-LIST/PASS:PASS-CNT/FLAGS:FLAG-LIST/EOP: EOP-INCR

THE DIAGNOSTIC IN CORE IS EXECUTED IN ACCORDANCE WITH THE SWITCHES SPECIFIED. THE MESSAGE "#UNITS?" IS PRINTED THE START COMMAND MAY BE ISSUED WHEN DIAGNOSTIC COMMAND MODE HAS BEEN ENTERED VIA ONE OF THE FOLLOWING: A) OPERATOR TYPED 'RUN DIAGNOSTIC' B) DIAGNOSTIC FINISHED EXECUTING C) ERROR WAS ENCOUNTERED WITH 'HOE FLAG SET' D) OPERATOR ENTERED CONTROL/C.

AFTER THE OPERATOR RESPONDS TO "# UNITS?" THE HARDWARE DIALOGUE IS INITIATED. WHEN IT IS COMPLETED THE QUESTIONS "CHANGE SW2" IS ISSUED, AND THE ANSWERS IF GIVEN BECOME THE NEW DEFAULTS. THEREFORE IT IS NECESSARY TO RELOAD THE PROGRAM IN ORDER TO RETURN TO THE LOAD DEFAULTS.

THE SWITCH ARGUMENTS ARE AS FOLLOWS:

"TEST-LIST" IS A SEQUENCE OF DECIMAL NUMBERS (1:2 ETC.) OR RANGES OF DECIMAL NUMBERS (1-5:8-10 ETC.) THAT SPECIFY THE TESTS TO BE EXECUTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS RANGE FROM 1 TO THE LARGEST TEST NUMBER IN THE DIAGNOSTIC. THEY MAY BE SPECIFIED IN ANY ORDER. TESTS WILL BE EXECUTED IN NUMERICAL ORDER REGARDLESS OF THE ORDER OF SPECIFICATION. THE DEFAULT IS TO EXECUTE ALL TESTS.

"PASS-CNT" IS A DECIMAL NUMBER INDICATING THE DESIRED NUMBER OF PASSES A PATIENT IS DEFINED AS THE EXECUTION OF THE FULL DIAGNOSTIC (CALL SELECTED AGAINST ALL UNITS SUBMITTED. THE FULL DEFAULT IS NON-ENDING EXECUTION. "B" FLAG-LIST IS A SEQUENCE OF ELEMENTS OF THE FORM <FLAG> <FLAG=1> OR <FLAG=0>, SEPARATED BY COLONS, WHERE <FLAG> HAS ONE OF THE FOLLOWING VALUES:

- HOE HALT ON ERROR, CAUSING COMMAND MODE TO BE ENTERED WHEN AN ERROR IS ENCOUNTERED
- LOE LOOP ON ERROR, CAUSING THE DIAGNOSTIC TO LOOP CONTINUOUSLY WITHIN THE SMALLEST DEFINED BLOCK OF CODING (SEGMENT, SUBTEST, OR TEST) CONTAINING THE ERROR
- IER INHIBIT, ERROR REPORTING
- IXE INHIBIT BASIC ERROR REPORTS
- PRI INHIBIT EXEMPTED TESTS TO ALL LINE PRINTER
- PNT DIRECT ALL MESSAGES TO ALL LINE PRINTER
- BOE BELL ON ERROR
- UAM RUN IN UNATTENDED MODE, BYPASSING MANUAL INTERVENTION TESTS
- ISR INHIBIT STATISTICAL REPORTS
- IDU INHIBIT DROPPING OF UNITS BY DIAGNOSTIC

THE FLAGS NAMED OR EQUATED TO 1 ARE SET. THOSE EQUATED TO 0 ARE CLEARED. A FLAG NOT SPECIFIED IS CLEARED. IF THE FLAGS SWITCH IS NOT GIVEN ALL FLAGS ARE CLEARED.

"EOP-INCR" IS A DECIMAL NUMBER INDICATING HOW OFTEN (IN TERMS OF PASSES) IT IS DESIRED THAT THE END OF PASS MESSAGE BE PRINTED. THE DEFAULT IS AT THE END OF EVERY PASS.

RES(TART)/TEST-LIST/PASS:PASS-CNT/FLAGS:FLAG-LIST/EOP-INCR/UNITS:UNIT-LIST

THE DIAGNOSTIC IN CORE IS EXECUTED IN ACCORDANCE WITH THE SWITCHES SPECIFIED HOWEVER, NEW P-TABLES ARE NOT BUILT. INSTEAD, THE ONES IN CORE ARE USED. "CHANGE SW?" IS ASKED, AND THE ANSWERS IF GIVEN BECOME THE NEW DEFAULTS. THE COMMAND MAY BE ISSUED WHEN COMMAND MODE HAS BEEN ENTERED VIA A) DIAGNOSTIC IS FINISHED B) HALT ON ERROR C) CONTROL/C.

THE SWITCH ARGUMENTS ARE AS IN THE START COMMAND EXCEPT:

1. "UNIT-LIST" IS A SEQUENCE OF LOGICAL UNIT NUMBERS RANGING FROM 1 TO 10 (N = NUMBER OF UNITS BEING TESTED) SPECIFYING WHICH UNITS ARE TO BE TESTED. THE LOGICAL UNIT NUMBER DESIGNATES THE POSITION OF THE P-TABLE IN CORE ACCORDING TO THE ORDER IN WHICH THEY WERE BUILT. THE UNITS SPECIFIED MUST NOT HAVE BEEN DROPPED BY THE OPERATOR COMMAND. THE UNIT-LIST DEFAULTS TO "ALL" THAT HAVE NOT BEEN DROPPED BY OPERATOR COMMAND. THE EFFECT OF THE UNIT-LIST UNITS IS THE NEXT START (WHERE IT IS AUTOMATICALLY RESET TO "ALL") OR THE NEXT RESTART.

2. ALL UNSPECIFIED FLAG SETTINGS ARE UNCHANGED.

CON(TINUE)/PASS:<PASS-CNT/FLAGS:<FLAG-LIST>

COMMAND MODE MUST HAVE BEEN ENTERED DUE TO A HALT ON ERROR OR A TEST THAT WAS BEING EXECUTED WHEN THE HALT OR CONTROL/C TOOK PLACE. SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED. HARDWARE PARAMETERS MAY NOT BE CHANGED.

THE SWITCH ARGUMENTS ARE AS IN THE START COMMAND EXCEPT:

1. DEFAULT FOR PASS-CNT IS THE UNSATISFIED PASS-CNT FROM THE PREVIOUS START OR RESTART
2. UNSPECIFIED FLAG SETTINGS ARE UNCHANGED

PROCEED)/FLAGS:<FLAG-LIST>

COMMAND MODE MUST HAVE BEEN ENTERED VIA A HALT ON ERROR. THE EFFECT OF THE COMMAND IS TO BEGIN EXECUTION AT THE LOCATION FOLLOWING THE ERROR CALL. NEITHER HARDWARE NOR SOFTWARE PARAMETERS MAY BE ALTERED.

THE SWITCH ARGUMENTS ARE THE SAME AS THE START COMMAND EXCEPT:

1. UNSPECIFIED FLAG SETTINGS ARE UNCHANGED

CCITEST:TEST-LIST/PASS:PASS-CNT/FLAGS:FLAG-LIST/EOP: EOP-INCR

THE DIAGNOSTIC EXECUTES THRU ALL OPERATOR DIALOGUE AND HALTS AT THE HARDWARE TEST CODE. NOW THE OPERATOR CAN DUMP THE CORE IMAGE TO THE MEDIUM WITH A BIC EXTENSION.

THE BIC FILE MUST BE HANDLED DIFFERENTLY DEPENDING ON WHETHER IT IS RUN MANUALLY OR IN CHAIN MODE. IF RUN MANUALLY IT CAN BE INVOKED EITHER WITH A "START" (IN WHICH CASE IT WILL BEHAVE LIKE THE BIN FILE: THE PRE-GENERATED ANSWERS TO OPERATOR QUESTIONS WILL BE IGNORED) OR WITH A "RESTART" (IN WHICH CASE THE PRE-GENERATED OPERATOR ANSWERS WILL BE USED).

IF RUN IN CHAIN MODE AUTOMATIC EXECUTION WILL COMMENCE IMMEDIATELY FROM THE XXDP COMMAND ".R DIAG". THE COMMAND PROMPT "DS-B>" WILL NOT BE ISSUED.

ANY SWITCHES SPECIFIED ON THE CCI COMMAND WILL CARRY OVER WHEN THE BIC FILE IS RUN IN CHAIN MODE (EXCEPT THAT OAM IS ALWAYS SET THERE) BUT WILL NOT CARRY OVER WHEN IT IS RUN MANUALLY.

TO DO A CCI ON A FULL SIZED DIAGNOSTIC (14.5K WORDS) A MACHINE SIZE LARGER THAN 16K IS REQUIRED. THE EXACT SIZE NEEDED DEPENDS ON WHICH UTILITY IS USED TO EXECUTE THE DIAGNOSTIC AT CCI TIME.

DROPP)/UNITS:UNIT-LIST

THE UNITS SPECIFIED ARE DROPPED FROM TESTING UNTIL THEY ARE ADDED BACK OR UNTIL A START COMMAND IS GIVEN. A DROP CANNOT BE FOLLOWED BY A PROCEED.

THERE IS ALSO A "DROP" MACRO INTERNAL TO THE DIAGNOSTIC, WHICH GIVES THE FACILITY OF AUTO-DROPPING. THE DURATION OF A PROGRAM DROP, HOWEVER, IS ONLY UNTIL THE NEXT START OR RESTART.

ADD/UNITS:UNIT-LIST

THE UNITS SPECIFIED ARE ADDED BACK (THEY MUST HAVE BEEN PREVIOUSLY DROPPED BY THE DROP COMMAND) TO THE TEST SEQUENCE. AN ADD CANNOT BE FOLLOWED BY A PROCEED.

PRI(NT)

ALL STATISTICS TABLES ACCUMULATED BY THE DIAGNOSTIC ARE PRINTED. THE ISR (INHIBIT STATISTICAL REPORTING) FLAG IS CLEARED.

DIS(PLAY)/UNITS:<UNIT-LIST>

THE HARDWARE P-TABLES FOR ALL UNITS UNDER TEST ARE PRINTED OUT IN THE FORMAT IN WHICH THEY WERE ENTERED. ANY UNITS THAT WERE DROPPED BY THE OPERATOR "DROP" COMMAND ARE SO DESIGNATED.

FLA(GS)

THE CURRENT SETTINGS OF ALL FLAGS ARE PRINTED.

ZFL(AGS)

ALL FLAGS ARE CLEARED.

2.4 EXTENDED P-TABLE DIALOGUE

THE FULL CAPABILITY OF THE HARDWARE DIALOGUE IS REVEALED BY THE FOLLOWING DISCUSSION OF WHAT HAPPENS INTERNALLY.

AS SOON AS THE QUESTION "# UNITS?" IS ANSWERED (WITH THE NUMBER N, SAY) SPACE IN CORE IS ALLOCATED FOR N P-TABLES. ALL OF THE P-TABLES ARE OF THE SAME FORMAT, AND THERE IS A ONE-TO-ONE CORRESPONDENCE BETWEEN THE HARDWARE PARAMETER QUESTIONS AND THE SLOTS IN THE P-TABLE FORMAT.

ON THE FIRST TRIP THRU THE QUESTIONS, ALL OF THE SLOTS IN ALL OF THE P-TABLES ARE FILLED. IF THE OPERATOR TYPES IN LESS THAN AN EXPLICIT VALUE IN RESPONSE TO A PARTICULAR QUESTION, THESE VALUES ARE PLACED IN THE P-TABLES (ONE VALUE GOING INTO THE PROPER SLOT OF EACH P-TABLE BEGINNING WITH THE FIRST P-TABLE) UNTIL THE STRING OF VALUES IS EXHAUSTED. THE LAST VALUE THAT SLOT IN THE REMAINING P-TABLES.

ON SUBSEQUENT TRIPS THRU THE QUESTIONS, THE SAME PROCESS IS CARRIED OUT EXCEPT THAT THE EARLIEST P-TABLE NOT TO HAVE RECEIVED AN EXPLICIT VALUE IN ANY OF ITS SLOTS NOW ASSUMES THE ROLE THAT TABLE PLAYED IN THE FIRST TRIP.

THE SERIES OF QUESTIONS IS REISSUED UNTIL AT LEAST ONE QUESTION HAS RECEIVED N EXPLICIT VALUES FROM THE OPERATOR.

IN GIVING A STRING OF VALUES, COMMAS WITHOUT INTERVENING VALUES MAY BE USED TO INDICATE A REPETITION OF THE LAST NAMED VALUE.

A STRING OF VALUES MAY BE GIVEN AS A RANGE (6-10 FOR EXAMPLE). IF THE VALUES REPRESENT PURE NUMERICAL DATA, THIS SAMPLE RANGE TRANSLATES TO THE STRING 6,7,8,9,10 (AN INCREMENT OF 1). IF THE VALUES ARE ADDRESSES, THE SAMPLE RANGE TRANSLATES TO THE STRING 6,8,10 (AN INCREMENT OF 2).

NOW LET US SEE HOW WE COULD USE THESE CAPABILITIES TO CONSTRUCT A SET OF P-TABLES. ASSUME THAT WE HAVE 64 UNITS, AND THAT THERE ARE THREE HARDWARE PARAMETERS FOR EACH (THREE SLOTS IN THE P-TABLE, FOR THREE HARDWARE QUESTIONS IN THE DIALOGUE). LET THE DESIRED VALUE FOR THE FIRST PARAMETER BE THE NUMBER 75 FOR ALL 64 TABLES. LET THE DESIRED VALUE FOR THE SECOND PARAMETER BE EQUAL TO THE UNIT NUMBER (1-N) WHERE N IS THE DESIRED VALUE FOR THE UNIT. NUMBER 49. LET THE DESIRED VALUE FOR THE THIRD PARAMETER BE THE NUMBER 76 FOR THE FIRST 20 UNITS AND THE NUMBER 77 FOR THE LAST 44 UNITS.

THE FOLLOWING DIALOGUE WOULD ACCOMPLISH THIS GOAL:

```
# UNITS (D) ? 64
UNIT 1
<QUESTION 1> ? 75
<QUESTION 2> ? 1-20
<QUESTION 3> ? 76
UNIT 21
<QUESTION 1> ? 21-49, 51-64
<QUESTION 2> ? 77
<QUESTION 3> ? 77
```


THE FIRST TIME THE SERIES IS ASKED, SLOT ONE RECEIVES A 75 IN ALL 64 TABLES. SLOT TWO RECEIVES THE VALUES 1, 2, 3, ... 20 IN TABLES 1 THRU 20 AND A CONSTANT 20 IN TABLES 21 THRU 64. ... SLOT THREE RECEIVES A CONSTANT 76 IN ALL 64 TABLES.

THE SECOND TIME THRU THE SERIES, TABLES 21 THRU THE END ARE GOING TO BE AFFECTED (NOTE THAT THIS PIECE OF INFORMATION IS PRINTED OUT FOR THE OPERATOR IN THE FORM "UNIT XX" AT THE BEGINNING OF EACH SERIES). QUESTION 1 IS RESPONDED TO BY A <CR> SO SLOT ONE STAYS A CONSTANT 75 IN TABLES 21 THRU 64 EXPLICIT VALUES ARE TYPED IN SLOT TWO GETS THE VALUES 1, 2, 3, ... 49 IN TABLES 21 THRU 49 AND IN GETS A 49 IN SLOT 50, AND GETS THE VALUES 51, 52, 53, ... 64 IN TABLES 51 THRU 64. ... SLOT THREE GETS THE VALUE 77 IN TABLES 21 THRU 64.

THE DIALOGUE IS TERMINATED WHEN THE SOFTWARE RECOGNIZES THAT 64 EXPLICIT VALUES HAVE BEEN GIVEN FOR AT LEAST ON QUESTION (NAMELY QUESTION 2).

2.5 HARDWARE PARAMETERS

THE FOLLOWING QUESTIONS WILL BE ASKED ON A START COMMAND. THE VALUE LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN ON A CARRIAGE RETURN RESPONSE.

RL11 (L) Y?

ANSWER YES(Y) IF YOU HAVE AN RL11 CONTROLLER, NO(N) IF YOU HAVE AN RL11 CONTROLLER.

BUS ADDRESS (O) 17440?

ANSWER WITH THE BUS ADDRESS OF THE CONTROLLER.

VECTOR (O) 330?

ANSWER WITH THE INTERRUPT VECTOR OF THE CONTROLLER.

BR LEVEL (O) 5?

ANSWER WITH THE INTERRUPT PRIORITY OF THE CONTROLLER.

DRIVE (O) 0?

ANSWER WITH THE DRIVE(S) CONNECTED TO THE CONTROLLER.

2.6 SOFTWARE PARAMETERS

THE FOLLOWING QUESTIONS ARE ASKED IF REQUESTED ON A START, RESTART, OR CONTINUE. THEY ALLOW FLEXIBILITY IN THE WAY THE PROGRAM BEHAVES. THE SOFTWARE PARAMETERS GIVE THE PROGRAM FLEXIBILITY IN THE WAY IT RUNS. THE PARAMETERS CAN BE MODIFIED ON A START, RESTART, OR CONTINUE BY ANSWERING (Y)ES TO THE FOLLOWING QUESTION:

CHANGE S.W. ?

A YES ANSWER WILL ASK THE FOLLOWING SOFTWARE PARAMETER QUESTIONS, WITH THE PRESENT DEFAULT VALUE PRINTED TO THE LEFT OF THE QUESTION MARK. (THE LAST ANSWER GIVEN IS THE DEFAULT) THE DEFAULT IS TAKEN ON A <CR>. CONTROL Z (^Z) WILL DEFAULT ALL REMAINING QUESTIONS AND START THE TEST.

DROP ON ERROR LIMIT (L) Y?

TO ALLOW THE UNIT TO BE DROPPED ONCE A PREDETERMINED NUMBER OF ERRORS ARE ENCOUNTERED.

ANSWER Y OR N

ERROR LIMIT (D) 10?

NUMBER OF ERRORS ALLOWED BEFORE DROPPING UNIT.

ANSWER 1 TO 65K

AUTOSIZE (L) N?

TO CHECK TO SEE IF UNIT SPECIFIED ACTUALLY EXISTS BEFORE TESTING IT (VIA DRIVE READY), IF NOT UNIT WILL NOT BE TESTED.

ANSWER Y OR N

COMPARE DATA ON DCK (L) N?

WHEN A DATA CHECK IS ENCOUNTERED AND DATA IS KNOWN, ALLOW AN INCORE COMPARISON OF DATA.

ANSWER Y OR N

OF WORDS IN ERROR REPORTED (D) 3?

NUMBER OF MISCOMPARES TO BE PRINTED ON CONSOLE DEVICE.

ANSWER 0 - 128

3.0 ERROR INFORMATION

ALL ERROR INFORMATION IS PRINTED ON THE CONSOLE DEVICE. ERROR REPORTS ARE AIMED AT BEING SELF EXPLANATORY. THE GENERAL FORMAT IS:

DZRL? XXX ERR YVYVY TST ZZZ SUB PPP PC: RRRRRR

WHERE:

? XXX IS PROGRAM LETTER
IS SFT - SOFT ERROR
HRD - HARD ERROR

DV FAT - DEVICE FATAL ERROR
 SYS FAT - SYSTEM FATAL ERROR
 VVVVV IS THE ERROR NUMBER
 ZZZ IS THE TEST NUMBER
 PPP IS THE SUBTEST NUMBER
 RRRRRR IS THE PROGRAM LISTING LOCATION

ERRORS GIVE THE REGISTER CONTENTS BEFORE AND AFTER THE ERROR
 ALONG WITH A ONE LINE DESCRIPTION AND RELEVANT DATA.

EXAMPLE:

ONE LINE DESCRIPTION
 (OPTIONAL SECOND LINE)
 (OPTIONAL THIRD LINE)
 BEFORE COMMAND: CS:XXXXXX BA:XXXXXX DA:XXXXXX MP:XXXXXX
 TIME OF ERROR: CS:XXXXXX BA:XXXXXX DA:XXXXXX MP:XXXXXX XXXXXX XXXXXX

3.2 ERROR HALTS

ERROR HALTS ARE SUPPORTED PER DESCRIBED IN THE PREVIOUS SECTION WITH
 /FLAG:HOE. THERE ARE NO OTHER HALTS.

4.0 PERFORMANCE AND PROGRESS REPORTS

4.1 PERFORMANCE REPORTS

THIS PROGRAM WILL NOT GIVE ANY PERFORMANCE REPORTS.

4.2 PROGRESS REPORTS

THIS PROGRAM WILL NOT GIVE ANY PROGRESS REPORTS.

5.0 DEVICE INFORMATION TABLES

THE RL11/RLV11 CONTROLLER HAS THE FOLLOWING FOUR(4) REGISTERS FOR
 CONTROL OF THE SUBSYSTEM.

RLCS - CONTROL AND STATUS REGISTER (XXXXX0)

BIT 15 - COMPOSITE ERROR
 BIT 14 - DRIVE ERROR
 BIT 13 - NON EXISTANT MEMORY ERROR
 BIT 12 - HEADER NOT FOUND (WITH BIT 10 SET)
 BIT 11 - DATA LATE (WITH BIT 10 CLEAR)
 BIT 10 - HEADER CRC (WITH BIT 10 SET)
 BIT 9 - DATA CRC (WITH BIT 10 CLEAR)
 BIT 8 - OPERATION INCOMPLETE
 BIT 7/8 - DRIVE SELECT (0-3)

BIT 7 - CONTROLLER READY
 BIT 6 - INTERRUPT ENABLE
 BIT 5 - EXTENDED BUS ADDRESS (BIT 17)
 BIT 4 - EXTENDED BUS ADDRESS (BIT 16)
 BIT 3-1 - FUNCTION CODE
 0 - NOP (LDP-11) MAINT (LSI-11)
 1 - WRITE CHECK
 2 - GET DRIVE STATUS
 3 - SEEK
 4 - READ HEADER
 5 - WRITE DATA
 6 - READ DATA
 7 - READ WITHOUT HEADER COMPARE
 BIT 0 - DRIVE READY

RLBA - BUS ADDRESS REGISTER (XXXXX2)

BITS 15-1 BUS ADDRESS OF DATA TRANSFER
 BIT 0 SHOULD BE 0

RLDA - DISK ADDRESS REGISTER (XXXXX4)

FOR READ/WRITE FUNCTIONS

BIT 15 - MUST BE ZERO(0)
 BIT 14-7 - CYLINDER ADDRESS FOR TRANSFER
 BIT 6 - SURFACE FOR TRANSFER
 BIT 5-0 - SECTOR FOR TRANSFER (0-47)

FOR SEEK FUNCTION

BIT 15 - MUST BE ZERO(0)
 BIT 14-7 - DIFFERENCE TO NEW CYLINDER
 BIT 6-5 - MUST BE ZERO(0)
 BIT 4 - SURFACE
 BIT 3 - MUST BE ZERO
 BIT 2 - SEEK DIRECTION(1 - IN / 0 - OUT)
 BIT 1 - MUST BE ZERO
 BIT 0 - MUST BE ONE(1)

FOR GET STATUS FUNCTION

BIT 15-4 - IGNORED SHOULD BE ZERO
 BIT 3 - DRIVE RESET
 BIT 2 - MUST BE ZERO
 BIT 1 - MUST BE ONE
 BIT 0 - MUST BE ONE

RLMP - MULTIPURPOSE REGISTER

FOR READ/WRITE FUNCTION

BIT 15 - 0 - WORD COUNT(TWO'S COMPLIMENT)

FOR READ HEADER FUNCTION

BIT 15-0 - DISK HEADER OF SECTOR (FIRST READ)
- ZERO WORD (SECOND READ)
- HEADER CRC (THIRD READ)

FOR GET STATUS FUNCTION

HAS DRIVE STATUS

- BIT 15 - WRITE DATA ERROR
- BIT 14 - CURRENT HEAD ERROR(CHE)
- BIT 13 - WRITE LOCK STATUS(WL)
- BIT 12 - SEEK TIME OUT(SKID)
- BIT 11 - SPIN ERROR(SPE)
- BIT 10 - WRITE GATE ERROR(WGE)
- BIT 9 - VOLUME CHECK(VC)
- BIT 8 - DRIVE SELECT ERROR(DSE)
- BIT 7 - RESERVED(0)
- BIT 6 - SURFACE OPEN
- BIT 5 - HEADS HOME
- BIT 4 - BRUSHES HOME
- BIT 3 - STATE BITS
- BIT 2-0 - LOAD STATE
 - 0 - SPIN UP
 - 1 - BRUSH CYCLE
 - 2 - LOAD HEADS
 - 3 - TRACK COUNTING
 - 4 - SEEK - LINEAR MODE
 - 5 - UNLOAD HEADS
 - 6 - SPIN DOWN
 - 7 -

6.0 TEST SUMMARIES

TEST 01 - WRITE NPR INTEGRITY

THIS TEST WILL VERIFY THAT THE WRITE FUNCTION WILL NOT CAUSE
A BUS TRAP THEREFORE VERIFYING THE NPR LOGIC BETWEEN THE
CONTROLLER AND PROCESSOR.

TEST 02 - WRITE FUNCTION

THIS TEST WILL VERIFY THAT THE WRITE FUNCTION WILL RESET CONTROLLER READY AND POST NO ERRORS.

TEST 03 - WRITE FUNCTION INTERRUPT

THIS TEST WILL VERIFY THAT THE WRITE FUNCTION WILL GENERATE AN INTERRUPT ON COMPLETION.

TEST 04 - PROPER INCREMENT OF RLBA ON WRITE

THIS TEST WILL VERIFY THAT THE BUS ADDRESS REGISTER INCREMENTS PROPERLY ON A WRITE FUNCTION.

TEST 05 - PROPER INCREMENT OF RLDA ON WRITE

THIS TEST WILL VERIFY THAT THE DISK ADDRESS REGISTER INCREMENTS PROPERLY ON A WRITE FUNCTION.

TEST 06 - FORCE HEADER NOT FOUND WITH WRITE

THIS TEST WILL FORCE A HEADER NOT FOUND ERROR ON A WRITE. THE RLDA IS SET UP TO LOOK FOR SECTOR 40. A WRITE IS THEN ISSUED. THE HEADER NOT FOUND ERROR SHOULD THEN SET.

TEST 07 - FORCE INTERRUPT WITH HNF

THIS TEST WILL FORCE A HEADER NOT FOUND ERROR UNDER INTERRUPT CONTROL.

TEST 08 - CHECK OPI TIME WITH HNF

THIS TEST WILL TIME THE SETTING OF HNF (OPI) FROM ISSUANCE. THIS IS DONE BY ISSUING A WRITE TO SECTOR 40. THE TIME OF OPI SHOULD BE AROUND 200 MILLISECONDS.

TEST 09 - MULTIPLE SECTOR TRANSFER ON WRITE

THIS TEST THE ABILITY FOR THE WRITE FUNCTION TO WRITE MORE THAN ONE SECTOR. WE SET UP FOR A TWO SECTOR WRITE.

TEST 10 - CHECK DIRECTION OF WRITE NPR

THIS TEST WILL VERIFY THAT THE NPR DIRECTION OF A WRITE FUNCTION IS FROM MEMORY TO THE CONTROLLER. THIS IS DONE BY WRITING A PATTERN IN MEMORY AND ISSUING A WRITE, THEN CHECKING MEMORY TO VERIFY THAT IT DID NOT GET DISTURBED.

TEST 11 - CHECK FULL INCREMENT OF RLBA

THIS TEST WILL CHECK THAT THE RLBA CAN INCREMENT OF THE FULL 16 BIT RANGE. THIS IS DONE BY ISSUING A ONE WORD WRITE TO CHECK EACH BIT TOGGLE FROM 1-0 AND 0-1. THIS IS DONE FROM 0 TO 177776 REGARDLESS OF MEMORY SIZE.

TEST 12 - BA BIT 16 INCREMENT

THIS TEST WILL CHECK THAT BUS ADDRESS BIT 16 WILL SET WHEN THE RLBA IS 177776. AND THAT THE RLBA GOES TO 0.

TEST 13 - BA BIT 17 INCREMENT

THIS TEST WILL CHECK THAT BUS ADDRESS BIT 17 WILL SET WHEN BIT 16 AND THE RLBA ARE SET. THE RLBA AND BIT 16 ARE CHECKED TO GO TO ZERO.

TEST 14 - READ NPR INTEGRITY

THIS TEST WILL VERIFY THAT THE READ FUNCTION WILL NOT CAUSE A BUS TRAP THEREFORE VERIFYING THE NPR LOGIC BETWEEN THE CONTROLLER AND PROCESSOR.

TEST 15 - READ FUNCTION

THIS TEST WILL VERIFY THAT THE READ FUNCTION WILL RESET CONTROLLER READY AND POST NO ERRORS.

TEST 16 - READ FUNCTION INTERRUPT

THIS TEST WILL VERIFY THAT THE READ FUNCTION WILL GENERATE AN INTERRUPT ON COMPLETION.

TEST 17 - CHECK DIRECTION OF READ NPR

THIS TEST WILL VERIFY THAT THE NPR DIRECTION OF A READ FUNCTION IS FROM CONTROLLER TO THE MEMORY. THIS IS DONE BY WRITING A PATTERN IN MEMORY AND ISSUING A READ. THEN CHECKING MEMORY TO VERIFY THAT IT DID NOT GET DISTURBED.

TEST 18 - PROPER INCREMENT OF RLBA ON READ

THIS TEST WILL VERIFY THAT THE BUS ADDRESS REGISTER INCREMENTS PROPERLY ON A READ FUNCTION.

- TEST 19 - PROPER INCREMENT OF RLDA ON READ
 THIS TEST WILL VERIFY THAT THE DISK ADDRESS REGISTER INCREMENTS PROPERLY ON A READ FUNCTION.
- TEST 20 - FORCE HEADER NOT FOUND WITH READ
 THIS TEST WILL FORCE A HEADER NOT FOUND ERROR ON A READ. THE RLDA IS SET UP TO LOOK FOR SECTOR 40. A READ IS THEN ISSUED. THE HEADER NOT FOUND ERROR SHOULD THEN SET.
- TEST 21 - FORCE INTERRUPT WITH HNF
 THIS TEST WILL FORCE A HEADER NOT FOUND ERROR UNDER INTERRUPT CONTROL.
- TEST 22 - CHECK HEADER COMPARE LOGIC
 THIS TEST WILL EXTENSIVELY CHECK THE CYLINDER AND HEAD BITS OF THE HEADER WORD TO COMPARE CORRECTLY. THIS IS DONE BY WALKING AND GROWING 0'S AND 1'S THRU THE PROPER RLDA BITS AND ISSUING READ TO SEE IF ALL BIT POSITIONS CAN COMPARE.
- TEST 23 - MULTIPLE SECTOR TRANSFER ON READ
 THIS TEST THE ABILITY FOR THE READ FUNCTION TO WRITE MORE THAN ONE SECTOR. WE SET UP FOR A TWO SECTOR READ.
- TEST 24 - FORCE HNF AT END OF TRACK
 THIS TEST WILL CHECK THE ABILITY TO DETECT HEADER NOT FOUND AT THE END OF A TRACK. THIS DONE BY SETTING UP FOR A TWO SECTOR READ AT SECTOR 39.
- TEST 25 - FORCE NON-EXISTANT MEMORY ERROR
 THIS TEST WILL CHECK THAT THE NON-EXISTANT MEMORY ERROR (NXM) CAN SET. WE WILL ISSUE A READ TO THE MAXIMUM ADDRESS AND EXPECT A NXM ERROR. (THIS TEST WILL NOT BE DONE ON A 128K MACHINE.)
- TEST 26 - FORCE NXM UNDER INTERRUPT
 THIS TEST WILL ATTEMPT TO FORCE AN INTERRUPT VIA NXM. (THIS TEST WILL NOT BE DONE ON A 128K MACHINE.)
- TEST 27 - CHECK READ WRITE LOOP

THIS TEST WILL WRITE A PATTERN TO SECTOR 0 AND TRY TO RECOVER IT WITH A WRITE.

TEST 28 - CHECK OF SILO LINES

THIS TEST WILL CHECK THAT WE CAN WRITE AND READ UNIQUE BIT PATTERNS VERIFY THAT THE LINES ON THE SILO ARE NOT STUCK OR TIED TOGETHER. THIS IS DONE WITH WALKING AND GROWING 0'S AND 1'S.

TEST 29 - CHECK THROUGHPUT OF SILO

THIS TEST WILL ATTEMPT TO CHECK THAT THE FALL THROUGH OF THE SILO IS WORKING CORRECTLY. WE WRITE A SECTOR OF 128 UNIQUE PATTERNS AND READ IT BACK CHECKING THAT EACH LOCATION IS UNIQUE AND CORRECT.

TEST 30 - CHECK ZERO FILL ON WRITE

THIS TEST WILL CHECK THE ABILITY OF THE CONTROLLER TO FILL THE REMAINING SECTOR WITH ZEROS ON A WRITE. WE WRITE A SECTOR WITH FROM 1 TO 127 WORDS, READ IT BACK AND VERIFY THAT THE NON WRITTEN WORDS ARE ZERO.

TEST 31 - CHECK SECTOR BITS ON HEADER COMPARE

THIS TEST WILL CHECK THAT THE SECTOR BITS CAN COMPARE CORRECTLY. THIS IS DONE BY WRITING THE SECTORS ADDRESS INTO THE SECTOR FOR A FULL TRACK. EACH SECTOR IS READ TO VERIFY THE SECTOR HAS THE CORRECT DATA, IF NOT THEN THE SECTOR BITS ARE NOT COMPARING CORRECTLY.

TEST 32 - WRITE CHECK NPP INTEGRITY

THIS TEST WILL CHECK THAT THE WRITE CHECK WILL FUNCTION WITHOUT CAUSING A BUS TRAP. TEST IS SET UP TO HANDLE BUS TRAPS.

TEST 33 - WRITE CHECK FUNCTION

THIS TEST WILL CHECK THAT A WRITE CHECK FUNCTION WILL COMPLETE WITH THE SPECIFIED TIME WITHOUT POSTING ERRORS.

TEST 34 - WRITE CHECK FUNCTION INTERRUPT

THIS TEST WILL CHECK THAT AN INTERRUPT CAN BE GENERATED FROM ISSUING A WRITE CHECK.

TEST 35 - PROPER INCREMENT OF PLBA ON WRITE CHECK

THIS TEST WILL CHECK THAT THE PLBA INCREMENTS PROPERLY DURING A

WRITE CHECK.

- TEST 36 - PROPER INCREMENT OF RLDA ON WRITE CHECK
THIS TEST WILL CHECK THAT THE RLDA INCREMENTS PROPERLY DURING A WRITE CHECK.
- TEST 37 - MULTIPLE SECTOR WRITE CHECK
THIS TEST WILL CHECK THAT WE CAN WRITE CHECK MORE THAN ONE SECTOR AT A TIME.
- TEST 38 - FORCE DCK WITH WRITE CHECK
THIS TEST WILL CHECK THAT WE CAN DETECT A DCK DURING A WRITE CHECK. THIS IS DONE BY MODIFYING MEMORY BETWEEN A WRITE AND A WRITE CHECK.
- TEST 39 - FORCE DCK WITH WRITE CHECK INTERRUPT
THIS TEST WILL CHECK THAT A DCK DURING A WRITE CHECK WILL CAUSE AN INTERRUPT TO OCCUR.
- TEST 40 - CHECK ZERO FILL ON WRITE WITH WRITE CHECK
THIS TEST WILL VERIFY THAT WE CAN SUCCESSFULLY WRITE CHECK ALL WORD COUNTS FROM 1 - 127.
- TEST 41 - 42 - EXTENDED CHECK OF WRITE CHECK
THESE TESTS VERIFY THAT WE CAN WRITE CHECK SUCCESSFULLY ALL PATTERNS. PATTERNS USED ARE WALKING 1'S, 0'S, GROWING 1'S, 0'S.
- TEST 43 - READ WITHOUT HEADER COMPARE
THIS TEST VERIFIES THAT THE FUNCTION READ WITHOUT HEADER COMPARE (7) RESETS THE CONTROLLER READY AND POSTS NO ERRORS. THE DISK ADDRESS IS SET TO ALL ONES.
- TEST 44 - READ WITHOUT HEADER COMPARE INTERRUPT
THIS TEST WILL VERIFY THAT THE FUNCTION READ WITHOUT HEADER COMPARE (7) CAN GENERATE AN INTERRUPT ON COMPLETION.
- TEST 45 - CHECK RD W/O HDR CMP READS
THIS TEST CHECKS THAT THE FUNCTION CAN ACTUALLY RECOVER DATA. WE WRITE A PATTERN IN MEMORY AND CHECK THAT THE FUNCTION CAN OVERLAY IT WITH DATA.
- TEST 46 - CHECK RLBA INCREMENT WITH RD W/O HDR CMP

THIS TEST CHECKS THAT THE RLBA CAN INCREMENT PROPERLY ON THE FUNCTION.

TEST 47 - CHECK RLDA DOES INCREMENT

THIS TEST CHECKS THAT THE RLDA DOES INCREMENT WITH THE FUNCTION READ WITHOUT HEADER COMPARE.

```

1          .ENABLE ANA
2          .ENABLE ABS
3          .WLST ME,CND,HD
4
5
6
7
8
9
10         002000          .-=2000
11
12         002000          SVC
13         000000          SVCINS=0
14         000000          SVCTAG=0
15
16         002000          POINTER BCNSW,BCNSFT,BCNDU
17
18         002000          BGNMOD MOHEDR
19
20         002000          HEADER CZRLB,B,0,60,60,4,RL01
21         002001          .ASCII /C/
22         002002          .ASCII /Z/
23         002003          .ASCII /R/
24         002004          .ASCII /L/
25         002005          .ASCII /B/
26         002006          .BYTE 0
27         002007          .BYTE 0
28         002010          .ASCII /B/
29         002011          .ASCII /O/
30         002012          .WORD 0
31         002013          .WORD 4
32         002014          .WORD LSHARD
33         002015          .WORD LSSOFT
34         002016          .WORD LSHW
35         002017          .WORD LSSW
36         002018          .WORD LSLAST
37         002019          .WORD 0
38         002020          .WORD 0
39         002021          .WORD 0
40         002022          .WORD 0
41         002023          .WORD 0
42         002024          .WORD 0
43         002025          .WORD 0
44         002026          .WORD 0
45         002027          .WORD 0
46         002028          .WORD 0
47         002029          .WORD 0
48         002030          .WORD 0
49         002031          .WORD 0
50         002032          .WORD 0
51         002033          .WORD 0
52         002034          .WORD 0
53         002035          .WORD 0
54         002036          .WORD 0
55         002037          .WORD 0
56         002038          .WORD 0
57         002039          .WORD 0
58         002040          .WORD 0
59         002041          .WORD 0
60         002042          .WORD 0
61         002043          .WORD 0
62         002044          .WORD 0
63         002045          .WORD 0
64         002046          .WORD 0
65         002047          .WORD 0
66         002048          .WORD 0
67         002049          .WORD 0
68         002050          .WORD 0
69         002051          .WORD 0
70         002052          .WORD 0
71         002053          .WORD 0
72         002054          .WORD 0
73         002055          .WORD 0
74         002056          .WORD 0
75         002057          .WORD 0
76         002058          .WORD 0
77         002059          .WORD 0
78         002060          .WORD 0
79         002061          .WORD 0
80         002062          .WORD 0
81         002063          .WORD 0
82         002064          .WORD 0
83         002065          .WORD 0
84         002066          .WORD 0
85         002067          .WORD 0
86         002068          .WORD 0
87         002069          .WORD 0
88         002070          .WORD 0
89         002071          .WORD 0
90         002072          .WORD 0
91         002073          .WORD 0
92         002074          .WORD 0

```

```

(4) 002076 020104          .WORD L$DU
(4) 002102 000000          .WORD 14
(4) 002104 017140          .WORD 0
(4) 002106 020010          .WORD L$INIT
(4) 002106 020010          .WORD L$CLEAN
(4) 002110          ENDMOD
(5) 002110          DEVREG
(4) 002112 000000          .WORD 0
(4) 002112 000001          .BLKW
(3) 002114          DEVTYP <RL01>
(2) 002114 046122 030460 000 .ASCIIZ /RL01/
(2) 002114 002122          .EVEN
(3) 002122          BGNMOD GLBEQAT
(3) 002122          EQUALS
40         000001          ;DRIVE READY (RLCS)
41         000100          ;INTERRUPT ENABLE (RLCS)
42         100000          ;RL11 ERROR (RLCS)
43         040000          ;RL01 DRIVE ERROR (RLCS)
44         002000          ;OPERATION INCOMPLETE (RLCS)
45         000200          ;CONTROLLER READY (RLCS)
46         000040          ;EXTENDED ADDRESS BIT 17 (RLCS)
47         000020          ;EXTENDED ADDRESS BIT 16 (RLCS)
48         020000          ;NON-EXISTANT MEMORY (RLCS)
49         000000          ;DRIVE SELECT 0 (RLCS)
50         000400          ;DRIVE SELECT 1 (RLCS)
51         001000          ;DRIVE SELECT 2 (RLCS)
52         001400          ;DRIVE SELECT 3 (RLCS)
53         000000          ;FUNCTION-NOOP(0)
54         000002          ;WRITE CHECK FUNCTION
55         000004          ;GET STATUS FUNCTION
56         000006          ;SEEK FUNCTION
57         000010          ;READ HEADER FUNCTION
58         000012          ;WRITE DATA FUNCTION
59         000014          ;READ DATA FUNCTION
60         000016          ;READ W/O HEADER VERIFICATION
61         000202          ;CRDY AND DRDY
62         000100          ;DRIVE RESET (RLDA)
63         000002          ;GET STATUS BIT (RLDA)
64         000001          ;MARKER BIT (RLDA)
65         000004          ;SIGN BIT (RLDA)
66         000100          ;HEAD SELECT IN READ HEADER
67         000100          ;HEAD SELECT IN STATUS BACK
68         000020          ;HEAD SELECT IN SEEK
69         000020          ;OFFSET FOR HARDWARE P-TABLE
70
71         000000          CSR=0
72         000002          VECT=2
73         000004          PRIOR=4
74         000006          DRBT=6
75         000010          CNT=10
76
77         ;OFFSET FOR SOFTWARE P-TABLE

```

88	GLOBAL DATA
154	LIST TO CHECK HEADER COMPARE LOGIC
221	BUFFER FOR READ/WRITE
227	GLOBAL TEXT
334	GLOBAL ERRORS
589	INITIALIZATION CODE
723	GLOBAL SUBROUTINES
757	ROUTINE TO CHECK FOR CONTROLLER ERRORS
819	LOAD RLCS
1056	**TEST 1** - WRITE NPR INTEGRITY
1107	**TEST 2** - WRITE FUNCTION
1163	**TEST 3** - WRITE FUNCTION INTERRUPT
1205	**TEST 4** - PROPER INCREMENT OF RLBA ON WRITE
1248	**TEST 5** - PROPER INCREMENT OF RLDA ON WRITE
1291	**TEST 6** - FORCE HEADER NOT FOUND WITH WRITE
1334	**TEST 7** - FORCE HEADER NOT FOUND WITH WRITE INTERRUPT
1390	**TEST 8** - CHECK OPI TIME WITH HDR NT FND
1453	**TEST 9** - MULTIPLE SECTOR TRANSFER ON WRITE
1506	**TEST 10** - CHECK DIRECTION OF WRITE NPR
1564	**TEST 11** - CHECK FULL RLBA INCREMENT
1614	**TEST 12** - BA BIT 16 INCREMENT
1670	**TEST 13** - BA BIT 17 INCREMENT
1726	**TEST 14** - TEST READ NPR INTEGRITY
1769	**TEST 15** - READ FUNCTION
1803	**TEST 16** - READ FUNCTION INTERRUPT
1843	**TEST 17** - CHECK READ NPR DIRECTION
1905	**TEST 18** - PROPER INCREMENT OF RLBA ON READ
1945	**TEST 19** - PROPER INCREMENT OF RLDA ON READ
1987	**TEST 20** - FORCE HEADER NOT FOUND WITH READ
2026	**TEST 21** - FORCE HEADER NOT FOUND WITH READ INTERRUPT
2075	**TEST 22** - CHECK HEADER COMPARE LOGIC
2206	**TEST 23** - CHECK MULTIPLE SECTORS ON READ
2265	**TEST 24** - FORCE HDR NT FND AT END OF TRACK
2301	**TEST 25** - FORCE NON-EXISTANT MEMORY ERROR
2344	**TEST 26** - FORCE NON-EXISTANT MEMORY ERROR INTERRUPT
2391	**TEST 27** - CHECK READ WRITE LOOP
2477	**TEST 28** - CHECK SILO LINES
2574	**TEST 29** - CHECK THROUGHPUT OF SILO
2670	**TEST 30** - CHECK ZERO FILL ON WRITE
2775	**TEST 31** - CHECK SECTOR BITS OF HEADER COMPARE
2887	**TEST 32** - WRITE CHECK NPR INTEGRITY
2970	**TEST 33** - WRITE CHECK FUNCTION
3035	**TEST 34** - WRITE CHECK FUNCTION INTERRUPT
3106	**TEST 35** - PROPER INCREMENT OF RLBA ON WRITE CHECK
3179	**TEST 36** - PROPER INCREMENT OF RLDA ON WRITE CHECK
3252	**TEST 37** - MULTIPLE SECTOR WRITE CHECK
3338	**TEST 38** - FORCE DCK WITH WRITE CHECK
3411	**TEST 39** - FORCE DCK WITH WRITE CHECK INTERRUPT
3495	**TEST 40** - CHECK ZERO FILL ON WRITE WITH WRITE CHECK
3572	**TEST 41** - EXTENDED CHECK OF WRITE CHECK FUNCTION
3653	**TEST 42** - EXTENDED CHECK OF WRITE CHECK FUNCTION
3734	**TEST 43** - READ WITHOUT HEADER COMPARE FUNCTION
3764	**TEST 44** - READ WITHOUT HEADER COMPARE FUNCTION INTERRUPT
3800	**TEST 45** - CHECK RD W/O HDR CMP ACTUALLY READS
3862	**TEST 46** - CHECK RLBA INCREMENT WITH RD W/O HDR CMP
3908	**TEST 47** - CHECK RLDA DOES INCREMENT WITH RD W/O HDR CMP

4015	DIAGNOSTIC SUPERVISOR -- LOW CORE SET UP
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78					
79	000000		DLT=0		
80	000002		ELT=2		
81	000004		SIZE=4		
82	000006		DMPC=6		
83	000010		DLWT=10		
84					
85	002122				
86	002122				
87			BGNMOD	ENDMOD	
88				GLBDAT	
89			.SBTTL GLOBAL DATA		
90	002122	000000	CHECK:	.WORD	0
91	002124	000000	T.CRC:	.WORD	0
92	002126	000000	WHY:	.WORD	0
93	002130	000000	CDCMT:	.WORD	0
94	002132	000004	ERRVEC:	.WORD	4
95	002134	000000	DRIVE:	.WORD	0
96	002136	000000	UUT:	.WORD	0
97	002140	000000	UNITST:	.WORD	0
98	002142	000000	TRPFLG:	.WORD	0
99	002144	000000	TRTFLG:	.WORD	0
100	002146	000000	LDSC:	.WORD	0
101	002150	000077	SECMASK:	.WORD	77
102	002152	120001	XPOLY:	.WORD	120001
103	002154	000000	BCCFBK:	.WORD	0
104	002156	000000	CALBCC:	.WORD	0
105	002160	000000	TMP0:	.WORD	0
106	002162	000000	TMP1:	.WORD	0
107	002164	000000	TMP2:	.WORD	0
108	002166	000000	GDDAT:	.WORD	0
109	002170	000000	BDDAT:	.WORD	0
110	002172	000000	TEMP:	.WORD	0
111	002174	000000	TEMP:	.WORD	0
112	002176	000000	TEMP:	.WORD	0
113	002200	000000	FIRST:	.WORD	0
114	002202	177700	CYLMSK:	.WORD	177700
115	002204	000050	MXSECT:	.WORD	40
116	002206	000047	MAXSEC:	.WORD	39
117	002210	000000	WORDC:	.WORD	0
118	002212	077600	MAXCYL:	.WORD	77600
119	002214	000000	SVHD:	.WORD	0
120	002216	000000	B.CS:	.WORD	0
121	002218	000000	B.BA:	.WORD	0
122	002220	000000	B.DA:	.WORD	0
123	002222	000000	B.MP:	.WORD	0
124	002224	000000	E.CS:	.WORD	0
125	002226	000000	E.BA:	.WORD	0
126	002228	000000	E.DA:	.WORD	0
127	002230	000000	E.MP:	.WORD	0
128	002232	000000	E.MP1:	.WORD	0
129	002234	000000	E.MP2:	.WORD	0
130	002236	000000	RLCS:	.WORD	0
131	002238	000000	RLBA:	.WORD	0
132	002240	000000	RLDA:	.WORD	0
133	002242	000000	RLWA:	.WORD	0
134	002244	000000	RLWA:	.WORD	0
135	002246	000000	RLWA:	.WORD	0
136	002248	000000	RLWA:	.WORD	0
137	002250	000000	RLWA:	.WORD	0

```

;INTERRUPT OCCURANCE FLAG
;LOCATION TO FORM RLCS
;MASK OUT SECTOR
;POLYNOMIAL FOR CRC 16
;LOCATION USED BY "SIMBCC"
;LOCATION USED BY "SIMBCC"

;LOCATION USED BY "SIMBCC"
;LOCATION USED BY "SIMBCC"
;LOCATION USED BY "SIMBCC"
;FIRST SECTOR READ
;MASK CYLINDER AND HEAD SELECT
;MAX SECTOR ADDRESS +1
;MAX SECTOR ADDRESS
;DIFFERENCE WORD (SEEK)
;MAXIMUM CYLINDER ADDRESS
;SAVE CURRENT HEAD SELECT
;CS - BEFORE OPERATION
;BA - BEFORE OPERATION
;DA - BEFORE OPERATION
;MP - BEFORE OPERATION
;CS - AT OCCURANCE OF ERROR
;BA - AT OCCURANCE OF ERROR
;DA - AT OCCURANCE OF ERROR
;MP - AT OCCURANCE OF ERROR
  
```

134	002252	000000	BCSR:	.WORD	0
135	002254	000000	BVEC:	.WORD	0
136	002256	000000	BPRIO:	.WORD	0
137	002258	000000	FNDPNC:	.WORD	0
138	002260	000000	XHEW:	.WORD	0
139	002264	000000	TRVFN:	.WORD	0
140	002266	000000	ERFLG:	.WORD	0
141	002270	001212	LOPIMX:	.WORD	650
142	002274	000233	LOPIMN:	.WORD	155
143	002278	000240	UOPIMX:	.WORD	400
144	002282	000240	UOPIMN:	.WORD	160
145	002300	000000	OPIMN:	.WORD	0
146	002302	000000	OPIMX:	.WORD	0
147	002304	000000	PWRFLG:	.WORD	0
148	002306	000000	T.CNTR:	.WORD	0
149	002310	000000	DERFLG:	.WORD	0
150	002312	000000	ERPOINT:	.WORD	0
151	002314	000074	ERCOUNT:	.BLKW	60
152					
153					
154					
155	002504	000000	.SBTTL LIST TO CHECK HEADER COMPARE LOGIC		
156	002506	000001	HDRTAB:	.WORD	0
157	002510	000002		.WORD	BIT0
158	002514	000004		.WORD	BIT1
159	002518	000010		.WORD	BIT2
160	002522	000020		.WORD	BIT3
161	002526	000040		.WORD	BIT4
162	002530	000100		.WORD	BIT5
163	002534	000200		.WORD	BIT6
164	002538	000400		.WORD	BIT7
165	002542	000800		.WORD	BIT8
166	002546	001600		.WORD	BIT9
167	002550	003200		.WORD	BIT10
168	002554	006400		.WORD	BIT11
169	002558	012800		.WORD	BIT12
170	002562	025600		.WORD	BIT13
171	002566	051200		.WORD	BIT14
172	002570	010007		.WORD	3
173	002574	000017		.WORD	7
174	002578	000037		.WORD	17
175	002582	000137		.WORD	37
176	002586	000337		.WORD	137
177	002590	000837		.WORD	337
178	002594	001737		.WORD	737
179	002598	003337		.WORD	1737
180	002602	007737		.WORD	3737
181	002606	017737		.WORD	7737
182	002610	037737		.WORD	17737
183	002614	077737		.WORD	37737
184	002576	077736		.WORD	77736
185	002600	077734		.WORD	77734
186	002604	077730		.WORD	77730
187	002608	077720		.WORD	77720
188	002612	077700		.WORD	77700
189	002616	077600		.WORD	77600

```

;GROW 1
;GROW 0
  
```

190 002612 077400 .WORD 77400
191 002614 077000 .WORD 77000
192 002616 076000 .WORD 76000
193 002620 074000 .WORD 74000
194 002622 070000 .WORD 70000
195 002624 060000 .WORD 60000
196 002626 077735 .WORD 77735
197 002630 077733 .WORD 77733
198 002632 077727 .WORD 77727
199 002634 077637 .WORD 77637
200 002636 077637 .WORD 77637
201 002640 077537 .WORD 77537
202 002642 077337 .WORD 77337
203 002644 076337 .WORD 76337
204 002646 073337 .WORD 73337
205 002650 073337 .WORD 73337
206 002652 057737 .WORD 57737
207 002654 057737 .WORD 57737
208 002656 037737 .WORD 37737
209 002660 000000 .WORD 0
HDREND: .WORD 0
211 002662 000001 000002 000004 DATPAT: .WORD 1,2,4,10,20,40,100,200,400,1000,2000,4000,10000,20000,40000,100000
212 002670 000010 000020 000040
213 002730 177776 177775 .WORD 177777,177776,177775,177773,177767,177757,177737,177677
214 002740 177577 175777 176777 .WORD 177577,177377,176777,175777,173777,167777,157777,137777
215 002760 177770 177760 177740 .WORD 177777,177774,177770,177760,177740,177700,177600,177400
216 002770 177600 177400 174000 .WORD 177000,176000,174000,170000,160000,140000,3,7,17,37,77
217 003030 000037 000077 000177 .WORD 177,377,777,1777,3777,7777,17777,37777,0
218 003040 000177 000377 000777
219 003044 017777 037777 000000
220
221 003052 002000 .SBTTL BUFFER FOR READ/WRITE
222 BUF: .BLKW 1024.
223
224
225 007052 ENDMOD
226
227 .SBTTL GLOBAL TEXT
228 BGMNOD CLBITI
229 007052 047516 041440 047117 HORES: .ASCIZ /NO CONTROLLER/

233 007070 047516 042040 044522 .ASCIZ /NO DRIVE/
234 007101 103 035123 000040 .ASCIZ /CS: /
235 007103 041040 035101 000040 .ASCIZ /BA: /
236 007113 042040 035101 000040 .ASCIZ /DA: /
237 007113 042040 035101 000040 .ASCIZ /DB: /
238 007130 047506 042522 .ASCIZ /BEFORE COMMAND: /
239 007151 124 046511 020105 .ASCIZ /TIME OF ERROR: /
240 007172 047503 052116 047522 .ASCIZ /CONTROLLER TIMED OUT/
241 007247 104 044522 042522 .ASCIZ /DRIVE READY TIMED OUT/
242 007452 047040 046510 000000 .ASCIZ /RKM/
243 007452 047040 046510 000000 .ASCIZ /RKM/
244 007452 047040 046510 000000 .ASCIZ /RKM/
245 007264 044040 051103 000103 .ASCIZ /HCRCL/
246 007272 044040 043116 000000 .ASCIZ /HMF/
247 007304 042040 042522 000113 .ASCIZ /DCK/
248 007311 015 052114 000000 .ASCIZ /DLT/
249 007311 015 000000 .ASCIZ /LF:
250 007313 015 000012 .ASCIZ /M\$CRLF:
251 007316 041440 046517 000120 .ASCIZ /COMP/
252 007324 047506 041522 042105 .ASCIZ /FORCED OPI(GET STATUS) CAUSED OTHER ERRORS/
253 007324 047506 041522 020120 .ASCIZ /NOOP OPERATION-FLAG MODE/
254 007430 047516 050117 047440 .ASCIZ /NOOP OPERATION-INTR. MODE/
255 007462 051122 052111 020105 .ASCIZ /WRITE CHECK OPERATION-FLAG MODE/
256 007522 051122 052111 020105 .ASCIZ /WRITE CHECK OPERATION-INTR. MODE/
257 007563 122 040505 020104 .ASCIZ /READ HEADER OPERATION-FLAG MODE/
258 007623 122 040505 020104 .ASCIZ /READ HEADER OPERATION-INTR. MODE/
259 007666 042522 045805 047440 .ASCIZ /SEEK OPERATION-FLAG MODE/
260 007713 107 052105 051440 .ASCIZ /SEEK OPERATION-INTR. MODE/
261 007747 107 052105 051440 .ASCIZ /GET STATUS OPERATION-FLAG MODE/
262 010006 042507 020124 052123 .ASCIZ /GET STATUS OPERATION-INTR. MODE/
263 010045 122 040505 020104 .ASCIZ /READ OPERATION-FLAG MODE/
264 010076 042522 042101 047440 .ASCIZ /READ OPERATION-INTR. MODE/
265 010127 122 044522 042522 .ASCIZ /WRITE OPERATION-FLAG MODE/
266 010161 122 044522 042522 .ASCIZ /WRITE OPERATION-INTR. MODE/
267 010213 122 040505 020104 .ASCIZ /\$READ W/O HEADER - FLAG MODE/
268 010247 122 040505 020104 .ASCIZ /\$READ W/O HEADER - INTR. MODE/
269 010303 103 047101 052047 .ASCIZ /CAN'T SEEK TO TRACK 0/
270 010323 103 044522 042522 .ASCIZ /WRITE LOCK ERROR/
271 010323 046122 051203 041440 .ASCIZ /RLCS CONTAINED FOLLOWING ERROR(S): /
272 010417 000170 EM1: .ASCIZ 120.
273 010607 020117 047111 EM4: .ASCIZ /NO INTERRUPT ON READ OPERATION/
274 010646 042522 042101 047440 EM5: .ASCIZ /READ OPERATION DID NOT WRITE MEMORY/
275 010712 046522 040502 042040 EM6: .ASCIZ /REBA DID NOT INCREMENT PROPERLY DURING READ/
276 010769 042522 042522 042504 EM7: .ASCIZ /SECTOR DID NOT INCREMENT PROPERLY AFTER READ/
277 011043 047505 032504 EM10: .ASCIZ /HEADER NOT FOUND COULD NOT BE FORCED/
278 011110 051127 047117 020107 EM11: .ASCIZ /WRONG CYLINDER ON SEEK/
279 011137 110 040505 042504 EM12: .ASCIZ /HEADER NOT FOUND WOULD NOT SET/
280 011176 051104 053111 020105 EM13: .ASCIZ /DRIVE READY WOULD NOT SET/
281 011230 044503 045523 040440 EM14: .ASCIZ /DISK ADDRESS INCORRECT AFTER MULTIPLE SECTOR READ/
282 011324 051104 020117 047111 EM19: .ASCIZ /NO INTERRUPT ON WRITE OPERATION/
283 011411 122 041114 020101 EM20: .ASCIZ /REBA DID NOT INCREMENT PROPERLY DURING WRITE/
284 011466 042523 052103 051117 EM21: .ASCIZ /SECTOR DID NOT INCREMENT PROPERLY AFTER WRITE/
285 011544 044504 045523 040440 EM22: .ASCIZ /DISK ADDRESS (RLDA) INCORRECT AFTER MULTIPLE SECTOR WRITE/
286 011596 042110 020122 047519 EM23: .ASCIZ /HDR NOT FND COULD NOT BE FORCED AT END OF TRACK/
287 011716 047516 026516 054105 EM24: .ASCIZ /NON-EXISTANT MEMORY ERROR COULD NOT BE FORCED/
288

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289 011774 040504 040524 041440 EM25: .ASCIZ %DATA COMPARISON ERROR - READ/WRITE ERROR%
290
291 012045 127 044522 042524 EM26: .ASCIZ /WRITE OPERATION MODIFIED MEMORY/
292 012105 125 051122 051117 EM27: .ASCIZ /ERROR ON PARTIAL SECTOR WRITE - ZERO FILL CHECK/
293 012165 122 041114 020101 EM30: .ASCIZ /RLBA DID NOT INCREMENT PROPERLY/
294 012225 102 020101 044502 EM31: .ASCIZ /BA BIT 16 DID NOT SET ON INCREMENT/
295 012270 040502 041040 052111 EM32: .ASCIZ /BA BIT 17 SET ON BA16 INCREMENT TEST/
296 012330 122 041114 020101 EM33: .ASCIZ /RLBA DID NOT INCREMENT WITH BA16/
297 012390 040502 041040 052111 EM34: .ASCIZ /BA BIT 17 DID NOT SET ON INCREMENT/
298 012441 102 020101 044502 EM35: .ASCIZ /BA BIT 16 DID NOT CLEAR ON INCREMENT/
299 012506 046122 040502 042040 EM36: .ASCIZ /RLBA DID NOT INCREMENT WITH BA17/
300 012547 122 040505 022104 EM40: .ASCIZ /READ(FUNCTION ?) DID NOT INTERRUPT/
301 012612 042522 042101 043050 EM41: .ASCIZ /READ(FUNCTION ?) ERROR - BAD DATA/
302 012653 042522 043050 043050 EM42: .ASCIZ /READ(FUNCTION ?) ERROR AT END OF TRACK/
303 012723 116 020117 047111 EM43: .ASCIZ /NO INTERRUPT WITH HDR WT PHD FORCED/
304 012767 116 020117 047111 EM44: .ASCIZ /NO INTERRUPT WITH W/R FORCED/
305 013024 051105 047522 020122 EM45: .ASCIZ %ERROR ON BIT BANG OF SILOS
306 013056 044523 047514 047440 EM47: .ASCIZ /SILO OPERATION FAILURE/
307 013109 110 040505 042504 EM50: .ASCIZ /HEADER COMPARE FAILURE - SECTOR/
308 013145 127 044522 042524 EM51: .ASCIZ /WRITE WPR CAUSED BUS TRAP/
309 013177 122 040505 020104 EM52: .ASCIZ /READ WPR CAUSED BUS TRAP/
310 013230 042522 042101 053440 EM55: .ASCIZ %READ W/O HDR CMP OPERATION DID NOT WRITE MEMORY?
311 013310 046122 040502 042040 EM53: .ASCIZ %RLBA DID NOT INCREMENT PROPERLY DURING READ W/O HDR CMP?
312 013400 046122 040504 042040 EM54: .ASCIZ %RLDA DID NOT INCREMENT AFTER READ W/O HDR CMP?
313 013456 050117 020111 044524 EM56: .ASCIZ /OPT TIMING ERROR/
314 013493 122 044522 042524 EM57: .ASCIZ /WRITE CHECK WPR CAUSED BUS TRAP/
315 013537 127 044522 042524 EM60: .ASCIZ /WRITE CHECK DID NOT INTERRUPT/
316 013575 122 041114 020101 EM61: .ASCIZ /RLBA DID NOT INCREMENT PROPERLY DURING WRCHK/
317 013652 046122 040504 042040 EM62: .ASCIZ /RLDA DID NOT INCREMENT PROPERLY DURING WRCHK/
318 013737 122 042114 020101 EM63: .ASCIZ /RLDA DID NOT INCREMENT PROPERLY AFTER A MULTIPLE SECTOR WRITE CHK/
319 014023 122 044522 042524 EM64: .ASCIZ /WRITE CHECK OF PARTIAL SECTOR WRITE FAILURE/
320 014104 103 041011 047040 EM65: .ASCIZ /CAN NOT FORCE DCK ON WRITE CHECK/
321 014146 040503 020116 047516 EM66: .ASCIZ /CAN NOT FORCE INTERRUPT WITH DCK ON WRCHK/
322 014220 051127 052111 020105 EM70: .ASCIZ /WRITE CHECK FAILURE/
323
324
325
326
327
328
329
330
331 014244 ENDMOD
332 014244 BGNMOD GLBERR
333
334
335 014244 .SBTTL GLOBAL ERRORS
336 BGNMSG ERRO
337 014244 004737 015256 JSR PC,LINE1
338 014250 004737 015312 JSR PC,LINE2
339
340
341 014254 004537 020126 JSR R5,CKERLT ;INCREMENT ERROR AND CHECK LIMIT
342
343 014260 ENDMMSG
344 014260 104023 L10000: EMT C$MSG
345 014262 BGNMSG ERR1
    
```

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46
47 014262 004737 015256 JSR PC,LINE1
48
49
50 014266 004537 020126 JSR R5,CKERLT ;INCREMENT ERROR AND CHECK LIMIT
51
52 014272 ENDMMSG
53 014272 104023 L10001: EMT C$MSG
54
55 014274 BGNMSG ERR2
56 014274 004737 015256 JSR PC,LINE1
57 014300 013746 002170 PRINTB #DRMT4,GDDAT,BDDAT
58 014304 013746 002166 MOV DRMT4,-(SP)
59 014310 012746 015733 MOV GDDAT,-(SP)
60 014314 012746 000003 MOV #FRMT4,-(SP)
61 014320 010600 MOV #3,-(SP)
62 014324 104014 MOV SP,RO
63 014324 062706 000010 EMT C$PNTB
64 ADD #10,SP
65
66 014330 004537 020126 JSR R5,CKERLT ;INCREMENT ERROR AND CHECK LIMIT
67
68 014334 ENDMMSG
69 014334 104023 L10002: EMT C$MSG
70
71 014336 BGNMSG ERR3
72 014336 004737 015256 JSR PC,LINE1
73 014342 004737 015312 JSR PC,LINE2
74 014346 PRINTB #FRMT5,TMPO,BDDAT,GDDAT
75 014346 013746 002166 MOV GDDAT,-(SP)
76 014352 013746 002170 MOV BDDAT,-(SP)
77 014356 012746 015771 MOV TMPO,-(SP)
78 014362 012746 015771 MOV #FRMT5,-(SP)
79 014366 012746 000004 MOV #4,-(SP)
80 014372 010600 MOV SP,RO
81 014374 104014 EMT C$PNTB
82 014376 062706 000012 ADD #12,SP
83
84
85
86
87
88
89 014402 004537 020126 JSR R5,CKERLT ;INCREMENT ERROR AND CHECK LIMIT
90
91 014406 ENDMMSG
92 014406 104023 L10003: EMT C$MSG
93
94 014410 BGNMSG ERR4
95 014410 004737 015256 JSR PC,LINE1
96 014420 004737 015312 JSR PC,LINE2
97 014420 013746 002170 PRINTB #FRMT4,GDDAT,BDDAT
98 MOV BDDAT,-(SP)
    
```



```

(8) 014424 013746 002166      MOV      GDDAT,-(SP)
(7) 014430 012746 015733      MOV      #FRMT4,-(SP)
(6) 014440 010600 000003      MOV      SP,R0
(5) 014442 104014      EMT      C$PNTB
(4) 014444 062706 000010      ADD      #10,SP
380
381 014450 004537 020126      JSR      R5,CKERLT          ;INCREMENT ERROR AND CHECK LIMIT
382
383
384 014454      ENDMMSG
(3) 014454      L10004: EMT      C$MSG
(2) 014454 104023      BGNMSG  ERR5
386
387 014456
388 014456 004737 015256      JSR      PC,LINE1
389 014462      PRINTB  #FRMT3,RESTMS
(8) 014462 013746 020440      MOV      RESTMS,-(SP)
(7) 014462 013746 015726      MOV      #FRMT3,-(SP)
(6) 014462 010600 000002      MOV      SP,R0
(5) 014476 010600      EMT      C$PNTB
(4) 014500 104014      ADD      #6,SP
(3) 014502 062706 000006
390
391 014506 004537 020126      JSR      R5,CKERLT          ;INCREMENT ERROR AND CHECK LIMIT
392
393
394 014512      ENDMMSG
(3) 014512      L10005: EMT      C$MSG
(2) 014512 104023      BGNMSG  ERR6
396
397 014514
398 014514 004737 015256      JSR      PC,LINE1
399 014520 004737 015534      JSR      PC,LINE3
400 014524 004737 015312      JSR      PC,LINE2
401
402
403 014530      PRINTB  #FRMT99
(7) 014530 012746 016667      MOV      #FRMT99,-(SP)
(6) 014534 012746 000001      MOV      #1,-(SP)
(5) 014540 010600      MOV      SP,R0
(4) 014542 104014      EMT      C$PNTB
(3) 014544 062706 000004      ADD      #4,SP
404 014550 004537 020126      JSR      R5,CKERLT          ;INCREMENT ERROR AND CHECK LIMIT
405
406 014554      ENDMMSG
(3) 014554      L10006: EMT      C$MSG
(2) 014554 104023      BGNMSG  ERR7
407
408 014556
409
410
411
412 014556 004537 020126      JSR      R5,CKERLT          ;INCREMENT ERROR AND CHECK LIMIT
    
```

```

413
414 014562      L10007: ENDMMSG
(3) 014562 104023      EMT      C$MSG
415
416
417 014564      BGNMSG  ERR8
418
419 014564
420 014564 004737 015256      JSR      PC,LINE1
421 014570 004737 015312      JSR      PC,LINE2
422 014574      PRINTB  #FRMT6,TMP1,GDDAT,BDDAT
(10) 014574 013746 002170      MOV      BDDAT,-(SP)
(9) 014600 013746 002166      MOV      GDDAT,-(SP)
(8) 014604 013746 002162      MOV      TMP1,-(SP)
(7) 014610 012746 016042      MOV      #FRMT6,-(SP)
(6) 014614 012746 000004      MOV      #4,-(SP)
(5) 014620 010600      MOV      SP,R0
(4) 014622 104014      EMT      C$PNTB
(3) 014624 062706 000012      ADD      #12,SP
424
425 014630 004537 020126      JSR      R5,CKERLT          ;INCREMENT ERROR AND CHECK LIMIT
426
427
428 014634      L10010: ENDMMSG
(3) 014634 104023      EMT      C$MSG
(2) 014634
429 014636      BGNMSG  ERR9
430
431 014636 004737 015256      JSR      PC,LINE1
432 014642 004737 015312      JSR      PC,LINE2
433 014646      PRINTB  #FRMT4,TMP0,R2
(9) 014646 010246      MOV      R2,-(SP)
(8) 014650 013746 002160      MOV      TMP0,-(SP)
(7) 014654 013746 015733      MOV      #FRMT4,-(SP)
(6) 014660 012746 000003      MOV      #3,-(SP)
(5) 014664 010600      MOV      SP,R0
(4) 014666 104014      EMT      C$PNTB
(3) 014670 062706 000010      ADD      #10,SP
434
435 014674 004537 020126      JSR      R5,CKERLT          ;INCREMENT ERROR AND CHECK LIMIT
436
437
438 014700      ENDMMSG
(3) 014700      L10011: EMT      C$MSG
(2) 014700 104023      BGNMSG  ERR10
440
441 014702
442 014702 004737 015256      JSR      PC,LINE1
443 014706 004737 015312      JSR      PC,LINE2
444 014712      PRINTB  #FRMT7,TMP1,GDDAT,BDDAT
(10) 014712 013746 002170      MOV      BDDAT,-(SP)
(9) 014716 013746 002166      MOV      GDDAT,-(SP)
(8) 014722 013746 002162      MOV      TMP1,-(SP)
    
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(7) 014726 012746 016117      MOV    #FRMT7,-(SP)
(6) 014732 012746 000004      MOV    SP,RO
(3) 014736 010600      EMT    C$PNTB
(4) 014740 104014      ADD    #12,SP
(4) 014742 062706 000012      ADD

446
447 014746 004537 020126      JSR    R5,CKERLT          ;INCREMENT ERROR AND CHECK LIMIT
448
449
450 014752      ENDMMSG
(3) 014752      L10012: EMT    C$MSG
(3) 014752 104023
451 014754      BGNMSG ERR11
452
453 014754 004737 015256      JSR    PC,LINE1
454 014760 004737 015312      JSR    PC,LINE2
455 014764      PRINTB #FRMT8,TMP0,GDDAT,BDDAT
(10) 014764 013746 002170      MOV    BDDAT,-(SP)
(9) 014770 013746 002166      MOV    GDDAT,-(SP)
(8) 014774 013746 002160      MOV    TMP0,-(SP)
(7) 015000 012746 016171      MOV    #FRMT8,-(SP)
(6) 015004 012746 000004      MOV    #4,-(SP)
(3) 015010 010600      MOV    SP,RO
(4) 015012 104014      EMT    C$PNTB
(4) 015014 062706 000012      ADD    #12,SP

457
458 015020 004537 020126      JSR    R5,CKERLT          ;INCREMENT ERROR AND CHECK LIMIT
459
460
461 015024      ENDMMSG
(3) 015024      L10013: EMT    C$MSG
(3) 015024 104023
462 015026      BGNMSG ERR12
463
464 015026 004737 015256      JSR    PC,LINE1
465 015032 004737 015312      JSR    PC,LINE2
466 015036      PRINTB #FRMT9,TMP1,R3,GDDAT,BDDAT
(11) 015036 013746 002170      MOV    BDDAT,-(SP)
(10) 015042 013746 002166      MOV    GDDAT,-(SP)
(9) 015046 010346      MOV    R3,-(SP)
(8) 015050 013746 002162      MOV    TMP1,-(SP)
(7) 015054 013746 016171      MOV    #FRMT9,-(SP)
(6) 015060 012746 000005      MOV    #5,-(SP)
(3) 015064 010600      MOV    SP,RO
(4) 015066 104014      EMT    C$PNTB
(4) 015070 062706 000014      ADD    #14,SP

467
468
469 015074 004537 020126      JSR    R5,CKERLT          ;INCREMENT ERROR AND CHECK LIMIT
470
471
472 015100      ENDMMSG
(3) 015100      L10014: EMT    C$MSG
(3) 015100 104023

```

```

473 015102      BGNMSG ERR13
474
475 015102 004737 015256      JSR    PC,LINE1
476 015106 013746 002170      PRINTB #FRMT10,OPIMN,OPIMX,BDDAT
(10) 015106 013746 002302      MOV    BDDAT,-(SP)
(9) 015112 013746 002300      MOV    OPIMX,-(SP)
(8) 015116 013746 002300      MOV    OPIMN,-(SP)
(7) 015122 012746 016415      MOV    #FRMT10,-(SP)
(6) 015126 010600 000004      MOV    #4,-(SP)
(3) 015130 010600      MOV    SP,RO
(4) 015134 104014      EMT    C$PNTB
(4) 015136 062706 000012      ADD    #12,SP

477
478
479 015142 004537 020126      JSR    R5,CKERLT          ;INCREMENT ERROR AND CHECK LIMIT
480
481
482 015146      ENDMMSG
(3) 015146      L10015: EMT    C$MSG
(3) 015146 104023
483 015150      BGNMSG ERR14
484
485 015150 004737 015256      JSR    PC,LINE1
486 015154 004737 015312      JSR    PC,LINE2
487 015160      PRINTB #FRMT11,TMP1,#BUF
(9) 015160 013746 002162      MOV    #BUF,-(SP)
(8) 015164 013746 002162      MOV    TMP1,-(SP)
(7) 015170 012746 016241      MOV    #FRMT11,-(SP)
(6) 015174 012746 000003      MOV    #3,-(SP)
(3) 015200 010600      MOV    SP,RO
(4) 015202 104014      EMT    C$PNTB
(4) 015204 062706 000010      ADD    #10,SP

488
489
490 015210 004537 020126      JSR    R5,CKERLT          ;INCREMENT ERROR AND CHECK LIMIT
491
492
493 015214      ENDMMSG
(3) 015214      L10016: EMT    C$MSG
(3) 015214 104023
494 015216      BGNMSG ERR15
495
496 015216 004737 015256      JSR    PC,LINE1
497 015222 004737 015312      JSR    PC,LINE2
498 015226      PRINTB #FRMT12,R2
(8) 015226 010246      MOV    R2,-(SP)
(7) 015230 012746 016723      MOV    #FRMT12,-(SP)
(6) 015234 012746 000002      MOV    #2,-(SP)
(3) 015240 010600      MOV    SP,RO
(4) 015242 104014      EMT    C$PNTB
(4) 015244 062706 000006      ADD    #6,SP
499 015250 004537 020126      JSR    R5,CKERLT

500
501 015254      ENDMMSG
(3) 015254      L10017: EMT    C$MSG
(3) 015254 104023

```

```
502 015256 005046 LINE1: PRINTB #FRMT1,RLCS,<B,DRIVE+1>  
503 015256 CLR -(SP)  
504 015260 153716 002135 BLSB DRIVE+1,(SP)  
505 015264 013746 002242 MOV RLC<B>-(SP)  
506 015270 014746 015606 MOV #FRMT1-(SP)  
507 015274 014746 000003 MOV SP,RO-(SP)  
508 015300 010600 MOV SP,RO  
509 015302 104014 EMT C<SP>NTB  
510 015304 062706 000010 ADD #10,SP  
511 015310 000207 RTS PC  
  
512 015312 LINE2: PRINTB #FRMT2,#BEREG,#ARLCS,B,CS,#ARLBA,B,BA  
513 015312 MOV B,BA-(SP)  
514 015316 013746 002220 MOV #ARLBA-(SP)  
515 015320 013746 007106 MOV B,CS-(SP)  
516 015322 013746 002216 MOV #ARLCS-(SP)  
517 015326 012746 007101 MOV #BEREG-(SP)  
518 015330 014746 007130 MOV #FRMT2-(SP)  
519 015334 013746 000008 MOV #B-(SP)  
520 015336 010600 MOV SP,RO  
521 015340 104014 EMT C<SP>NTB  
522 015342 062706 000016 ADD #16,SP  
523 015344 013746 002224 PRINTB #FRMT2A,#ARLDA,B,DA,#ARLMP,B,MP  
524 015346 013746 007122 MOV #ARLMP-(SP)  
525 015350 013746 002222 MOV B,DA-(SP)  
526 015354 012746 007114 MOV #ARLDA-(SP)  
527 015356 012746 015664 MOV #FRMT2A-(SP)  
528 015360 010600 MOV SP,RO  
529 015362 104014 EMT C<SP>NTB  
530 015364 062706 000014 ADD #14,SP  
531 015366 013746 002230 PRINTB #FRMT2,#AFREG,#ARLCS,E,CS,#ARLBA,E,BA  
532 015368 013746 007106 MOV #ARLBA-(SP)  
533 015372 014746 002226 MOV E,CS-(SP)  
534 015374 012746 007101 MOV #ARLCS-(SP)  
535 015376 012746 007151 MOV #AFREG-(SP)  
536 015378 014746 015645 MOV #FRMT2-(SP)  
537 015380 010600 MOV SP,RO  
538 015382 104014 EMT C<SP>NTB  
539 015384 062706 000016 ADD #16,SP  
540 015386 013746 002240 PRINTB #FRMT2B,#ARLDA,E,DA,#ARLMP,E,MP,E,MP1,E,MP2  
541 015388 013746 007136 MOV #MP1-(SP)  
542 015390 013746 002234 MOV E,MP-(SP)  
543 015392 012746 007122 MOV #ARLMP-(SP)  
544 015394 013746 002232 MOV E,DA-(SP)  
545 015396 012746 007114 MOV #ARLDA-(SP)  
546 015398 010600 MOV SP,RO  
547 015400 104014 EMT C<SP>NTB  
548 015402 062706 000020 ADD #20,SP
```

```
510 015532 000207 RTS PC  
511 015534 LINE3: PRINTB #FRMT3,#EM1  
512 015534 MOV #EM1-(SP)  
513 015540 012746 010352 MOV #FRMT3-(SP)  
514 015544 012746 015726 MOV SP,RO-(SP)  
515 015548 010600 000002 MOV SP,RO  
516 015552 104014 EMT C<SP>NTB  
517 015554 062706 000006 ADD #6,SP  
518 015560 012746 010417 PRINTB #FRMT3,#EM100  
519 015560 MOV #EM100-(SP)  
520 015564 012746 015726 MOV #FRMT3-(SP)  
521 015568 010600 000002 MOV SP,RO  
522 015574 104014 EMT C<SP>NTB  
523 015576 062706 000006 ADD #6,SP  
524 015600 000207 RTS PC  
  
525 015606 040445 047503 052116 FRMT1: -ASCIZ /%ACONTROLLER: %06%A DRIVE: %01/  
526 015606 04 022516 046101 FRMT2: -ASCIZ /%N%T%T%06%T%06/  
527 015606 052045 047445 022524 FRMT2: -ASCIZ /%T%06%T%06/  
528 015610 04 022524 033117 FRMT2: -ASCIZ /%T%06%T%06% %06% %06/  
529 015614 04704 052045 000 FRMT3: -ASCIZ /%N%T%T%/  
530 015618 045 022516 042501 FRMT4: -ASCIZ /%N%EXP%D: %06%A REC%D: %06%N/  
531 015622 04 022516 046101 FRMT5: -ASCIZ /%N%ALAST: %06%A PRES: %06%A EXP%D: %06%N/  
532 015626 04704 040445 052502 FRMT6: -ASCIZ /%N%ABUS ADR: %06%A EXP%D: %06%A REC%D: %06%N/  
533 015630 016171 04 022516 053501 FRMT7: -ASCIZ /%N%WORD: %D3%A EXP%D: %06%A REC%D: %06%N/  
534 015634 04 022516 042101 FRMT8: -ASCIZ /%N%ADA: %06%A REC%D: %06%A EXP%D: %06%N/  
535 015638 045 022516 053501 FRMT14: -ASCIZ /%N%AWORDS WRITTEN: %D3%A BUS ADDR: %06%N/  
536 015642 04704 040445 047527 FRMT9: -ASCIZ /%N%AWORDS WRITTEN: %D3%A BUS ADDR: %06%A EXP%D: %06%A REC%D: %06%N/  
537 015646 04 022516 051101 FRMT10: -ASCIZ /%N%ARANGE %D3%A - %D3%A MILLISECONDS WAS %D6%N/  
538 015650 049445 040515 044530 FRMT11: -ASCIZ /%N%MAXIMUM TIMEOUT OF PROGRAM IS %3 SECONDS%N/  
539 015654 04704 040445 051105 FRMT12: -ASCIZ /%N%ERROR LIMIT EXCEEDED - DROPPED%N/  
540 015658 04 042101 044522 FRMT9: -ASCIZ /%N%ADRIIVE DID NOT RECOVER FROM POWER FAILURE/  
541 015662 045 000516 000516 FRMT9: -ASCIZ /%N%  
542 015666 04704 052045 040445 FRMT13: -ASCIZ /%N%T%A - WILL NOT TEST%N/  
543 015670 045 022516 050101 FRMT15: -ASCIZ /%N%APATTERN WAS: %06/  
  
544 .EVEN  
545  
546  
547 016750 BGNMOD ENDMOD  
548 016750 HPTCODE  
549  
550 BGNHW  
551 016750 L10020-LSHW/2  
552 016750 174400 JCSR  
553 016754 000160 JVECTOR  
554 016756 000240 JRIORITY  
555 016762 000001 0 40 JDRIVE (BITS 8,9,10)  
556 016762 000001 1 JRL11=1 RLV11=0  
557 016764 ENDDW  
558 (3) 016764 L10020:
```

```

558      016764      ENDMOD
559      016764      BGNMOD SPTCODE
560      016764      BGNMW      .WORD L10021-L$SW/2
561      016766      000000      DROP:      .WORD 0
562      016770      000012      MERLMT:    .WORD 10.
563      016772      000000      T.SIZE:    .WORD 0
564      016774      000000      T.DMP:     .WORD 0
565      016776      000000      T.LMT:     .WORD 0
566      017000      ENDSW
567      017000      L10021:
568      017000      ENDMOD
569      017000      BGNMOD DSPCODE
570      017000      DISPATCH  47
571      017000      .WORD 47
572      017002      .WORD T1
573      017004      .WORD T2
574      017006      .WORD T3
575      017010      .WORD T4
576      017014      .WORD T5
577      017016      .WORD T6
578      017020      .WORD T7
579      017024      .WORD T8
580      017028      .WORD T9
581      017032      .WORD T10
582      017036      .WORD T11
583      017040      .WORD T12
584      017044      .WORD T13
585      017048      .WORD T14
586      017052      .WORD T15
587      017056      .WORD T16
588      017060      .WORD T17
589      017064      .WORD T18
590      017068      .WORD T19
591      017072      .WORD T20
592      017076      .WORD T21
593      017080      .WORD T22
594      017084      .WORD T23
595      017088      .WORD T24
596      017092      .WORD T25
597      017096      .WORD T26
598      017100      .WORD T27
599      017104      .WORD T28
600      017108      .WORD T29
601      017112      .WORD T30
602      017116      .WORD T31
603      017120      .WORD T32
604      017124      .WORD T33
605      017128      .WORD T34
    
```

```

(6) 017104 033274      .WORD T34
(6) 017106 033564      .WORD T35
(6) 017110 034060      .WORD T36
(6) 017114 034952      .WORD T37
(6) 017118 035244      .WORD T38
(6) 017122 035604      .WORD T39
(6) 017126 036116      .WORD T40
(6) 017130 036402      .WORD T41
(6) 017134 036672      .WORD T42
(6) 017138 036972      .WORD T43
(6) 017142 037114      .WORD T44
(6) 017146 037312      .WORD T45
(6) 017150 037450      .WORD T46
(6) 017154 037450      .WORD T47
(6) 017158      ENDMOD
580      .SBTTL  INITIALIZATION CODE
581      BGNMOD  INITCODE
582      017140      BGNINIT
583      017140      SETPRI  #PRI07
584      017140      MOV     #PRI07,RO
585      017144      EMT     C$SPRI
586      017146      READEF #EF.PWR
587      017148      MOV     #EF.PWR,RO
588      017152      EMT     CSREFG
589      017154      BNCOMPLET NOPWR
590      017156      BCC     NOPWR
591      017160      MOV     L$UNIT,PWRFLG
592      017164      BR      CONT
593      017166      NOPWR: READEF #EF.RESTART
594      017170      MOV     #EF.RESTART,RO
595      017172      EMT     CSREFG
596      017174      BNCOMPLET START1
597      017176      BCS     START1
598      017178      READEF #EF.START
599      017182      MOV     #EF.START,RO
600      017202      EMT     CSREFG
601      017204      BNCOMPLET CONTINUET
602      017204      BCC     CONTINUET
603      017206      MOV     #ERCOUNT,RO
604      017210      NOV     #64,R1
605      017212      1$:   CLR     (R0)+
598      017216      DEC     R1
599      017220      BNE     1$
600      017222      BR      START
601      017224      CONTINUE: READEF #EF.CONTINUE
602      017226      MOV     #EF.CONTINUE,RO
603      017232      EMT     CSREFG
604      017234      BNCOMPLET CONT
605      017234      BCS     CONT
    
```

```

606 017236 005737 002136      NXT:  TST      UUT
607 017242 001011              BNE      XXX
608 017244 012737 177777 002140  START:  MOV      #-1,UNITST
609 017252 013737 002012 002136  MOV      L,UNIT,UIT
610 017260 012737 002312 002312  MOV      #ERCOUNT-2,ERPOINT
611
613 017266 005237 002140      XXX:  INC      UNITST
614 017272 062737 000002 002312  ADD      #2,ERPOINT
615 017300 005337 002136      DEC      004
616 017304 013700 002140  REST:  GPHARD  UNITST,RO
617 017310 104042          MOV      UNITST,RO
618 017312 103406          EMT      C,SGPHRD
619 017320 001746          BCS     COMPLETE 2$
620 017322 005337 002304      TST      PWRFLG
621 017326 000743          BEQ     NXT
622 017334 012037 002252      DEC     PWRFLG
623 017340 012037 002254      BR      NXT
624 017344 012037 002256      MOV     (RO)+,BCSR
625 017350 012037 002306      MOV     (RO)+,BVEC
626          MOV     (RO)+,BPRIOR
627          MOV     (RO)+,DRIVE
628          MOV     (RO)+,T.CNTRLR
629          ;GET BUS ADDRESS
630          ;GET VECTOR
631          ;GET PRIORITY
632          ;GET DRIVE
633          ;GET CONTROLLER TYPE
634          ;CREATE REGISTERS
635
636 017354 013700 002252      CONT:  MOV     BCSR,RO
637 017360 010037 002242          MOV     RO,RLCS
638 017364 062700 000002          ADD     #2,RO
639 017370 010037 002244          MOV     RO,RLBA
640 017374 062700 000002          ADD     #4,RO
641 017400 010037 002246          MOV     RO,RLDA
642 017404 062700 000002          ADD     #2,RO
643 017410 010037 002250          MOV     RO,RLMP
644
645 017414 005737 002304      TST     PWRFLG
646 017420 001007          BNE     5$
647 017424 001007          TST     5$
648 017426 001461          BEQ     5$
649 017430 005037 002142          CLR     TRPFLG
650 017434 012746 000340          SETVEC ERRVEC,#TRPHAN,#340
651 017440 012746 001350          MOV     #340,-(SP)
652 017444 012746 001332          MOV     TRPHAN,-(SP)
653 017450 012746 000003          MOV     #3,-(SP)
654 017454 104037          EMT     C,SSVEC
655 017456 062700 000010          ADD     #10,SP
656 017462 005777 162554          TST     RLCS
657 017466 010037          CLRVEC
658 017472 104036          MOV     ERRVEC,RO
659 017474 005737 002142          EMT     C,SCVEC
660 017500 001404          TST     TRPFLG
661 017502 012737 007052 002126  BEQ     7$
662 017510 004151          MOV     #NORES,WHY
663          BR      8$
664
665 017512 012777 000200 162522 7$:  MOV     #200,@RLCS
666 017520 053777 002134 162514  BIS     DRIVE,@RLCS
667          ;NOW CHECK DRIVE FOR READY

```

```

651 017526 032777 000001 162506      BIT     #1,@RLCS
652 017534 001016          BNE     8$
653 017536 012737 007070 002126      MOV     #NORDY,WHY
654 017544 013746 002126 8$:  PRINTB #FRMT13,WHY
655 017550 012746 016672          MOV     WHY,-(SP)
656 017554 010600          MOV     #3,-(SP)
657 017560 010600          MOV     SP,RO
658 017562 104014          EMT     C,SPNTB
659 017564 062700 000006          ADD     #6,SP
660 017570 000444          BR      5$
661
662 017572 005737 002304      5$:  TST     PWRFLG
663 017576 001451          BEQ     END
664 017600 012777 000200 162434  MOV     #200,@RLCS
665 017606 053777 002134 162426  BIS     DRIVE,@RLCS
666 017614 012701 000074      3$:  MOV     #60,-R1
667 017620 012700 000012          MOV     R10,RO
668 017624 104026          EMT     C,SWTH
669 017626 032777 000001 162406  BIT     #1,@RLCS
670 017634 001032          BNE     END
671 017636 005301          DEC     R1
672 017640 001367          BNE     3$
673
674 017642          PRINTF #FRMT99
675 017644 012746 016667          MOV     #FRMT99,-(SP)
676 017646 012746 000001          MOV     #1,-(SP)
677 017652 010600          MOV     SP,RO
678 017654 104017          EMT     C,SPNTF
679 017656 062706 000004          ADD     #4,SP
680 017662 012746 016615          PRINTF #FRMT98
681 017664 012746 000001          MOV     #FRMT98,-(SP)
682 017666 010600          MOV     #1,-(SP)
683 017674 104017          MOV     SP,RO
684 017676 062706 000004          EMT     C,SPNTF
685 017702 004737 015256      6$:  ADD     #4,SP
686 017706 013700 002140      JSR     PC,LINE1
687 017710 104053          DODU   UNITST,RO
688 017714 104044          EMT     C,SDODU
689 017716 000137          DOCLN  CSDCLN
690          JMP     NXT
691
692 017722 013737 002276 002300  END:  MOV     UOPIHN,@PIHN
693 017730 013737 002274 002302  MOV     UOPIHX,@PIHX
694 017736 005737 002306      TST     T.CNTRLR
695 017742 001000          BNE     1$
696 017744 013737 002272 002300  MOV     LOPIHN,@PIHN
697 017750 013737 002270 002302  MOV     LOPIHX,@PIHX
698          1$:  SETVEC BVEC,#INTSRV,#340
699          MOV     #340,-(SP)
700          MOV     #INTSRV,-(SP)
701          MOV     BVEC,-(SP)
702          ;RL11??
703          ;YES, THEN KEEP LIMITS SET

```

```

(4) 017774 012746 000003      MOV    #3, -(SP)
(3) 020000 104037 000010      EMT   C$SVEC
(3) 020002 062706 000010      ADD   #10, SP
684
685
686 020006                      L10022: ENDINIT
(3) 020006                      EMT   C$INIT
687 020006 104011
688 020010                      ENDMOD
689
690 020010                      BGNMOD CLNCODE
691
692 020010                      BGNCLN
693
694
695 020010                      SETVEC ERRVEC, @TRPHAN, #340
(7) 020010 012746 000340      MOV   #340, -(SP)
(5) 020014 017746 021350      MOV   @TRPHAN, -(SP)
(5) 020020 013746 002132      MOV   ERRVEC, -(SP)
(4) 020024 012746 000003      MOV   #3, -(SP)
(3) 020030 104037 000010      EMT   C$SVEC
(2) 020032 062706 000010      ADD   #10, SP
696 020036 032777 000200 162176 1$: BIT   @CRDY, @RLCS
697 020044 001774                      BEQ   1$
698
699 020046 042777 000100 162166      BIC   #INTEN, @RLCS
700
701 020054                      CLRVEC BVEC, R0
(3) 020054 013700 002254      MOV   BVEC, R0
(3) 020060 104036                      EMT   C$CVEC
702 020062 005737 002304      TST   PWRFLG
703 020066 001402                      BEQ   2$
704 020070 005337 002304      DEC   PWRFLG
705 020074                      CLRVEC ERRVEC, R0
(3) 020074 013700 002132      MOV   ERRVEC, R0
(3) 020100 104036                      EMT   C$CVEC
706
707
708
709 020102                      L10023: ENDCLN
(3) 020102 104012      EMT   C$CLEAN
710
711 020104                      ENDMOD
712
713 020104                      BGNMOD DRPCODE
714
715 020104                      BGNDU
716
717 020104 000240                      NOP
718
719 020106                      L10024: ENDDU
(3) 020106 104055      EMT   C$DU
(3)
    
```

```

720
721 020110                      ENDMOD
722
723 .SBTTL GLOBAL SUBROUTINES
724
725 020110                      BGNMOD GLBSUB
726
727 020110 005237 002144      BCNSRV INTSRV: INC   INTFLG          ;SET INTERRUPT OCCURANCE FLAG
728 020110
729 020114                      ENDSRV
(3) 020114                      L10025: RTI
(2) 020114 000002
730
731 ;ROUTINE USED IN TIMING OPI
732
733
734 020116 005237 002144      TIMSRV: INC   INTFLG
735 020122 010021      ABORTWAIT
(3) 020122 104021      EMT   C$ABRT
736 020124 000002      RTI
737
738 020126 000240      CKERLT: NOP
739 020130                      INLOOP
(3) 020130 104020      EMT   C$INLP
740 020132                      BCOMPLETE 99$
(2) 020132 103427      BCS   99$
741
742 020134 005737 016766      TST   DROP
743 020140 001424      BEQ   99$
744 020142 005277 162144      INC   @ERPOINT
745 020146 027737 162140 016770      CMP   @ERPOINT, @MERLMT
746 020154 002416      BLT   99$
747
748 020156                      PRINTF #FRMT11
(7) 020156 012746 016550      MOV   #FRMT11, -(SP)
(6) 020162 017746 000001      MOV   #1, -(SP)
(3) 020166 010600      MOV   SP, R0
(3) 020170 104017      EMT   C$PNTF
(4) 020172 062706 000004      ADD   #1, SP
749 020176 004737 015256      JSR   PC, LINE1
750 020202                      DDDU   UNITST, DROP THIS UNIT
(3) 020206 013700 002140      MOV   UNITST, R0
(3) 020210 104053      EMT   C$DDDU
(3) 020210 104044      DOCLN
752
753 020212                      99$:
754 020212 000205      RTS   R5
755
756 .SBTTL ROUTINE TO CHECK FOR CONTROLLER ERRORS
757
758 ;*****
759 ;*THIS ROUTINE WILL CHECK RLCS FOR ERRORS AND PRINT THEM
760 ;*ACCORDINGLY. IT WILL MERGE THE ERROR PRINTOUT WITH THE TEST
761 ;*ERROR MESSAGE.
762
    
```

```

763          ;*
764          ;* ROUTINE USES R0,R1 AND PICKS HEADER FROM R3
765          ;*
766          ;* CALL JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
767          ;*
768          ;*
769          ;*
770          ;*
771          020214 005037 002124          CLR T,CRC
772          020220 032737 176000 002226  CHERR: BIT #176000,E.CS ;ANY ERROR BITS SET?
773          020226 001001          BNE RS ;YES,FIND OUT WHICH
774          020230 000205          RTS RS ;NO EXIT
775          020232 012791          MOV #EM100,R1 ;GET START OF STRING
776          020236 000937 002226 2$: TST E,CS ;IS COMPOSITE ERROR SET?(BETTER BE)
777          020242 100003          BPL 99$ ;IS NOT SOMETHING IS WRONG
778          020244 004537 020752          JSR R5,FIX ;YES,PUT "COMP" IN STRING
779          020250 007316          COMP ;"COMP"
780          020252 032737 040000 002226 99$: BIT #DERR,E.CS ;DRIVE ERROR SET?
781          020260 001405          BEQ JS ;NO,CONTINUE
782          020262 005237          INC DERFLG ;YES,PUT "DRV" INTO STRING
783          020266 004537 020752          JSR R5,FIX ;"DRV"
784          020272 007245          DENES ;NON-EXISTENT MEMORY ERROR?
785          020274 032737 020000 002226 3$: BIT #NXM,E.CS ;NON-EXISTENT MEMORY ERROR?
786          020302 001403          BEQ JS ;NO,CONTINUE
787          020304 004537 020752          JSR R5,FIX ;YES,PUT "NXM" INTO STRING
788          020310 007257          NXMES ;"NXM"
789          020312 032737 002000 002226 4$: BIT #OPI,E.CS ;IS OPI SET?
790          020320 001422          BEQ JS ;NO,GO CHECK BITS 11 & 12
791          020322 004537 020752          JSR R5,FIX ;PUT "OPI" INTO STRING
792          020326 007257          OPMES ;"OPI"
793          020330 032737 004000 002226          BIT #BIT11,E.CS ;HEADERCRC ERROR?
794          020334 001403          BEQ JS ;NO,GO CHECK HEADER NOT FOUND
795          020340 004537 020752          JSR R5,FIX ;GO,PUT "HCRC" IN STRING
796          020344 007264          HCRCMES ;"HCRC"
797          020346 032737 010000 002226 5$: BIT #BIT12,E.CS ;HEADER NOT FOUND?
798          020354 001424          BEQ JS ;NO,GO PUT "CRLF" IN STRING
799          020356 004537 020752          JSR R5,FIX ;PUT "HNF" IN STRING
800          020362 007257          HNFES ;"HNF"
801          020364 000420          BR 8$ ;PUT "CRLF" IN STRING
802          020366 032737 004000 002226 6$: BIT #BIT11,E.CS ;DATA CRC ERROR?
803          020374 001405          BEQ JS ;NO,GO CHECK DATA LATE
804          020376 005237          INC L,CRC ;PUT "DCK" IN STRING
805          020380 004537 020752          JSR R5,FIX ;"DCK"
806          020406 007277          DCKMES ;DATA LATE ERROR?
807          020410 032737 010000 002226 7$: BIT #BIT12,E.CS ;DATA LATE ERROR?
808          020416 001403          BEQ JS ;NO,GO PUT IN "CRLF"
809          020420 004537 020752          JSR R5,FIX ;PUT "DLT" IN STRING
810          020424 007294          DLTMES ;"DLT"
811          020426 004294          BR 8$ ;PUT "CRLF" INTO STRING
812          020432 007313          MSCRLF ;"CRLF"
813          020434 004537 020752          JSR R5,FIX ;MOVE HEADER
814          020440 000000          RESTMS: .WORD 0 ;HEADER FROM TEST
815          020442 105011          CLR R0 ;PUT TERMINATOR IN
816          020444          ERDF (R1)
817          (3) 020444          TRAP 300,LF,ERR6
818          (5) 020446          .WORD 300
    
```

```

(5) 020450 007311          .WORD LF,ERR6
(5) 020452 014524          .WORD ERR6
(5) 020454 000205          .WORD R5 ;EXIT ROUTINE
819          ;SBITL LOAD RLCS
820          ;*****
821          ;* ROUTINE TO LOAD RLCS WITH FUNCTION TO BE PERFORMED
822          ;* CALL: JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
823          ;* ;BITS TO BE LOADED,FUNCTION
824          ;* ;AND INTR ENABLE ONLY
825          ;*
826          ;*
827          ;*
828          ;*
829          ;*
830          020456 032777 040000 161556 161554 002222  LDFUNC: BIT #BIT14,@RLCS ;DRIVE ERROR SET
831          020464 001426          BEQ JS
832          020466 017737 161554 002222          MOV @RLDA,B,DA
833          020474 012777 000013 161544          MOV #13,@RLDA
834          020502 012737 000200 002216          MOV #200,B,CS
835          020510 053737 002134 002216          BIC DRIVE,B,CS
836          020524 032777 000200 161510 6$: BIT #200,@RLCS
837          020532 001774          BEQ JS
838          020534 013777 002222 161504          MOV B,DA,@RLDA
839          020542 012537 002146          MOV (R5)+,LDCSR ;GET BITS TO LOAD
840          020550 042737 177661 002146          BIC #17661,LDCSR ;SAVE R3
841          020556 013737 002146 002260          MOV LDCSR,FNDFNC ;CLEAR ALL BUT FUNC & INTR EN
842          020564 042737 000100 002260          BIC #INTEN,FNDFNC ;SAVE FUNCTION
843          020572 012703 020712          MOV #HDRLST,R3 ;ONLY FUNCTION
844          020576 006237 002260          ASR FNDFNC ;GET HEADER LIST
845          020602 001404          BEQ 1$ ;ALIGN TO LEFT
846          020604 023233          CMP (R3)+,(R3)+ ;IF EQUAL TO ZERO, SET R3
847          020606 005337 002260          FNDFNC ;BUMP R3 BY 4
848          020612 001374          BNE 1$ ;DEC FUNCTION
849          020614 032737 000100 002146 2$: BIT #INTEN,LDCSR ;FOUND IT? NO-GO BACK
850          020622 001403          BEQ JS ;YES,DO WE WANT FLAG OR INTR?
851          020624 005237          TST (R3)+ ;FLAG BRANCH
852          020626 011303          MOV (R3),R3 ;INTR POINT TO THAT ONE
853          020630 010337 020440          MOV R3,RESTMS ;SET HEADER
854          020634 010337 002264          MOV R3,TRVPNC ;SET UP HEADER
855          020640 053737 002262          BIC #XMEN,LDCSR ;SAVE HEADER FOR LATER
856          020646 000100          CLR XMEN ;LOAD E.A. BITS
857          020652 053737 002146          BIC DRIVE,LDCSR ;CLEAR OUT THE BITS
858          020660 052737 000200          BIS #200,LDCSR ;SELECT DRIVE
859          020666 013777 002146          MOV LDCSR,@RLCS ;LOAD FUNCTION
860          020674 004537 020764          JSR R5,BEFORE ;READ REGISTERS
861          020700 042607 000200 161334 4$: BIC #200,@RLCS ;ISSUE COMMAND
862          020710 000205          MOV (R3)+,R3 ;RESTORE R3
863          020712          RTS R5 ;EXIT
864          ;
865          ;
866          ;
867          ;
868          ;
869          ;
870          ;
871          HDRLST: NOPMES
872          020714          .WORD 0
873          020716          .WORD 0
874          020718          .WORD 0
875          020720          .WORD 0
    
```

```

871 020722 007747 GSTMES
872 020724 010006 GSTINT
873 020726 007664 SERMES
874 020730 007715 SERINT
875 020732 007593 RDWMS
876 020734 007523 RHDINT
877 020736 010127 WRTMES
878 020740 010161 WRTINT
879 020742 010045 RDDMES
880 020744 010076 RDDINT
881 020746 010216 RDWMS
882 020750 010247 RDNINT
883
884 ;*****
885 ;ROUTINE TO MOVE ASCII STRINGS
886 ;USES REGISTERS R1 - WHERE STRING IS BEING BUILT
887
888 ;* CALL JSR R5, FIX ;ADDRESS OF STRING TO MOVE
889 ;* .WORD ;
890
891 020752 012504 FIX: MOV (R5)+, R4 ;GET ADDRESS AND MOVE RETURN
892 020754 012471 1$: MOVB (R4)+, (R1)+ ;GET BYTE AND UPDATE
893 020756 001376 BNE 1$ ;WATCH 0 BYTE TERMINATOR
894 020760 105741 TSTB -(R1) ;BACK UP OVER ZERO BYTE
895 020762 000205 RTS ;EXIT
896
897 ;ROUTINE TO READ REGISTERS PRIOR TO OPERATION
898 ;CALL: JSR R5, BEFORE
899
900 020764 017737 161252 002216 BEFORE: MOV @RLCS, B.CS ;READ CS
901 020772 017737 161246 002220 MOV @RLBA, B.BA ; BA
902 020774 017737 161242 002222 MOV @RLDA, B.DA ; DA
903 020776 017737 161236 002224 MOV @RLMP, B.MP ; MP
904 021014 000205 RTS
905
906 ;ROUTINE TO READ REGISTERS AT TIME OF ERROR
907 ;CALL: JSR R5, AFTER
908
909 021016 017737 161220 002226 AFTER: MOV @RLCS, E.CS ;READ CS
910 021024 017737 161214 002230 MOV @RLBA, E.BA ; BA
911 021032 017737 161210 002232 MOV @RLDA, E.DA ; DA
912 021040 017737 161204 002234 MOV @RLMP, E.MP ; MP
913 021046 017737 161176 002236 MOV @RLMP, E.MP1 ; MP
914 021062 000205 RTS ; MP
915
916 SIMBCC: MOV R0, -(SP) ;SAVE R0
917 MOV R1, -(SP) ;SAVE R1
918 MOV R2, -(SP) ;SAVE R2
919 MOV (R5)+, TEMP2 ;GET NUMBER OF BITS
920 MOV (R5)+, TEMP3 ;GET DATA FOR CRC CALCULATION
921 MOV (R5)+, TEMP4 ;GET STARTING CRC
922 1$: MOV @R0, R0 ;GET PRESENT CRC
923
924
925
926
    
```

```

927 021116 006037 002174 ROR TEMP3 ;ROTATE NEW DATA
928 021122 005500 ADC R0 ;MERGE NEW WITH OLD
929 021124 032700 BIT R1, R0 ;BIT 0 SET
930 021130 001407 BND 2$ ;IF NOT CONTINUE
931 021136 005137 002154 COM BCCFBK ;
932 021142 013700 002152 2$: MOV XPOLY, R0 ;GET CRC POLYNOMIAL (CRC-16)
933 021144 005100 COM R0 ;COMPLIMENT POLYNOMIAL
934 021150 040037 002154 BIC R0, BCCFBK
935 021152 000241 CLC ;CLEAR CARRY
936 021156 006037 002176 MOV TEMP4, R0
937 021162 013700 002154 MOV BCCFBK, R0
938 021166 013701 002176 MOV TEMP4, R1
939 021170 010102 MOV R1, R2
940 021172 040100 BIC R1, R0
941 021174 043702 002154 BIC BCCFBK, R2
942 021200 050200 BIC R2, R0
943 021206 043737 002152 002176 BIC XPOLY, TEMP4
944 021212 050037 002176 BIS R0, TEMP4
945 021216 005337 002172 DEC TEMP2
946 021218 001333 BNE 1$
947
948 021220 013737 002176 002156 MOV TEMP4, CALBCC
949 021226 012602 MOV (SP)+, R2
950 021230 012601 MOV (SP)+, R1
951 021232 012600 MOV (SP)+, R0
952 021234 000205 RTS ;RETURN
953
954 ;ROUTINE TO WAIT FOR DRIVE READY
955
956 021236 012701 000144 160772 WTRDY: MOV #100, R1
957 021242 032777 000001 1$: BIT WDRDY, @RLCS
958 021250 001011 BNE 2$
959
960 021252 WAITUS #20.
961 021254 MOV #20, R0
962 021256 EMT CSWT0
963 021260 DEC R1
964 021262 001367 BNE 1$
965
966 021264 ERRDF 200. DRTIM, ERR5
967 021266 TRAP #SERCODE
968 021270 .WORD 200
969 021272 007217 .WORD DRTIM
970 021274 014456 .WORD ERR5
971
972 2$: RTS R5
973
974 ;ROUTINE TO WAIT FOR CONTROLLER
975
976 021276 012701 000620 WTCRDY: MOV #400, R1
977 021302 000200 160732 1$: BIT WCRDY, @RLCS
978 021310 001014 BNE 2$
    
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977
978 021312          WAITUS #20, R0
(3) 021312 012700 000024  MOV #20, R0
(3) 021316 104027  EMT CSMTU
979 021320 005301  DEC R1
980 021322 001367  BNE 13
981 021324 004537  JSR R5, AFTER
983 021330          ERRDF 100, CRTIM, ERR5
(3) 021330 104462  TRAP TSERCODE
(5) 021332 000144  -WORD 100
(5) 021334 007172  -WORD CRTIM
(5) 021336 014458  -WORD ERR5
984 021340 000205  RTS
985
986 021342 004537 021016 2$: JSR R5, AFTER
987 021346 000205  RTS
988
989
990 021350 005237 002142  TRPHAN: INC TRPFLG
991 021354 000002  RTI
992
993 021356          HDHOME:
994
995 021356          BGNSEG          ;%%START OF SEGMENT%%
(3) 021356 104004  EMT C$BSEG
996          ;ISSUE DRIVE RESET
997
998 021360 012737 000001 002266  MOV #1, ERFLG ;SET ERROR FLAG
999 021366 012777 000013 160652  MOV #RSTIMIGSBIT, @RLDA ;RSTIMIGSBIT, @RLDA
1000 021374 004537 020456  JSR R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1001 021400 000004  GSTAT
1002 021402 004537 021276  JSR R5, WTCRDY
1003 021406          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021406 104010  EMT C$ESCAPE
(3) 021410 007172  -WORD 10000$-
1004 021412 004537 020214  JSR R5, CHERR ;CHECK CNTLR FOR ERRORS
1005 021416          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021416 104010  EMT C$ESCAPE
(3) 021420 000206  -WORD 10000$-
1006
1007
1008 021422 004537 020456  JSR R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1009 021426 000010  RDHDR
1010 021430          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021430 104010  EMT C$ESCAPE
(3) 021432 000174  -WORD 10000$-
1011 021434 004537 021276  JSR R5, WTCRDY
1012 021440          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021440 104010  EMT C$ESCAPE
(3) 021442 000164  -WORD 10000$-
1013
1014 021444 004537 020214  JSR R5, CHERR ;CHECK CNTLR FOR ERRORS
1015 021450          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021450 104010  EMT C$ESCAPE
(3) 021452 000154  -WORD 10000$-

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1016 021454 013737 002234 002160  MOV E.MP, TMPO ;GET HEADER
1018 021462 042737 000077 002160  BIC #77, TMPO
1019 021470 001424  BEQ 99$ ;SEEK IS NOT NECESSARY
1020 021472 042737 000100 002160  BIC #100, TMPO
1021 021500 012777 000001 160540  MOV #MK6, @RLDA ;SET TO SEEK
1022 021506 053777 002160 160532  BIS TMPO, @RLDA ;SET IN DIFFERENCE
1023
1024 021514 004537 020456  JSR R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1025 021520 000006  SEEK
1026 021522 004537 021276  JSR R5, WTCRDY
1027 021526          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021526 104010  EMT C$ESCAPE
(3) 021530 000076  -WORD 10000$-
1028
1029 021532 004537 020214  JSR R5, CHERR ;CHECK CNTLR FOR ERRORS
1030 021536          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021536 104010  EMT C$ESCAPE
(3) 021540 000066  -WORD 10000$-
1031
1032 021542 004537 020456 99$: JSR R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1033 021546 000010  RDHDR
1034 021550 004537 021276  JSR R5, WTCRDY
1035 021554          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021554 104010  EMT C$ESCAPE
(3) 021556 000050  -WORD 10000$-
1036 021560 004537 020214  JSR R5, CHERR
1037 021564          ESCAPE SEG
(3) 021564 104010  EMT C$ESCAPE
(3) 021566 000040  -WORD 10000$-
1038
1039 021570 013737 002234 002160  MOV E.MP, TMPO ;GET HEADER
1040 021576 043737 002150 002160  BIC SECMASK, TMPO ;IGNORE SECTOR
1041 021604 001404  BEQ 1$ ;ON ZERO
1042
1043 021606          ERRDF 400, SKHOME, ERRO ;CAN'T SEEK TO TRACK 0
(3) 021606 104462  TRAP TSERCODE
(5) 021610 000620  -WORD 400
(5) 021612 010303  -WORD SKHOME
(5) 021614 014244  -WORD ERRO
1044
1045 021616          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021616 104010  EMT C$ESCAPE
(3) 021620 000006  -WORD 10000$-
1046
1047 021622 005037 002266  CLR ERFLG ;INDICATE SUCCESS BACK TO MAIN PROGRAM
1048
1049
1050 021626          ENDSEG          ;%%END OF SEGMENT%%
(3) 021626 104005  EMT C$ESEG
(3) 021626 000207  RTS PC
1052 021630
1053
1054 021632  ENDMOD
1055

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1056 .SBTTL **TEST 1** - WRITE NPR INTEGRITY
1057
1058 021632 BGNST ;**START OF TEST**
1059
1060 021632 STARS
1061 ;*****
1062 ;CHECK THAT NPR WILL NOT INTERFERE WITH THE OPERATION OF THE
1063 ;UNIBUS. WE SET UP LOCATION 4 TO HANDLE THE TRAP IF IT HAPPENS.
1064 STARS
1065 ;*****
1066 021632 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
1067 021636 CKERFG ;HEADS GO HOME OKAY
1068 (4) 021644 EMT C$EXIT
1069 021646 000232 .WORD L10026-.
1070
1071 021650 BGNSEG ;**START OF SEGMENT**
1072 021650 104004 EMT C$BSEG
1073
1074 1$: SETVEC ERRVEC,#TRPHAN,#340 ;SET UP FOR TRAP
1075 (6) 021652 012746 000340 MOV #340,-(SP)
1076 (5) 021656 012746 021350 MOV #TRPHAN,-(SP)
1077 (4) 021662 012746 002132 MOV ERRVEC,-(SP)
1078 (3) 021666 012746 000003 MOV #3,-(SP)
1079 (2) 021674 104037 000010 EMT C$SVEC
1080 021700 062706 002142 ADD #10,SP
1081 021704 005037 000010 CLR TRPFLG ;CLEAR TRAP OCCURANCE
1082 021704 012777 003052 MOV #BUF,@RLBA ;BUS ADDRESS
1083 021712 005077 160332 CLR @RLDA ;LOAD DISK ADDRESS
1084 021716 012777 160324 MOV #-1,@RLMP ;WORD COUNT OF 1
1085 021720 005037 002166 CLR GDDAT ;SET UP CSR TO LOAD
1086 021724 013737 002134 MOV DRIVE,GDDAT ;SET IN DRIVE
1087 021730 052737 000012 BIC #WRITE,GDDAT ;SET IN FUNCTION
1088 021744 004537 020764 JSR R5,BEFORE ;LOAD FOR ERROR PRINTOUT
1089 021750 013737 002166 MOV GDDAT,B.CS ;SET IN COMMAND
1090 021756 052737 000201 BIC #201,B.CS ;LOAD CRDY
1091 021762 042737 002000 BIC #0B14,B.CS ;CLEAR (BIT 10)
1092 021772 013737 002166 MOV @R14,@RLCS ;ISSUE WRITE
1093 022000 012701 000144 MOV #100,R1 ;WAIT FOR CRDY
1094 022004 032777 000200 BIT #CRDY,@RLCS ;NPR DONE
1095 022012 001013 160230 5$: BNE 6$ ;YES 6$
1096 022014 012700 000024 WAITUS #20,RO ;WAIT A WHILE
1097 (3) 022020 104027 MOV C$MTU
1098 022022 005301 DEC R1
1099 022024 001367 BNE 5$ ;A WHILE UP
1100 ;NO, GO BACK
1101
1102 022026 004537 021016 JSR R5,AFTER
1103 022030 000000 ERRDF 0,CRTIM,ERR5 ;CONTROLLER TIMED OUT
1104 (3) 022032 104462 TRAP T$ERRCODE
1105 (5) 022034 000000 .WORD 0
1106 (5) 022036 007172 .WORD CRTIM
1107 (5) 022040 014456 .WORD ERR5
1108 193: 022042 013700 002132 6$: CLRVEC ERRVEC ;CLEAR VECTOR
1109 MOV ERRVEC,R0

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1094 022046 104036 EMT C$CVEC
1095 022050 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
1096 (3) 022052 104010 EMT C$ESCAPE
1097 022052 000024 .WORD 10001$-.
1098
1099 022054 005737 002142 TST TRPFLG ;DID TRAP OCCUR?
1100 022060 001406 BEQ 7$ ;NO
1101 022062 004537 021016 JSR R5,AFTER
1102 022066 ERRSF 1,C$MS1,ERR0 ;TRAP ON WRITE
1103 (3) 022070 104461 TRAP T$ERRCODE
1104 (5) 022072 013145 .WORD 1
1105 (5) 022074 014244 .WORD EMS1
1106 1100: 022076 1100: 022076 7$: ERRO
1107
1108 022076 10001$: ENDSEG ;**END OF SEGMENT**
1109 (3) 022076 104005 EMT C$ESEG
1110
1111 022100 000001 ENDTST ;**END OF TEST**
1112 (3) 022100 104001 LI0026: EMT C$ETST
1113
1114 .SBTTL **TEST 2** - WRITE FUNCTION
1115
1116 022102 BGNST ;**START OF TEST**
1117
1118 STARS
1119 ;*****
1120 ;CHECK OF WRITE LOGIC UNDER FLAG MODE, WE WILL FIRST ISSUE A
1121 ;READ HEADER SO THAT WE DON'T WRITE ON THE BAD SECTOR
1122 ;FILE TRACK. WE WILL WRITE A FULL SECTOR (128 WORDS) FROM
1123 ;MEMORY (BUF). WE CHECK THAT NO ERRORS OCCUR. IF WE
1124 ;HAVE A DRIVE ERROR WE WILL DO A "GET STATUS" TO SEE
1125 ;IF WRITE PROTECT IS SET IF IT IS WE WILL ABORT THE
1126 ;TEST. AN ERROR ON THE WRITE WILL LOOP ON JUST THE
1127 ;WRITE PORTION. LOOP ON TEST WILL READ HEADER, SEEK (IF
1128 ;NECESSARY) AND WRITE.
1129 STARS
1130 ;*****
1131 022102 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
1132 022106 CKERFG ;HEADS GO HOME OKAY
1133 (4) 022114 EMT C$EXIT
1134 022116 000126 .WORD L10027-.
1135
1136 022120 BGNSEG ;**START OF SEGMENT**
1137 (3) 022120 104004 EMT C$BSEG
1138
1139 3$: CLR @RLDA ;SET DISK ADDRESS
1140 MOV #-128,@RLMP ;WORD COUNT

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1134 022134 012777 003052 160102      MOV      #BUF, @RLBA      ;BUS ADDRESS
1135 022142 004537 020456      JSR      R5, LDFUNC      ;LOAD THE FUNCTION IN NEXT WORD
1136 022146 000012                WRITE                    ;WRITE
1137
1138 022150 004537 021276      JSR      R5, WTCRDY      ;WAIT FOR CONTROLLER READY
1139 022154                ESCAPE  SEG             ;CHECK FOR FL:LOE, ELSE EXIT SEG
1140 022156 000064                EMT      C$ESCAPE
1141                .WORD 10000$-
1142
1143 022160 032777 040000 160054      BIT      #DERR, @RLCS    ;DRIVE ERROR SET?
1144 022166 001425                BEQ      4$              ;BRANCH IF NOT
1145
1146 022170 012777 000003 160050      MOV      #MKIGSBIT, @RLDA ;SET GET STATUS OF DRIVE
1147 022176 004537 020456      JSR      R5, LDFUNC      ;LOAD THE FUNCTION IN NEXT WORD
1148 022202 000004                GSTAT                    ;GET STATUS
1149 022204 004537 021276      JSR      R5, WTCRDY      ;WAIT FOR CONTROLLER READY
1150 022210                ESCAPE  SEG             ;CHECK FOR FL:LOE, ELSE EXIT SEG
1151 022212 104010                EMT      C$ESCAPE
1152                .WORD 10000$-
1153
1154 022214 013737 002234 002166      MOV      E, MP, GDDAT     ;READ DRIVE STATUS
1155 022222 013737 020000 002166      BIT      #BIT13, GDDAT    ;WRITE LOCK ERROR?
1156 022230 001404                BEQ      4$              ;NO, BRANCH
1157
1158 022232                ERRSF  3, #RLOCK, ERRO    ;WRITE LOCK ERROR
1159 022234                TRAP  1, $ERCODE
1160 022236                .WORD 010331
1161 022240                .WORD 014244
1162 022242                .WORD ERRO
1163
1164 4$:
1165
1166 022242                ENDSEG                    ;%%END OF SEGMENT%%
1167 022244                10000$: EMT      C$ESEG
1168 022244                ENDTST                    ;**END OF TEST**
1169 022244                L10027: EMT      C$ETST
1170 022244 104001
1171
1172 .SBTTL **TEST 3** - WRITE FUNCTION INTERRUPT
1173
1174 BGNTST                    ;**START OF TEST**
1175
1176 022246                STARS
1177 ;*****
1178 ;CHECK OF WRITE LOGIC UNDER INTERRUPT MODE, WE WILL ISSUE A
1179 ;READ HEADER SO THAT WE DON'T WRITE ON THE BAD SECTOR FILE
1180 ;TRACK. WE WILL WRITE A FULL SECTOR (128 WORDS) FROM MEMORY (BUF).
1181 ;WE CHECK THAT NO ERRORS OCCUR. WE DO NOT CHECK RLDA OR RLBA
1182 ;INCREMENT AT THIS TIME.
1183 ;*****
1184 ;*****
1185 ;*****
1186 ;*****
1187 ;*****
1188 ;*****
1189 ;*****
1190 ;*****
1191 ;*****
1192 ;*****
1193 ;*****
1194 ;*****
1195 ;*****
1196 ;*****
1197 ;*****
1198 ;*****
1199 ;*****
1200 ;*****
1201 ;*****
1202 ;*****
1203 ;*****
1204 ;*****
1205 ;*****
1206 ;*****
1207 ;*****
1208 ;*****
1209 ;*****
1210 ;*****
1211 ;*****
    
```

```

1176 022246 004737 021356      JSR      PC, HDHOME      ;HEADS OVER TRACK 0
1177 022252                CKERFC                    ;HEADS GO HOME OKAY
1178 022260 104032                EMT      C$EXIT
1179 022262 000112                .WORD L10030-
1180
1181 022264                BGNSEG                    ;%%START OF SEGMENT%%
1182 022264 104004                EMT      C$BSEG
1183
1184 022266 005037 002144      CLR      INTFLG          ;CLEAR INTERRUPT OCCURANCE FLAG
1185 022270 005077 157750      CLR      @RLDA
1186 022276 012777 177600 157744      MOV      R1-128, @RLMP   ;SET UP WORD COUNT
1187 022304 012777 003052 157732      MOV      #BUF, @RLBA     ;SET UP BUS ADDRESS
1188
1189 022312                SETPRI #PRI00            ;PRIORITY TO 0
1190 022316 012700 000000      MOV      #PRI0, R0
1191 022320 104041                EMT      C$SPRI
1192 022322 004537 020456      JSR      R5, LDFUNC      ;LOAD THE FUNCTION IN NEXT WORD
1193 022324 000112                WRITEINTEN                ;WRITE UNDER INTERRUPT
1194 022326 004537 021276      JSR      R5, WTCRDY      ;WAIT FOR INTERRUPT
1195 022332                ESCAPE  SEG             ;CHECK FOR FL:LOE, ELSE EXIT SEG
1196 022334 104010                EMT      C$ESCAPE
1197                .WORD 10000$-
1198
1199 022336                SETPRI #PRI07            ;SET PRIORITY TO 7
1200 022340 012700 000340      MOV      #PRI07, R0
1201 022342 104041                EMT      C$SPRI
1202 022344 005737 002144      TST      INTFLG          ;DID INTERRUPT OCCUR?
1203 022350 001004                BNE      2$              ;YES-BRANCH NO-REPORT
1204
1205 022352                ERDRF  4, #EM17, ERRO    ;WRITE DID NOT INTERRUPT
1206 022354                TRAP  4, $ERCODE
1207 022356                .WORD 4
1208 022360                .WORD EM17
1209 022362                .WORD ERRO
1210 022364 104010                ESCAPE  SEG             ;CHECK FOR FL:LOE, ELSE EXIT SEG
1211 022366 000006                EMT      C$ESCAPE
1212                .WORD 10000$-
1213
1214 022366 004537 020214      JSR      R5, CHERR        ;CHECK CNTLR FOR ERRORS
1215
1216                ENDSEG                    ;%%END OF SEGMENT%%
1217 022372                10000$: EMT      C$ESEG
1218 022374                ENDTST                    ;**END OF TEST**
1219 022374                L10030: EMT      C$ETST
1220 022374 104001
1221
1222 .SBTTL **TEST 4** - PROPER INCREMENT OF RLBA ON WRITE
1223
1224 BGNTST                    ;**START OF TEST**
1225
1226 022376                STARS
1227 ;*****
1228 ;CHECK THAT THE RLBA WILL INCREMENT PROPERLY AFTER THE
    
```

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-30
CZRLBB.P11 22-NOV-78 15:28 **TEST 4** - PROPER INCREMENT OF RLBA ON WRITE                               SEQ 0060
1212
1213
1214
1215 022376
1216
1217
1218 022376 004737 021356
1219 022402
1220 022410 104032
1221 022412 000116
1222
1223 022414 104004
1224 022414
1225 022416 005077 157624
1226 022426 012777 003052 157614
1227 022430 012777 177600
1228 022436 012737 003052 002166
1229 022444 062737 000400 002166
1230
1231 022452 004537 020456
1232 022452 000012
1233 022460 004537 021276
1234 022464
1235 022464 104010
1236 022466 000040
1237
1238 022470 004537 020214
1239 022474
1240 022474 104010
1241 022476 000030
1242 022506 017737 157540 002170
1243 022506 017737 002170 002166
1244 022514 001404
1245
1246 022516 104462
1247 022526 000006
1248 022522 011411
1249 022524 014410
1250
1251 022526
1252
1253 022526
1254 022526
1255 022526 104005
1256 022530
1257 022530 104001
1258
1259
1260
1261
1262
1263
1264
1265
1266
1267
1268
1269
1270
1271
1272
1273
1274
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1289
1290

```

```

;WRITE WAS FINISHED THE RLBA SHOULD BE 128 WORDS (256 BYTES)
;CREATER. STARTING RLBA IS "BUF". ENDING SHOULD BE "BUF + 256."
;WE WILL MONITOR ALL ERRORS AND REPORT THEM ACCORDINGLY
STARS
;*****
JSR PC,HDHOME ;HEADS OVER TRACK 0
CKRFG ;HEADS GO HOME OKAY
EMT C$EXIT
.WORD L10031-.
BGNSEG ;%%START OF SEGMENT%%
EMT C$BSEG
3$:
CLR @RLDA
MOV @BUF,@RLBA ;SET UP BUS ADDRESS
MOV #128,@RLMP ;WORD COUNT
MOV @BUF,@DDAT ;FORM EXPECTED BUS ADDRESS
ADD @256,@DDAT ;AFTER WRITE
JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
WRITE R5,WTCRDY ;WRITE
;WAIT FOR CONTROLLER READY
ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-.
JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-.
MOV @RLDA,@DDAT ;READ "RLBA" FOR PRESENT ADDRESS
CMP @DDAT,@DDAT ;DID "BA" INCREMENT PROPERLY?
BEQ Z$ ;YES, CONTINUE
ERRDF 5,EM20,ERR4 ;BA DID NOT INCREMENT
TRAP 1,ERRCODE
.WORD EM20
.WORD ERR4
2$:
ENDSEG ;%%END OF SEGMENT%%
EMT C$ESEG
10000$:
ENDTST L10031: ;**END OF TEST**
EMT C$ETST
.SBTTL **TEST 5** - PROPER INCREMENT OF RLDA ON WRITE
BGNST ;**START OF TEST**

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-31
CZRLBB.P11 22-NOV-78 15:28 **TEST 5** - PROPER INCREMENT OF RLDA ON WRITE                               SEQ 0061
1252 022532
1253
1254
1255
1256 022532
1257
1258
1259
1260 022532 004737 021356
1261 022536
1262 022544 104032
1263 022546 000114
1264
1265 022550 104004
1266 022550
1267 022552 005037 002166
1268 022556 013777 002166 157462
1269 022564 005237 002166
1270 022570 012777 177600 157452
1271 022576 012777 003052 157440
1272
1273 022604 004537 020456
1274 022610 000012
1275 022612 004537 021276
1276 022616
1277 022616 104010
1278 022620 000040
1279
1280 022622 004537 020214
1281 022626
1282 022626 104010
1283 022630 000030
1284
1285 022632 013737 002232 002170
1286 022640 023737 002166 002170
1287 022646 001404
1288
1289 022650 104462
1290 022650 000006
1291 022652 011466
1292 022656 014410
1293
1294 022660
1295
1296
1297
1298 022660 104005
1299 022662
1300 022662 104001
1301

```

```

STARS
;*****
;CHECK THAT THE SECTOR INCREMENTS AFTER THE WRITE WAS FINISHED.
;WE RANDOMLY PICK A SECTOR (OTHER THAN LAST TRACK) AND ISSUE
;A FULL SECTOR WRITE THE RLDA SHOULD REFLECT AN INCREMENT
;OF THE SECTR. "GDDAT" WAS THE EXPECTED RLDA.
STARS
;*****
JSR PC,HDHOME ;HEADS OVER TRACK 0
CKRFG ;HEADS GO HOME OKAY
EMT C$EXIT
.WORD L10032-.
BGNSEG ;%%START OF SEGMENT%%
EMT C$BSEG
3$:
CLR GDDAT
MOV GDDAT,@RLDA ;SETUP DISK ADDRESS
INC GDDAT ;CREATE EXPECTED SECTOR
MOV #128,@RLMP ;WORD COUNT
MOV @BUF,@RLBA ;SETUP BUS ADDRESS
JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
WRITE R5,WTCRDY ;WRITE
;WAIT FOR CONTROLLER READY
ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-.
JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-.
MOV @DA,@BDDAT ;READ DISK ADDRESS
CMP @GDDAT,@BDDAT ;DID SECTOR INCREMENT PROPERLY
BEQ Z$ ;YES, BRANCH NO, REPORT ERROR
ERRDF 6,EM21,ERR4 ;DA DID NOT INCREMENT
TRAP 1,ERRCODE
.WORD EM21
.WORD ERR4
2$:
ENDSEG ;%%END OF SEGMENT%%
EMT C$ESEG
10000$:
ENDTST L10032: ;**END OF TEST**
EMT C$ETST

```

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-32
CZRLBB-P11 22-NOV-78 15:28 **TEST 6** - FORCE HEADER NOT FOUND WITH WRITE
SEQ 0062

1291
1292
1293 022664
1294
1295 022664
1296
1297
1298
1299
1300 022664
1301
1302 022664 004737 021356
1303 022670
(4) 022676 104032
(4) 022700 000120
1304
1305 022702 104004
(3) 022702
1306
1307
1308 022704 012777 000050 157334
(4) 022712 012777 003052 157334
1309 022720 012777 177777 157322
1310
1311
1312 022726 004537 020456
(4) 022732 000017
1313 022740 004537 021276
(4) 022740
(3) 022740 104010
(3) 022742 000054
1314
1315
1316 022744 013737 002226 002160
(4) 022752 022737 001777 002160
1317 022760 022737 112000 002160
1318 022766 001402
1319
1320
1321 022770 004537 020214
(3) 022774 104006
1322
1323
1324
1325 022776 022737 112000 002160
(3) 023004 001404
1326 023006
(4) 023006 104462
(5) 023010 000027
(5) 023012 011043
(5) 023014 014244
1327
1328
1329 023016
(3) 023016
(3) 023016 104005
1330 023020
1331
1332

.SBTTL **TEST 6** - FORCE HEADER NOT FOUND WITH WRITE
BGNTST ;**START OF TEST**

STARS
;*****
;FORCE HEADER NOT FOUND ERROR TO OCCUR. THIS IS DONE
;BY SETTING SECTOR 40 OF THE RLDA AND ISSUING A
;WRITE. SECTOR 40 DOES NOT EXIST ON THE RL01 PACK
;THEREFORE HDR NT FOUND SHOULD SET.
STARS
;*****

JSR PC,HDHOME ;HEADS OVER TRACK 0
CKRFG ;HEADS GO HOME OKAY
EMT C$EXIT
.WORD L10033-.

BGNSEG ;**START OF SEGMENT**
EMT C$BSEG

MOV #40, @RLDA ;INSURE NOT TO FIND HEADER BY
MOV #BUF, @RLBA ;SETTING SECTOR 40 OF CYL. ADDR.
MOV #-1, @RLMP ;WORD COUNT

JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
WRITE R5,WTCRDY ;WRITE
ESCPE SEC ;WAIT FOR CONTROLLER READY
EMT C$ESCAPE ;CHECK FOR FL:LOE, ELSE EXIT SEG
.WORD 10000$-.

MOV E.CS, TMP0 ;GET RLCS
BIC #177, TMP0 ;SAVE ERROR BITS
CMP #BIT15|BIT12|BIT10, TMP0 ;HDR NOT FOUND SET.
BEQ IS ;YES, CONTINUE

JSR R5,CHERR
1$: CKLOOP
EMT C$CLP1

CMP #BIT15|BIT12|BIT10, TMP0
BEQ 2$
ERRDF 2$, EM10, ERRO
TRAP T$ERCODE
.WORD 23
.WORD EM10
.WORD ERRO ;WHEN FORCED

ENDSEG ;**END OF SEGMENT**
10000$: EMT C$ESEG
ENDTST ;**END OF TEST**

```

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-33
CZRLBB-P11 22-NOV-78 15:28 **TEST 6** - FORCE HEADER NOT FOUND WITH WRITE
SEQ 0063

(3) 023020
(3) 023020 104001
1333
1334
1335
1336 023022
1337
1338
1339 023022
(2) 023022
(4) 023022
1341
1342
1343 023022
(2) 023022
1344
1345
1346 023022 004737 021356
(4) 023026
(4) 023034 104032
(4) 023036 000160
1348
1349
1350 023040 104004
(3) 023040
1351
1352 023042 012700 000000
(3) 023042 104041
1353 023050 005037 002144
1354 023054 012777 000050 157164
1355 023062 012777 003052 157154
1356 023070 012777 177777 157152
1357
1358 023076 004537 020456
(3) 023102 000112
1359 023104 004537 021276
1360
1361 023110 104006
(3) 023112
(3) 023112 012700 000340
(3) 023116 104041
1362
1363 023120 005737 002144
(3) 023124 001004
1364
1365
1366 023126 104462
(3) 023126 000030
(5) 023130 000030
(5) 023132 012723
(5) 023134 014244
1367
1368 023136
(3) 023136 104010
(3) 023140 000054
1369
1370

.L10033: EMT C$SETST

.SBTTL **TEST 7** - FORCE HEADER NOT FOUND WITH WRITE INTERRUPT
BGNTST ;**START OF TEST**

STARS
;*****
;TEST THAT HEADER NOT FOUND ERROR WILL GENERATE AN INTERRUPT
;ON OCCURANCE. HEADER NOT FOUND WILL BE FORCED BY SETTING
;SECTOR 40 OF RLDA AND ISSUING A WRITE
STARS
;*****

JSR PC,HDHOME ;HEADS OVER TRACK 0
CKRFG ;HEADS GO HOME OKAY
EMT C$EXIT
.WORD L10034-.

BGNSEG ;**START OF SEGMENT**
EMT C$BSEG

SETPRI #PRI00
MOV #PRI00, R0
EMT C$SPRI

CLR INTFLG ;CLEAR INTERRUPT OCCURANCE FLAG
MOV #40, @RLDA ;INSURE NOT TO FIND HEADER BY
MOV #BUF, @RLBA ;SETTING SECTOR 40 OF CYL. ADDR.
MOV #-1, @RLMP ;WORD COUNT

JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
WRITEIINTEM R5,WTCRDY ;WRITE
ESCPE SEC ;WAIT FOR CONTROLLER READY

CKLOOP
EMT C$CLP1
SETPRI #PRI07, R0
EMT C$SPRI

TST INTFLG ;DID INTERRUPT OCCUR
BNE 2$ ;YES OKAY

ERRDF 24, EM43, ERRO ;NO INTERRUPT FROM OPI
TRAP T$ERCODE
.WORD 24
.WORD EM43
.WORD ERRO

ESCPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-.
2$:

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-34
CZRLBB.P11 22-NOV-78 15:28 **TEST 7** - FORCE HEADER NOT FOUND WITH WRITE INTERRUPT
                                                                    SEQ 0064
1371 023142 013737 002226 002160      MOV      E,CS,TMPO      ;SET RLCS
1372 023150 043737 001777 002160      BIC     #177,TMPO      ;SAVE ERROR BITS
1373 023156 023737 112000 002160      CMP     #BIT15|BIT12|BIT10,TMPO ;WDR NOT FOUND SET.
1374 023164 001402                BEQ     IS              ;YES, CONTINUE
1375
1376 023166 004537 020214                JSR     R5,CHERR
1377 023172 104006                1$:     CKLOOP
1378                EMT     C$CLP1
1379 023174 022737 112000 002160      CMP     #BIT15|BIT12|BIT10,TMPO
1380 023202 001404                BEQ     IS
1381 023204                ERRODF #2,EM10,ERR0
1382 023206 000031                TRAP   TSERCODE
1383 023210 011043                .WORD  25
1384 023212 014244                .WORD  EM10
1385                .WORD  ERRO                ;WHEN FORCED
1386 023214                3$:
1387                ENDSEG                ;**END OF SEGMENT**
1388                10000$:
1389                EMT     C$ESEG
1390                ENDTST                ;**END OF TEST**
1391 L10034:
1392                EMT     C$ETST
1393
1394 .SBTTL **TEST 8** - CHECK OPI TIME WITH HDR NT FND
1395 BGNTST                ;**START OF TEST**
1396 STARS
1397 ;*****
1398 ;CHECK OPI TIME IT SHOULD BE AROUND 200 MILLISECONDS (ON UNIBUS)
1399 ;CHECK THIS BY TIMING OPI ON A FORCED HEADER NOT FOUND
1400 ;ISSUE WRITE WITH SECTOR 40 SET IN THE DISK ADDRESS
1401 STARS
1402 ;*****
1403 023220 004737 021356      JSR     PC,HDHOME      ;HEADS OVER TRACK 0
1404 023222 104032      CKERFG      ;HEADS GO HOME OKAY
1405 023224 000264      EMT     C$EXIT
1406 023234 104004      .WORD  L10035-.
1407                BGMSEG                ;**START OF SEGMENT**
1408                EMT     C$BSEG
1409                CLRVEC BVEC                ;CLEAR PRESENT INTERRUPT VECTOR
1410                MOV   BVEC,R0
1411                EMT   C$CVEC
1412                SETVEC BVEC,#INTSRV,#340 ;SET INTR. VEC. WITH ABORT WAIT
1413                MOV   #340,-(SP)
1414                MOV   #INTSRV,-(SP)
1415                MOV   BVEC,-(SP)
1416                MOV   #3,-(SP)
1417
1418 023236 104004      023240 013700 002254      CLRVEC BVEC
1419 023242 104036      023244 104036      MOV   BVEC,R0
1420 023246 012746 000340      SETVEC BVEC,#INTSRV,#340 ;SET INTR. VEC. WITH ABORT WAIT
1421 023248 012746 020116      MOV   #340,-(SP)
1422 023250 013746 002254      MOV   #INTSRV,-(SP)
1423 023252 013746 000003      MOV   BVEC,-(SP)
1424 023254 012746 000003      MOV   #3,-(SP)

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-35
CZRLBB.P11 22-NOV-78 15:28 **TEST 8** - CHECK OPI TIME WITH HDR NT FND
                                                                    SEQ 0065
1407 023266 104037                EMT     C$SVEC
1408 023270 062706 000010      ADD     #10,SP
1409 023274                SETPRI #PRI00
1410 023276 012700 000000      MOV     #PRI00,R0
1411 023300 104041                EMT     C$SPRI
1412 023302 005037 002144      CLR     INTFLG        ;CLEAR INTERRUPT FLAG
1413 023306 012777 000050 156732      MOV     #40,&RLDA     ;SET UP FOR HDR NT FND
1414 023308 012777 003052 156732      MOV     #30,&RLBA     ;BUS ADDRESS
1415 023310 012777 177777 156720      MOV     #-1,&RLMP     ;WORD COUNT
1416
1417 023330 004537 020456      JSR     R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
1418 023334 000112      WRITE!INTEN
1419
1420 023336 013700 002302      MOV     OPIMX,R0
1421 023342 006300      ASL     R0
1422 023344 006300      ASL     R0
1423 023346 006300      ASL     R0
1424 023350 063700 002302      ADD     OPIMX,R0
1425 023354 063700 002302      ADD     OPIMX,R0
1426 023360                WAITUS R0                ;WAIT MAX MILLISECONDS
1427 023362 104027      EMT     C$WTU
1428 023364 010037 002170      MOV     R0,BDDAT     ;SETUP FOR WORST CASE
1429 023366 005737 002144      TST    INTFLG        ;DID INTERRUPT OCCUR
1430 023372 001427      BEQ     4$            ;NO, REPORT ERROR
1431
1432 023374                GETTIM BDDAT            ;GET TIME EXPIRED
1433 023376 104052      EMT     C$GTIM
1434 023402 005000 002170      MOV     R0,BDDAT
1435 023404 162737 000012 002170 1$:     CLR     #10.,BDDAT   ;DIVIDE
1436 023412 100402      BMI    4$            ;ANSWER
1437 023414 005200      INC     R0            ;BY 10 TO GET
1438 023416 000772      BR     1$            ;RIGHT ANSWER
1439 023420 010037 002170      3$:     MOV     R0,BDDAT
1440                ;CHECK THAT OPI TIME IS WITHIN LIMITS
1441                2$:
1442 023424                SETPRI #PRI07
1443 023426 012700 000340      MOV     #PRI07,R0
1444 023430 104041      EMT     C$SPRI
1445 023432 023737 002302 002170      CMP     OPIMX,BDDAT   ;IS IT WITHIN LIMITS
1446 023434 002404      BLT    4$            ;NO, REPORT ERROR
1447
1448 023442 023737 002300 002170      CMP     OPIMN,BDDAT   ;WITHIN LIMITS
1449 023450 003404      BLE    5$            ;YES
1450
1451 023452                4$:
1452 023454 104462      ERRODF 974.,EM56,ERR13 ;OPI TIMING INCORRECT
1453 023456 001716      TRAP   TSERCODE
1454 023458 013456      .WORD  974
1455 023460 015102      .WORD  EM56
1456                .WORD  ERR13
1457
1458 023462                5$:
1459 023464 013700 002254      CLRVEC BVEC            ;CLEAR PRESENT VECTOR
1460 023466 104036      MOV   BVEC,R0
1461 023470      EMT   C$CVEC
1462      SETVEC BVEC,#INTSRV,#340 ;SET IN OLD VECTOR

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(7) 023470 012746 000340      MOV     #340,-(SP)
(6) 023474 012746 020110      MOV     #INTSRV,-(SP)
(5) 023500 012746 002754      MOV     #VEC,-(SP)
(4) 023504 012746 000003      MOV     #3,-(SP)
(3) 023510 104037      EMT     C$EVEC
(2) 023512 062706 000010      ADD     #10,SP
1448      ENDSEG                                ;**END OF SEGMENT**
1449      10000$: EMT     C$ESEG
(3) 023516 104005
1450      ENDTST L10036: ;**END OF TEST**
(3) 023520
(3) 023520 104001      EMT     C$ESET
1453      .SBTTL **TEST 9** - MULTIPLE SECTOR TRANSFER ON WRITE
1454      BGNTST ;**START OF TEST**
1455      STARS
1456      ;*****
1457      ;CHECK FOR MULTIPLE SECTOR TRANSFER ON WRITE. THIS TEST CHECKS
1458      ;THAT TWO SECTORS CAN BE SUCCESSFULLY WRITTEN. WE LOAD
1459      ;A WORD COUNT OF 129 WORDS (ONE SECTOR + 1 WORD) STARTING AT
1460      ;SECTOR 0 THRU SECTOR 37 AND VERIFY THAT THE RLDA DOES
1461      ;A DOUBLE INCREMENT EACH TIME.
1462      STARS
1463      ;*****
1464      023522 004737 021356      JSR     PC,HDHOME ;HEADS OVER TRACK 0
1465      023526      CKERFG ;HEADS GO HOME OKAY
1466      023534 104032      EMT     C$EXIT
1467      023536 000152      -WORD L10036-.
1470      023540 005037 002160      CLR     TWP0 ;CLEAR TEMP LOCATIONS
1471      023544 005037 002162      CLR     TMP1
1472      023550      BGNSEG ;**START OF SEGMENT**
1473      023550 104004      EMT     C$BSEG
1474      1$: MOV     TMP1,GDDAT ;GET CYLINDER
1475      023552 013737 002162 002166      BIS     TWP0,GDDAT ;GET SECTOR
1476      023560 053737 002160 002166      MOV     GDDAT,RLDA ;SET DISK ADDRESS-SECTOR 0
1477      023566 013777 002166 156452      ADD     #2,GDDAT ;SET EXPECTED + 2
1478      023574 062737 000002 002166      MOV     #BUF,RLBA ;SET BUS ADDRESS
1479      023602 012777 003957 156434      ADD     #129,RLBA ;WORD COUNT-SECTOR+1 WORD
1480      023610 012777 179577 156432      MOV     R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1481      023616 004537 020456      JSR     R5,WTRCDY ;WRITE
1482      023622 000012 ;WAIT FOR CONTROLLER READY?
1483      023624 004537 021276      JSR     ESCAPE ;CHECK FOR FL:LOE, ELSE EXIT SEG
1484      023630      EMT     C$ESCAPE
1485      023632 000054      -WORD L10000$-
    
```

```

1487      023634 004537 020214      JSR     R5,CHERR ;CHECK CNTLR FOR ERRORS
1488      023640      ESCAPE ;CHECK FOR FL:LOE, ELSE EXIT SEG
1489      023642 000044      EMT     C$ESCAPE
1490      023644 013737 002232 002170      -WORD L10000$-
1491      023652 013737 002170 002166      MOV     E-DA,BDDAT ;READ DISK ADDRESS
1492      023660 001404      CMP     BDDAT,GDDAT ;IS DISK ADDRESS CORRECT
1493      023662 001404      BEQ     2$ ;YES, BRANCH NO, REPORT ERROR
1494      023662      ERRDF 7-EN22,ERR4 ;DISK ADDRESS NOT CORRECT
1495      023662      TRAP 7 ;SERCODE
1496      023664 010007      -WORD 7
1497      023666 011544      -WORD 7
1498      023670 014410      -WORD EN22
1499      023672      -WORD ERR4
1500      023672 005237 002160      INC     TWP0 ;NEXT SECTOR
1501      023676 023737 000046 002160      CMP     #46,TWP0 ;AT END?
1502      023704 001322      BNE     1$ ;NO, GO BACK
1503      023706      ENDSEG ;**END OF SEGMENT**
1504      023706 104005      EMT     C$ESEG
1505      023710      ENDTST L10036: ;**END OF TEST**
1506      023710 104001      EMT     C$ESET
1507      .SBTTL **TEST 10** - CHECK DIRECTION OF WRITE NPR
1508      BGNTST ;**START OF TEST**
1509      STARS
1510      ;*****
1511      ;VERIFY THAT A WRITE IS WRITING NOT READING. WE WRITE A
1512      ;KNOWN PATTERN IN "BUF" (128 WORD), WE THEN ISSUE A WRITE.
1513      ;ONCE THE WRITE IS FINISHED WE CHECK THAT "BUF" IS INTACT.
1514      ;THIS IS DONE TO PROVE THAT THE NPR IS GOING THE RIGHT
1515      ;WAY.
1516      STARS
1517      ;*****
1518      023712 004737 021356      JSR     PC,HDHOME ;HEADS OVER TRACK 0
1519      023716      CKERFG ;HEADS GO HOME OKAY
1520      023724 104032      EMT     C$EXIT
1521      023726 000160      -WORD L10037-.
1522      023730      BGNSEG ;**START OF SEGMENT**
1523      023730 104004      EMT     C$BSEG
1524      2$: MOV     #BUF,R2 ;WRITE BUFFER FOR WRITE OPERATION
1525      023732 012702 003052      MOV     #128,R1 ;ONE SECTOR'S WORTH
1526      023736 012701 000200      MOV     #125252,(R2)+ ;WRITE BUFFER
1527      023742 012722 125252
    
```

```

1528 023746 005301          DEC R1          ;DONE?
1529 023750 001374          BNE 3$         ;NO, GO BACK
1530
1531 023752 005077 156270    CLR @RLDA     ;LOAD DISK ADDRESS
1532 023756 012777 177600    MOV #128,@RLMP ;WORD COUNT
1533 023764 012777 003052 156264  MOV #BUF,@RLBA ;LOAD DISK ADDRESS
1534 023772 004537 020456 156252  JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1535
1536 024000 004537          WRITE          ;WRITE SOME DATA
1537 024004          ESCAPE SEG    ;WAIT FOR IT TO FINISH
1538 024004 104010          EMT          ;CHECK FOR FL:LOE, ELSE EXIT SEG
1539 024006 000076          .WORD 10000$-
1540
1541 024010 004537 020214    JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1542 024014          ESCAPE SEG    ;CHECK FOR FL:LOE, ELSE EXIT SEG
1543 024014 104010          EMT          ;CHECK FOR FL:LOE, ELSE EXIT SEG
1544 024016 000066          .WORD 10000$-
1545
1546 024020 012702 003052    MOV #BUF,R2   ;SET UP TO CHECK BUFFER
1547 024024 012701 000200    MOV #128,R1  ;CHECK 128 WORDS
1548
1549 024030          BGNSEG       ;**START OF SEGMENT**
1550 024030          EMT C$BSEG
1551
1552 024032 012737 125252 002166 4$: MOV #125252,GDDAT ;DATA SHOULD BE 125252
1553 024040 011237 002170 4$: MOV #2,BDDAT ;LOAD DATA INTO BDDAT
1554 024044 023737 002166 002170 4$: CMP GDDAT,BDDAT ;IS IT OKAY?
1555 024052 001406          BEQ 5$       ;YES, CONTINUE
1556
1557 024054 010237 002162    MOV R2,TMP1  ;LOAD MEMORY LOCATION OF FAILURE
1558 024060          ERDF 9,EM30,ERR8 ;
1559 024060          TRAP T$ERRCODE ;
1560 024062          .WORD 8 ;
1561 024064          .WORD EM26 ;
1562 024066          .WORD ERR8 ;
1563
1564 024070          ESCAPE SEG    ;CHECK FOR FL:LOE, ELSE EXIT SEG
1565 024072          EMT C$SESEG
1566 024072          .WORD 100015- ;
1567 024074          .WORD 005722 ;
1568 024076          TST (R2)+ ;NEXTI
1569 024100          DEC R1      ;DONE?
1570          BNE 4$   ;NO, GO BACK
1571
1572          ENDSEG ;**END OF SEGMENT**
1573          EMT C$ESEG
1574          ENDSEG ;**END OF SEGMENT**
1575
1576 024104 104005          EMT C$ESEG ;**END OF TEST**
1577 024106 104005          ENDTST L10037:
1578 024106          EMT C$SETST
1579          .SBTTL **TEST 11** - CHECK FULL RLBA INCREMENT
1580          BGNSTT ;**START OF TEST**
1581
1582 024110          BGNSTT ;**START OF TEST**
    
```

```

1567 024110          STARS
1568          ;*****
1569          ;TEST THAT THE RLBA WILL INCREMENT, WE DO NOT DO A FULL 16
1570          ;BIT INCREMENT WE CHECK THAT EACH BIT WILL TOGGLE 0 TO 1
1571          ;AND 1 TO 0. WE DO CHECK ALL BITS EVEN IF ALL MEMORY
1572          ;IS NOT AVAILABLE. (WE IGNORE NON-EXISTANT MEMORY ERRORS).
1573          ;WE USE THE SAME DISK ADDRESS (RANDOM) AND A 1 WORD TRANSFER.
1574          ;*****
1575
1576 024110 004737 021356    JSR PC,HDHOME ;HEADS OVER TRACK 0
1577 024114          ERDF 9,EM30,ERR4 ;HEADS GO HOME OKAY
1578 024122 104032          EMT C$EXIT
1579 024124 000134          .WORD L10040-
1580
1581 024126 005037 002162    CLR TMP1     ;CLEAR LOCATION
1582
1583          BGNSEG       ;**START OF SEGMENT**
1584 024132          EMT C$BSEG
1585
1586 024134          CLR @RLDA     ;ONLY ONE (1) WORD
1587 024142 005077 156106 3$: MOV #1,@RLDA ;LOAD DISK ADDRESS
1588 024146 013777 002162 156070  MOV #BUF,@RLBA ;BUS ADDRESS
1589
1590 024154 004537 020456    JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1591 024162          WRITE          ;WRITE SOME DATA
1592 024162 004537 021276    JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1593 024166          ESCAPE SEG    ;CHECK FOR FL:LOE, ELSE EXIT SEG
1594 024166          EMT C$SESEG
1595 024170          .WORD 10000$- ;
1596
1597 024172 013737 002162 002166 4$: MOV TMP1,GDDAT ;SET UP EXPECTED RLBA
1598 024200 062737 000002 002166 4$: ADD #2,GDDAT ;PREVIOUS RLBA+2
1599 024206 013737 002230 002170 4$: MOV #8A,BDDAT ;READ RLBA
1600 024214 023737 002166 002170 4$: CMP GDDAT,BDDAT ;WAS IT UPDATED PROPERLY?
1601 024222 001404          BEQ 5$       ;YES, CONTINUE
1602
1603 024224          ERDF 9,EM30,ERR4 ;BA INCREMENT ERROR
1604 024224          TRAP T$ERRCODE ;
1605 024226          .WORD 9 ;
1606 024230          .WORD EM30 ;
1607 024234          .WORD ERR4 ;
1608 024234          ESCAPE SEG    ;CHECK FOR FL:LOE, ELSE EXIT SEG
1609 024236          EMT C$SESEG
1610          .WORD 10000$- ;
1611
1612 024240          ASL TMP1   ;NEXT PATTERN TO TEST RLBA
1613 024244 103404          BCS 6$     ;DONE?
1614 024246 000002 002162 6$: BIT #BIT1,TMP1 ;NO, SET IN BIT 1
1615 024254          BR 3$   ;GO CHECK NEXT.
1616
1617          ENDTEST ;END TEST
    
```



```

1610
1611 024256
1612 024260 104005
1613 024260
1614 024260 104001
1615
1616 024262
1617
1618 024262
1619
1620
1621
1622
1623 024262
1624
1625
1626 024262 004737 021356
1627 024266
1628 024274 104032
1629 024276 000160
1630
1631 024300
1632 024300 104004
1633
1634 024302
1635 024302 012777 177776 155734 2$:
1636 024310 005037 002262
1637 024314 012777 177777 155726
1638 024322 005077 155720
1639 024326 004537 020456
1640 024334 000012
1641 024334 004537 021276
1642 024340
1643 024340 104010
1644 024342 000112
1645 024344 032737 020000 002226
1646 024352 001002
1647
1648 024354 004537 020214
1649 024360
1650 024360 104010 3$:
1651 024362 000072
1652
1653 024364 032737 000020 002226
1654 024372 001004
1655
1656 024374
1657 024376 104462
1658 024376 000012
1659 024400 012225
    
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1660 024402 014244
1661 024404
1662 024404 104006
1663 024406 032737 000040 002226
1664 024414 001404
1665
1666 024416
1667 024416 104462
1668 024420 000013
1669 024422 012270
1670 024424 014244
1671
1672 024426
1673 024426 104006
1674
1675 024430
1676 024434 013737
1677 024442 001404
1678 024444
1679 024444 104462
1680 024446 000014
1681 024450 012335
1682 024452 014410
1683
1684 024454
1685
1686 024454
1687 024454
1688 024454 104005
1689 024456
1690 024456 104001
1691
1692 024460
1693
1694 024460
1695
1696
1697
1698 024460
1699
1700
1701
1702 024460 004737 021356
1703 024464
1704 024472 104032
1705 024474 000162
1706
1707
    
```

```

(3) 024476 104004          EMT      C$BSEG
1687
1688 024500
1689 024500 012777 177776 155536 2$:  MOV     #177776, @R16    ;SET MAX BA TO INC. BA16
1690 024506 012777 000040 002282  MOV     @R16, @MEM     ;SET BA16 IN RLCS
1691 024514 012777 177777 155526  MOV     #1, @RLMP     ;ONE WORD TRANSFER
1692 024522 005077 155520  CLR     @R16         ;LOAD THE FUNCTION IN NEXT WORD
1693 024526 004537 020456  JSR     R5, LDFUNC
1694 024532 000012  WRITE
1695 024534 004537 021276  JSR     R5, WTCRDY    ;WAIT FOR WRITE TO FINISH
1696 024540  ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 024542 104010  EMT      C$ESCAPE
1697 024544 032737 020000 002226  EMT      C$ESCAPE
1698 024552 001002  BIT      #NMX, E.C.S  ;NON-EXISTANT MEMORY ERROR?
1699 024554 004537 020214  BNE     $S          ;YES, CONTINUE
1700 024560 004537 020214  JSR     R5, CHERR    ;CHECK CNTLR FOR ERRORS
1701 024560 004537 020214  ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 024562 104010  EMT      C$ESCAPE
(3) 024562 000072  .WORD  10000$-
1702 024564 032737 000040 002226  BIT      #BA17, E.C.S ;DID BA17 SET?
1703 024572 001004  BNE     $S          ;YES, CONTINUE
1704 024574 004574 004574 004574  ERRDF   13, EM34, ERRO ;BA 17 DID NOT SET
(3) 024574 104462  TRAP   T$ERRCODE
(5) 024576 000016  .WORD  13
(5) 024600 014244  .WORD  EM34
(5) 024602 014244  .WORD  ERRO
1707 024604 004604 004604 4$:  CKLOOP
(3) 024604 104006  EMT      C$CLP1
1709 024606 032737 000020 002226  BIT      #BA16, E.C.S ;DID BA16 SET ALSO?
1710 024614 001404  BEQ     $S          ;NO, GOOD CONTINUE
1711 024616 004616 004616 004616  ERRDF   14, EM35, ERRO ;BA 16 DIDN'T KNOW WHEN TO QUIT.
(3) 024616 104462  TRAP   T$ERRCODE
(5) 024620 000016  .WORD  14
(5) 024624 014244  .WORD  ERRO
1714 024626 004626 004626 5$:  CKLOOP
(3) 024626 104006  EMT      C$CLP1
1715 024630 005037 002166  CLR     @DAT         ;CHECK THAT BA15-BA0 IS CLEAR
1716 024634 011337 002230  MOV     @BA, @DDAT   ;READ BA
1717 024642 001404  BEQ     $S          ;IS BA ZERO?
1718 024644 004644 004644 004644  ERRDF   15, EM36, ERR4 ;BA SHOULD BE ZERO
(3) 024644 104462  TRAP   T$ERRCODE
(5) 024646 000016  .WORD  15
(5) 024650 014244  .WORD  EM36
(5) 024652 014410  .WORD  ERR4
1720 024654
1721 024654 6$:
1722 024654          ;
1723 024654          ENDSEG          ;%%END OF SEGMENT%%
  
```

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(3) 024654 10000$: EMT      C$ESEG
(3) 024654 104005  ENDTST
1724 024656 110042: EMT      C$SETST
(3) 024656 104001  L10042: EMT      C$SETST
1725
1726 .SBTTL  **TEST 14** - TEST READ NPR INTEGRITY
1727
1728 024660  BGNST   ;**START OF TEST**
1729
1730
1731
1732
1733 024660  STARS
(2) *****
1734 ;CHECK THAT NPR WILL NOT INTERFERE WITH THE OPERATION OF THE UNIBUS
1735 ;WE SETUP LOCATION 4 TO HANDLE THE TRAP IF IT HAPPENS
1736 024660  STARS
(2) *****
1737
1738 024660 004737 021356  JSR     PC, HDHOME   ;HEADS OVER TRACK 0
1739 024664 000132  CLR     @R0         ;HEADS GO HOME OKAY
(4) 024672 104032  EMT      C$EXIT
(4) 024674 000132  .WORD  L10043-
1741 024676 104004  BGNSEG          ;%%START OF SEGMENT%%
1742 024676 104004  EMT      C$BSEG
1743
1744
1745 024700 1$:  SETVEC  ERRVEC, @TRPHAN, #340 ;SET UP VECTOR
(7) 024700  MOV     #340, -(SP)
(6) 024710  MOV     @TRPHAN, -(SP)
(6) 024710  MOV     @TRPHAN, -(SP)
(4) 024714  MOV     #3, -(SP)
(3) 024720  EMT      C$SVEC
(2) 024722  ADD     #1, SP
1746 024726  CLR     @R0         ;CLEAR TRAP FLAG
1747 024732  MOV     @RUF, @R16  ;LOAD BA
1748 024740  CLR     @R16         ;LOAD DA
1749 024744  MOV     #1, @RLMP   ;LOAD MC
1750 024752  JSR     R5, LDFUNC  ;LOAD THE FUNCTION IN NEXT WORD
1751 024756  READ
1752 024760 004537 021276  JSR     R5, WTCRDY    ;CLEAR OUT VECTOR
(3) 024764 013700 002132  MOV     @R0, @R0    ;CLEAR OUT VECTOR
(3) 024770 104036  EMT      C$SVEC
1754 024772  ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 024772 104010  EMT      C$ESCAPE
(3) 024774 000300  .WORD  10000$-
1755 024776 004537 020214  JSR     R5, CHERR    ;CHECK CNTLR FOR ERRORS
(3) 025002 004537 020214  ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 025002 104010  EMT      C$ESCAPE
(3) 025004 000020  .WORD  10000$-
1757
1758 025006 005737 002142  TST     @R0         ;DID TRAP OCCUR?
  
```

```

1759 025012 001404      BEQ      7$          ;NO, OKAY
1760 025014          ERDF  19,EM52,ERRO ;YES, PRINT ERROR
(2) 025014          TRAP  T$ERCODE
(3) 025016          .WORD  17
(5) 025020          .WORD  EM52
(5) 025022          .WORD  ERRO
1763 025024          7$:
1764
1765
1766 025024          10000$: ENDSEG          ;**END OF SEGMENT**
(3) 025024          EMT      C$ESEG
(3) 025024 104005          EMT      C$ESEG
1768
1769 025026          ENDTST L10043:          ;**END OF TEST**
(3) 025026          EMT      C$SETST
(3) 025026 104001          .SBTTL  **TEST 15** - READ FUNCTION
1770
1771 025030          BGNST          ;**START OF TEST**
1772
1773 025030          STARS
(2) ;*****
(2) ;CHECK OF THE READ FUNCTION. WE WILL FIRST DO A READ
(2) ;HEADER TO FIND OUT WHERE WE ARE AND THEN ISSUE
(2) ;A FULL SECTOR READ, WAIT FOR READY AND CHECK FOR
(2) ;ANY ERRORS
1774 025030          STARS
(2) ;*****
1775
1776 025030 004737 021356      JSR      PC,HDHOME    ;HEADS OVER TRACK 0
1777 025034          CKERFG          ;HEADS GO HOME OKAY
(4) 025042          EMT      C$EXIT
(4) 025044          .WORD  L10044-.
1778
1779 025046          BGNSEG          ;**START OF SEGMENT**
(3) 028046          EMT      C$BSEG
1785
1786 025050 012737 001750 002160      1$: MOV      #1000-,TMP0
1787 025056 005077 176164          CLR      @RLDA        ;LOAD DISK ADDRESS
1788 025062 012777 176330          MOV      #128,@RLMP   ;SET WORD LENGTH
1789 025070 012777 003052 155160      MOV      #BUF,@RLBA   ;SET BUS ADDRESS
1790
1791 025076 004537 020456      JSR      R5,LDFUNC    ;LOAD THE FUNCTION IN NEXT WORD
1792 025102 000014          READ          ;READ
1793 025104 004537 021276      JSR      R5,WTCRDY    ;WAIT FOR CONTROLLER READY
1794 025110          EMT      C$ESCAPE    ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 025110          .WORD  10000$-.
(3) 025112 000014
1795
1796 025114 004537 020214      JSR      R5,CHERR    ;CHECK CNTLR FOR ERRORS
1797
1798 025120 005337 002160      DEC      TMP0
1799 025124 001354          BNE      1$
  
```

```

1800 025126          ENDSEG          ;**END OF SEGMENT**
(3) 025128
(3) 025130 104005          10000$: EMT      C$ESEG
1801 025130          ENDTST L10044:          ;**END OF TEST**
(3) 025130 104001          EMT      C$SETST
1802
1803 025132          .SBTTL  **TEST 16** - READ FUNCTION INTERRUPT
1804
1805 025132          BGNST          ;**START OF TEST**
1806
1807 025132          STARS
(2) ;*****
(2) ;CHECK OF THE READ FUNCTION UNDER INTERRUPT CONTROL, WE WILL
(2) ;ISSUE A READ HEADER TO GET POSITION AND THEN READ
(2) ;A FULL SECTOR WAITING FOR THE INTERRUPT. CHECK FOR
(2) ;ERRORS ON INTERRUPT.
1808 025132          STARS
(2) ;*****
1809
1810 025132 004737 021356      JSR      PC,HDHOME    ;HEADS OVER TRACK 0
1811 025136          CKERFG          ;HEADS GO HOME OKAY
(4) 025144          EMT      C$EXIT
(4) 025146          .WORD  L10045-.
1817
1818 025150          BGNSEG          ;**START OF SEGMENT**
(3) 025150          EMT      C$BSEG
1819
1820 025152 005037 002144          CLR      INTFLG      ;CLEAR INTERRUPT INDICATOR
1821 025152 005077 155064          CLR      @RLDA        ;SET DISK ADDRESS
1822 025162 012777 176600          MOV      #128,@RLMP   ;SET UP WORD COUNT
1823 025170 012777 003052 155046      MOV      #BUF,@RLBA   ;SET UP BUS ADDRESS
1824
1825 025176          SETPRI  #PRI00        ;PRIORITY TO 0
(3) 025176          MOV      #PRI00,R0
(3) 025202 104041          EMT      C$SPRI
1826 025204 004537 020456      JSR      R5,LDFUNC    ;LOAD THE FUNCTION IN NEXT WORD
1827 025210 000114          READ|INTEM          ;READ UNDER INTERRUPT
1828 025212 004537 021276      JSR      R5,WTCRDY    ;WAIT FOR INTERRUPT
1829 025216          CKLOOP
(3) 025216          EMT      C$CLP1
1830 025220 104006          EMT      #107
(3) 025220 104006          MOV      #PRI07,R0   ;PRIORITY TO 7
(3) 025224 104041          EMT      C$SPRI
1831
1832 025226 005737 002144          TST      INTFLG      ;DID INTERRUPT OCCUR?
1833 025232 001004          BNE      1$          ;YES-BRANCH NO-REPORT
1834
1835 025234          ERDF  19,EM4,ERRO ;READ DID NOT INTERRUPT
(3) 025234 104462          TRAP  T$ERCODE
(5) 025236 000023          .WORD  19
(5) 025240 010607          .WORD  EM4
(5) 025244 014244          .WORD  ERRO
1836 025244          .WORD  ERRO
(3) 025244 104006      1$: C$LOOP          ;CHECK FOR LOOP
(3) 025244          EMT      C$CLP1
  
```

```

1837 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1838 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
1839 (3) 025400 104010 C$ESCAPE 10000$-.
1840 (3) 025402 000044 .WORD 10000$-.
1841 10000$: EMT C$ESEG ;**END OF TEST**
1842 ENDTST L10046: EMT C$ETST
1843 .SBTTL **TEST 17** - CHECK READ NPR DIRECTION
1844 BGNTST ;**START OF TEST**
1845 025256
1846 STARS
1847 (2) ;*****
1848 ;CHECK THAT THE READ FUNCTION ACTUALLY READS (INTO MEMORY)
1849 ;WE WILL WRITE A PATTERN INTO MEMORY AND THEN ISSUE
1850 ;A READ TO OVERLAY THAT PATTERN. AFTER THE READ
1851 ;WE CHECK TO SEE IF THE WRITTEN PATTERN HAS CHANGED.
1852 ;IF NOT WE ISSUE IT AGAIN AT THE SAME SECTION AFTER
1853 ;HAVING MODIFIED OUR PATTERN IN MEMORY (SINCE THERE IS
1854 ;ONE CHANGE THAT THE DISK COULD HAVE OUR PATTERN). AFTER
1855 ;THE SECOND READ WE CHECK THE BUFFER AGAIN. IF IT'S
1856 ;NO CHANGED WE REPORT AN ERROR
1857 STARS
1858 ;*****
1859 JSR PC,HDHOME ;HEADS OVER TRACK 0
1860 CKERFG ;HEADS GO HOME OKAY
1861 EMT C$EXIT
1862 (4) 025470 104032 .WORD L10046-.
1863 (4) 025272 000156
1864 025274 104004 BGNSEG ;**START OF SEGMENT**
1865 EMT C$BSEG
1866 025276 012737 123456 002160 MOV #123456,TMPO ;SET PATTERN TO WRITE
1867 025304 005037 002162 CLR TMP1 ;CLEAR PASS INDICATOR
1868 025310 012700 003052 1$: MOV #BUF,R0 ;SET UP BUFFER BEGINNING
1869 025314 012701 000200 MOV #1,R1
1870 025320 005301 002160 MOV TMPO,(R0)+ ;WRITE BUFFER
1871 025326 001374 DEC R1 ;DONE??
1872 025330 005077 154712 BNE 2$ ;NO, GO BACK
1873 025334 012777 177600 CLR @RLDA ;LOAD DISK ADDRESS
1874 025342 012777 003052 MOV #128,@RLMP ;LOAD WORD COUNT
1875 025350 012777 003052 MOV #BUF,@RBA ;LOAD BUS ADDRESS
1876 025352 003052 002160 MOV #BUF,@DDAT ;FOR ERROR PRINT
1877 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1878 READ ;
1879 ESCAPE SEC ;WAIT FOR CONTROLLER READY
1880 (3) 025370 104010 EMT C$ESCAPE ;CHECK FOR FL:LOE, ELSE EXIT SEG
1881 (3) 025372 000054 .WORD 10000$-.
  
```

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1882 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1883 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
1884 (3) 025400 104010 C$ESCAPE 10000$-.
1885 (3) 025402 000044 .WORD 10000$-.
1886 10000$: EMT C$ESEG ;**END OF TEST**
1887 ENDTST L10046: EMT C$ETST
1888 .SBTTL **TEST 18** - PROPER INCREMENT OF RLBA ON READ
1889 BGNTST ;**START OF TEST**
1890 STARS
1891 (2) ;*****
1892 ;CHECK THAT THE RLBA WILL INCREMENT WITH THE READ
1893 ;THE RLBA SHOULD CONTAIN "BUF +256." AFTER A FULL SECTOR
1894 ;READ
1895 STARS
1896 ;*****
1897 JSR PC,HDHOME ;HEADS OVER TRACK 0
1898 CKERFG ;HEADS GO HOME OKAY
1899 EMT C$EXIT
1900 (4) 025464 104032 .WORD L10047-.
1901 (4) 025466 000116
1902 025470 104004 BGNSEG ;**START OF SEGMENT**
1903 025470 104004 EMT C$BSEG
1904 025472 005077 154550 CLR @RLDA ;SET UP DISK ADDRESS
1905 025476 012777 003052 154540 MOV #BUF,@RLBA ;SET UP BUS ADDRESS
  
```

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-48
CZRLBB.P11 22-NOV-78 15:28 **TEST 18** - PROPER INCREMENT OF RLBA ON READ SEQ 0078

1923 025504 012777 177600 154536 MOV #128,@RLMP ;WORD COUNT
1924 025512 012737 003052 002166 MOV #BUF,GDDAT ;FORM EXPECTED BUS ADDRESS
1925 025520 062737 000400 002166 ADD #256,GDDAT ;AFTER READ
1926
1927 025526 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1928 025532 000014 READ ;READ
1929 025534 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
1930 025540 004537 021276 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 025540 104010 EMT C$ESCAPE
(3) 025542 000040 .WORD 10000$-
1931
1932 025544 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1933 025550 000014 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 025550 104010 EMT C$ESCAPE
(3) 025552 000030 .WORD 10000$-
1934 025554 013737 002230 002170 MOV E.BA,BDDAT ;READ "RLBA" FOR PRESENT ADDRESS
1935 025562 023737 002170 002166 CMP BDDAT,GDDAT ;DID "BA" INCREMENT PROPERLY?
1936 025570 001404 BEQ 15 ;YES, CONTINUE
1937
1938 025572 ERRDF 21,EM6,ERR4 ;BA DID NOT INCREMENT PROPERLY
(3) 025572 104462 TRAP T$ERRCODE
(5) 025574 000025 .WORD 21
(5) 025576 010712 .WORD EM6
(5) 025600 014410 .WORD ERR4
1939
1940 025602 1$:
1941
1942 025602 10000$: ENDSEG ;%%END OF SEGMENT%%
(3) 025602 104005 EMT C$ESEG
(3) 025604 104005 ENDTST L10047: ;**END OF TEST**
1943 025604 104001 EMT C$ETST
(3) 025604 104001
1944
1945 .SBTTL **TEST 19** - PROPER INCREMENT OF RLDA ON READ
1946 BGNST ;**START OF TEST**
1947
1948 025606 STARS
1949 025606 ;*****
1950 ;CHECK THAT THE RLDA INCREMENTS BY ONE AFTER A
1951 ;FULL SECTOR READ, WE FIRST READ A HEADER TO FIND
1952 ;OUT WHERE WE ARE, THEN ISSUE A READ AFTER
1953 ;THE READ THE RLDA SHOULD BE RLDA (START) + 1
1954 025606 STARS
1955 ;*****
1956 025606 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
1957 025612 000114 CKERFG ;HEADS GO HOME OKAY
(4) 025620 104032 EMT C$EXIT
(4) 025622 000114 .WORD L10050-
1958
1959 025624 104004 BGNSEG ;%%START OF SEGMENT%%
(3) 025624 104004 EMT C$BSEG
1960
1961

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-49
CZRLBB.P11 22-NOV-78 15:28 **TEST 19** - PROPER INCREMENT OF RLDA ON READ SEQ 0079

1962 025626 005037 002166 CLR GDDAT
1963 025632 013777 002166 MOV GDDAT,@RLDA ;SETUP DISK ADDRESS
1964 025640 005237 002166 INC GDDAT ;CREATE EXPECTED SECTOR
1965 025644 012777 177600 154376 MOV #128,@RLMP ;WORD COUNT
1966 025652 012777 003052 154364 MOV #BUF,@RLBA ;SETUP BUS ADDRESS
1967
1968 025660 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1969 025664 000014 READ ;READ
1970 025666 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
1971 025672 004537 021276 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 025672 104010 EMT C$ESCAPE
(3) 025674 000040 .WORD 10000$-
1972
1973 025676 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1974 025702 000014 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 025702 104010 EMT C$ESCAPE
(3) 025704 000030 .WORD 10000$-
1975
1976 025706 013737 002232 002170 MOV E.DA,BDDAT ;READ DISK ADDRESS
1977 025714 023737 002166 002170 CMP GDDAT,BDDAT ;DID SECTOR INCREMENT PROPERLY
1978 025722 001404 BEQ 15 ;YES, BRANCH NO, REPORT ERROR
1979
1980 025724 ERRDF 22,EM7,ERR4 ;DISK ADDRESS DID NOT INCREMENT
(3) 025724 104462 TRAP T$ERRCODE
(5) 025726 000025 .WORD 22
(5) 025730 010766 .WORD EM7
(5) 025732 014410 .WORD ERR4
1981
1982 025734 1$:
1983
1984 025734 10000$: ENDSEG ;%%END OF SEGMENT%%
(3) 025734 104005 EMT C$ESEG
(3) 025736 104005 ENDTST L10050: ;**END OF TEST**
1985 025736 104001 EMT C$ETST
(3) 025736 104001
1986
1987 .SBTTL **TEST 20** - FORCE HEADER NOT FOUND WITH READ
1988 BGNST ;**START OF TEST**
1989
1990 025740 STARS
1991 025740 ;*****
1992 ;FORCE HEADER NOT FOUND ERROR TO OCCUR. THIS IS DONE
1993 ;BY SETTING SECTOR 40 OF THE RLDA AND ISSUING A
1994 ;READ. SECTOR 40 DOES NOT EXIST ON THE RL01 PACK
1995 ;THEREFORE HDR NOT FOUND SHOULD SET.
1996 025740 STARS
1997 ;*****
1998 025740 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
1999 025744 000102 CKERFG ;HEADS GO HOME OKAY
(4) 025752 104032 EMT C$EXIT
(4) 025754 000102 .WORD L10051-
2000
2001 025756 BGNSEG ;%%START OF SEGMENT%%

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-50
CZRLBB-P11 22-NOV-78 15:28 **TEST 20** - FORCE HEADER NOT FOUND WITH READ          SEQ 0080

(3) 025756 104004          EMT          C$BSEG
2002
2003
2004 025760 012777 000050 154260      MOV          #40, &RLDA      ;INSURE NOT TO FIND HEADER BY
2005 025766 012777 003052 154250      MOV          #BUF, &RLBA    ;SETTING SECTOR 40 OF CYL. ADDR.
2006 025774 012777 177777 154246      MOV          #-1, &RLMP    ;WORD COUNT
2007
2008 026002 004537 020456          JSR          RS, LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
2009 026006 000014          READ        ;READ
2010 026010 004537 021276          JSR          RS, WTCRDY    ;WAIT FOR CONTROLLER READY
2011 026014          ESCAPE     SEG           ;CHECK FOR FL:LOE, ELSE EXIT SEG
2012 (3) 026016 104010          EMT          C$ESCAPE     ;CHECK FOR FL:LOE, ELSE EXIT SEG
2013 (3) 000036          .WORD       100005-
2014
2015 026020 013737 002226 002160      MOV          E, CS, TMPO   ;GET RLCS
2016 026026 022737 001777 002160      BIC          #1777, TMPO   ;SAVE ERROR BITS
2017 026034 022737 112000 002160      CMP          #BIT15|BIT12|BIT10, TMPO ;WDR NOT FOUND SET.
2018 026042 001404          BEQ         IS           ;YES, CONTINUE
2019
2020 026044          ERRDF      23, EM10, ERRO ;HEADER NOT FOUND WOULD NOT SET
2021 (3) 026044 104462          TRAP       T$ERCODE
2022 (5) 026046 000027          .WORD     23
2023 (3) 026050 011043          .WORD     EM10
2024 (3) 026052 014244          .WORD     ERRO
2025
2026 026054          IS:
2027 ;
2028
2029 026054          ENDSEG          ;%%END OF SEGMENT%%
2030 (3) 026054          10000$: EMT          C$ESEG
2031 (3) 026054 104005          EMT          C$ESEG
2032 (3) 026056          ENDTST         ;**END OF TEST**
2033 (3) 026056          L10051: EMT          C$SETST
2034 (3) 026056 104001          EMT          C$SETST
2035
2036 .SBTTL **TEST 21** - FORCE HEADER NOT FOUND WITH READ INTERRUPT
2037 BGNTST          ;**START OF TEST**
2038
2039 026060          STARS
2040 ;*****
2041 ;TEST THAT HEADER NOT FOUND ERROR WILL GENERATE AN INTERRUPT
2042 ;ON OCCURANCE- HEADER NOT FOUND WILL BE FORCED BY
2043 ;SECTOR 40 OF RLDA AND ISSUING A READ
2044 STARS
2045 ;*****
2046
2047 026060          JSR          PC, HDHOME    ;HEADS OVER TRACK 0
2048 026060 004737 021356          CKERFC     ;HEADS GO HOME OKAY
2049 (3) 026072 104032          EMT          C$EXIT
2050 (4) 026074 000142          .WORD     L10052-
2051
2052 026076          BGNSEG          ;%%START OF SEGMENT%%
2053 (3) 026076 104004          EMT          C$BSEG

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-51
CZRLBB-P11 22-NOV-78 15:28 **TEST 21** - FORCE HEADER NOT FOUND WITH READ INTERRUPT          SEQ 0081

2042
2043 026100          SETPRI        #PRI00
2044 (3) 026100 012700 000000          MOV          #PRI00, R0
2045 (3) 026104 104041          EMT          C$SPRI
2046 026106 005031 002144          CLR          INTFLG      ;CLEAR INTERRUPT OCCURANCE FLAG
2047 026112 012777 000050 154126      MOV          #40, &RLDA    ;INSURE NOT TO FIND HEADER BY
2048 026120 012777 003052 154116      MOV          #BUF, &RLBA    ;SETTING SECTOR 40 OF CYL. ADDR.
2049 026126 012777 177777 154114      MOV          #-1, &RLMP    ;WORD COUNT
2050
2051 026134 004537 020456          JSR          RS, LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
2052 026140 004537 021276          READ        ;READ
2053 026142 004537 021276          JSR          RS, WTCRDY    ;WAIT FOR CONTROLLER READY
2054
2055 026146          CKLOOP
2056 (3) 026146 104006          EMT          C$CLP1
2057 026150          SETPRI        #PRI07
2058 (3) 026150 012700 000340          MOV          #PRI07, R0
2059 (3) 026154 104041          EMT          C$SPRI
2060
2061 026156 005737 002144          TST        INTFLG      ;DID INTERRUPT OCCUR
2062 026162 001004          BNE         YES
2063
2064 026164          ERRDF      24, EM43, ERRO ;HNF DID NOT INTERRUPT
2065 (3) 026164 104462          TRAP       T$ERCODE
2066 (5) 026166 000030          .WORD     24
2067 (5) 026170 012723          .WORD     EM43
2068 (5) 026172 014244          .WORD     ERRO
2069
2070 026174          2$: ESCAPE     SEG           ;CHECK FOR FL:LOE, ELSE EXIT SEG
2071 (3) 026174 104010          EMT          C$ESCAPE
2072 (3) 026176 000036          .WORD     100005-
2073
2074 026200 013737 002226 002160      MOV          E, CS, TMPO   ;GET RLCS
2075 026206 022737 001777 002160      BIC          #1777, TMPO   ;SAVE ERROR BITS
2076 026214 022737 112000 002160      CMP          #BIT15|BIT12|BIT10, TMPO ;WDR NOT FOUND SET.
2077 026222 001404          BEQ         IS           ;YES, CONTINUE
2078
2079 026224          ERRDF      25, EM10, ERRO ;WHEN FORCED
2080 (3) 026224 104462          TRAP       T$ERCODE
2081 (5) 026226 000031          .WORD     25
2082 (5) 026230 011043          .WORD     EM10
2083 (5) 026232 014244          .WORD     ERRO
2084
2085 026234          IS:
2086 ;
2087
2088 026234          ENDSEG          ;%%END OF SEGMENT%%
2089 (3) 026234          10000$: EMT          C$ESEG
2090 (3) 026234 104005          EMT          C$ESEG
2091 (3) 026236          ENDTST         ;**END OF TEST**
2092 (3) 026236          L10052: EMT          C$SETST
2093 (3) 026236 104001          EMT          C$SETST
2094
2095 .SBTTL **TEST 22** - CHECK HEADER COMPARE LOGIC
2096 BGNTST          ;**START OF TEST**
2097
2098 026240

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2079 026240
(2)
2080
2081
2082
2083
2084
2085
2086
2087
2088
2089 026240
(2)
2090
2091
2092
2093 026240 004737 021356
(4) 026244
(4) 026252 104032
(4) 026254 000540
2094 026256
2095 026256 104004
(3) 026256
2096
2097 026260
(3) 026260 012700 000340
(3) 026264 104041
2098 026266 012703 002504
2100 026272
(3) 026272 104004
2101 026274
2102 026274 004537 020456
2103 026300 000010
2104 026302 004537 021276
2105 026306
(3) 026306 104010
(3) 026310 000500
2106 026312
2108 026312 004537 020214
(3) 026316 104010
(3) 026320 000470
2109 026322 013737 002234 002162
2110 026330 042737 000177 002162
2112 026336 012777 000001 153702
2113 026344 011337 002164
2114 026350 042737 000177 002164
2115 026356 163737 002162 002164
2116 026364 103404
2117 026366 052777 000004 153652
2118 026374 000402
2119 026376 005437 002164
2120 026402 053777 002164 153636
2121 026410 032713 000100
2122 026414 001403

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```

STARS
;*****
;CHECK THE HEADER COMPARE LOGIC WORKS. UP TO THIS POINT WE
;KNOW THAT THE LOGIC FUNCTIONS PROPERLY BUT NOW WE WILL
;CHECK ALL THE BITS IN THE HEADER WORD. FOUR PATTERNS
;ARE USED A WALKING 1, GROWING 1, WALKING 0, GROWING 0. A SEEK
;IS ISSUED BEFORE EACH READ TO INSURE WE ARE ON THE PROPER
;TRACK. ONCE WE ARE ON THE RIGHT TRACK WE LOAD THE RLDA
;AND ISSUE THE READ. UPON COMPLETION WE WILL CHECK FOR ERRORS
;WE THEN LOAD THE COMPLIMENT PATTERN INTO THE RLDA
;EXPECTING A HEADER NOT FOUND TO SET
STARS
;*****
JSR PC,HDHOME ;HEADS OVER TRACK 0
CERFG SEC ;HEADS GO HOME OKAY
EMT C$EXIT
.WORD L10053--
BGNSEG ;%%START OF SEGMENT%%
EMT C$BSEG
SETPRI #PRI07 ;PRIORITY TO 7
MOV #PRI07,R0
EMT C$SPRI
MOV #HDIRTAB,R3 ;GET LIST START
BGNSEG ;%%START OF SEGMENT%%
EMT C$BSEG
1$: JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
RDHDR ;READ HEADER
JSR R5,WTCRDY ;WAIT FOR CONTROLLRE READY
EMT SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
.WORD C$ESCAPE
10001$--
JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
EMT SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
.WORD C$ESCAPE
10001$--
MOV E.MP,TMP1 ;READ AND SAVE HEADER
BIC #177,TMP1 ;CLEAR OUT SECTOR AND H.S.
MOV #1,RLDA ;SETUP MARKER FOR SEEK
MOV #R3,TMP2 ;GET HEADER PATTERN
BIC #177,TMP2 ;CLEAR OUT SECTOR AND H.S.
SUB TMP1,TMP2 ;CALCULATE DIFFERENCE TO SEEK
BCS 2$ ;BRANCH FOR SEEK OUT
BR #5IGN,RLDA ;SEEK TOWARDS SPINDLE
;GO PUT IN DIFFERENCE WORD
NEG TMP2 ;WE HAVE TO NEGATE DIFFERENCE
BIS TMP2,RLDA ;SET IN DIFFERENCE WORD
BIT #RHS,(R3) ;DO WE WANT HEAD SELECT AS 0?
BEQ 4$ ;YES, SKIP OVER SETTING H.S.

```

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2123 026416 052777 000020 153622
2124 026424 004537 020456
2125 026430 000006
2126
2127
2128 026432 004537 021276
2129 026436
(3) 026436 104010
(3) 026440 000350
2131 026442 004537 020214
2132 026446
(3) 026446 104010
(3) 026450 000340
2133 026452 004537 021236
2135 026456
(3) 026456 104010
(3) 026460 000330
2136 026462 004537 020456
2137 026466 000010
2138 026470 004537 021276
2139 026474
(3) 026474 104010
(3) 026476 000312
2140 026500 004537 020214
2142 026504
(3) 026504 104010
(3) 026506 000302
2143 026510 013737 002234 002170
2144 026516 043737 002150 002170
2146 026524 011337 002166
2147 026530 043737 002150 002166
2148 026536 023737 002166 002170
2149 026544 001404
2150 026546
(3) 026546 104462
(5) 026550 000033
(5) 026552 011110
(5) 026554 014410
2153 026556
(3) 026556 104010
(3) 026560 000230
2154 026562 011377 153460
2155 026566 013777 000077 153452
2157 026574 017777 153436
2158 026602 012777 003052 153434
2159
2160 026610 004537 020456
2161 026614 000014
2162 026616 004537 021276

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4$: BIS #DAHS,RLDA ;SET HEAD SELECT TO ONE
JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
SEEK ;SEEK
JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
EMT SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
.WORD C$ESCAPE
10001$--
JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
EMT SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
.WORD C$ESCAPE
10001$--
JSR R5,WTCRDY ;WAIT FOR DRIVE READY
EMT SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
.WORD C$ESCAPE
10001$--
JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
RDHDR ;READ HEADER (VERIFY SEEK)
JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
EMT SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
.WORD C$ESCAPE
10001$--
JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
EMT SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
.WORD C$ESCAPE
10001$--
MOV E.MP,BDDAT ;READ HEADER
SECMSK BDDAT ;SAVE CYLINDER FOR COMPARE
MOV #R3,GDDAT ;GET EXPECTED HEADER
SECMSK GDDAT ;SAVE CYLINDER FOR COMPARE
CMP GDDAT,BDDAT ;SEEK END UP OKAY
BEQ 5$ ;YES, CONTINUE
ERRDF 27,EM11,ERR4 ;SEEK INCORRECT
TRAP T$ERRCODE
.WORD 27
.WORD EM11
ERR4
5$: ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
10001$--
MOV #R3,RLDA ;SET UP DISK ADDRESS
BIC #1,RLDA
MOV #R1,RLWP ;WORD COUNT
MOV #BUF,RLBA ;BUS ADDRESS
JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
READ ;READ
JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-54
CZRLBB.P11 22-NOV-78 15:28 **TEST 22** - CHECK HEADER COMPARE LOGIC
                                                                    SEQ 0084
2163 026622 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 026622 EMT C$ESEG
(3) 026624 000164 .WORD 100015-
2164 026626 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2165 026632 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 026632 EMT C$ESEG
(3) 026634 000154 .WORD 100015-
2167 026636 011377 153404 MOV (R3),@RLDA ;SET UP DISK ADDRESS AS
2168 026642 005177 153400 COM @RLDA ;COMPLIMENT TO CAUSE HDR NT FND
2169 026646 012777 177777 MOV #1,@RLMP ;WORD COUNT
2171 026654 012777 003052 153374 MOV #BUF,@RLBA ;BUS ADDRESS
2172 026662 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2173 026666 000014 READ ;READ
2174 026670 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
2175 026674 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 026674 EMT C$ESEG
(3) 026676 104010 .WORD 100015-
2177 026700 013737 002226 002160 MOV E,CS,TMPO ;GET CS
2178 026706 042737 001777 002160 BIC #1777,TMPO ;SAVE ERROR BITS
2179 026714 022737 112000 002160 CMP #BIT15|BIT12|BIT10,TMPO ;DID HEADER NOT FOUND SET
2180 026722 001402 BEQ ;YES, CONTINUE
2181 026724 004537 020214 JSR R5,CHERR
2182 026730 85: CKLOOP
(3) 026730 EMT C$CLP1
2183 026732 022737 112000 002160 CMP #BIT15|BIT12|BIT10,TMPO
2184 026740 001413 BEQ 65:
2185 026742 011337 002166 MOV (R3),GDDAT ;SET UP DATA FOR ERROR
2186 026746 013737 002166 002170 MOV GDDAT,BDDAT ;PRINT OUT
2187 026754 005137 COM BDDAT
2188 026760 28,EM12,ERR4 ERRDF ;HDR NOT FOUND WOULD NOT SET
(3) 026760 TRAP T$ERRCODE
(3) 026762 28 .WORD 28
(3) 026764 011137 .WORD EM12
(3) 026766 014410 .WORD ERR4
2193 026770 65: ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 026770 EMT C$ESEG
(3) 026772 104010 .WORD 100015-
2195 026774 005723 TST (R3)+ ;GET NEXT PATTERN
2196 026776 020327 CMP R3,#HREND ;AT END?
2197 027002 001402 BEQ 75: ;YES, EXIT TEST
2198 027004 000137 JMP 15 ;NO, GO BACK
2199 027010 75:
2200 027010 ENDSEG ;%%END OF SEGMENT%%
(3) 027010 100015: EMT C$ESEG
(3) 027010 104005 .WORD 100015-
2203 027012 ENDSEG ;%%END OF SEGMENT%%

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-55
CZRLBB.P11 22-NOV-78 15:28 **TEST 22** - CHECK HEADER COMPARE LOGIC
                                                                    SEQ 0085
(3) 027012 100005: EMT C$ESEG
(3) 027012 104005 ENDTST ;**END OF TEST**
(3) 027014 L10053: EMT C$SETST
(3) 027014 104001 .SBTTL **TEST 23** - CHECK MULTIPLE SECTORS ON READ
2205 027016 BGNTST ;**START OF TEST**
2206 027016 STARS
2207 ;*****
2208 ;VERIFY THAT MULTIPLE SECTORS CAN BE READ, WE WILL CHECK
2209 ;THAT THE RLDA INCREMENTS PROPERLY.
2210 STARS
2211 ;*****
2212 027016 JSR PC,HDDHOME ;HEADS OVER TRACK 0
2213 027022 CKERFG ;HEADS GO HOME OKAY
(3) 027030 EMT C$EXIT
(3) 027032 000156 .WORD L10054-
2218 027034 005037 002160 CLR TMPO ;CLEAR LOCATIONS
2219 027040 005037 002162 CLR TMP1
2220 027044 15: BGNSEG ;**START OF SEGMENT**
(3) 027044 EMT C$BSEG
2225 027046 15: MOV TMP1,GDDAT ;GET CYLINDER
2226 027046 BIS TMPO,GDDAT ;GET SECTOR
2227 027054 053737 002166 002166 MOV GDDAT,@RLDA ;SET DISK ADDRESS-SECTOR 0
2228 027062 013777 002166 153156 MOV #2,GDDAT ;SET EXPECTED + 2
2229 027070 062737 000009 002166 MOV #BUF,@RLBA ;SET BUS ADDRESS
2230 027076 012777 003052 153140 MOV #129,@RLMP ;WORD COUNT-SECTOR+1 WORD
2231 027104 012777 177577 153136 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2232 027112 004537 020456 READ ;READ
2233 027116 000014 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY?
2235 027120 004537 021276 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2236 027124 104010 EMT C$ESEG
(3) 027126 000060 .WORD 100005-
2237 027130 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2238 027134 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 027134 EMT C$ESEG
(3) 027136 000050 .WORD 100005-
2240 027140 013737 002232 002170 MOV E,DA,BDDAT ;READ DISK ADDRESS
2241 027146 023737 002170 002166 CMP BDDAT,GDDAT ;IS DISK ADDRESS CORRECT
2243 027154 001404 BEQ 25 ;YES, BRANCH NO, REPORT ERROR
2244 027156 ERRDF 29,EM14,ERR4 ;DA DID NOT INCREMENT
(3) 027156 TRAP T$ERRCODE

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(5) 027160 000035          .WORD 29
(5) 027162 011230          .WORD EM14
(5) 027164 014410          .WORD ERR4
2246 027166          2$: ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 027166 104010          EMT C$ESCAPE
(3) 027170 000016          .WORD 10000$-.
2248 027172 005237 002160  INC TMPO          ;NEXT SECTOR?
2249 027176 022737 000046 002160  CMP #46, TMPO      ;DONE?
2250 027204 001320          BNE 1$           ;NO, GO BACK
2251
2252 027206          10000$: ENDSEG          ;**END OF SEGMENT**
(3) 027206 104005          EMT C$ESEG
2253 027210          ENDTST          ;**END OF TEST**
(3) 027210 104001          L10054: EMT C$SETST
2254 027212          STARS
(2) *****
2255 ;CHECK THAT WE CAN FORCE A HEADER NOT FOUND AT THE
2256 ;END OF A TRACK DOING A MULTIPLE SECTOR READ. WE
2257 ;SET UP TO READ TWO SECTORS STARTING AT SECTOR 39
2258 ;WE SHOULD TRANSFER 128 WORDS THEN ABORT WITH A
2259 ;HEADER NOT FOUND FOR SECTOR 40
2260 027212          STARS
2261 *****
2262
2263 .SBTTL **TEST 24** - FORCE HDR NT FND AT END OF TRACK
2264 027212          BGNTST          ;**START OF TEST**
2265
2266 027212 004737 021356      JSR PC, HDHOME    ;HEADS OVER TRACK 0
2267 027216          CKERFG          ;HEADS GO HOME OKAY
(4) 027224 104032          EMT C$EXIT
(4) 027226 000126          .WORD L10055-.
2273 027230          BGNSEG          ;**START OF SEGMENT**
(3) 027230 104004          EMT C$BSEG
2274
2275 027232 012737 000047 002166  MOV #39, GDDAT    ;CREATE LAST SECTOR
2276 027240 013777 002166 153000  MOV GDDAT, @RLDA ;LOAD DISK ADDRESS
2277 027246 012777 175777 152774  MOV #129, @RLMP   ;WORD COUNT
2278 027254 012777 030052 152762  MOV #BUF, @RLBA  ;BUS ADDRESS
2279 027262 004537 020456      JSR R5, LDFUNC    ;LOAD THE FUNCTION IN NEXT WORD
2280 027266          READ          ;READ
2281 027270 004537 021276      JSR R5, WTCRDY    ;WAIT FOR CONTROLLER READY
2282 027274          ESCAPE SEG      ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 027276 104010          EMT C$ESCAPE
(3) 027276 000054          .WORD 10000$-.
2283
2284 027300 013737 002226 002170  MOV E, CS, @BDDAT ;READ CS
2285 027306 042737 001777 002170  BIC #1777, @BDDAT ;SAVE ERROR BITS
  
```

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2286 027314 022737 112000 002170  CMP #112000, @BDDAT ;HDR NOT FOUND SET?
2287 027322 001402          BEQ 4$           ;YES, CONTINUE
2288 027324 004537 020214      JSR R5, CHERR
2289 027330 104006          4$: CKLOOP
(3) 027330          EMT C$CLP1
2290
2291 027332 022737 112000 002170  CMP #112000, @BDDAT
2292 027340 001404          BEQ 1$           1$:
2293
2294 027342          ERRDF 30, EM23, ERRO ;HEADER NOT FOUND DID NOT SET
(3) 027344          TRAP 1$ERCODE
(3) 027344 000036          .WORD 30
(5) 027346 011636          .WORD EM23
(5) 027350 014244          .WORD ERRO
2295
2296 027352          1$:
2297
2298 027352          10000$: ENDSEG          ;**END OF SEGMENT**
(3) 027352 104005          EMT C$ESEG
2299 027354          ENDTST          ;**END OF TEST**
(3) 027354 104001          L10055: EMT C$SETST
2300
2301 .SBTTL **TEST 25** - FORCE NON-EXISTANT MEMORY ERROR
2302 027356          BGNTST          ;**START OF TEST**
2303
2304 027356          STARS
2305 *****
2306 ;FORCE A NON-EXISTANT MEMORY ERROR,
2307 ;WE SET THE RLBA TO EQUAL THE
2308 ;LAST ADDRESS IN MEMORY AND ISSUE A READ. THE
2309 ;READ SHOULD ABORT AFTER ONE WORD TRANSFERRED
2310 027356          STARS
2311 *****
2312
2313
2314 027356 004737 021356      JSR PC, HDHOME    ;HEADS OVER TRACK 0
2315 027362          CKERFG          ;HEADS GO HOME OKAY
(4) 027370 104032          EMT C$EXIT
(4) 027372 000076          .WORD L10056-.
2316
2317 027374          BGNSEG          ;**START OF SEGMENT**
(3) 027374 104004          EMT C$BSEG
2318
2319
2320
2321 027376 012777 177774 152640  MOV #177774, @RLBA ;LEAD BA
2322 027404 012737 000060 002262  MOV #A161BA17, XMEM ;SET EA BIT
2323 027412 005077 152630      CLR @RLDA         ;LOAD DISK AVAILABLE
2324 027416 012777 177600 152624  MOV #128, @RLMP   ;WORD COUNT
2325 027424 004537 020456      JSR R5, LDFUNC    ;LOAD THE FUNCTION IN NEXT WORD
2326 027430 000014          READ          ;READ
  
```

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2328 027432 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER
2329 027436 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2330 027436 104010 EMT C$ESCAPE
2331 027440 000026 .WORD 10000$-.
2332 027442 032737 020000 002226 BIT #NXM,E-CS ;DID NXM SET?
2333 027450 001004 BNE 3$ ;YES, CONTINUE
2334 027452 ERRDF 31,EM24,ERRO ;NXM DID NOT SET
2335 027454 104462 TRAP T$ERCODE
2336 027454 000037 .WORD 31
2337 027456 011716 .WORD EM24
2338 027460 014244 .WORD ERRO
2339 027462 3$: ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2340 027462 104010 EMT C$ESCAPE
2341 027464 000002 .WORD 10000$-.
2342 027466 ENDSEG ;%%END OF SEGMENT%%
2343 027466 104005 10000$: EMT C$ESEG
2344 027470 ENDTST ;**END OF TEST**
2345 027470 L10056: EMT C$ETST
2346 027470 104001 .SBTTL **TEST 26** - FORCE NON-EXISTANT MEMORY ERROR INTERRUPT
2347 027472 BGNSTST ;**START OF TEST**
2348 STARS
2349 ;*****
2350 ;CHECK THAT WE CAN FORCE AN INTERRUPT WITH A
2351 ;NON-EXISTANT MEMORY ERROR.
2352 STARS
2353 ;*****
2354 027472 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
2355 027476 CKERFG #1,RLMP ;HEADS GO HOME OKAY
2356 027504 104032 EMT C$EXIT
2357 027506 000140 .WORD L10057-.
2358 027510 104004 BGNSEG ;%%START OF SEGMENT%%
2359 027510 005037 002144 CLR INTFLG ;CLEAR INTERRUPT OCCURANCE FLAG
2360 027516 SETPRI #PRI00
2361 027516 012700 000000 MOV #PRI00,R0
2362 027522 104041 EMT C$SPRI
2363 027524 012777 177774 152512 MOV #177774,0RLBA ;PRELOAD BA
2364 027532 012737 000060 002262 MOV #BA16!BA17,XMEM ;SET BA BITS
  
```

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2365 027540 005077 152502 CLR @RLDA ;LOAD DA
2366 027544 012777 177777 152476 MOV #1,0RLMP ;WORD COUNT
2367 027552 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2368 027556 000114 READ!INTEN ;READ
2369 027560 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER
2370 027564 012700 000340 SETPRI #PRI07 ;PRIORITY TO 7
2371 027570 104041 MOV #PRI07,R0
2372 027572 104010 EMT C$SPRI
2373 027572 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2374 027574 000050 EMT C$ESCAPE
2375 027576 005737 002144 TST INTFLG ;INTERRUPT OCCUR?
2376 027602 001004 BNE 4$ ;YES OKAY
2377 027604 ERRDF 32,EM44,ERRO ;NO INTERRUPT W/NXM
2378 027604 104462 TRAP T$ERCODE
2379 027606 000040 .WORD 32
2380 027610 012767 .WORD EM44
2381 027612 014244 .WORD ERRO
2382 027614 4$: ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2383 027614 104010 EMT C$ESCAPE
2384 027616 000026 .WORD 10000$-.
2385 027620 032737 020000 002226 BIT #NXM,E-CS ;DID NXM SET?
2386 027626 001004 BNE 3$ ;YES, CONTINUE
2387 027630 ERRDF 33,EM24,ERRO ;NO NXM
2388 027630 104462 TRAP T$ERCODE
2389 027632 000041 .WORD 33
2390 027634 011716 .WORD EM24
2391 027636 014244 .WORD ERRO
2392 027640 3$: ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2393 027640 104010 EMT C$ESCAPE
2394 027642 000002 .WORD 10000$-.
2395 027644 ENDSEG ;%%END OF SEGMENT%%
2396 027644 104005 10000$: EMT C$ESEG
2397 027646 ENDTST ;**END OF TEST**
2398 027646 L10057: EMT C$ETST
2399 027646 104001 .SBTTL **TEST 27** - CHECK READ WRITE LOOP
2400 027650 BGNSTST ;**START OF TEST**
2401 STARS
2402 ;*****
2403 ;VERIFY THAT THE WRITE ACTUALLY WRITES. AT THIS
2404 ;TIME WE KNOW THAT THE WRITE FUNCTION GOES THRU
2405 ;THE MOTIONS BUT WE DON'T KNOW THAT THE DATA
2406 ;ACTUALLY GETS RECORDED ON THE PLATTER.
  
```

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2400 027650          STARS
2401                ;*****
2402
2403 027650 004737 021356 JSR PC,HDHOME      ;HEADS OVER TRACK 0
2404 027654          CKERFC          ;HEADS GO HOME OKAY
2405 027654 004032          EMT C$EXIT
2406 027654 000362          .WORD L10060-.
2407
2408 027666          BGNSEG          ;%%START OF SEGMENT%%
2409 027666 104004          EMT C$BSEG
2410 027670 012700 003052 MOV #BUF,R0        ;SET UP WRITE BUFFER
2411 027674 012701 000200 MOV #128,R1        ;128 WORDS/ONE SECTOR
2412 027700 012720 125252 MOV #125252,(R0)+ ;WRITE PATTERN TO BUFFER
2413 027704 005301          DEC R1             ;DONE?
2414 027706 001374          BNE 3$              ;NO, BRANCH BACK
2415 027710 005077 152332 CLR #RLDA          ;DISK ADDRESS
2416 027714 012771 177600 MOV #128,RLBA     ;WORD COUNT
2417 027722 012771 003052 MOV #BUF,RLBA     ;BUS ADDRESS
2418 027730 004537 020456 JSR R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
2419 027734 000012          WRITE          ;WRITE THE PATTERN
2420 027736 004537          JSR R5,WTCRDY    ;WAIT FOR CONTROLLER READY
2421 027742 000012          ESCAPE SEG      ;CHECK FOR FL:LOE, ELSE EXIT SEG
2422 027744 000300          EMT C$ESCAPE
2423 027744 000300          .WORD 10000$-.
2424
2425 027746 004537 020214 JSR R5,CHERR      ;CHECK CNTLR FOR ERRORS
2426 027752 000012          ESCAPE SEG      ;CHECK FOR FL:LOE, ELSE EXIT SEG
2427 027754 000270          EMT C$ESCAPE
2428 027754 000270          .WORD 10000$-.
2429
2430 027756          BGNSEG          ;%%START OF SEGMENT%%
2431 027756 104004          EMT C$BSEG
2432 027760 012700 003052 MOV #BUF,R0        ;CLEAR OUT BUFFER BEFORE
2433 027764 012701 000200 MOV #128,R1        ;READING
2434 027770 005077 000200 MOV #128,(R0)+    ;CLEAN BUFFER
2435 027772 005301          DEC R1             ;DONE?
2436 027774 001375          BNE 4$              ;NO, BRANCH BACK
2437
2438 027776 005077 152244 CLR #RLDA          ;LOAD DISK ADDRESS
2439 027778 012771 177600 MOV #128,RLBA     ;WORD COUNT/ONE SECTION
2440 030010 012771 003052 MOV #BUF,RLBA     ;LOAD BUS ADDRESS
2441 030016 004537 020456 JSR R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
2442 030022 000014          READ           ;GO READ
2443 030024 004537          JSR R5,WTCRDY    ;WAIT FOR CONTROLLER READY
2444 030026 000012          ESCAPE SEG      ;CHECK FOR FL:LOE, ELSE EXIT SEG
2445 030028 000300          EMT C$ESCAPE
2446 030030 000300          .WORD 10001$-.
2447
2448 030032 000210          JSR R5,CHERR      ;CHECK CNTLR FOR ERRORS
2449 030034 004537 020214 TST T.CRC         ;WAS ERROR A DCK??
2450 030040 005737 002124 BNE 8$            ;YES, SEE IF WE A DUMP
2451 030046 001003          ESCAPE SEG      ;CHECK FOR FL:LOE, ELSE EXIT SEG
2452 030046 001003          .WORD 10001$-.
2453
2454 030052          BR 9$              ;SKIP AROUND
    
```

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2443 030054 005737 016774 8$: TST T.DMP          ;DO WE STILL WANT TO CHECK IT
2444 030060 001772          BEO 10$           ;NO
2445 030062 104006          CKLOOP          ;YES, CHECK FOR LOOP FIRST
2446 030062          EMT C$CLP1
2447
2448 030064 005037 002130 99$: CLR CDCNT          ;CLEAR NUMBER WE'RE TO PRINT
2449 030070 005037 002132 CHECK            ;ALL? HEADER ON FIRST PRINT
2450 030074 012700 000200 MOV #BUF,R2        ;COMPARE BUFFER TO CHECK WRITE
2451 030100 012701 000200 MOV #128,R1        ;128 WORDS
2452 030104 012737 125252 MOV #125252,GDDAT ;SET UP EXPECTED
2453 030112 011237 002170 5$: MOV (R2),BDDAT     ;GET DATA
2454 030114 001771 002166 CMP GDDAT,BDDAT   ;IS DATA OKAY
2455 030126 010237 002162 BEO 6$              ;YES, CONTINUE
2456 030132 023737 002130 016776 MOV R2,TMP1       ;LOAD BAD NEW LOCATION
2457 030140 001002          CMP CDCNT,T.LMT   ;CHECKED ENOUGH??
2458 030142          BNE 333$          ;NO
2459 030144 000076          ESCAPE SEG      ;CHECK FOR FL:LOE, ELSE EXIT SEG
2460 030146 005237 002130 333$: EMT C$ESCAPE
2461 030152 005737 002122          .WORD 10001$-.
2462 030160          INC CDCNT          ;ACCOUNT FOR IT
2463 030160 104462          TST CHECK        ;HEADER OR JUST DATA
2464 030162 000042          BNE 9$           ;JUST DATA
2465 030164 011774          ERDF 34,EM25,ERR8 ;BAD DATA
2466 030166 014564          TRAP T$ERRCODE
2467 030174 000416          .WORD 34
2468 030176          .WORD EM25
2469 030178          .WORD ERR8
2470 030222 000012          IFC CHECK        ;ACCOUNT FOR PRINT OF HEADER
2471 030224          BR 6$
2472
2473 030176 013746 002170 9$: PRINTB #FRMT6,TMP1,GDDAT,BDDAT
2474 030178 013746 002166 MOV BDDAT,-(SP)
2475 030202 013746 002162 MOV GDDAT,-(SP)
2476 030212 012746 016042 MOV TMP1,-(SP)
2477 030216 012746 000044 MOV #4,-(SP)
2478 030222 010600          MOV SP,R0
2479 030224 104014          EMT C$PNTB
2480 030226 062706          ADD #12,SP
2481
2482 030232          CKLOOP          ;BUMP BUFFER POINTER
2483 030234 005722          EMT C$CLP1
2484 030236 005301          TST (R2)+
2485 030242 001324          DEC R1             ;DONE?
2486 030244          BNE 5$              ;NO, GO BACK
2487
2488 030242          ENDSEG          ;%%END OF SEGMENT%%
2489 030242 104005          EMT C$ESEG
2490 030244          ENDSEG          ;%%END OF SEGMENT%%
2491 030244 104005          EMT C$ESEG
2492 030246          ENDTST          ;**END OF TEST**
2493 030246 104001          L10060: EMT C$ETST
2494
    
```

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    .SBTTL **TEST 28** - CHECK SILO LINES
    BCNTST                                     ;**START OF TEST**

    030250
    030250
    030250
    030250 004737 021356      JSR PC,HDHOME      ;HEADS OVER TRACK 0
    030250 104032             CEXIT             ;HEADS GO HOME ORAY
    030250 008404             .WORD L10001-.

    030266 012703 002662      MOV #DATPAT,R3

    030272 104004             BGNSEC           ;**START OF SEGMENT**
    030272 002700             MOV #R0,R0      ;WRITE PATTERN INTO MEMORY
    030272 000200             MOV #R1,R1      ;128 WORDS
    030272 001376             MOV #R3,(R0)+   ;WRITE THE PATTERN
    030272 001376             DEC R3          ;NO GO BACK
    030272 001376             BNE 2$         ;NO GO BACK

    030272 012777 003052 151724  MOV #BUF,ORLBA   ;SETUP TO WRITE PATTERN ONTO DISK
    030272 002700             CLR ORLDA       ;LOAD BA
    030272 002700             MOV #128,ORLMP ;WORD COUNT
    030272 002456             JSR R5,LDFUNC   ;LOAD THE FUNCTION IN NEXT WORD
    030272 004537 021276      JSR R5,WTCRDY   ;CHECK FOR FL:LOE, ELSE EXIT SEG
    030272 104010             ESCAPE SEG      ;CHECK FOR FL:LOE, ELSE EXIT SEG
    030272 004537             ENT C$ESCAPE    ;CHECK CTRLR FOR ERRORS
    030272 004537             .WORD 10001$- ;CHECK FOR FL:LOE, ELSE EXIT SEG
    030272 004537             ESCAPE SEG      ;CHECK CTRLR FOR ERRORS
    030272 104010             ENT C$ESCAPE    ;CHECK FOR FL:LOE, ELSE EXIT SEG
    030272 000310             .WORD 10000$-

    030272 104004             BGNSEC           ;**START OF SEGMENT**
    030272 012700             MOV #R0,R0      ;CLEAR MEMORY BEFORE READING IT BACK
    030272 000200             MOV #R1,R1      ;128 WORDS
    030272 000300             CLR R1          ;CLEAR
    030272 000300             DEC R1          ;HOME
    030272 001376             BNE 3$         ;NO

    030400 012777 003052 151636  MOV #BUF,ORLBA   ;SETUP TO READ IT BACK
    030400 012777 177806 151634  MOV #128,ORLMP  ;128 WORDS
    030400 005077             CLR ORLDA       ;SECTOR ZERO
    
```

```

    030400 004537 020456      JSR R5,LDFUNC   ;LOAD THE FUNCTION IN NEXT WORD
    030400 004537 021276      JSR R5,WTCRDY   ;CHECK FOR FL:LOE, ELSE EXIT SEG
    030400 104010             ESCAPE SEG      ;CHECK FOR FL:LOE, ELSE EXIT SEG
    030400 004537             ENT C$ESCAPE    ;CHECK CTRLR FOR ERRORS
    030400 004537             .WORD 10001$- ;CHECK IF WE A DUMP
    030400 004537             TST C$CRC      ;CHECK FOR FL:LOE, ELSE EXIT SEG
    030400 001003             JSR R5,CRC      ;CHECK FOR FL:LOE, ELSE EXIT SEG
    030400 104010             ESCAPE SEG      ;CHECK FOR FL:LOE, ELSE EXIT SEG
    030400 000206             ENT C$ESCAPE    ;SKIP AROUND
    030400 000206             .WORD 10001$- ;DO WE STILL WANT TO CHECK IT
    030400 000206             BNE 8$         ;YES, CHECK FOR LOOP FIRST
    030400 001772             BR 10$         ;YES, CHECK FOR LOOP FIRST
    030400 104006             CRLOOP

    030406 005237 002130 99$:   CLR CDCNT       ;CLEAR NUMBER WE'RE TO PRINT
    030406 005237             CHECK R5        ;ALLOW HEADER ON FIRST PRINT
    030406 005237             MOV #R3,GDDAT  ;COMPARE WHAT WE READ BACK
    030406 005237             MOV #TMP2,BUF  ;BUFFER START
    030406 005237             MOV #1,TMP1    ;START WITH FIRST

    030406 017777 151442 002170 5$:   MOV #TMP2,BDDAT ;GET DATA
    030406 005237             GDDAT,BDDAT   ;GOOD?
    030406 005237             BEQ 4$         ;YES, BRANCH

    030406 023737 002130 016776  CMP CDCNT,T.LMT ;CHECKED ENOUGH??
    030406 005237             BGT 5$         ;NO
    030406 005237             ESCAPE SEG      ;CHECK FOR FL:LOE, ELSE EXIT SEG
    030406 005237             ENT C$ESCAPE    ;CHECK FOR FL:LOE, ELSE EXIT SEG
    030406 005237             .WORD 10001$- ;ACCOUNT FOR IT
    030406 005237             INC CDCNT       ;ACCOUNT FOR IT

    030406 005737 002122       TST CHECK       ;HEADER OR JUST DATA
    030406 001007             BR 9$         ;JUST DATA
    030406 005737             BRNDP          ;BAD DATA BACK
    030406 005737             TRAP #ERRCODE
    030406 005737             .WORD 35
    030406 005737             .WORD EM45
    030406 005737             .WORD ERR10

    030406 005737             INC CHECK       ;ACCOUNT FOR PRINT OF HEADER
    030406 005737             BR 4$         ;ACCOUNT FOR PRINT OF HEADER

    030406 006000 013746 002170 9$:   PRINTB #FRMT7,TMP1,GDDAT,BDDAT
    030406 006000             MOV BDDAT,-(SP)
    030406 006000             MOV GDDAT,-(SP)
    030406 006000             MOV TMP1,-(SP)
    030406 006000             MOV #FRMT7,-(SP)
    030406 006000             MOV #4,-(SP)
    030406 006000             MOV #C$CR,B
    030406 006000             ENT ADD
    030406 006000             ADD #12,SP
    030406 006000             CRLOOP
    
```

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(3) 030634 104006 EMT C$CLP1
2580 030636 062737 000002 002164 ADD #2,TMP2 ;NEXT LOCATION
2581 030644 005237 002162 INC TMP1 ;NEXT WORD
2582 030650 023727 002162 000201 CMP TMP1,#129. ;DONE
2583 030656 001317 BNE 5$ ;NO, GO BACK
2584 030660 ENDSEG ;**END OF SEGMENT**
(3) 030660 10001$: EMT C$ESEG
2585 030662 005723 TST (R3)+ ;DONE ALL PATTERNS
2586 030664 001203 BNE 6$ ;NO, GO BACK
2587 030666 ENDSEG ;**END OF SEGMENT**
(3) 030666 10000$: EMT C$ESEG
2588 030670 104005 ENDTST ;**END OF TEST**
(3) 030670 L10061: EMT C$TST
2589 030670 104001 .SBTTL **TEST 29** - CHECK THROUGHPUT OF SILO
2590 030672 BGNST ;**START OF TEST**
2591 030672 STARS
2592 *****
2593 ;TEST THAT THE SILO OPERATES CORRECTLY, WE WILL WRITE A PATTERN THAT CONTAINS
2594 ;A UNIQUE PATTERN IN EACH LOCATION, WE EXPECT IT BACK IN PROPER
2595 ;ORDER, WE DO A ONE SECTOR TRANSFER
2596 STARS
2597 *****
2598 030672 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
2599 030676 000410 CKERFG ;HEADS GO HOME OKAY
2600 030704 104032 EMT C$EXIT
2601 030706 000410 .WORD L10062-.
2602 030710 104004 BGNSEG ;**START OF SEGMENT**
(3) 030710 EMT C$BSEG
2603 030712 012700 000001 MOV #1,R0 ;INITIAL 1
2604 030716 012701 000200 MOV #128,R1 ;128 WORDS
2605 030720 012702 003052 MOV #BUF,R2 ;BUFFER
2606 030726 010022 2$: INC R0,(R2)+ ;WRITE A WORD
2607 030730 005200 MOV R0,(R2)+ ;NEXT PATTERN (1-128)
2608 030732 005301 DEC R1 ;DONE
2609 030734 001374 BNE 2$ ;NO
2610 030736 012777 003052 151300 MOV #BUF,@RLBA ;SETUP TO WRITE
2611 030744 012777 177600 151276 MOV #-128,@RLMP ;128 WORDS
2612 030752 005077 151270 CLR @RLDA ;DISK ADDRESS 0
    
```

```

2604 030756 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2605 030762 000014 WRITE R5,WTCRDY
2606 030764 004537 021276 JSR R5,WTCRDY
2607 030770 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 030770 104010 EMT C$ESCAPE
2608 030772 000322 .WORD 10000$-.
2609 030774 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2610 031000 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 031000 104010 EMT C$ESCAPE
2611 031002 000312 .WORD 10000$-.
2612 031004 104004 BGNSEG ;**START OF SEGMENT**
(3) 031004 EMT C$BSEG
2613 031006 012700 003052 MOV #BUF,R0 ;CLEAR BUFFER
2614 031012 012701 000200 MOV #128,R1 ;128 IN LENGTH
2615 031016 005020 CLR (R0)+ ;CLEAR
2616 031020 005301 DEC R1 ;DOWN COUNT
2617 031022 001375 BNE 3$ ;DONE?
2618 031024 012777 003052 151212 MOV #BUF,@RLBA ;BUS ADDRESS
2619 031032 012777 177600 151210 MOV #-128,@RLMP ;WORD COUNT
2620 031040 005077 151202 CLR @RLDA ;DISK ADDRESS
2621 031044 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2622 031050 000014 READ
2623 031052 004537 021276 JSR R5,WTCRDY
2624 031056 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 031056 104010 EMT C$ESCAPE
2625 031060 000232 .WORD 10001$-.
2626 031062 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2627 031066 005737 002124 TST T.CRC ;WAS ERROR A DCK??
2628 031072 001003 BNE 8$ ;YES, SEE IF WE A DUMP
2629 031074 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 031074 104010 EMT C$ESCAPE
2630 031076 000464 .WORD 10001$-.
2631 031102 005737 016774 8$: TST #DMP ;SKIP AROUND
2632 031106 001772 BEQ 10$ ;DO WE STILL WANT TO CHECK IT
2633 031110 104006 CKLOOP ;NO
(3) 031110 EMT C$CLP1 ;YES, CHECK FOR LOOP FIRST
2634 031112 005037 002130 99$: CLR CDCNT ;CLEAR NUMBER WE'RE TO PRINT
2635 031116 005037 002122 CLR CHECK ;ALLOW HEADER ON FIRST PRINT
2636 031122 012737 000001 MOV #1,GDDAT ;START GOOD AT 1
2637 031130 012737 003052 MOV #BUF,TMP2 ;START OF BUFFER
2638 031136 012737 000001 MOV #1,TMP1 ;FIRST WORD
2639 031144 017737 151014 4$: MOV @TMP2,BDDAT ;GET WORD
2640 031152 023737 002170 002166 CMP BDDAT,GDDAT ;CORRECT?
2641 031160 001440 BEQ 6$ ;YES
2642 031162 023737 002130 016776 CMP CDCNT,T.LMT ;CHECKED ENOUGH??
2643 031170 001002 BNE 33$ ;NO
2644 031172 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 031172 104010 EMT C$ESCAPE
2645 031174 000116 .WORD 10001$-.
    
```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-66
 CZRLBB.P11 22-NOV-78 15:28 **TEST 29** - CHECK THROUGHPUT OF SILO SEQ 0096

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2648 031176 005237 002130 333$: INC CDCWT ;ACCOUNT FOR IT
2649 031202 005737 002122 TST CHECK ;HEADER OR JUST DATA
2650 031206 001007 BNE 9$ ;JUST DATA
2651 031210 104462 ERDF 36,EM47,ERR10 ;BAD DATA
2652 031210 000044 TRAP 1,ERRCODE
2653 031214 013896 -WORD 36
2654 031216 014702 -WORD EM47
2655 031220 005237 002122 INC ERR10 ;ACCOUNT FOR PRINT OF HEADER
2656 031224 000416 BR 6$

2657 031226 9$: PRINTB #PRMT7,TMP1,GDDAT,BDDAT
2658 031226 MOV BDDAT,-(SP)
2659 031230 MOV GDDAT,-(SP)
2660 031236 MOV TMP1,-(SP)
2661 031242 MOV #PRMT7,-(SP)
2662 031246 MOV #4,-(SP)
2663 031246 SP,RO
2664 031250 EMT C$PRINTB
2665 031256 ADD #12,SP
2666 031262 6$: CKLOOP
2667 031262 EMT C$CLP1

2668 031264 062737 000002 002164 ADD #2,TMP2 ;NEXT
2669 031272 005237 002162 INC TMP1 ;NEXT
2670 031276 005237 002166 INC GDDAT ;NEXT
2671 031302 023777 002162 000201 CMP TMP1,#129. ;DONE?
2672 031310 001315 BNE 4$

2673 031312 10001$: ENDSEG ;**END OF SEGMENT**
2674 031314 EMT C$ESEG
2675 031312 104005

2676 031314 10000$: ENDSEG ;**END OF SEGMENT**
2677 031314 EMT C$ESEG
2678 031316 104005
2679 031316 ENDTST ;**END OF TEST**
2680 031316 L10062: EMT C$ETST
2681 104001

2682 .SBTTL **TEST 30** - CHECK ZERO FILL ON WRITE
2683 BCNTST ;**START OF TEST**

2684 STARS
2685 ;*****
2686 ;WHEN WRITING PARTIAL SECTORS (LESS THAN 128 WORDS) THE
2687 ;CONTROLLER WILL FILL IN THE REMAINING PORTION OF
2688 ;THE SECTOR WITH ZERO WORDS. CHECK THIS FEATURE
2689 ;WITH WORD COUNTS FROM 1 TO 127
2690 STARS
2691 ;*****
2692

```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-67
 CZRLBB.P11 22-NOV-78 15:28 **TEST 30** - CHECK ZERO FILL ON WRITE SEQ 0097

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2693 031320 004737 021356 JSR PC,HDRHOME ;HEADS OVER TRACK 0
2694 031324 000442 C$RFG ;HEADS GO HOME OKAY
2695 031332 104032 EMT C$EXIT
2696 031334 000442 -WORD L10063-.

2697 031336 BGNSEG ;**START OF SEGMENT**
2698 031336 EMT C$BSEG

2699 031340 012737 000001 002162 35$: MOV #1,TMP1 ;START WITH 1 WORD WRITE
2700 031346 012700 003052 MOV #BUF,RO ;WRITE BUFFER WITH 52525, WE'LL
2701 031352 012701 000200 MOV #128,R1 ;WRITE 128 WORDS ALL THOUGH WE'RE
2702 031356 012720 052525 3$: MOV #52525,(RO)+ ;ONLY GOING TO TRANSFER < 128
2703 031364 001374 DEC R1 ;DONE WITH BUFFER?
2704 031366 013700 002162 33$: BNE 3$ ;NO, GO BACK
2705 031372 005400 MOV TMP1,RO ;GET TRANSFER WORD COUNT
2706 031374 010077 150650 NEG RO ;NEGATE FOR RLMP
2707 031376 010077 150650 MOV #0,RLMP ;STORE WORD COUNT AWAY
2708 031400 012777 030052 MOV #BUF,@RLBA ;SET UP RLBA
2709 031406 005077 150634 CLR RLD,@RLBA
2710 031412 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2711 031416 000012 WRITE ;WRITE IT
2712 031420 004537 021276 JSR R5,WTCRDY ;WAIT FOR WRITE TO FINISH
2713 031424 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2714 031426 000346 EMT C$ESCAPE
2715 -WORD 100005-.

2716 031430 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2717 031434 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2718 031436 000336 EMT C$ESCAPE
2719 -WORD 100005-.

2720 031440 BGNSEG ;**START OF SEGMENT**
2721 031440 EMT C$BSEG
2722 031442 104004 MOV #BUF,RO ;WE'RE GOING TO OVERLAY BUFFER BEFORE
2723 031446 012700 000200 MOV #128,R1 ;READING IT BACK-
2724 031452 012720 125252 18$: MOV #125252,(RO)+ ;OVERLAY IT WITH COMPLIMENT
2725 031460 001374 DEC R1 ;DONE?
2726 031462 001374 BNE 18$ ;NO, KEEP GOING

2727 031462 012777 093052 150554 MOV #BUF,@RLBA ;SET UP TO READ
2728 031470 012777 177600 MOV #128,@RLMP ;128 WORDS TO CHECK ZERO FILL
2729 031476 005077 150544 CLR RLD,@RLBA ;SECTOR
2730 031500 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2731 031506 000014 READ
2732 031510 004537 021276 JSR R5,WTCRDY ;WAIT TIL WE FINISH THE READ
2733 031514 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2734 031516 000234 EMT C$ESCAPE
2735 -WORD 100015-.

2736 031520 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2737 031524 005737 002124 TST T.CRC ;WAS ERROR A DCK??
2738 031530 001003 BNE 8$ ;YES, SEE IF WE A DUMP
2739 031536 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2740 031538 000216 EMT C$ESCAPE
2741 031536 -WORD 100015-.
2742 031540 000404 BR 9$ ;SKIP AROUND
2743 031540 005737 016774 8$: TST T.DMP ;DO WE STILL WANT TO CHECK IT

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-68
CZRLBB.P11 22-NOV-78 15:28 **TEST 30** - CHECK ZERO FILL ON WRITE SEQ 0098

2727 031544 001772 BEQ 10$ ;NO
2728 031546 104006 CKLOOP ;YES, CHECK FOR LOOP FIRST
2729 031546 104006 EMT C$CLP1 ;CLEAR NUMBER WE'RE TO PRINT
2730 031550 005037 99$: CLR CDCNT ;ALLOW HEADER ON FIRST PRINT
2731 031560 013702 MOV TMP1,R2 ;WORDS WRITTEN IN R2
2732 031564 012701 000200 MOV #128.,R1 ;CHECK 128 WORDS
2733 031570 012703 MOV #BUF,R3 ;SET UP BUFFER BEGINNING
2734 031574 005037 CLR TMP2 ;ZERO WORD COUNT
2735 031580 012737 002166 MOV #525,GDDAT ;SET UP EXPECTED
2736 031606 011337 4$: MOV (R3),BDDAT ;GET WORD
2737 031612 023737 002166 CMP BDDAT,GDDAT ;IS WORD CORRECT?
2738 031620 001441 BEQ 12$ ;YES, GO CHECK COUNTS AND REPEAT
2739 031622 023737 002130 016776 CMP CDCNT,T.LMT ;CHECKED ENOUGH??
2740 031630 001002 BNE 333$ ;NO
2741 031632 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2742 031632 104010 EMT C$ESCAPE
2743 031634 000116 .WORD 100015- ;ACCOUNT FOR IT
2744 031636 005237 002130 INC CDCNT
2745 031642 005737 002122 TST CHECK ;HEADER OR JUST DATA
2746 031646 001007 BNE 9$ ;JUST DATA
2747 031650 001007 ERRDF 37,EM27,ERR12
2748 031650 104462 TRAP 1$ERCODE
2749 031654 012105 .WORD 3
2750 031660 015026 .WORD EM27
2751 031664 005237 002122 INC ERR12 ;ACCOUNT FOR PRINT OF HEADER
2752 031664 000417 BR 12$
2753 031666 9$: PRINTB #FRMT9,TMP1,R3,GDDAT,BDDAT
2754 031672 013746 MOV BDDAT,-(SP)
2755 031676 013746 MOV GDDAT,-(SP)
2756 031700 010346 MOV R3,-(SP)
2757 031704 013746 MOV TMP1,-(SP)
2758 031708 016312 MOV #FRMT9,-(SP)
2759 031712 010600 MOV SP,RO
2760 031716 104014 ENT C$PNTB
2761 031720 062706 ADD #14,SP
2762 031724 104006 12$: CKLOOP
2763 031728 005723 EMT C$CLP1
2764 031730 005301 6$: INC (R3)+
2765 031734 005301 INC TMP2
2766 031736 001405 DEC R1 ;DONE ALL WORDS?
2767 031740 005302 BEQ 7$ ;EXIT TEST
2768 031744 005302 DEC R2 ;DONE CHECKING NON-ZERO WORDS
2769 031748 005302 BGT 4$ ;NO BRANCH BACK
2770 031750 005037 CLR GDDAT ;YES, SET EXP'D AS ZERO
2771 031752 000716 BR 4$ ;BRANCH BACK
2772 031752 7$: ;EXIT TEST
2773 031752 100015$: ENDSEG ;**END OF SEGMENT**

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-69
CZRLBB.P11 22-NOV-78 15:28 **TEST 30** - CHECK ZERO FILL ON WRITE SEQ 0099

2774 031752 104005 EMT C$ESEC
2775 031754 005237 002162 INC TMP1
2776 031758 001402 000200 CLR TMP1,#128.
2777 031760 001337 031346 BEQ 3$
2778 031774 JMP 35$
2779 031774 34$:
2780 031774 10000$: ENDSEG ;**END OF SEGMENT**
2781 031774 104005 EMT C$ESEC
2782 031776 031776 ENDTST ;**END OF TEST**
2783 031776 L10063: EMT C$ETST
2784 031776 104001 .SBTTL **TEST 31** - CHECK SECTOR BITS OF HEADER COMPARE
2785 032000 BGNST ;**START OF TEST**
2786 032000 STARS
2787 ;*****
2788 ;TEST THAT ALL SECTOR BITS OF HEADER WORD CAN COMPARE
2789 ;UNIQUELY. WE TESTED THE HEADER COMPARE LOGIC EARLIER
2790 ;BUT THAT WAS NOT AN EXTENSIVE TEST OF THE SECTOR BITS.
2791 ;THE TEST PROCEDURE IS TO WRITE EACH SECTOR OF TRACK
2792 ;WITH THE SECTOR ADDRESS, THEN GO BACK AND READ
2793 ;EACH SECTOR. IF ANY SECTOR HAS ANY DATA THEN THAT
2794 ;WHICH WAS EXPECTED THEN WE HAVE AN ERROR
2795 ;ERROR PRINT OUT WILL GIVE SECTOR, EXPECTED AND RECEIVED
2796 STARS
2797 ;*****
2798 032004 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
2799 032004 104032 CKERFG ;HEADS GO HOME OKAY
2800 032012 000414 EMT C$EXIT
2801 032014 000414 .WORD L10064-.
2802 032016 104004 BGNSEG ;**START OF SEGMENT**
2803 032016 005037 002160 EMT C$BSEG
2804 032024 005037 1$: CLR TMP0 ;CLEAR
2805 032024 104004 BGNSEG ;**START OF SEGMENT**
2806 032024 005037 EMT C$BSEG
2807 032026 012702 199$: MOV #BUF,R2 ;WRITE A PATTERN FOR THE WRITE
2808 032032 013701 MOV #128.,R1 ;ONE SECTOR'S WORTH
2809 032036 013722 2$: MOV TMP0,(R2)+ ;WRITE IT
2810 032042 005301 DEC R1 ;DONE,

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-70
 CZRLBB.P11 22-NOV-78 15:28 **TEST 31** - CHECK SECTOR BITS OF HEADER COMPARE SEQ 0100

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2810 032044 001374 BNE 2$ ;IF NOT, GO BACK
2811 032046 012777 177600 150174 MOV #128, @R1LMP ;ONE SECTOR WORD COUNT
2812 032054 012777 003052 150162 MOV @BUF, @R1LBA ;WRITE FROM BUF
2813 032062 013777 002160 150156 MOV @R5, @R1LBA ;SECTOR
2814 032070 004537 021276 JSR R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2815 032074 004537 021276 WRITE R5, LDFUNC
2816 032076 004537 021276 JSR R5, WTCRDY ;WAIT FOR WRITE TO FINISH
2817 032102 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2818 032102 EMT C$ESCAPE
2819 032104 104010 .WORD 100025- ;
2820 032104 000320 INC TMP0 ;NEXT SECTOR
2821 032106 005237 002160 000050 CMP TMP0, #40. ;ALL DONE?
2822 032122 005037 002160 BNE 199$ ;NO GO BACK
2823 032122 CLR TMP0 ;CLEAR
2824 032126 BGNSEG ;**START OF SEGMENT**
2825 032126 EMT C$BSEG
2826 032130 012702 003052 98$: MOV @BUF, R2 ;CLEAR THE BUFFER FIRST
2827 032134 012701 000200 3$: MOV @R2, R1 ;128 WORDS
2828 032140 005022 CLR R1
2829 032144 001375 BNE 3$
2830 032146 013777 177600 150072 MOV @R5, @R1LBA ;GET SECTOR
2831 032154 012777 003052 150062 MOV @BUF, @R1LBA ;SETUP BUS ADDRESS
2832 032162 012777 177600 150060 MOV #128, @R1LMP ;READ A SECTOR
2833 032170 004537 020456 JSR R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2834 032174 000014 READ
2835 032176 004537 021276 JSR R5, WTCRDY
2836 032202 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2837 032202 EMT C$ESCAPE
2838 032204 104010 .WORD 100025- ;
2839 032206 004537 020214 JSR R5, CHERR ;CHECK CNTLR FOR ERRORS
2840 032212 005737 002124 TST B$ CRC ;WAS ERROR A DCK??
2841 032216 001003 B$ ESCAPE SEG ;YES, SEE IF WE A DUMP
2842 032222 000200 EMT C$ESCAPE ;CHECK FOR FL:LOE, ELSE EXIT SEG
2843 032222 104010 .WORD 100025- ;
2844 032224 000404 BR 99$ ;SKIP AROUND
2845 032226 005737 016774 8$: TST TMP ;DO WE STILL WANT TO CHECK IT
2846 032232 001772 BEQ 10$ ;NO
2847 032234 104006 CKLOOP 10$ ;YES, CHECK FOR LOOP FIRST
2848 032234 EMT C$CLP1
2849 ;CHECK NOW TO SEE IF WE READ THE RIGHT SECTOR
2850 032236 005037 002130 99$: CLR CDCNT ;CLEAR NUMBER WE'RE TO PRINT
2851 032242 005037 002122 CLR CHECK ;ALLOW HEADER ON FIRST PRINT
2852 032246 013737 002160 002166 MOV @R5, @R1LBA ;EXPECTED DATA
2853 032254 012702 003052 MOV @BUF, R2 ;BUFFER
2854 032260 012701 000200 MOV #128, R1 ;WORD COUNT
2855 032264 012237 002170 5$: MOV @R2, @R1LBA ;

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-71
 CZRLBB.P11 22-NOV-78 15:28 **TEST 31** - CHECK SECTOR BITS OF HEADER COMPARE SEQ 0101

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2858 032270 023737 002170 002166 CMP BDDAT, GDDAT
2859 032276 001440 BEQ 6$
2860 032300 023737 002130 016776 CMP CDCNT, T.LMT ;CHECKED ENOUGH??
2861 032306 001002 BNE 333$ ;NO
2862 032310 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2863 032312 000110 EMT C$ESCAPE
2864 032314 005237 002130 333$: .WORD 100025- ;ACCOUNT FOR IT
2865 032320 005737 002122 INC CDCNT ;HEADER OR JUST DATA
2866 032324 001007 TST CHECK ;JUST DATA
2867 032326 104462 BNE 9$
2868 032330 000046 ERRDF 38, EM50, ERR11 ;
2869 032332 013105 TRAP T$ERRCODE ;
2870 032334 005237 002122 .WORD EM50 ;NO
2871 032336 005237 000012 INC ERR11 ;ACCOUNT FOR PRINT OF HEADER
2872 032342 000416 BR 6$
2873 032344 013746 002170 9$: PRINTB #FRMT8, TMP0, GDDAT, BDDAT
2874 032344 013746 MOV BDDAT, -(SP)
2875 032344 013746 MOV GDDAT, -(SP)
2876 032344 013746 MOV TMP0, -(SP)
2877 032360 012746 MOV #FRMT8, -(SP)
2878 032364 012746 MOV #4, -(SP)
2879 032370 010600 MOV SP, R0
2880 032372 104014 ADD C$CNTB
2881 032374 062706 000012 ADD #12, SP
2882 032400 032400 104006 EMT C$CLP1
2883 032402 005301 DEC R1 ;ALL OF SECTOR CHECKED?
2884 032404 001327 BNE 5$ ;GO BACK IF NOT
2885 032406 005237 002160 000050 INC TMP0 ;NEXT SECTOR
2886 032412 023727 002160 000050 CMP TMP0, #40. ;DONE?
2887 032420 001243 BNE 98$ ;NO, GO BACK
2888 032422 104005 ENDSEG ;**END OF SEGMENT**
2889 032422 104005 EMT C$ESEG
2890 032424 104005 ENDSEG ;**END OF SEGMENT**
2891 032424 104005 EMT C$ESEG
2892 032426 104005 ENDSEG ;**END OF SEGMENT**
2893 032426 104005 EMT C$ESEG
2894 032426 104005 EMT C$ESEG ;**END OF TEST**
2895 032430 104001 ENDTST L10064:
2896 032430 104001 EMT C$SETST
2897 .SBTTL **TEST 32** - WRITE CHECK NPR INTEGRITY
2898 BGNST
2899 032432
2900

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```

2891 032432
2892 (2)
2893 (3)
2894 032432
2895 (2)
2896 (3)
2897 032432 004737 021356
2898 (4) 032444 104032
2899 (4) 032446 000372
2900 032450
2901 (3) 032450 104004
2902 032452 012700 003052
2903 032456 012701 000200
2904 032462 012720 125252
2905 032466 005301
2906 032470 001374
2907
2908 032472 012777 003052 147544
2909 032500 012777 177600 147542
2910 032506 005077 147534
2911 032512 004537 020456
2912 (3) 032512
2913 032520 004537 021276
2914 032524
2915 (3) 032524 104010
2916 032526 000310
2917 032530 004537 020214
2918 (3) 032534 104010
2919 (3) 032536 000300
2920
2921 032540 005077 147502
2922 032544 012777 003052 147472
2923 032552 012777 177600 147470
2924 032560 005077 020456
2925 032564 00014
2926 032566 004537 021276
2927 032572
2928 (3) 032572 104010
2929 032574 000242
2930 032576 004537 020214
2931 032600
2932 (3) 032604 104010
2933 (3) 032606 000232
2934
2935 032606 104004
2936 (3) 032606
  
```

```

STARS
;*****
;CHECK THAT NPR WILL NOT INTERFERE WITH THE OPERATION OF THE
;UNIBUS. WE SET UP LOCATION 4 TO HANDLE THE TRAP IF IT HAPPENS.
STARS
;*****

JSR PC,HDHOME ;HEADS OVER TRACK 0
CKERFG ;HEADS GO HOME OKAY
EMT
.WORD L10065-.

BGNSEG ;%%START OF SEGMENT%%
EMT C$BSEG

MOV #BUF,R0 ;SETUP AND WRITE
MOV #128,R1 ;128 WORDS
299$: MOV #125252,(R0)+ ;WRITE
DEC R1 ;DONE??
BNE 299$

MOV #BUF,@RLBA ;LOAD BUS ADDRESS
MOV #128,@RLMP ;WORD COUNT
CLR @RLDA ;CLEAR DISK ADDRESS
R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD

JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-.

JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-.

;VERIFY WRITE WITH READ BEFORE WRCHK

CLR @RLDA
MOV #BUF,@RLBA
MOV #128,@RLMP
JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
READ

JSR R5,WTCRDY
ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-.

JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-.

BGNSEG ;%%START OF SEGMENT%%
EMT C$BSEG
  
```

```

2933 032610
2934 (7) 032610 012746 000340
2935 (5) 032616 013748 021350
2936 (4) 032624 012746 000003
2937 (3) 032630 104037
2938 032632 062706 000010
2939 032636 005037 002142
2940 032642 012777 003052 147374
2941 032650 005077 147372
2942 032654 012777 177600 147366
2943 032662 005037 002166
2944 032666 013737 002134
2945 032672 054737 000002 002166
2946 032702 004537 020764
2947 032706 013737 002166
2948 032714 052737 000201 002216
2949 032722 042737 002000 002216
2950 032730 013777 002166 147304
2951 032736 012701 000144
2952 032742 032777 000200 147272
2953 032750 001013
2954 032752
2955 (3) 032752 012700 000024
2956 (3) 032756 104027
2957 032762 001367
2958
2959 032764 004537 021016
2960 032770
2961 (5) 032770 104462
2962 (5) 032774 000000
2963 (5) 032774 007172
2964 (5) 032776 014456
2965 033000
2966 (3) 033000 013700 002132
2967 033004 104036
2968 033006
2969 (3) 033006 104010
2970 (3) 033010 000024
2971
2972 033012 005737 002142
2973 033016 003006
2974 033020 004537 021016
2975 033024
2976 (3) 033024 104461
2977 (5) 033026 000001
2978 (5) 033030 013477
2979 033034 014244
2980
2981 033034
2982 (3) 033034 104005
2983 033036
  
```

```

1$: SETVEC ERRVEC,#TRPHAN,#340 ;SET UP FOR TRAP
MOV #340,(SP)
MOV #TRPHAN,(SP)
MOV ERRVEC,(SP)
MOV #3,(SP)
EMT C$SVEC
ADD #10,SP
CLR TRPFLG ;CLEAR TRAP OCCURANCE
@RLDA @RLBA ;BUS ADDRESS
CLR @RLDA ;LOAD DISK ADDRESS
MOV #128,@RLMP ;WORD COUNT OF 128
CLR GDDAT ;SET UP CSR TO LOAD
MOV DRIVE,GDDAT ;SET IN DRIVE
BIS #WRCHK,GDDAT ;SET IN FUNCTION
MOV #BEFORE,GDDAT ;LOAD FOR ERROR PRINTOUT
GDDAT,B,CS ;SET IN COMMAND
BIS #201,B,CS ;LOAD CRDY
BIC #OPT,B,CS ;CLEAR (BIT 10)
MOV GDDAT,@RLCS ;ISSUE WRITE CHECK
MOV #100,R1 ;WAIT FOR CRDY
BIT #CRDY,@RLCS ;NPR DONE
RNE 6$ ;YES, 6$
WAITUS #20, ;WAIT A WHILE
MOV #20,R0
EMT C$WTO ;A WHILE UP
DEC R5 ;NO, GO BACK
RNE 5$

JSR R5,AFTER
ERRDF 0,CRTIM,ERR5 ;CONTROLLER TIMED OUT
TRAP T$ERRCODE
.WORD 0
.WORD CRTIM
.WORD ERR5
6$: CLRVEC ERRVEC ;CLEAR VECTOR
MOV ERRVEC,R0
EMT C$CVEC
ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10001$-.

TST TRPFLG ;DID TRAP OCCUR?
SEG 7$ ;NO
ERR R5,AFTER
ERRSF 1,EM57,ERRO ;TRAP ON WRITE
TRAP T$ERRCODE
.WORD 1
.WORD EM57
.WORD ERRO
7$:

ENDSEG ;%%END OF SEGMENT%%
EMT C$ESEG
ENDSEG ;%%END OF SEGMENT%%
  
```

```

ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-74
CZRLBB.P11      22-NOV-78 15:28  **TEST 32** - WRITE CHECK NPR INTEGRITY                               SEQ 0104

(3) 033036
(3) 033036 104005
2967
2968 033040
(3) 033040
(3) 033040 104001
2969
2970
2971
2972 033042
2973
2974 033042
(2)
2975
2976
2977
2978 033042
2979
2980
2981 033042 004737 021356
2982 033046
(4) 033054 104032
(4) 033056 000214
2983
2984 033060
(3) 033060 104004
2985
2986 033062 012700 003052
2987 033066 012701 000200
2988 033072 012720 125252 299$:
2989 033076 005301
2990 033100 001374
2991
2992 033102 012777 003052 147134
2993 033110 012777 177600 147132
2994 033116 005077 147124
2995 033122 004537 020456
2996 033126 000012
2997 033130 004537 021276
2998 033134
(3) 033134 104010
(3) 033136 000132
2999 033140 004537 020214
3000 033144
(3) 033144 104010
(3) 033146 000122
3001 033150
(3) 033150 104004
3002
3003
3004
3005 033152 005077 147070
3006 033156 012777 003052 147060
3007 033164 012777 177600 147056
3008 033172 004537 020456

10000$: EMT C$ESEG
ENDTST L10065:
EMT C$ETST
.SBTTL **TEST 33** - WRITE CHECK FUNCTION
BGNTST
STARS
;*****
;CHECK OF WRITE CHECK LOGIC UNDER FLAG MODE
;WE WILL WRITE CHECK A FULL SECTOR (128 WORDS) FROM
;MEMORY (BUF). WE CHECK THAT NO ERRORS OCCUR.
STARS
;*****
JSR PC,HDHOME ;HEADS OVER TRACK 0
CKERFG ;HEADS GO HOME OKAY
EMT C$EXIT
.WORD L10066-.
BGNSEG ;%%START OF SEGMENT%%
EMT C$BSEG
MOV #BUF,R0 ;SETUP AND WRITE
MOV #128,R1 ;128 WORDS
MOV #125252,(R0)+ ;WRITE
DEC R1 ;DONE??
BNE 299$
MOV #BUF,@RLBA ;LOAD BUS ADDRESS
MOV #128,@RLMP ;WORD COUNT
CLR @RLDA ;CLEAR DISK ADDRESS
JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
WRITE
JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-
JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-
BGNSEG ;%%START OF SEGMENT%%
EMT C$BSEG
;VERIFY WRITE WITH READ BEFORE WRCHK
CLR @RLDA
MOV #BUF,@RLBA
MOV #128,@RLMP
JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD

```

```

ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-75
CZRLBB.P11      22-NOV-78 15:28  **TEST 33** - WRITE CHECK FUNCTION                               SEQ 0105

3009 033176 000014
3010 033200 004537 021276
3011 033204
(3) 033204 104010
3012 033210 000050
3013 033214 004537 020214
(3) 033214 104010
(3) 033216 000050
3014
3015 033220
(3) 033220 104004
3016
3017 033222
3018 033222 005077 147020 3$:
3019 033224 012777 177600 147014
3020 033228 012777 003052 147002
3021 033242 004537 020456
3022 033246 000002
3023
3024 033250 004537 021276
3025 033254
(3) 033254 104010
(3) 033256 000006
3026
3027 033260
3028 033260 004537 020214
3029
3030 033264
(3) 033264
3031 033264 104005 10002$:
3032 033266 10001$:
(3) 033266 104005 10001$:
(3) 033266 104005 10000$:
3033 033270 104005 10000$:
(3) 033270
(3) 033270 104005
3034 033272
(3) 033272 104001
3035
3036
3037
3038 033274
3039 033274
(2)
3040
3041
3042
3043
3044 033274
(2)
3045
3046 033274 004737 021356
3047

ENDTST L10065:
EMT C$ETST
.SBTTL **TEST 34** - WRITE CHECK FUNCTION INTERRUPT
BGNTST
STARS
;*****
;CHECK OF WRITE CHECK LOGIC UNDER INTERRUPT MODE
;WE WILL WRITE CHECK A FULL SECTOR (128 WORDS) FROM MEMORY (BUF).
;WE CHECK THAT NO ERRORS OCCUR. WE DO NOT CHECK RLDA OR RLBA
;INCREMENT AT THIS TIME.
STARS
;*****
JSR PC,HDHOME ;HEADS OVER TRACK 0

```

```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-76
CZRLBB.P11 22-NOV-78 15:28 ***TEST 34** - WRITE CHECK FUNCTION INTERRUPT SEQ 0106

3048 033300 CKERFG ;HEADS GO HOME OKAY
(4) 033306 EMT CSEXIT
033310 .WORD L10067-.

3049 033312 BGNSEG ;**START OF SEGMENT**
(3) 033312 EMT CSBSEG 104004

3052 033314 012700 003052 MOV #BUF,RO ;SETUP AND WRITE
3053 033320 012701 000200 MOV #128,R1 ;128 WORDS
3054 033324 012720 125252 299$: MOV #125252,(RO)+ ;WRITE
3055 033330 005303 DEC R1 ;DONE??
3056 033332 001374 BNE 299$

3058 033334 012777 003052 146702 MOV #BUF,RLBA ;LOAD BUS ADDRESS
3059 033342 012777 177600 146700 MOV #-128,RLMP ;WORD COUNT
3060 033350 005077 146672 CLR RLDA ;CLEAR DISK ADDRESS
3061 033354 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3062 033360 000012 WRITE
3063 033362 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
3064 033366 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033366 EMT C$E$CAP
3065 033370 001170 .WORD 10005-
3066 033372 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
(3) 033376 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033376 EMT C$E$CAP
(3) 033400 .WORD 10000S-
;VERIFY WRITE WITH READ BEFORE WRCHK

3067 033402 005077 146640 CLR RLDA
3068 033406 012777 003052 146630 MOV #BUF,RLBA
3070 033414 012777 177600 MOV #-128,RLMP
3071 033422 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3072 033426 000012 READ
3073 033430 004537 021276 JSR R5,WTCRDY
3074 033434 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033434 EMT C$E$CAP
(3) 033436 000122 .WORD 10000S-
3076 033440 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3077 033444 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033444 EMT C$E$CAP
(3) 033446 000112 .WORD 10000S-

3078 033450 BGNSEG ;**START OF SEGMENT**
3079 033450 EMT CSBSEG 104004

3080 033452 005037 002144 CLR INTFLG ;CLEAR INTERRUPT OCCURANCE FLAG
3081 033456 005077 146564 CLR RLDA
3082 033462 012777 177600 MOV #-128,RLMP ;SET UP WORD COUNT
3083 033466 003052 146546 MOV #BUF,RLBA ;SET UP BUS ADDRESS
3084 033470 SETPRI #PRI00 ;PRIORITY TO 0
(3) 033476 MOV #PRI00,RO
(3) 033502 EMT C$SPRI
3088 033504 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3089 033510 000102 WRCHKIINTEN ;WRITE CHECK UNDER INTERRUPT

```

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-77
CZRLBB.P11 22-NOV-78 15:28 ***TEST 34** - WRITE CHECK FUNCTION INTERRUPT SEQ 0107

3090 033512 004537 021276 JSR R5,WTCRDY ;WAIT FOR INTERRUPT
3091 033516 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033516 EMT C$E$CAP
(3) 033520 000036 .WORD 10001S-

3093 033522 SETPRI #PRI07 ;SET PRIORITY TO 7
(3) 033522 MOV #PRI07,RO
(3) 033526 104041 EMT C$SPRI
3094 033530 005737 002144 TEST INTFLG ;DID INTERRUPT OCCUR?
3095 033534 001004 BNE Z$ ;YES-BRANCH NO-REPORT

3096 033536 ERRDF 4,EM60,ERRO ;WRITE DID NOT INTERRUPT
(3) 033536 TRAP T$ERCODE
(5) 033540 000004 .WORD 4
(5) 033542 013537 .WORD EM60
3098 033546 014244 2$: ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033546 EMT C$E$CAP
(3) 033550 000006 .WORD 10001S-

3099 033552 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3100 033556 ENDSEG ;**END OF SEGMENT**
3101 033556 EMT C$E$CAP
(3) 033560 104005 ENDSEG ;**END OF SEGMENT**
3103 033560 104005 EMT C$E$CAP ;**END OF TEST**
3104 033562 ENDTST
(3) 033562 L10067: EMT C$E$TST
(3) 033562 .SBTTL **TEST 35** - PROPER INCREMENT OF RLBA ON WRITE CHECK
3105 033564 BGNST ;**START OF TEST**
3106 033564 STARS
3107 *****
3108 ;CHECK THAT THE RLBA WILL INCREMENT PROPERLY AFTER THE
3109 ;WRITE CHECK WAS FINISHED THE RLBA SHOULD BE 128 WORDS (256 BYTES)
3110 ;CREATED. STARTING RLBA IS "BUF", ENDING SHOULD BE "BUF + 256."
3111 ;WE WILL MONITOR ALL ERRORS AND REPORT THEM ACCORDINGLY
3112 *****
3113 033564
3114
3115
3116
3117
3118
3119 033564 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
3120 033570 CKERFG ;HEADS GO HOME OKAY
(4) 033576 EMT CSEXIT
(4) 033600 .WORD L10070-.

3121 033602 BGNSEG ;**START OF SEGMENT**
(3) 033602 EMT CSBSEG 104004
3123 033604 012700 003052 MOV #BUF,RO ;SETUP AND WRITE
3124

```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-78
 CZRLBB.P11 22-NOV-78 15:28 ***TEST 35** - PROPER INCREMENT OF RLBA ON WRITE CHECK SEQ 0108

```

3125 033610 012701 000200          MOV     #128,R1          ;128 WORDS
3126 033614 012701 125252          MOV     #125252,(R0)+  ;WRITE
3127 033620 001374          DEC     R1              ;DONE??
3128 033622 001374          BNE    299$           299$
3129 033624 012777 003052 146412          MOV     #BUF,@RLBA    ;LOAD BUS ADDRESS
3130 033632 012777 177600 146410          MOV     #128,@R1     ;WORD COUNT
3131 033634 005077 020456          CLR     @RLDA,@R1    ;CLEAR DISK ADDRESS
3132 033644 004537 020456          JSR    R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
3133 033650 000012          WRITE
3134 033652 004537 021276          JSR    R5,WTCRDY    ;WAIT FOR CONTROLLER READY
3135 033656          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3136 033660          EMT C$ESCAPE
3137 033662 104010 020214          -WORD 10000$-
3138 033666 004537 020214          JSR    R5,CHERR     ;CHECK CNTLR FOR ERRORS
3139 033666          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3140 033670 104010          EMT C$ESCAPE
3141 033670          -WORD 10000$-
3142          ;VERIFY WRITE WITH READ BEFORE WRCHK
3143          CLR @RLDA
3144 033672 005077 146350          MOV     #BUF,@RLBA  ;SET UP BUS ADDRESS
3145 033676 012777 003052 146340          MOV     #128,@R1    ;WORD COUNT
3146 033704 012777 177600 146336          MOV     #BUF,GDDAT  ;FORM EXPECTED BUS ADDRESS
3147 033712 004537 020456          JSR    R5,LDFUNC     ;AFTER WRITE
3148 033714 000014          READ
3149 033720 004537 021276          JSR    R5,WTCRDY    ;LOAD THE FUNCTION IN NEXT WORD
3150 033724          ESCAPE SEG          ;WRITE CHECK
3151 033724 104010          EMT C$ESCAPE        ;WAIT FOR CONTROLLER READY
3152 033726 000129          -WORD 10001$-      ;CHECK FOR FL:LOE, ELSE EXIT SEG
3153 033734 004537 020214          JSR    R5,CHERR     ;CHECK CNTLR FOR ERRORS
3154 033734          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3155 033734 104010          EMT C$ESCAPE
3156 033736 000116          -WORD 10001$-
3157          BGNSEG
3158 033740 104004          EMT C$BSEG          ;**START OF SEGMENT**
3159          3$:
3160          CLR @RLDA
3161 033742 005077 146300          MOV     #BUF,@RLBA  ;SET UP BUS ADDRESS
3162 033746 012777 003052 146270          MOV     #128,@R1    ;WORD COUNT
3163 033754 012737 003052 002166          MOV     #BUF,GDDAT  ;FORM EXPECTED BUS ADDRESS
3164 033756 062737 000400 002166          ADD    #256,GDDAT   ;AFTER WRITE
3165 033770          JSR    R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
3166 033776 004537 020456          JSR    R5,WTCRDY    ;WRITE CHECK
3167 034002 000004 021276          JSR    R5,WTCRDY    ;WAIT FOR CONTROLLER READY
3168 034004 004537          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3169 034010 104010          EMT C$ESCAPE
3170 034012 000040          -WORD 10001$-
3171          JSR    R5,CHERR     ;CHECK CNTLR FOR ERRORS
3172 034014 004537 020214          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3173 034020 104010          EMT C$ESCAPE
3174 034022 000030          -WORD 10001$-
3175 034024 017737 146214 002170          MOV     @RLBA,BDDAT  ;READ "RLBA" FOR PRESENT ADDRESS

```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-79
 CZRLBB.P11 22-NOV-78 15:28 ***TEST 35** - PROPER INCREMENT OF RLBA ON WRITE CHECK SEQ 0109

```

3168 034032 023737 002170 002166          CMP     BDDAT,GDDAT  ;DID "BA" INCREMENT PROPERLY?
3169 034040 001404          BEQ    2$            ;YES, CONTINUE
3170          ERRDF 5,EM61,ERR4  ;BA DID NOT INCREMENT
3171 034042          TRAP  T$ERCODE
3172 034044          -WORD 5
3173 034050 014410          -WORD ERR4
3174          2$:
3175          ENDSEG
3176          10001$:
3177          EMT C$ESEG          ;**END OF SEGMENT**
3178          ENDSEG
3179          10000$:
3180          EMT C$ESEG          ;**END OF SEGMENT**
3181          ENDTST
3182          L10070:
3183          EMT C$ETST
3184          .SBTTL **TEST 36** - PROPER INCREMENT OF RLDA ON WRITE CHECK
3185          BGNST
3186          ;**START OF TEST**
3187          STARS
3188          ;*****
3189          ;CHECK THAT THE SECTOR INCREMENTS AFTER THE WRITE CHECK WAS FINISHED.
3190          ;A FULL SECTOR WRITE CHECK THE RLDA SHOULD REFLECT AN INCREMENT
3191          ;OF THE SECTOR. "GDDAT" WAS THE EXPECTED RLDA.
3192          STARS
3193          ;*****
3194          JSR    PC,HDHOME  ;HEADS OVER TRACK 0
3195          CKERFG          ;HEADS GO HOME OKAY
3196          EMT C$EXIT
3197          -WORD L10071-
3198          BGNSEG
3199          EMT C$BSEG          ;**START OF SEGMENT**
3200          MOV     #BUF,R0
3201          MOV     #128,R1
3202          MOV     #125252,(R0)+  ;SETUP AND WRITE
3203          DEC     R1              ;128 WORDS
3204          BNE    299$           ;WRITE
3205          299$:
3206          MOV     #BUF,@RLBA  ;LOAD BUS ADDRESS
3207          MOV     #128,@R1    ;WORD COUNT
3208          CLR     @RLDA,@R1  ;CLEAR DISK ADDRESS
3209          JSR    R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
3210          WRITE
3211          JSR    R5,WTCRDY    ;WAIT FOR CONTROLLER READY
3212          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3213          EMT C$ESCAPE
3214          104010

```

```
(3) 034154 000172  
3208 034156 004537 020214  
3209 034162 104010  
(3) 034162 104010  
(3) 034164 000162  
;VERIFY WRITE WITH READ BEFORE WRCHK  
3210  
3211 034166 005077 146054  
3212 034170 012777 003052 146044  
3213 034200 012777 177600 146042  
3214 034206 004537 020456  
3215 034212 000014  
3216 034212 004537 021276  
3217 034220 104010  
3218 034222 000124  
3219 034224 004537 020214  
3220 034230 104010  
3221 034232 000114  
3222 034234  
3223 034234 104004 BGNSEG  
3224 EMT CSBSEG ;**START OF SEGMENT**  
3225  
3226 034236 005037 002166 3$: CLR GDDAT  
3227 034242 013777 002166 145776 MOV GDDAT,@RLDA ;SETUP DISK ADDRESS  
3228 034250 005237 002166 INC GDDAT ;CREATE EXPECTED SECTOR  
3229 034262 012777 177600 145766 MOV #129,@RLMP ;WORD COUNT  
3230 034262 012777 003052 145754 MOV #BUF,@RLBA ;SETUP BUS ADDRESS  
3231  
3232 034270 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD  
3233 WRCHK ;WRITE CHECK  
3234 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY  
3235 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG  
3236 EMT C$ESCAPE  
3237 -WORD 10001$-  
3238  
3239 034306 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS  
3240 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG  
3241 EMT C$ESCAPE  
3242 -WORD 10001$-  
3243  
3244 034316 013737 002232 002170 MOV E,DA,BDDAT ;READ DISK ADDRESS  
3245 034324 023737 002166 002170 CMP GDDAT,BDDAT ;DID SECTOR INCREMENT PROPERLY  
3246 034332 001404 BEQ ;YES, BRANCH NO, REPORT ERROR  
3247  
3248 034334 ERROF 6,EM62,ERR4 ;DA DID NOT INCREMENT  
3249 TRAP T$ERRCODE  
3250 -WORD 6  
3251 -WORD EM62  
3252 -WORD ERR4  
3253  
3254 034344 2$:  
3255 034344 ENDSEG ;**END OF SEGMENT**
```

```
(3) 034344 10001$: EMT C$ESEG ;**END OF SEGMENT**  
3248 034346 10000$: EMT C$ESEG ;**END OF TEST**  
3249 034346 104005 ENDTST L10071: EMT C$SETST ;**END OF TEST**  
3250 034350 104001  
3251  
3252  
3253  
3254  
3255  
3256 034352 BGNST ;**START OF TEST**  
3257 034352  
3258 STARS  
3259 ;*****  
3260 ;CHECK FOR MULTIPLE SECTOR WRITE CHECK. THIS TEST CHECKS  
3261 ;THAT TWO SECTORS CAN BE SUCCESSFULLY CHECKED. WE LOAD  
3262 ;A WORD COUNT OF 129 WORDS (ONE SECTOR + 1 WORD) STARTING AT  
3263 ;SECTOR 0 THRU SECTOR 37 AND VERIFY THAT THE RLDA DOES  
3264 ;A DOUBLE INCREMENT EACH TIME.  
3265 ;*****  
3266 034352 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0  
3267 034366 104032 000354 CKERPG ;HEADS GO HOME OKAY  
3268 EMT C$EXIT  
3269 -WORD L10072-  
3270  
3271 034370 104004 BGNSEG ;**START OF SEGMENT**  
3272 034370 EMT CSRSEG  
3273 034372 012737 000000 002160 MOV #0,TMP0  
3274 034400 012737 000000 002162 MOV #0,TMP1  
3275 034406 012700 003052 MOV #BUF,R0 ;SETUP AND WRITE  
3276 034412 012701 000201 MOV #129,R1 ;129 WORDS  
3277 034416 012720 125252 299$: MOV #125252,(R0)+ ;WRITE  
3278 DEC R1 ;DONE??  
3279 BNE 299$  
3280 034426 012777 003052 145610 1$: MOV #BUF,@RLBA ;LOAD BUS ADDRESS  
3281 034434 012777 177577 145606 MOV #129,@RLMP ;WORD COUNT  
3282 034442 013737 002162 002166 MOV TMP1,GDDAT  
3283 034450 053737 002160 002166 BCS TMP0,GDDAT  
3284 034456 013777 002166 145562 MOV GDDAT,@RLDA  
3285 034464 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD  
3286 034470 000012 WRITE  
3287 034472 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY  
3288 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG  
3289 EMT C$ESCAPE  
3290 -WORD 10000$-  
3291 034476 104010 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS  
3292 034500 002240 020214 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG  
3293 034506 004537  
3294 (3) 034506 104010 EMT C$ESCAPE
```

```

(3) 034510 000230          .WORD 10000$-
33290          ;VERIFY WRITE WITH READ BEFORE WRCHK
33291
33292          MOV     TMP1,GDDAT
33293          BIS     TMP0,GDDAT
33294          MOV     GDDAT,@RLDA
33295          MOV     #BUF,@RLBA
33296          MOV     #128,@RLMP
33297          JSR     R5,LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
33298          READ
33299          R5,WTCRDY
33300          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
33301          EMT     C$ESCAPE
33302          .WORD 10000$-
33303          JSR     R5,CHERR          ;CHECK CNTLR FOR ERRORS
33304          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
33305          EMT     C$ESCAPE
33306          .WORD 10000$-
33307          BGNSEG          ;**START OF SEGMENT**
33308          EMT     C$BSEG
33309
33310          MOV     TMP1,GDDAT          ;GET CYLINDER
33311          BIS     TMP0,GDDAT          ;GET SECTOR
33312          MOV     GDDAT,@RLDA          ;SET DISK ADDRESS-SECTOR 0
33313          MOV     #2,GDDAT          ;SET EXPECTED + 2
33314          MOV     #BUF,@RLBA          ;SET BUS ADDRESS
33315          MOV     #128,@RLMP          ;WORD COUNT-SECTOR+1 WORD
33316          JSR     R5,LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
33317          WRCHK          ;WRITE CHECK
33318          R5,WTCRDY          ;WAIT FOR CONTROLLER READY?
33319          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
33320          EMT     C$ESCAPE
33321          .WORD 10001$-
33322          JSR     R5,CHERR          ;CHECK CNTLR FOR ERRORS
33323          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
33324          EMT     C$ESCAPE
33325          .WORD 10001$-
33326          MOV     E,DA,BDDAT          ;READ DISK ADDRESS
33327          CMP     BDDAT,GDDAT          ;IS DISK ADDRESS CORRECT
33328          BEQ     2$          ;YES, BRANCH NO, REPORT ERROR
33329          ERDF     7,EM63,ERR4          ;DISK ADDRESS NOT CORRECT
33330          TRAP     $ERRCODE
33331          .WORD 104462
33332          .WORD 000007
33333          .WORD 013727
33334          .WORD 014410
33335          2$: CKLOOP
33336          EMT     C$CLP1
    
```

```

33331          10001$: ENDSEG          ;**END OF SEGMENT**
33332          EMT     C$SEEG
33333          INC     TMP0          ;NEXT SECTOR
33334          CMP     #46,TMP0          ;AT END?
33335          BNE     1$          ;NO, GO BACK
33336          ENDSEG          ;**END OF SEGMENT**
33337          10000$: EMT     C$SEEG
33338          ENDTST          ;**END OF TEST**
33339          L10072: EMT     C$ETST
33340          .SBTTL **TEST 38** - FORCE DCK WITH WRITE CHECK
33341          BGNST          ;**START OF TEST**
33342          STARS
33343          ;*****
33344          ;FORCE A DCK WITH WRITE CHECK- THIS IS DONE BY WRITING
33345          ;A SECTOR AND CHANGING A WORD IN MEMORY BEFORE WRITE CHECK
33346          ;IS ISSUED..
33347          STARS
33348          ;*****
33349          034744          JSR     PC,HDHOME          ;HEADS OVER TRACK 0
33350          034750          CRERFG          ;HEADS GO HOME OKAY
33351          034756          EMT     C$EXIT
33352          034760          .WORD L10073-
33353          BGNSEG          ;**START OF SEGMENT**
33354          EMT     C$BSEG
33355          MOV     #BUF,R0          ;SETUP AND WRITE
33356          MOV     #128,R1          ;128 WORDS
33357          MOV     #125252,(R0)+          ;WRITE
33358          DEC     299$          ;DONE??
33359          BNE     299$
33360          MOV     #BUF,@RLBA          ;LOAD BUS ADDRESS
33361          MOV     #128,@RLMP          ;WORD COUNT
33362          CLR     @RLDA          ;CLEAR DISK ADDRESS
33363          JSR     R5,LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
33364          WRITE
33365          JSR     R5,WTCRDY          ;WAIT FOR CONTROLLER READY
33366          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
33367          EMT     C$ESCAPE
33368          .WORD 10000$-
33369          JSR     R5,CHERR          ;CHECK CNTLR FOR ERRORS
33370          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
33371          EMT     C$ESCAPE
33372          .WORD 10000$-
33373          ;VERIFY WRITE WITH READ BEFORE WRCHK
33374          CLR     @RLDA
33375          MOV     #BUF,@RLBA
    
```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-84 SEQ 0114
 CZRLBB.P11 22-NOV-78 15:28 ***TEST 38** - FORCE DCK WITH WRITE CHECK

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3372 035064 012777 177600 145156      MOV      #128, @RLMP
3373 035072 004537 020456      JSR     R5, LDFUNC      ;LOAD THE FUNCTION IN NEXT WORD
3374 035076 000014          READ
3375 035100 004537      JSR     R5, WTCRDY
3376 035104          ESCAPE  SEG             ;CHECK FOR FL:LOE, ELSE EXIT SEG
3377 035106 104010          EMT     C$ESCAPE
3378 035110 000132          .WORD  10000$-
3379 035114 004537      JSR     R5, CHERR      ;CHECK CNTLR FOR ERRORS
3380 035116 104010          ESCAPE  SEG             ;CHECK FOR FL:LOE, ELSE EXIT SEG
3381 035116 000122          EMT     C$ESCAPE
3382 035116 000122          .WORD  10000$-
3383 035120          BGNSEG
3384 035120 104004          EMT     C$BSEG          ;**START OF SEGMENT**
3385 035122 005037 003052      CLR     @RLDA          BUF
3386 035126 005077 145114      CLR     @RLDA          ;SETTING SECTOR 40 OF CYL. ADDR.
3387 035130 012777 003052      MOV     #BUF, @RLBA   ;WORD COUNT
3388 035134 012777 177600 145102      MOV     #128, @RLMP
3389 035146 004537 020456      JSR     R5, LDFUNC
3390 035152 000002          WRCHK
3391 035154 004537      JSR     R5, WTCRDY     ;LOAD THE FUNCTION IN NEXT WORD
3392 035158 004537      JSR     R5, WTCRDY     ;WRITE CHECK
3393 035160 104010          ESCAPE  SEG             ;WAIT FOR CONTROLLER READY
3394 035162 000054          EMT     C$ESCAPE
3395 035162 000054          .WORD  10001$-
3396 035164 013737 002226 002160      MOV     E, C$, TMP0    ;GET RLCS
3397 035170 012737 001447 002160      BIC     #1777, TMP0    ;SAVE ERROR BITS
3398 035174 022737 104000 002160      CMP     #BIT15:BIT11, TMP0 ;DCK SET.
3399 035178 001402          BEQ     IS
3400 035210 004537      JSR     R5, CHERR      ;YES, CONTINUE
3401 035214          CKLOOP
3402 035214 104006          EMT     C$CLP1
3403 035216 022737 104000 002160      CMP     #BIT15:BIT11, TMP0
3404 035224 001404          BEQ
3405 035226          ERRDF  23, EM65, ERRO ;WHEN FORCED
3406 035226          TRAP   4, ERRCODE
3407 035226          .WORD  23
3408 035230          .WORD  EM65
3409 035234          .WORD  ERRO
3410 035236          ;**END OF SEGMENT**
3411 035236          ENDSEG
3412 035236 104005      EMT     C$ESEG          ;**END OF SEGMENT**
3413 035240          ENDSEG
3414 035240 104005      EMT     C$ESEG          ;**END OF TEST**
3415 035242          ENDTST
3416 035242 104001      L10073: EMT     C$ESETST
  
```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-85 SEQ 0115
 CZRLBB.P11 22-NOV-78 15:28 ***TEST 38** - FORCE DCK WITH WRITE CHECK

```

3410          .SBTTL  **TEST 39** - FORCE DCK WITH WRITE CHECK INTERRUPT
3411          BGNST          ;**START OF TEST**
3412 035244
3413 035244
3414 035244
3415 035244
3416 035244
3417 035244
3418 035244
3419 035244
3420 035244 004737 021356      JSR     PC, HDHOME     ;HEADS OVER TRACK 0
3421 035250          CKERFG             ;HEADS GO HOME OKAY
3422 035256 104032          EMT     C$EXIT
3423 035260 000322          .WORD  L10074-
3424 035262          BGNSEG
3425 035262 104004          EMT     C$BSEG          ;**START OF SEGMENT**
3426 035264 012700 003052      MOV     #BUF, R0
3427 035270 012701 000200      MOV     #128, R1       ;SETUP AND WRITE
3428 035274 012720 125252      MOV     #125252, (R0)+ ;128 WORDS
3429 035300 005301          DEC     R1             ;WRITE
3430 035302 001374          BNE     299$          ;DONE??
3431 035304 012777 003052 144732      MOV     #BUF, @RLBA   ;LOAD BUS ADDRESS
3432 035312 012777 177600 144730      MOV     #128, @RLMP   ;WORD COUNT
3433 035314 005077 144722          CLR     @RLDA          ;CLEAR DISK ADDRESS
3434 035314 004537 020456      JSR     R5, LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
3435 035330 000012          WRITE
3436 035332 004537      JSR     R5, WTCRDY     ;WAIT FOR CONTROLLER READY
3437 035336          ESCAPE  SEG             ;CHECK FOR FL:LOE, ELSE EXIT SEG
3438 035340          EMT     C$ESCAPE
3439 035340 104010          .WORD  10000$-
3440 035342 004537 020214      JSR     R5, CHERR      ;CHECK CNTLR FOR ERRORS
3441 035346          ESCAPE  SEG             ;CHECK FOR FL:LOE, ELSE EXIT SEG
3442 035346 104010          EMT     C$ESCAPE
3443 035350 000230          .WORD  10000$-
3444          ;VERIFY WRITE WITH READ BEFORE WRCHK
3445 035352 005077 144670          CLR     @RLDA          @RLDA
3446 035356 012777 003052 144660      MOV     #BUF, @RLBA   @RLBA
3447 035364 012777 177600 144656      MOV     #128, @RLMP   ;LOAD THE FUNCTION IN NEXT WORD
3448 035372 004537 020456      JSR     R5, LDFUNC
3449 035376 000014          READ
3450 035400          JSR     R5, WTCRDY
3451 035404          ESCAPE  SEG             ;CHECK FOR FL:LOE, ELSE EXIT SEG
3452 035404 104010          EMT     C$ESCAPE
3453 035406 000172          .WORD  10000$-
3454 035410 004537      JSR     R5, CHERR      ;CHECK CNTLR FOR ERRORS
3455 035414          ESCAPE  SEG             ;CHECK FOR FL:LOE, ELSE EXIT SEG
3456 035416 000162          EMT     C$ESCAPE
3457 035416 000162          .WORD  10000$-
  
```

```

ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-86
CZRLBB.P11                22-NOV-78 15:28      **TEST 39** - FORCE DCK WITH WRITE CHECK INTERRUPT
                                                    SEQ 0116

3453 035420 104004          BGNSEG          ;**START OF SEGMENT**
3454 035420          EMT          C$BSEG
3455 035422          SETPRI #PRI00
3456 035422          MOV #PRI00,RO
3457 035422          EMT          C$SPRI
3458 035422          CLR INTFLG          ;CLEAR INTERRUPT OCCURANCE FLAG
3459 035444          CLR BUF
3460 035452          MOV #BUF, @RLBA          ;SETTING SECTOR 40 OF CYL. ADDR.
3461 035460          MOV #128, @RLMP          ;WORD COUNT
3462 035460          JSR R5, LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
3463 035466          JSR R5, WTRCDY          ;WRITE CHECK
3464 035472          CKLOOP          ;WAIT FOR CONTROLLER READY
3465 035472          EMT          C$CLP1
3466 035474          SETPRI #PRI07
3467 035500          MOV #PRI07,RO
3468 035502          EMT          C$SPRI
3469 035506          TST INTFLG          ;DID INTERRUPT OCCUR
3470 035510          BNE 2$          ;YES OKAY
3471 035510          ERRDF 24, EM66, ERRO          ;NO INTERRUPT FROM DCK
3472 035512          TRAP T$ERCODE
3473 035514          .WORD 24
3474 035516          .WORD EM66
3475 035516          .WORD ERRO
3476 035520          2$: ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3477 035522          EMT          C$ESCAPE
3478 035522          .WORD 10001$-
3479 035524          MOV E, CS, TMP0          ;SET RLCS
3480 035532          BIC #177, TMP0          ;SAVE ERROR BITS
3481 035540          CMP #BIT15:BIT11, TMP0          ;DCK SET
3482 035546          BEQ 1$          ;YES, CONTINUE
3483 035550          JSR R5, CHERR
3484 035554          CKLOOP          C$CLP1
3485 035556          EMT          C$CLP1
3486 035564          CMP #BIT15:BIT11, TMP0
3487 035566          BEQ 3$
3488 035570          ERRDF 24, EM65, ERRO
3489 035572          .WORD 24
3490 035574          .WORD EM65
3491 035574          .WORD ERRO
3492 035576          3$:          ;WHEN FORCED
3493 035576          ENDSEG          ;**END OF SEGMENT**
3494 035576          10001$:

```

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-87
CZRLBB.P11                22-NOV-78 15:28      **TEST 39** - FORCE DCK WITH WRITE CHECK INTERRUPT
                                                    SEQ 0117

(3) 035576 104005          ENDSEG          C$ESEG          ;**END OF SEGMENT**
3491 035600          EMT          C$ESEG
3492 035600 104005          EMT          C$ESEG          ;**END OF TEST**
3493 035602          ENDTST
3494 035602 104001          L10074: EMT          C$SETST
3495          .SBTTL **TEST 40** - CHECK ZERO FILL ON WRITE WITH WRITE CHECK
3496          BGNST          ;**START OF TEST**
3497 035604
3498
3499
3500
3501 035604          STARS
3502          ;*****
3503          ;WHEN WRITING PARTIAL SECTORS (LESS THAN 128 WORDS) THE
3504          ;CONTROLLER WILL FILL IN THE REMAINING PORTION OF
3505          ;THE SECTOR WITH ZERO WORDS. CHECK THIS FEATURE CAN BE WRITE CHECKED
3506          ;WITH WORD COUNTS FROM 1 TO 127
3507          STARS
3508          ;*****
3509 035604 004737 021356          JSR PC, HDHOME          ;HEADS OVER TRACK 0
3510 035610          CKERFG          ;HEADS GO HOME OKAY
3511 035616          EMT          C$EXIT
3512 035620          .WORD L10075-
3513 035622          BGNSEG          ;**START OF SEGMENT**
3514 035622 104004          EMT          C$BSEG
3515 035624          MOV #1, TMP1          ;START WITH 1 WORD WRITE
3516 035632          MOV #256, R1          ;WRITE BUFFER WITH 52525 WE'LL
3517 035636          MOV #256, R1          ;WRITE 128 WORDS ALL THOUGH WE'RE
3518 035642          MOV #2525, (R0)+          ;ONLY GOING TO TRANSFER < 128
3519 035646          DEC R1          ;DONE WITH BUFFER?
3520 035650          BNE 3$          ;NO, GO BACK
3521 035652          MOV TMP1, RO          ;GET TRANSFER WORD COUNT
3522 035656          NEG RO          ;NEGATE FOR RLMP
3523 035660          MOV RO, @RLMP          ;STORE WORD COUNT AWAY
3524 035664          MOV #BUF, @RLBA          ;SET UP RLBA
3525 035672          CLR @RLDA
3526 035676          JSR R5, LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
3527 035702          WRITE          ;WRITE IT
3528 035704          JSR R5, WTRCDY          ;WAIT FOR WRITE TO FINISH
3529 035710          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3530 035710          EMT          C$ESCAPE
3531 035712          .WORD 10000$-
3532 035714          JSR R5, CHERR          ;CHECK CNTLR FOR ERRORS
3533 035720          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3534 035720          EMT          C$ESCAPE
3535 035722          .WORD 10000$-
3536          ;VERIFY WRITE WITH READ BEFORE WRCHK

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-88
CZRLBB-P11 22-NOV-78 15:28 **TEST 40** - CHECK ZERO FILL ON WRITE WITH WRITE CHECK SEQ 0118

3533 035724 005077 144316 CLR @RLDA
3534 035730 012777 003052 MOV #BUF,@RLBA
3535 035742 005400 MOV TMP1,RO
3536 035744 010077 144300 NEG RO
3537 035750 004537 020456 MOV RO,@RLMP
3538 035754 000014 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3539 035756 004537 021276 JSR R5,WTCRDY
3540 035762 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3541 035764 000126 EMT C$E$CAPE
3542 035766 004537 020214 .WORD 10000$- ;CHECK CNTLR FOR ERRORS
3543 035772 104010 JSR R5,CHERR ;CHECK FOR FL:LOE, ELSE EXIT SEG
3544 035774 000116 ESCAPE SEG
3545 035776 104004 EMT C$E$CAPE ;%%START OF SEGMENT%%
3546 036000 012777 003052 144236 BGNSEG C$B$SEG ;SET UP TO READ
3547 036006 013700 002162 MOV #BUF,@RLBA
3548 036012 005400 MOV TMP1,RO
3549 036014 010077 144230 NEG RO
3550 036020 005077 144222 MOV RO,@RLMP ;SECTOR
3551 036024 004537 020456 CLR @RLDA ;LOAD THE FUNCTION IN NEXT WORD
3552 036030 000002 JSR R5,LDFUNC
3553 036032 004537 021276 JSR R5,WTCRDY ;WAIT TIL WE FINISH THE WRCHK
3554 036036 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3555 036040 000034 EMT C$E$CAPE
3556 036042 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3557 036046 005737 002124 TST T.CRC ;WAS ERROR A DCK??
3558 036052 001003 BNE 8$ ;YES, GIVE MOR INFO
3559 036054 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3560 036056 000016 EMT C$E$CAPE
3561 036060 000405 BR 99$ ;SKIP AROUND
3562 036062 104006 CKLOOP ;YES, CHECK FOR LOOP FIRST
3563 036064 104462 EMT C$CLP1
3564 036066 000045 ERRDF 37,EM64,ERR14 ;ERRCODE
3565 036070 014031 TRAP 37 ;EM64
3566 036072 015150 .WORD EM64
3567 036074 99$: ERR14 ;EXIT TEST
3568 036074 10001$: ENDSEG ;%%END OF SEGMENT%%
3569 036074 104005 EMT C$E$SEG
3570 036076 005237 002162 INC TMP1
3571 036102 023727 000200 CMP TMP1,#128.
3572 036110 001250 BNE 33$
3573 036112 104005 ENDSEG ;%%END OF SEGMENT%%
3574 036112 104005 EMT C$E$SEG

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-89
CZRLBB-P11 22-NOV-78 15:28 **TEST 40** - CHECK ZERO FILL ON WRITE WITH WRITE CHECK SEQ 0119

3571 036114 ENDTST ;**END OF TEST**
3572 036114 104001 L10075:
3573 036116 .SBTTL *TEST 41** - EXTENDED CHECK OF WRITE CHECK FUNCTION
3574 036116 BGNSTST ;**START OF TEST**
3575 036116 STARS
3576 *****
3577 ;CHECK OF WRITE CHECK LOGIC UNDER FLAG MODE
3578 ;THIS TEST IS DONE WITH ALL BIT PATTERNS
3579 ;WE WILL WRITE CHECK A FULL SECTOR (128 WORDS) FROM
3580 ;MEMORY (BUF). WE CHECK THAT NO ERRORS OCCUR.
3581 STARS
3582 *****
3583 036116 004737 021356 JSR PC,HDRHOME ;HEADS OVER TRACK 0
3584 036120 032130 CKRFRG ;HEADS GO HOME OKAY
3585 036130 004032 EMT C$EXIT
3586 036132 000246 .WORD L10076-.
3587 036134 012703 002504 MOV #HDRTAB,R3
3588 036140 036140 BGNSEG ;%%START OF SEGMENT%%
3589 036140 104004 EMT C$B$SEG
3590 036142 012700 003052 298$: MOV #BUF,RO ;SETUP AND WRITE
3591 036146 012701 000200 MOV #128,R1 ;128 WORDS
3592 036152 011302 MOV (R3),R2
3593 036154 010220 299$: MOV R2,(RO)+ ;WRITE
3594 036156 005301 DEC R1 ;DONE??
3595 036160 001375 BNE 299$
3596 036162 012777 003052 144054 MOV #BUF,@RLBA ;LOAD BUS ADDRESS
3597 036170 012777 177600 144052 MOV #128,@RLMP ;WORD COUNT
3598 036176 005077 144044 CLR @RLDA ;CLEAR DISK ADDRESS
3599 036202 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3600 036206 000012 WRITE
3601 036210 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
3602 036214 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3603 036216 000160 EMT C$E$CAPE
3604 036220 004537 020214 .WORD 10000$- ;CHECK CNTLR FOR ERRORS
3605 036224 104010 JSR R5,CHERR ;CHECK FOR FL:LOE, ELSE EXIT SEG
3606 036224 000150 ESCAPE SEG
3607 036226 100005$- EMT C$E$CAPE
3608 036230 104004 BGNSEG ;%%START OF SEGMENT%%
3609 036230 104004 EMT C$B$SEG
3610 ;VERIFY WRITE WITH READ BEFORE WRCHK
3611 036232 005077 144010 CLR @RLDA
3612 036236 012777 003052 144000 MOV #BUF,@RLBA
3613 036244 012777 177600 143776 MOV #128,@RLMP
3614 036252 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-90          SEQ 0120
CZRLBB.P11      22-NOV-78 15:28          **TEST 41** - EXTENDED CHECK OF WRITE CHECK FUNCTION

3615 036256 000014          READ
3616 036260 004537 021276          JSR          R5,WTCRDY
3617 036264          ESCAPE     SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3618 036266          EMT          C$ESEC
3619 036270 104010          .WORD     10001$-
3620 036274 004537 020214          JSR          R5,CHERR          ;CHECK CNTLR FOR ERRORS
3621 036276 104010          ESCAPE     SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3622 036276 000066          .WORD     10001$-
3623 036300          BGNSEG          ;**START OF SEGMENT**
3624 036300 104004          EMT          C$BSEG
3625 036302          3$:
3626 036302 005077 143740          CLR          @RLDA          ;WORD COUNT
3627 036306 012777 177600          MOV          #-128,@RLMP    ;BUS ADDRESS
3628 036310 012777 143734          MOV          #BUF,@RLBA    ;LOAD THE FUNCTION IN NEXT WORD
3629 036312 004537 020456          JSR          R5,LDFUNC      ;WRITE CHECK
3630 036316 000002          WRCHK
3631 036330 004537 021276          JSR          R5,WTCRDY      ;WAIT FOR CONTROLLER READY
3632 036334          ESCAPE     SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3633 036336          EMT          C$ESEC
3634 036336          .WORD     10002$-
3635 036340 004537 020214          JSR          R5,CHERR      ;CHECK CNTLR FOR ERRORS
3636 036344 005737 002124          T          T,CRC          ;WRITE CHECK ERROR??
3637 036350 001404          BEQ          4$          ;NO
3638 036352          ERRHRD 410,ERR15,EM70
3639 036352 104463          TRAP       T$ERRCODE
3640 036354 000632          .WORD     410
3641 036356 015306          .WORD     ER15
3642 036360 014220          .WORD     EM70
3643 036362          4$:
3644 036362          ENDSEG          ;**END OF SEGMENT**
3645 036362          10002$:
3646 036362          EMT          C$ESEC
3647 036364          ENDSEG          ;**END OF SEGMENT**
3648 036364          10001$:
3649 036364          EMT          C$ESEC
3650 036366 104005          EMT          C$ESEC
3651 036370 005723          TST         (R3)+
3652 036374 020327 002660          CMP         R3,#HDREND
3653 036374 001262          BNE         298$
3654 036376          ENDSEG          ;**END OF SEGMENT**
3655 036376          10000$:
3656 036376          EMT          C$ESEC
3657 036400          ENDTST          ;**END OF TEST**
3658 036400          L10076:
3659 036400          EMT          C$SETST
3660 104001

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-91          SEQ 0121
CZRLBB.P11      22-NOV-78 15:28          **TEST 41** - EXTENDED CHECK OF WRITE CHECK FUNCTION

3652          .SBTTL **TEST 42** - EXTENDED CHECK OF WRITE CHECK FUNCTION
3653          BGNST          ;**START OF TEST**
3654 036402
3655 036402
3656          STARS
3657          ;*****
3658          ;CHECK OF WRITE CHECK LOGIC UNDER FLAG MODE
3659          ;TEST IS DONE WITH ALL BIT PATTERNS
3660          ;WE WILL WRITE CHECK A FULL SECTOR (128 WORDS) FROM
3661          ;MEMORY (BUF). WE CHECK THAT NO ERRORS OCCUR.
3662 036402          STARS
3663          ;*****
3664 036402 004737 021356          JSR          PC,HDHOME      ;HEADS OVER TRACK 0
3665 036406          CRERFG          ;HEADS GO HOME OKAY
3666 036414 104032          EMT          C$EXIT
3667 036416 000252          .WORD     L10077-
3668 036420 012703 002504          MOV          #HDRTAB,R3
3669 036424          BGNSEG          ;**START OF SEGMENT**
3670 036424 104004          EMT          C$BSEG
3671 036426 012700 003052          298$: MOV          #BUF,R0          ;SETUP AND WRITE
3672 036430 012701 000200          MOV          #128,R1        ;128 WORDS
3673 036432 011302          MOV          (R1),R2        ;GET PATTERN
3674 036440 052702 100000          BIS         #BIT15,R2
3675 036444 010220          299$: MOV          R2,(R0)+
3676 036446 005301          DEC         R1
3677 036450 001375          BNE         299$          ;DONE??
3678 036452 012777 003052 143564          MOV          #BUF,@RLBA    ;LOAD BUS ADDRESS
3679 036460 012777 177600 143562          MOV          #-128,@RLMP   ;WORD COUNT
3680 036466 005077 143554          CLR          @RLDA          ;CLEAR DISK ADDRESS
3681 036472 004537 020456          JSR          R5,LDFUNC      ;LOAD THE FUNCTION IN NEXT WORD
3682 036476 000012          WRITE
3683 036504          JSR          R5,WTCRDY      ;WAIT FOR CONTROLLER READY
3684 036504 104010          ESCAPE     SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3685 036506          EMT          C$ESEC
3686 036510 104010          .WORD     10000$-
3687 036514 004537 020214          JSR          R5,CHERR      ;CHECK CNTLR FOR ERRORS
3688 036514          ESCAPE     SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3689 036516 104010          EMT          C$ESEC
3690 036520 000150          .WORD     10000$-
3691 036520          BGNSEG          ;**START OF SEGMENT**
3692 036520          EMT          C$BSEG
3693          ;VERIFY WRITE WITH READ BEFORE WRCHK
3694 036522 005077 143520          CLR          @RLDA          ;LOAD THE FUNCTION IN NEXT WORD
3695 036526 012777 003052          MOV          #BUF,@RLBA    ;WORD COUNT
3696 036534 012777 177600          MOV          #-128,@RLMP   ;BUS ADDRESS
3697 036542 004537 020456          JSR          R5,LDFUNC      ;LOAD THE FUNCTION IN NEXT WORD
3698 036546 000014          READ

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ASSEMBLY ROUTINES      MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-92
CZRLBB-P11      22-NOV-78 15:28      **TEST 42** - EXTENDED CHECK OF WRITE CHECK FUNCTION
                                                    SEQ 0122

3698 036550 004537 021276      JSR      R5,WTCRDY
3699 036554          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 036554 104010      ESCAPE   SEG
3700 036556 000076      EMT     C$ESCAPE
3701 036560 004537 020214      JSR      R5,CHERR          ;CHECK CNTLR FOR ERRORS
(3) 036564 104010      ESCAPE   SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 036566 000066      EMT     C$ESCAPE
(3)          .WORD    10001$-
3703 036570 104004          BGNSEG          ;**START OF SEGMENT**
3704 036570 104004          EMT     C$BSEG
3705          3S:
3706 036572 005077 143450      CLR     @RLDA          ;WORD COUNT
3707 036576 012777 177600      MOV     #-128,@RLBA   ;BUS ADDRESS
3708 036574 012777 003052 143432      JSR     @RLBA          ;LOAD THE FUNCTION IN NEXT WORD
3709 036612 004537 020456      JSR     R5,LDFUNC     ;WRITE CHECK
3710 036616 000002      WRCHK
3711
3712 036620 004537 021276      JSR     R5,WTCRDY      ;WAIT FOR CONTROLLER READY
3713 036624 104010      ESCAPE   SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 036626 000024      EMT     C$ESCAPE
(3)          .WORD    10002$-
3714
3715 036630 004537 020214      JSR     R5,CHERR      ;CHECK CNTLR FOR ERRORS
3716 036634 005737 002124      TST     T,CRC
3717 036640 001404      BEQ     4$
3718
3719
3720 036642          ERRHRD  410,ERR15,EM70
(3) 036642 104463      TRAP   T$ERRCODE
(5) 036644 000632      .WORD  410
(5) 036646 015216      .WORD  ERR15
(5) 036650 014220      .WORD  EM70
3721
3722 036652          4$:
3723
3724
3725 036652          10002$: ENDSEG          ;**END OF SEGMENT**
(3) 036652 104005      EMT     C$ESEG
3726 036654          10001$: ENDSEG          ;**END OF SEGMENT**
(3) 036654 104005      EMT     C$ESEG
3727 036656 005723 002660      IST     (R3)+
3728 036660 020327      CMP     R3,@HDREND
3729 036664 001260      BNE    298$
3730
3731
3732 036666          10000$: ENDSEG          ;**END OF SEGMENT**
(3) 036666 104005      EMT     C$ESEG
3733 036670      ENDTST          ;**END OF TEST**
(3) 036670      L10077:
(3) 036670 104001      EMT     C$ETST
3734          .SBTTL  **TEST 43** - READ WITHOUT HEADER COMPARE FUNCTION

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ASSEMBLY ROUTINES      MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-93
CZRLBB-P11      22-NOV-78 15:28      **TEST 43** - READ WITHOUT HEADER COMPARE FUNCTION
                                                    SEQ 0123

3735
3736 036672      STARS
3737          ;*****
3738          ;TEST THAT READ WITHOUT HEADER VERIFICATION WORKS. THIS FUNCTION SHOULD
3739          ;READ AT THE NEXT SECTOR ENCOUNTERED. SET THE RLDA TO 0
3740          ;AND ISSUE THE SECTION IN FLAG MODE. UPON COMPLETION CHECK
3741          ;FOR ERRORS
3742 036672      STARS
3743          ;*****
3744          BGNST          ;**START OF TEST**
3745
3746 036672 004737 021356      JSR     PC,HDHOME     ;HEADS OVER TRACK 0
3747 036676          CKERFG          ;HEADS GO HOME OKAY
(4) 036704 104032      EMT     C$EXIT
(4) 036706 000052      .WORD  L10100-
3748
3749 036710          BGNSEG          ;**START OF SEGMENT**
(3) 036710 104004      EMT     C$BSEG
3750
3751 036712 012777 177600 143330      MOV     #-128,@RLMP   ;SET UP WORD COUNT
3752 036720 012777 003052 143316      MOV     @BUF,@RLBA   ;SETUP BUS ADDRESS
3753 036726 012777 177777 143312      MOV     #-1,@RLDA   ;HEADER SHOULDNT MATTER
3754 036734 004537 020456      JSR     R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
3755 036740 000016      RDNHD          ;READ DATA WITHOUT HEADER VERIFY
3756 036742 004537 021276      JSR     R5,WTCRDY     ;WAIT FOR IT TO FINISH
3757 036746          ESCAPE   SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 036746 104010      EMT     C$ESCAPE
(3) 036750 000006      .WORD    10000$-
3759 036752 004537 020214      JSR     R5,CHERR      ;CHECK CNTLR FOR ERRORS
3760
3761 036756          10000$: ENDSEG          ;**END OF SEGMENT**
(3) 036756 104005      EMT     C$ESEG
3762 036760      ENDTST          ;**END OF TEST**
(3) 036760 104001      EMT     C$ETST
3763          .SBTTL  **TEST 44** - READ WITHOUT HEADER COMPARE FUNCTION INTERRUPT
3764          BGNST          ;**START OF TEST**
3765
3766
3767
3768          STARS
3769          ;*****
3770          ;TEST THAT READ WITHOUT HEADER VERIFICATION WORKS IN
3771          ;INTERRUPT MODE.
3772 036762      STARS
3773          ;*****
3774 036762 004737 021356      JSR     PC,HDHOME     ;HEADS OVER TRACK 0
3775 036766          CKERFG          ;HEADS GO HOME OKAY
(4) 036774 104032      EMT     C$EXIT
(4) 036776 000114      .WORD  L10101-

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3776 037000          BGNSEG          ;**START OF SEGMENT**
(3) 037000          EMT          C$BSEG
3778 037002 005037 002144          CLR          INTFLG          ;CLEAR INTERRUPT OCCURANCE FLAG
3779 037006 012777 177600          MOV          #128, @RLMP      ;SET UP WORD COUNT FOR ONE SECTOR
3780 037014 012777 003052          MOV          #BUF, @RLDA      ;SETUP BUFFER ADDRESS
3781 037022 012777 143216          MOV          #100, @R1       ;DISK ADDRESS IS A DON'T CARE
3782 037030          SETPRI          #P100
(3) 037030          MOV          @PRI00, R0
(3) 037034 104041          EMT          C$SPRI
3783 037036 004537 020456          JSR          RS, LDFUNC      ;LOAD THE FUNCTION IN NEXT WORD
3784 037042 000116          RDNHDIINTEN  ;INTERRUPT ENABLED
3785 037044 004537 021276          JSR          RS, WTCRDY      ;WAIT FOR INTERRUPT
3786 037050          SETPRI          #PRI07
(3) 037054 104041          MOV          @PRI07, R0
(3) 037056          EMT          C$SPRI
3787 037056 104010          ESCAPE        SECS          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 037056 104010          EMT          C$ESCAPE
(3) 037060 000030          .WORD        10000$-
3788
3789 037062 005737 002144          TST          INTFLG          ;DID IT INTERRUPT
3790 037066 001004          BNE          IS             ;IF INTERRUPT GO TO IS
3791
3792 037070          ERRDF        40, EM40, ERRO  ;NO INTERRUPT
(3) 037072 000050          TRAP         T$ERRCODE
(3) 037074 000050          .WORD        40
(5) 037074 012547          .WORD        EM40
(5) 037076 014244          .WORD        ERRO
3793 037100          1$:          ESCAPE        SECS          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 037100 104010          EMT          C$ESCAPE
(3) 037102 000006          .WORD        10000$-
3794
3795 037104 004537 020214          JSR          RS, CHERR       ;CHECK CNTLR FOR ERRORS
3796
3797 037110          ENDSEG       ;**END OF SEGMENT**
(3) 037110          EMT          C$ESEG
3798 037112          ENDTST      ;**END OF TEST**
(3) 037112 104001          L10101:      EMT          C$SETST
(3) 037112 104001
3799
3800          .SBTTL     **TEST 45** - CHECK RD W/O HDR CMP ACTUALLY READS
3801          BGNST          ;**START OF TEST**
3802 037114
3803 037114
3804
3805          STARS
3806          ;*****
3807          ;CHECK THAT THE READ W/O HDR CMP FUNCTION ACTUALLY READS (INTO MEMORY)
3808          ;WE WILL WRITE A PATTERN INTO MEMORY AND THEN ISSUE
3809          ;A READ TO OVERLAY THAT PATTERN. AFTER THE READ
3810          ;WE CHECK TO SEE IF THE WRITTEN PATTERN HAS CHANGED.
3811          ;IF NOT WE ISSUE IT AGAIN AT THE SAME SECTION AFTER
3812          ;HAVING MODIFIED OUR PATTERN IN MEMORY (SINCE THERE IS
3813          ;ONE CHANCE THAT THE DISK COULD HAVE OUR PATTERN). AFTER
3814          ;THE SECOND READ WE CHECK THE BUFFER AGAIN. IF IT'S
3815          ;NOT CHANGED WE REPORT AN ERROR
    
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3814 037114          STARS
3815          ;*****
3816          ;CHECK THAT THE READ W/O HDR CMP FUNCTION ACTUALLY READS (INTO MEMORY)
3817          ;WE WILL WRITE A PATTERN INTO MEMORY AND THEN ISSUE
3818          ;A READ TO OVERLAY THAT PATTERN. AFTER THE READ
3819          ;WE CHECK TO SEE IF THE WRITTEN PATTERN HAS CHANGED.
3820          ;IF NOT WE ISSUE IT AGAIN AT THE SAME SECTION AFTER
3821          ;HAVING MODIFIED OUR PATTERN IN MEMORY (SINCE THERE IS
3822          ;ONE CHANCE THAT THE DISK COULD HAVE OUR PATTERN). AFTER
3823          ;THE SECOND READ WE CHECK THE BUFFER AGAIN. IF IT'S
3824          ;NOT CHANGED WE REPORT AN ERROR
3825
3826          ;*****
3827          ;CHECK THAT THE READ W/O HDR CMP FUNCTION ACTUALLY READS (INTO MEMORY)
3828          ;WE WILL WRITE A PATTERN INTO MEMORY AND THEN ISSUE
3829          ;A READ TO OVERLAY THAT PATTERN. AFTER THE READ
3830          ;WE CHECK TO SEE IF THE WRITTEN PATTERN HAS CHANGED.
3831          ;IF NOT WE ISSUE IT AGAIN AT THE SAME SECTION AFTER
3832          ;HAVING MODIFIED OUR PATTERN IN MEMORY (SINCE THERE IS
3833          ;ONE CHANCE THAT THE DISK COULD HAVE OUR PATTERN). AFTER
3834          ;THE SECOND READ WE CHECK THE BUFFER AGAIN. IF IT'S
3835          ;NOT CHANGED WE REPORT AN ERROR
3836
3837          ;*****
3838          ;CHECK THAT THE READ W/O HDR CMP FUNCTION ACTUALLY READS (INTO MEMORY)
3839          ;WE WILL WRITE A PATTERN INTO MEMORY AND THEN ISSUE
3840          ;A READ TO OVERLAY THAT PATTERN. AFTER THE READ
3841          ;WE CHECK TO SEE IF THE WRITTEN PATTERN HAS CHANGED.
3842          ;IF NOT WE ISSUE IT AGAIN AT THE SAME SECTION AFTER
3843          ;HAVING MODIFIED OUR PATTERN IN MEMORY (SINCE THERE IS
3844          ;ONE CHANCE THAT THE DISK COULD HAVE OUR PATTERN). AFTER
3845          ;THE SECOND READ WE CHECK THE BUFFER AGAIN. IF IT'S
3846          ;NOT CHANGED WE REPORT AN ERROR
3847
3848          ;*****
3849          ;CHECK THAT THE READ W/O HDR CMP FUNCTION ACTUALLY READS (INTO MEMORY)
3850          ;WE WILL WRITE A PATTERN INTO MEMORY AND THEN ISSUE
3851          ;A READ TO OVERLAY THAT PATTERN. AFTER THE READ
3852          ;WE CHECK TO SEE IF THE WRITTEN PATTERN HAS CHANGED.
3853          ;IF NOT WE ISSUE IT AGAIN AT THE SAME SECTION AFTER
3854          ;HAVING MODIFIED OUR PATTERN IN MEMORY (SINCE THERE IS
3855          ;ONE CHANCE THAT THE DISK COULD HAVE OUR PATTERN). AFTER
3856          ;THE SECOND READ WE CHECK THE BUFFER AGAIN. IF IT'S
3857          ;NOT CHANGED WE REPORT AN ERROR
    
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3858
3859 037306          ENDSEG                ;**END OF SEGMENT**
3860 (3) 037306          10000$: EMT C$ESEG                ;**END OF TEST**
3861 (3) 037306          ENDTST L10102:                ;**END OF TEST**
3862 (3) 037310          104005
3863 (3) 037310          104001
3864 (3)
3865 (3)
3866 (3)
3867 (2)
3868 (2)
3869 (2)
3870 037312          .SBTTL **TEST 46** - CHECK RLBA INCREMENT WITH RD W/O HDR CMP
3871 (2)
3872 (2)
3873 (2)
3874 (4) 037324          104032          BGNTST                ;**START OF TEST**
3875 (4) 037326          000120
3876 037330          BGNSEG                ;**START OF SEGMENT**
3877 (3) 037330          104004          EMT C$BSEG
3878 037332          012777          000050          142706          MOV #40, @RLDA ;SET UP BUS ADDRESS
3879 037340          012777          003052          142676          MOV #BUF, @RLBA ;WORD COUNT
3880 037346          012777          177600          142674          INC #128, @RLMP ;FORM EXPECTED BUS ADDRESS
3881 037354          012737          003052          002166          MOV #BUF, @DDAT ;AFTER READ
3882 037362          062737          000400          ADD #256, @DDAT
3883
3884 037370          004537          020456          JSR R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3885 037374          000016          RDNHD ;READ W/O HDR CMP
3886 037376          004537          021276          JSR R5, WTCRDY ;WAIT FOR CONTROLLER READY
3887 (3) 037402          104010          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3888 (3) 037404          000040          EMT C$ESCAPE
3889 (3) 037406          004537          020214          EMT .WORD 10000$-
3890 (3) 037412          104010          JSR R5, CHERR ;CHECK CNTLR FOR ERRORS
3891 (3) 037414          000030          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3892 (3) 037416          013737          002230          002170          EMT .WORD 10000$-
3893 (3) 037424          023737          002170          002166          MOV E, @BDDAT ;READ "RLBA" FOR PRESENT ADDRESS
3894 (3) 037432          001404          CMP BDDAT, @DDAT ;DID "BA" INCREMENT PROPERLY?
3895 (3) 037434          014462          BEQ 15 ;YES, CONTINUE
3896 (5) 037436          000025          ERRDF 21, EM53, ERR4
3897 (5) 037440          013310          TRAP T$ERRCODE
3898 (5) 037442          014410          .WORD 21
3899 (5) 037442          014410          .WORD EM53
3900 (5) 037442          014410          .WORD ERR4
  
```

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3897 037444          1$:
3898 (3) 037444          104005          10000$: ENDSEG                ;**END OF SEGMENT**
3899 (3) 037444          104005          ENDTST L10103:                ;**END OF TEST**
3900 (3) 037446          104001          EMT C$ESEG
3901 (3) 037446          104001          EMT C$SETST
3902 (3)
3903 (3)
3904 (3)
3905 (3)
3906 (3)
3907 (3)
3908 (3)
3909 (3)
3910 037450          .SBTTL **TEST 47** - CHECK RLDA DOES INCREMENT WITH RD W/O HDR CMP
3911 (3)
3912 (3)
3913 (3)
3914 (3)
3915 037450          BGNTST                ;**START OF TEST**
3916 (3)
3917 (3)
3918 (3)
3919 (3)
3920 (3)
3921 (3)
3922 (3)
3923 (3)
3924 037470          012737          000050          002166          MOV #40, @DDAT ;DA TO NONSENSE
3925 037476          013777          002166          142542          MOV @DDAT, @RLDA ;SETUP DISK ADDRESS
3926 037504          005237          002166          INC @DDAT
3927 037510          012777          177600          142532          MOV #128, @RLMP ;WORD COUNT
3928 037516          012777          003052          142520          MOV #BUF, @RLBA ;SETUP BUS ADDRESS
3929
3930 037524          004537          020456          JSR R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3931 037530          000016          RDNHD ;READ WITHOUT HEADER COMPARE
3932 037532          004537          021276          JSR R5, WTCRDY ;WAIT FOR CONTROLLER READY
3933 (3) 037536          104010          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3934 (3) 037536          104010          EMT C$ESCAPE
3935 (3) 037540          000040          EMT .WORD 10000$-
3936 (3) 037542          004537          020214          JSR R5, CHERR ;CHECK CNTLR FOR ERRORS
3937 (3) 037546          000016          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3938 (3) 037546          104010          EMT C$ESCAPE
3939 (3) 037550          000030          EMT .WORD 10000$-
3940 (3) 037552          013737          002232          002170          MOV E, @BDDAT ;READ DISK ADDRESS
3941 (3) 037560          023737          002166          002170          CMP @DDAT, @DDAT ;DID SECTOR INCREMENT PROPERLY
  
```

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3940 037566 001404          BEQ      1$          ;YES, BRANCH NO, REPORT ERROR
3941
3942 037570          ERRDF   22,EM54,ERR4
3943 (3) 037570          TRAP   T$ERRCODE
3944 (5) 037572          .WORD   22
3945 (5) 037574          .WORD   EM54
3946 (5) 037576          .WORD   ERR4
3947
3948          1$:
3949
3950          037600          ENDSEG          ;%%END OF SEGMENT%%
3951 (3) 037600          10000$:      EMT      C$ESEG
3952 (3) 037602          ENDTST     L10104:      ;**END OF TEST**
3953 (3) 037602          EMT      C$ESET
3954
3955          037604          BGNMOD     HRDPRM
3956 (3) 037604          BGNHRD
3957 (3) 037604          .WORD   L10105-L$HARD/2
3958
3959          037606          GPRML   CNTYPE,CNT,1,YES
3960 (4) 037606          .WORD   T$CODE
3961 (4) 037610          .WORD   CNTYPE
3962 (4) 037612          .WORD   1
3963 (4) 037614          .WORD   CSRMSG,CSR,0,160000,177776,YES
3964 (4) 037616          .WORD   T$CODE
3965 (4) 037620          .WORD   CSRMSG
3966 (4) 037622          .WORD   T$LOLIM
3967 (4) 037624          .WORD   GPRMA   VECMSG,VECT,0,0,776,YES
3968 (4) 037626          .WORD   T$CODE
3969 (4) 037630          .WORD   VECMSG
3970 (4) 037632          .WORD   T$LOLIM
3971 (4) 037634          .WORD   T$SHILIM
3972 (4) 037636          GPRMD   BRMSG,PRIOR,0,340,0,7,YES
3973 (4) 037640          .WORD   T$CODE
3974 (4) 037642          .WORD   BRMSG
3975 (4) 037644          .WORD   340
3976 (4) 037646          .WORD   T$LOLIM
3977 (4) 037650          .WORD   T$SHILIM
3978 (4) 037652          GPRMD   DRMSG,DRBT,0,03400,0,7,YES
3979 (4) 037654          .WORD   T$CODE
3980 (4) 037656          .WORD   DRMSG
3981 (4) 037658          .WORD   03400
3982 (4) 037660          .WORD   T$LOLIM
3983 (4) 037662          .WORD   T$SHILIM
3984
3985          037660          ENDRHD
3986 (2)
3987 (3) 037660          L10105:     .EVEN
3988
3989
3990
    
```

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3964 037660 046122 030461 000 CNTYPE: .ASCIZ /RL11/
3965 037665 102 051525 040440 .ASCIZ /BUS ADDRESS/
3966 037672 042104 042522 051523
3967 037700 000
3968 037701 102 020122 042514 .ASCIZ /BR LEVEL/
3969 037712 042526 052103 VECMSG: .ASCIZ /VECTOR/
3970 037720 000
3971 037721 104 044522 042526 DRMSG: .ASCIZ /DRIVE/
3972 037726 000 .EVEN
3973 037730
3974
3975          037730          ENDMOD
3976
3977          037730          BGNMOD     SFTPRM
3978 (3) 037730          BGNSFT
3979 (3) 037730          .WORD   L10106-L$SOFT/2
3980
3981          037732          GPRML   DMSG,DLT,1,YES
3982 (4) 037732          .WORD   T$CODE
3983 (4) 037734          .WORD   DMSG
3984 (4) 037736          .WORD   1
3985 (4) 037740          .WORD   XFERF   1$
3986 (5) 037742          .WORD   T$CODE
3987 (4) 037744          GPRMD   EMMSG,ELT,D,177777,0,177777,YES
3988 (4) 037746          .WORD   T$CODE
3989 (4) 037748          .WORD   EMMSG
3990 (4) 037750          .WORD   177777
3991 (4) 037752          .WORD   T$LOLIM
3992 (4) 037754          .WORD   T$SHILIM
3993 (4) 037756          1$:      GPRML   SMMSG,SIZE,1,YES
3994 (4) 037760          .WORD   T$CODE
3995 (4) 037762          .WORD   SMMSG
3996 (4) 037764          .WORD   1
3997 (4) 037766          GPRML   CMMSG,DMPCK,1,YES
3998 (4) 037768          .WORD   T$CODE
3999 (4) 037770          .WORD   CMMSG
3990 (5) 037772          .WORD   XFERF   2$
3991 (4) 037774          .WORD   T$CODE
3992 (4) 037776          GPRMD   LMSG,DLMT,D,177777,1,128.,YES
3993 (4) 037778          .WORD   T$CODE
3994 (4) 037780          .WORD   LMSG
3995 (4) 037782          .WORD   177777
3996 (4) 040000          .WORD   T$LOLIM
3997 (4) 040002          .WORD   T$SHILIM
3998 (4) 040004
3999
3990          040004          ENDSFT
3991 (2)
3992 (3) 040004          L10106:     .EVEN
3993
3994
3995          040004          051104 050117 047440 DMSG: .ASCIZ /DROP ON ERROR LIMIT/
3996
3997
3998
3999
    
```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-100
 CZRLBB.P11 22-NOV-78 15:28 **TEST 47** - CHECK RLDA DOES INCREMENT WITH RD W/O HDR CMP

SEQ 0130

```

040012 020116 051105 047522
040020 020122 044514 044515
040026 000124
3991 040030 052501 047524 044523 SMSG: .ASCIZ /AUTOSIZE/
040036 042532 000
3992 040041 103 046517 040520 CMSG: .ASCIZ /COMPARE DATA ON DCK/
040046 042522 042040 052101
040054 020101 047117 042040
040062 045503 000
3993 040065 043 047440 020106 LMSG: .ASCIZ /# OF WORDS IN ERROR REPORTED/
040072 047527 042122 020123
040100 047111 042440 051122
040106 051117 051040 050105
040114 051117 047524 000104
3994 040122 051105 047522 020122 EMSG: .ASCIZ /ERROR LIMIT/
040130 044514 044515 000124
3995
3996 040136 ENDMOD
3997
3998
3999
4000 040514 .=40514
4001
4002 ;AREA RESERVED AS PATCH AREA FOR DIAGNOSTICS.
4003 ;.=40514 WAS SELECTED AS "LASTAD" TO PROVIDE APT TO LSI-11 COMPATIBILITY.
4004 ;BIT 7 OF "LASTAD" MUST BE CLEARED TO ACHIEVE A VALID MAILBOX ADDRESS
4005 ;WHEN RUNNING ON THE LSI-11 UNDER APT.
4006
4007 040514 LASTAD
(3) 040514 L$LAST: .EVEN
4008
4009
4010
4011
4012
4013

```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 2
 CZRLBB.SUP 23-OCT-78 09:52 DIAGNOSTIC SUPERVISOR -- LOW CORE SET UP

SEQ 0131

```

4015 .SBTTL DIAGNOSTIC SUPERVISOR -- LOW CORE SET UP
14886 071310 000000 .WORD 0 ;SPACE FOR USER POOL POINTER
14887 071312 000000 .WORD 0 ;SIZE
14888 071314 000000 .WORD 0 ;CHECKSUM (NOT CURRENTLY USED)
14889 071316 000000 .WORD 0 ;SIZE OF H.W. PTAB. ALLOCATION
14890 071322 END.SUPV.=+2
14891 000200 .END 200

```


Table with 4 columns: Address (e.g., L10000), Symbol (e.g., NUM.LA), Value (e.g., 054602), and Attribute (e.g., G). Lists various assembly symbols and their corresponding addresses and values.

Table with 4 columns: Address (e.g., TSSUBN=), Symbol (e.g., T11), Value (e.g., 0241110), and Attribute (e.g., G). Lists assembly symbols and their corresponding addresses and values.

. ABS. 071320 000

ERRORS DETECTED: 0

DSKZ: CZRLBB, DSKZ: CZRLBB=CZRLBB/ML, CZRLBB.P11, CZRLBB.SUP
RUN-TIME: 66 62 1 SECONDS
RUN-TIME RATIO: 323/130=2.4
CORE USED: 16K (31 PAGES)