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IDENTIFICATION

PRODUCT ID: AC T716A-MC
PRODUCT TITLE: CZTSAAO TSU05 DIAG PART 1
DEPARTMENT: COMPUTER SPECIAL SYSTEMS/PPG
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1.0 GENERAL INFORMATION

1.1 PROGRAM ABSTRACT

THIS IS A PDP-11 RESIDENT DIAGNOSTIC WHICH CHECKS THE FUNCTIONALITY OF A TSU05 MAGTAPE SUBSYSTEM WHILE CONNECTED TO A PDP-11//23 SYSTEM (UNIBUS). THE PROGRAM PROVIDES ERROR MESSAGES WHICH IDENTIFY FAILING FUNCTIONS THAT AID IN THE REPAIR OF THE DEVICE. THIS DIAGNOSTIC CONSIST OF ELEVEN TEST WHICH ARE EXECUTED IN SEQUENCE.

THIS DIAGNOSTIC HAS BEEN WRITTEN FOR USE WITH THE DIAGNOSTIC RUNTIME SERVICES SOFTWARE (SUPERVISOR). THESE SERVICES PROVIDE THE INTERFACE TO THE OPERATOR AND TO THE SOFTWARE ENVIRONMENT. THIS PROGRAM CAN BE USED WITH XXDP+, ACT, APT, SLIDE AND PAPER TAPE. FOR A COMPLETE DESCRIPTION OF THE RUNTIME SERVICES, REFER TO THE XXDP+ USER'S MANUAL. THERE IS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES IN SECTION 2 OF THIS DOCUMENT.

1.2 SYSTEM REQUIREMENTS

PDP-11 PROCESSOR AND MEMORY
CAUTION:DIAGNOSTIC REQUIRES 32K WORDS OF MEMORY
(28K USEABLE I.E. 4K FOR I/O PAGE)
TSU05 MAGTAPE SUBSYSTEM (DRIVE AND CONTROLLER)
CONSOLE TERMINAL
PDP-11 DIAGNOSTIC SUPERVISOR (HSAAS.SYS VERSION 34 OR LATER)
PDP-11 DIAGNOSTIC LOADER/MONITOR (XXDP+)

1.3 RELATED DOCUMENTS AND STANDARDS

DIGITAL EQUIPMENT CORPORATION DOCUMENTS:

1. CHQUS XXDP+ USERS GUIDE; DOCUMENT NUMBER AC-F348E-MC
DATE: 14 JULY 1980.
2. TSU05 TRANSPORT SUBSYSTEM USER'S GUIDE; DOCUMENT NUMBER EK-TSU05-UG-001
DATE: AUGUST 1983
3. TSU05 TRANSPORT SUBSYSTEM TECHNICAL MANUAL; DOCUMENT NUMBER EK-TSU05-TM-001
DATE: AUGUST 1983
4. TSU05 TRANSPORT SUBSYSTEM INSTALLATION MANUAL; DOCUMENT NUMBER EK-TSU05 IN-001
DATE: AUGUST 1983

1.4 DIAGNOSTIC HIERARCHY PREREQUISITES

FUNCTIONAL PDP-11 CENTRAL PROCESSOR AND MEMORY
FUNCTIONAL CONSOLE TERMINAL
FUNCTIONAL STANDALONE DIAGNOSTIC SUPERVISOR
FUNCTIONAL DIAGNOSTIC LOADER/MONITOR (XXDP+)

1.5 ASSUMPTIONS

ALL HARDWARE EXCEPT THE HARDWARE UNDER TEST IS ASSUMED TO WORK PROPERLY OR FALSE ERRORS CAN BE REPORTED.
THE TAPE BEING USED ON THE TS05 TRANSPORT IS A KNOWN GOOD REEL OF TAPE.

2.0 OPERATING INSTRUCTIONS

THIS SECTION CONTAINS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES. FOR DETAILED INFORMATION, REFER TO THE XXDP+ USER'S MANUAL (CHQUS).

2.1 COMMANDS

THERE ARE ELEVEN LEGAL COMMANDS FOR THE DIAGNOSTIC RUNTIME SERVICES (SUPERVISOR). THIS SECTION LISTS THE COMMANDS AND GIVES A VERY BRIEF DESCRIPTION OF THEM. THE XXDP+ USER'S MANUAL HAS MORE DETAILS.

COMMAND	EFFECT
START	START THE DIAGNOSTIC FROM AN INITIAL STATE
RESTART	START THE DIAGNOSTIC WITHOUT INITIALIZING
CONTINUE	CONTINUE AT TEST THAT WAS INTERRUPTED (AFTER +C)
PROCEED	CONTINUE FROM AN ERROR HALT
EXIT	RETURN TO XXDP+ MONITOR (XXDP+ OPERATION ONLY!)
ADD	ACTIVATE A UNIT FOR TESTING (ALL UNITS ARE CONSIDERED TO BE ACTIVE AT START TIME)
DROP	DEACTIVATE A UNIT
PRINT	PRINT STATISTICAL INFORMATION (IF IMPLEMENTED BY THE DIAGNOSTIC - SECTION 4.0)
DISPLAY	TYPE A LIST OF ALL DEVICE INFORMATION
FLAGS	TYPE THE STATE OF ALL FLAGS (SEE SECTION 2.3)
ZFLAGS	CLEAR ALL FLAGS (SEE SECTION 2.3)

A COMMAND CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. SO YOU MAY, FOR EXAMPLE, TYPE "STA" INSTEAD OF "START".

2.1.1 OPERATOR COMMANDS

THE TSU05 DIAGNOSTIC IS A PDP-11 DIAGNOSTIC SUPERVISOR COMPATIBLE PROGRAM. ALL LOADING AND RUNTIME INSTRUCTIONS CAN BE REFERENCED IN THE CHQUS XXDP+ USERS GUIDE, DOCUMENT NUMBER AC-F348E-MC. THE USER ENTRY IS IN QUOTES.

BOOT THE DIAGNOSTIC MEDIA

```
.R VTSA??
DIAG. RUN-TIME SERVICES REV D. APR 79
CZTSA-A-0
****TSU05 LOGIC DIAGNOSTIC****
UNIT IS TSU05
```

>DR

2.2 SWITCHES

THERE ARE SEVERAL SWITCHES WHICH ARE USED TO MODIFY SUPERVISOR OPERATION. THESE SWITCHES ARE APPENDED TO THE LEGAL COMMANDS. ALL OF THE LEGAL SWITCHES ARE TABULATED BELOW WITH A BRIEF DESCRIPTION OF EACH. IN THE DESCRIPTIONS BELOW, A DECIMAL NUMBER IS DESIGNATED BY "DDDDD".

SWITCH	EFFECT
/TESTS:LIST	EXECUTE ONLY THOSE TESTS SPECIFIED IN THE LIST. LIST IS A STRING OF TEST NUMBERS, FOR EXAMPLE - /TESTS:1:5:7-10. THIS LIST WILL CAUSE TESTS 1,5,7,8,9,10 TO BE RUN. ALL OTHER TESTS WILL NOT BE RUN.
/PASS:DDDDD	EXECUTE DDDDD PASSES (DDDDD = 1 TO 64000)
/FLAGS:FLGS	SET SPECIFIED FLAGS. FLAGS ARE DESCRIBED IN SECTION 2.3.
/EOP:DDDDD	REPORT END OF PASS MESSAGE AFTER EVERY DDDDD PASSES ONLY. (DDDDD = 1 TO 64000)
/UNITS:LIST	TEST/ADD/DROP ONLY THOSE UNITS SPECIFIED IN THE LIST. LIST EXAMPLE - /UNITS:0:5:10-12 USE UNITS 0,5,10,11,12 (UNIT NUMBERS = 0-63)

EXAMPLE OF SWITCH USAGE:

START/TESTS:1-5/PASS:1000/EOP:100

THE EFFECT OF THIS COMMAND WILL BE: 1) TESTS 1 THROUGH 5 WILL BE EXECUTED, 2) ALL UNITS WILL TESTED 1000 TIMES AND 3) THE END OF PASS MESSAGES WILL BE PRINTED AFTER EACH 100 PASSES ONLY. A SWITCH CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. YOU MAY, FOR EXAMPLE, TYPE "/TES:1-5" INSTEAD OF "/TESTS:1-5".

BELOW IS A TABLE THAT SPECIFIES WHICH SWITCHES CAN BE USED BY EACH COMMAND.

	TESTS	PASS	FLAGS	EOP	UNITS
START	X	X	X	X	X
RESTART	X	X	X	X	X
CONTINUE		X	X	X	
PROCEED			X		
DROP					X
ADD					X
PRINT					
DISPLAY					X
FLAGS					
ZFLAGS					
EXIT					

2.3 FLAGS

FLAGS ARE USED TO SET UP CERTAIN OPERATIONAL PARAMETERS SUCH AS LOOPING ON ERROR. ALL FLAGS ARE CLEARED AT S*ARTUP AND REMAIN

CLEARED UNTIL EXPLICITLY SET USING THE FLAGS SWITCH. FLAGS ARE ALSO CLEARED AFTER A START COMMAND UNLESS SET USING THE FLAG SWITCH. THE ZFLAGS COMMAND MAY ALSO BE USED TO CLEAR ALL FLAGS. WITH THE EXCEPTION OF THE START AND ZFLAGS COMMANDS, NO COMMANDS AFFECT THE STATE OF THE FLAGS; THEY REMAIN SET OR CLEARED AS SPECIFIED BY THE LAST FLAG SWITCH.

FLAG	EFFECT
HOE	HALT ON ERROR - CONTROL IS RETURNED TO RUNTIME SERVICES COMMAND MODE
LOE	LOOP ON ERROR
IER [▲]	INHIBIT ALL ERROR REPORTS
IBR [▲]	INHIBIT ALL ERROR REPORTS EXCEPT FIRST LEVEL (FIRST LEVEL CONTAINS ERROR TYPE, NUMBER, PC, TEST AND UNIT)
IXE [▲]	INHIBIT EXTENDED ERROR REPORTS (THOSE CALLED BY PRINTX MACRO'S)
PRI	DIRECT MESSAGES TO LINE PRINTER
PNT	PRINT TEST NUMBER AS TEST EXECUTES
BOE	"BELL" ON ERROR
UAM	UNATTENDED MODE (NO MANUAL INTERVENTION)
ISR	INHIBIT STATISTICAL REPORTS (DOES NOT APPLY TO DIAGNOSTICS WHICH DO NOT SUPPORT STATISTICAL REPORTING)
IDR	INHIBIT PROGRAM DROPPING OF UNITS
ADR	EXECUTE AUTODROP CODE
LOT	LOOP ON TEST

▲ERROR MESSAGES ARE DESCRIBED IN SECTION 3.1

SEE THE XXDP[▲] USER'S MANUAL FOR MORE DETAILS ON FLAGS. YOU MAY SPECIFY MORE THAN ONE FLAG WITH THE FLAG SWITCH. FOR EXAMPLE, TO CAUSE THE PROGRAM TO LOOP ON ERROR, INHIBIT ERROR REPORTS AND TYPE A "BELL" ON ERROR, YOU MAY USE THE FOLLOWING STRING:

```
/FLAGS:LOE:IER:BOE
```

2.4 HARDWARE QUESTIONS

WHEN A DIAGNOSTIC IS STARTED, THE RUNTIME SERVICES WILL PROMPT THE USER FOR HARDWARE INFORMATION BY TYPING "CHANGE HW (L) ?" YOU MUST ANSWER "Y" AFTER A START COMMAND UNLESS THE HARDWARE INFORMATION HAS BEEN "PRELOADED" USING THE SETUP UTILITY (SEE CHAPTER 14 OF THE XXDP[▲] USER'S MANUAL). WHEN YOU ANSWER THIS QUESTION WITH A "Y", THE RUNTIME SERVICES WILL ASK FOR THE NUMBER OF UNITS (IN DECIMAL).

AFTER INITIAL STARTING OF THE PROGRAM (START COMMAND TO THE DIAGNOSTIC SUPERVISOR), THE PROGRAM WILL ISSUE THE "CHANGE HW?" QUESTION TO ASK IF THE HARDWARE PARAMETERS ARE TO BE CHANGED (BY THE OPERATOR).

ON A "N" (NO) RESPONSE TO THE "CHANGE HW?" QUESTION, THE DIAGNOSTIC WILL

RUN USING THE DEFAULT VALUES FOR ALL QUESTIONS. THE DEFAULT ADDRESS AND VECTOR ARE:

TSBA/TSDB = 172520, VECTOR = 224

ON A "Y" (YES) RESPONSE TO THE QUESTION, THE FOLLOWING QUESTIONS WILL THEN BE ASKED TO ALLOW THE OPERATOR TO SELECT THE UNITS TO BE TESTED. A VALUE, IF PRESENT, LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN IF ONLY A CARRIAGE RETURN IS TYPED AS A RESPONSE. A "(D)" IN A QUESTION INDICATES THAT A DECIMAL NUMBER IS REQUIRED AS A RESPONSE. AN "(O)" INDICATES AN OCTAL NUMBER IS BEING SOLICITED. AN "(L)" INDICATES THAT A LOGICAL RESPONSE IS TO BE MADE: "Y" FOR YES, "N" FOR NO.

UNITS (D) ? <ENTER THE NUMBER OF M7455 CONTROLLERS
PRESENT TO BE TESTED>

UNIT 0

DEVICE ADDRESS (O) 172520 ? <ENTER THE ADDRESS OF THE
TSBA/TSDB REGISTER>

VECTOR (O) 224 ? <ENTER ADDRESS OF INTERRUPT
VECTOR>

THE ADDRESS AND VECTOR QUESTIONS WILL BE ASKED FOR EACH OF THE NUMBER OF UNITS (CONTROLLERS) SPECIFIED IN THE "# UNITS?" QUESTION. LOGICAL UNIT NUMBERS ARE ASSIGNED IN ORDER, BEGINNING AT 0. UP TO FOUR UNITS CAN BE SELECTED FOR TESTING AS FOLLOWS:
UP TO 4 TSU05 CONTROLLERS PER PDP-11 AND UP TO 2 DRIVES PER CONTROLLER

2.5 SOFTWARE QUESTIONS

AFTER YOU HAVE ANSWERED THE HARDWARE QUESTIONS OR AFTER A RESTART OR CONTINUE COMMAND, THE RUNTIME SERVICES WILL ASK FOR SOFTWARE PARAMETERS. THESE PARAMETERS WILL GOVERN SOME DIAGNOSTIC SPECIFIC OPERATION MODES. YOU WILL BE PROMPTED BY "CHANGE SW (L) ?" IF YOU WISH TO CHANGE ANY PARAMETERS, ANSWER BY TYPING "Y". THE SOFTWARE QUESTIONS AND THE DEFAULT VALUES ARE DESCRIBED IN THE NEXT PARAGRAPH(S).

THE FOLLOWING QUESTIONS ARE ASKED ON A START, RESTART, OR CONTINUE. THEY ALLOW FLEXIBILITY IN THE WAY THE PROGRAM BEHAVES.

CHANGE SW (L) ? <TYPE Y TO CAUSE THE FOLLOWING
QUESTIONS TO BE ASKED>

INHIBIT ITERATIONS (L) N ? <TYPE "Y" TO PREVENT MULTIPLE
ITERATIONS OF CERTAIN TESTS.
THIS CAUSES EACH TEST PASS TO
RUN AS QUICKLY AS POSSIBLE.
ONLY QUICK-RUNNING LOGIC
TESTS USE MULTIPLE
ITERATIONS.>

2.6 EXTENDED P-TABLE DIALOGUE

WHEN YOU ANSWER THE HARDWARE QUESTIONS, YOU ARE BUILDING ENTRIES IN A TABLE THAT DESCRIBES THE DEVICES UNDER TEST. THE SIMPLEST WAY TO BUILD THIS TABLE IS TO ANSWER ALL QUESTIONS FOR EACH UNIT TO BE TESTED. IF YOU HAVE A MULTIPLEXED DEVICE SUCH AS A MASS STORAGE CONTROLLER WITH SEVERAL DRIVES OR A COMMUNICATION DEVICE WITH SEVERAL LINES, THIS BECOMES TEDIOUS SINCE MOST OF THE ANSWERS ARE REPETITIOUS.

TO ILLUSTRATE A MORE EFFICIENT METHOD, SUPPOSE YOU ARE TESTING A DEVICE, THE XY11. SUPPOSE THIS DEVICE CONSISTS OF A CONTROL MODULE WITH EIGHT UNITS (SUB-DEVICES) ATTACHED TO IT. THESE UNITS ARE DESCRIBED BY THE OCTAL NUMBERS 0 THROUGH 7. THERE IS ONE HARDWARE PARAMETER THAT CAN VARY AMONG UNITS CALLED THE Q-FACTOR. THIS Q-FACTOR MAY BE 0 OR 1. BELOW IS A SIMPLE WAY TO BUILD A TABLE FOR ONE XY11 WITH EIGHT UNITS.

♦ UNITS (0) ? 8<CR>

UNIT 1

CSR ADDRESS (0) ? 160000<CR>

SUB-DEVICE # (0) ? 0<CR>

Q-FACTOR (0) 0 ? 1<CR>

UNIT 2

CSR ADDRESS (0) ? 160000<CR>

SUB-DEVICE # (0) ? 1<CR>

Q-FACTOR (0) 1 ? 0<CR>

UNIT 3

CSR ADDRESS (0) ? 160000<CR>

SUB-DEVICE # (0) ? 2<CR>

Q-FACTOR (0) 0 ? <CR>

UNIT 4

CSR ADDRESS (0) ? 160000<CR>

SUB-DEVICE # (0) ? 3<CR>

Q-FACTOR (0) 0 ? <CR>

UNIT 5

CSR ADDRESS (0) ? 160000<CR>

SUB-DEVICE # (0) ? 4<CR>

Q-FACTOR (0) 0 ? <CR>

UNIT 6

CSR ADDRESS (0) ? 160000<CR>

SUB-DEVICE # (0) ? 5<CR>

Q-FACTOR (0) 0 ? <CR>

UNIT 7

CSR ADDRESS (0) ? 160000<CR>

SUB-DEVICE # (0) ? 6<CR>

Q-FACTOR (0) 0 ? 1<CR>

UNIT 8

CSR ADDRESS (0) 160000<CR>

SUB-DEVICE # (0) ? 7<CR>

Q-FACTOR (0) 1 ? <CR>

NOTICE THAT THE DEFAULT VALUE FOR THE Q-FACTOR CHANGES WHEN A NON-DEFAULT RESPONSE IS GIVEN. BE CAREFUL WHEN SPECIFYING MULTIPLE UNITS!

AS YOU CAN SEE FROM THE ABOVE EXAMPLE, THE HARDWARE PARAMETERS DO NOT VARY SIGNIFICANTLY FROM UNIT TO UNIT. THE PROCEDURE SHOWN IS NOT VERY EFFICIENT.

THE RUNTIME SERVICES CAN TAKE MULTIPLE UNIT SPECIFICATIONS HOWEVER. LET'S BUILD THE SAME TABLE USING THE MULTIPLE SPECIFICATION FEATURE.

```
# UNITS (D) ? 8<CR>
```

```
UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0,1<CR>
Q-FACTOR (0) 0 ? 1,0<CR>
```

```
UNIT 3
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 2-5<CR>
Q-FACTOR (0) 0 ? 0<CR>
```

```
UNIT 7
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 6,7<CR>
Q-FACTOR (0) 0 ? 1<CR>
```

AS YOU CAN SEE IN THE ABOVE DIALOGUE, THE RUNTIME SERVICES WILL BUILD AS MANY ENTRIES AS IT CAN WITH THE INFORMATION GIVEN IN ANY ONE PASS THROUGH THE QUESTIONS. IN THE FIRST PASS, TWO ENTRIES ARE BUILT SINCE TWO SUB-DEVICES AND Q-FACTORS WERE SPECIFIED. THE SERVICES ASSUME THAT THE CSR ADDRESS IS 160000 FOR BOTH SINCE IT WAS SPECIFIED ONLY ONCE. IN THE SECOND PASS, FOUR ENTRIES WERE BUILT. THIS IS BECAUSE FOUR SUB-DEVICES WERE SPECIFIED. THE "-" CONSTRUCT TELLS THE RUNTIME SERVICES TO INCREMENT THE DATA FROM THE FIRST NUMBER TO THE SECOND. IN THIS CASE, SUB-DEVICES 2, 3, 4 AND 5 WERE SPECIFIED. (IF THE SUB-DEVICE WERE SPECIFIED BY ADDRESSES, THE INCREMENT WOULD BE BY 2 SINCE ADDRESSES MUST BE ON AN EVEN BOUNDARY.) THE CSR ADDRESSES AND Q-FACTORS FOR THE FOUR ENTRIES ARE ASSUMED TO BE 160000 AND 0 RESPECTIVELY SINCE THEY WERE ONLY SPECIFIED ONCE. THE LAST TWO UNITS ARE SPECIFIED IN THE THIRD PASS.

THE WHOLE PROCESS COULD HAVE BEEN ACCOMPLISHED IN ONE PASS AS SHOWN BELOW.

```
# UNITS (D) ? 8<CR>
```

```
UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0-7<CR>
Q-FACTOR (0) 0 ? 0,1,0,...,1,1<CR>
```

AS YOU CAN SEE FROM THIS EXAMPLE, NULL REPLIES (COMMAS ENCLOSING

A NULL FIELD) TELL THE RUNTIME SERVICES TO REPEAT THE LAST REPLY.

2.7 QUICK START-UP PROCEDURE (XXDP*)

TO START-UP THIS PROGRAM:

1. BOOT XXDP*
2. TYPE "R NAME", WHERE NAME IS THE NAME OF THE BIN OR BIC FILE FOR THIS PROGRAM
3. TYPE "START"
4. ANSWER THE "CHANGE HW" QUESTION WITH "Y"
5. ANSWER ALL THE HARDWARE QUESTIONS
6. ANSWER THE "CHANGE SW" QUESTION WITH "N"

WHEN YOU FOLLOW THIS PROCEDURE YOU WILL BE USING ONLY THE DEFAULTS FOR FLAGS AND SOFTWARE PARAMETERS. THESE DEFAULTS ARE DESCRIBED IN SECTIONS 2.3 AND 2.5.

3.0 ERROR INFORMATION

3.1 TYPES OF ERROR MESSAGES

THERE ARE THREE LEVELS OF ERROR MESSAGES THAT MAY BE ISSUED BY A DIAGNOSTIC: GENERAL, BASIC AND EXTENDED. GENERAL ERROR MESSAGES ARE ALWAYS PRINTED UNLESS THE "IER" FLAG IS SET (SECTION 2.3). THE GENERAL ERROR MESSAGE IS OF THE FORM:

```
NAME TYPE NUMBER ON UNIT NUMBER TST NUMBER PC:XXXXXX
ERROR MESSAGE
```

WHERE; NAME = DIAGNOSTIC NAME
 TYPE = ERROR TYPE (SYS FATAL, DEV FATAL, HARD OR SOFT)
 NUMBER = ERROR NUMBER
 UNIT NUMBER = 0 - N (N IS LAST UNIT IN PTABLE)
 TST NUMBER = TEST AND SUBTEST WHERE ERROR OCCURRED
 PC:XXXXXX = ADDRESS OF ERROR MESSAGE CALL

BASIC ERROR MESSAGES ARE MESSAGES THAT CONTAIN SOME ADDITIONAL INFORMATION ABOUT THE ERROR. THESE ARE ALWAYS PRINTED UNLESS THE "IER" OR "IBR" FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL MESSAGE.

EXTENDED ERROR MESSAGES CONTAIN SUPPLEMENTARY ERROR INFORMATION SUCH AS REGISTER CONTENTS OR GOOD/BAD DATA. THESE ARE ALWAYS PRINTED UNLESS THE "IER", "IBR" OR "IXE" FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL ERROR MESSAGE AND ANY ASSOCIATED BASIC ERROR MESSAGES.

3.2 SPECIFIC ERROR MESSAGES

BELOW ARE SAMPLE ERROR MESSAGES. EACH ERROR MESSAGE REPRESENTS DIFFERENT TYPES

OF ERRORS DETECTED BY THIS DIAGNOSTIC.

ERROR MESSAGE EXAMPLE 1

THIS ERROR IS INDICATIVE OF AN INCORRECT REGISTER OR STATUS WORD RETURNED TO THE DIAGNOSTIC. THE FIRST PART DEFINES THE TEST FUNCTION AND UNIT THAT FAILED. THE SECOND PART PROVIDES THE REGISTER BITS AND THEIR MNEMONICS FOR THE INCORRECT REGISTER OR STATUS WORDS. THE THIRD PART IS THE EXPECTED AND RECEIVED DATA.

TST: 016 FIFO EXERCISER TEST
CZTSA HRD ERR 01610 ON UNIT 00 TST 016 SUB 002 PC: 040624
FIFO STATUS (IN WORD 9) INCORRECT AFTER WRITE FIFO

TAPE BUS SIGNALS IN WORD #8: - DESIGNATOR <BIT #>
PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>
IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>
IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>

TAPE BUS SIGNALS IN WORD #9:
DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>

MESSAGE BUFFER ADDRESS = 047352

MESSAGE BUFFER CONTENTS:

WORD #0 EXPD:	100020	RECV:	100020	XOR:	000000
WORD #1 EXPD:	000012	RECV:	000012	XOR:	000000
WORD #2 EXPD:	000000	RECV:	000000	XOR:	000000
WORD #3 EXPD:	000010	RECV:	000010	XOR:	000000
WORD #4 EXPD:	000000	RECV:	000000	XOR:	000000
WORD #5 EXPD:	000000	RECV:	000000	XOR:	000000
WORD #6 EXPD:	000000	RECV:	000000	XOR:	000000
WORD #7 EXPD:	000000	RECV:	000000	XOR:	000000
WORD #8 EXPD:	070217	RECV:	070217	XOR:	000000
WORD #9 EXPD:	000074	RECV:	000034	XOR:	000040

ERROR MESSAGE EXAMPLE 2

THIS ERROR SHOWS A FATAL FUNCTION ERROR FROM THE TAPE DRIVE. IN THIS INSTANCE A UNRECOVERABLE ERROR OCCURED WHICH INDICATES THAT THE CONTROLLER MAY BE DEFECTIVE.

CZTSA HRD ERR 00159 ON UNIT 00 TST 001 SUB 005 PC: 026202

TSSR NOT CORRECT AFTER SPACE RECORDS COMMAND

TSSR = 100214

TSSR BITS SET: SC,SSR

TERMINATION CLASS CODE = UNRECOVERABLE ERROR

PACKET ADDRESS = 026420

PACKET WORD # = 140010

PACKET WORD # = 000010

PACKET WORD # = 000000

PACKET WORD # = 000024

ERROR MESSAGE EXAMPLE 3

THIS ERROR SHOWS THAT THE MOTION BIT DID NOT GET SET WHILE DOING A REWIND WITH EXTENDED FEATURES MODE ENABLED.

CVTS HRD ERR 00121 ON UNIT 00 TST 001 SUB 002 PC: 023306
MOT BIT (XSTO) NOT SET DURING REWIND (EXTENDED FEATURES MODE)
EXPD: 000312 RECV: 000112 XOR: 000200

4.0 PERFORMANCE AND PROGRESS REPORTS

AT THE END OF EACH PASS, THE PASS COUNT IS GIVEN ALONG WITH THE TOTAL NUMBER OF ERRORS REPORTED SINCE THE DIAGNOSTIC WAS STARTED. THE "EOP" SWITCH CAN BE USED TO CONTROL HOW OFTEN THE END OF PASS MESSAGE IS PRINTED. SECTION 2.2 DESCRIBES SWITCHES.

SUCCESSFUL RUN EXAMPLE (PDP-11)

```
DR>STA/FLA:PNT:HOE
UNITS (D) ? 1
UNIT 0
DEVICE ADDRESS (0) 172520 ? <CR>
VECTOR (0) 224 ? <CR>
CHANGE SW (L) ? N<CR>
```

THE ABOVE COMMAND WILL START THE DIAGNOSTIC. THE COMMAND HAS TWO SWITCHES ON WHICH ARE "PRINT EACH TEST NBR AS EXECUTED" AND "HALT ON ERROR".

```
TST: 001 INITIALIZE 1
TST: 002 WRAP DATA HIGH BYTE TEST
TST: 003 WRAP DATA LOW BYTE TEST
TST: 004 RAM TEST
TST: 005 INITIALIZE 2 TEST
TST: 006 COMMAND REJECT TEST
TST: 007 WRITE CHARACTERISTICS TEST
TST: 008 VOLUME CHECK
TST: 009 COMPLETION INTERRUPT TEST
TST: 010 BASIC PACKET PROTOCOL TEST
TST: 011 NON-TAPE-MOTION COMMANDS TEST
```

0 ERRORS

NOTE: THE DIAGNOSTIC WILL RUN CONTINUOUSLY UNLESS A PASS NUMBER LIMIT HAS BEEN SPECIFIED WITH THE "/PASS:" SWITCH.

PROGRAM RUN TIMES

THE AVERAGE RUN TIMES OF THE PROGRAM ARE LISTED BELOW. THESE FIGURES ARE TO BE USED AS A GUIDE. THE TIMING WAS DONE ON A PDP-11

PROCESSOR WITH A LA34 CONSOLE.

THE PROGRAM RUNS IN TWO MODES; NO ITERATIONS AND DEFAULT MODE. IN THE NO ITERATIONS MODE, EACH TEST IS RUN ONCE, WITH NO ITERATIONS. IN THE DEFAULT MODE EACH TEST IS REPEATED BY THE NUMBER OF TIMES INDICATED BY THE ITERATION COUNT. NO ITERATIONS MODE IS SELECTED BY ANSWERING THE INHIBIT ITERATIONS QUESTION WITH A "Y" (YES).

TEST NUMBER	N/I SECS.	ITER SECS	DEF SECS.
1	1	30	29
2	1	10	9
3	1	8	7
4	25	120	95
5	5	140	135
6	25	475	450
7	20	20	0
8	1	10	9
9	20	20	0
10	1	2	1
11	8	11	3

THE TIMES REQUIRED TO RUN TESTS 1 THROUGH 12 IN ONE COMMAND.

Q.V.	1 MIN 57 SECONDS
DEFAULT	12 MINS

5.0 DEVICE INFORMATION TABLES

WHENEVER THE PROGRAM IS STARTED, VIA THE STA(RT) COMMAND, THE SUPERVISOR REQUESTS THE FOLLOWING P-TABLES PARAMETER CHANGES:

CHANGE HW (L) ?

UNITS (D) ? <ENTER THE NUMBER OF M7455 CONTROLLERS
PRESENT TO BE TESTED>

UNIT 0

DEVICE ADDRESS (O) 172520 ? <ENTER THE ADDRESS OF THE
TSBA/TSDB REGISTER>

VECTOR (O) 224 ? <ENTER ADDRESS OF INTERRUPT
VECTOR>

THE ADDRESS AND VECTOR QUESTIONS WILL BE ASKED FOR EACH OF THE NUMBER OF UNITS (CONTROLLERS) SPECIFIED IN THE "# UNITS?" QUESTION. LOGICAL UNIT NUMBERS ARE ASSIGNED IN ORDER, BEGINNING AT 0. UP TO FOUR UNITS CAN BE SELECTED FOR TESTING.

IN ADDITION, ON A START, RESTART OR CONTINUE THE SUPERVISOR REQUESTS CHANGES TO THE SOFTWARE OPERATING PARAMETERS, AS

FOLLOWS:

CHANGE SW (L) ?

INHIBIT ITERATIONS (L) N ?

6.0 TEST SUMMARIES

TEST 1: BUS RESET TEST

THIS TEST VERIFIES THAT THE M7455 MODULE'S DEVICE REGISTERS ARE ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND ANY BASIC PROBLEMS IN THE MODULE. AREAS OF LOGIC TESTED BY THE SELF-TEST SEQUENCE ARE AS FOLLOWS: ROM AND PIPELINE REGISTER, SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, AND, RAM. THIS TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INIT SIGNAL VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER, WAITS A PERIOD OF TIME (TO ALLOW THE CONTROLLER'S INITIALIZATION MICRODIAGNOSTIC SEQUENCE TO BE COMPLETED), AND THEN CHECKS THE CONTENTS OF THE TSSR REGISTER. SUCCESSFUL INITIALIZATION IS INDICATED BY SUBSYSTEM READY (SSR) AND NEED BUFFER ADDRESS (NBA) BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17 AND A16 AND OFL, WHICH ARE IGNORED FOR THIS TEST) BEING CLEAR (0). IF THE CONTENTS OF TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES. THE ERROR REPORT ANALYZES THE TSSR CONTENTS AND DISCERNS AND REPORTS ONE OF THREE POSSIBILITIES:

1. TSSR CONTENTS ARE AMBIGUOUS (ANY OF BITS 11-14 ARE SET, OR STATES OF SSR AND SC BITS DO NOT CORRESPOND TO THE APPARENT ERROR CODE IN BITS 0-5); INDICATES THAT THE TSSR CONTENT CANNOT BE TRUSTED. INDICATES A CATASTROPHIC CONTROLLER MALFUNCTION. THIS IS A FATAL ERROR (EXECUTION IS ABORTED). FIELD ACTION WOULD BE TO REPLACE THE M7455. IF THE M7455 ITSELF IS BEING DEBUGGED, THE PROGRAM SHOULD BE RESTARTED WITH LOOP ON ERROR ENABLED IN ORDER TO PROBE FOR THE PROBLEM.
2. SSR = 0, SC = 0 AND THE ERROR CODE IN BITS 0-5 IS IN THE RANGE 17-13; THIS IS A FATAL ERROR. THE ERROR CODE IS DECODED AND THE APPROPRIATE DESCRIPTION GIVEN. INDICATES THAT A SERIOUS PROBLEM EXISTS.

TEST 2: WRAP DATA - HIGH BYTE

THIS TEST VERIFIES OPERATION OF:

1. PART OF THE PDP-11 BUS INTERFACE SECTION OF THE M7455 MODULE: PART OF THE INPUT FILE (TSD8 HIGH BYTE), PART

OF THE OUTPUT FILE (TSSR HIGH BYTE AND TSBA, BOTH BYTES), PART OF THE DCO05 TRANSCEIVER CIRCUITS (ADDRESS DECODER, BDAL DRIVERS, HIGH BYTE OF INTERNAL DAL BUS DRIVERS), AND BASIC PROGRAMMED I/O CONTROL SEQUENCES AND LOGIC;

2. PART OF 2901 MICROPROCESSOR ELEMENTS (Q-REGISTER, REGISTER 0, ROTATE AND NEGATE FUNCTIONS
3. Y AND SOURCE BUSES;
4. BASIC MICROPROGRAM SEQUENCES.

THE PROGRAM WRITES A TEST DATA BYTE INTO THE HIGH BYTE OF TSDB, WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA BYTES (0-377 OCTAL).

TEST 3: WRAP DATA - LOW BYTE

THIS TEST FURTHER VERIFIES OPERATION OF MANY OF THE SAME ELEMENTS TESTED IN TEST 2, AND ADDITIONALLY VERIFIES:

1. LOW BYTE OF THE TSDB INPUT FILE REGISTER,
2. LOW BYTE OF INTERNAL DAL BUS DRIVERS ON THE DCO05 TRANSCEIVER CIRCUITS,
3. BASIC FUNCTIONING OF PARTS OF THE RAM.

THE PROGRAM WRITES A TEST DATA BYTE INTO THE LOW BYTE OF TSDB, WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA BYTES (0-377 OCTAL).

TEST 4: RAM TEST

THIS TEST VERIFIES THAT ALL LOCATIONS OF THE RAM ON THE M7455 CAN PROPERLY STORE AND READ BACK ALL DATA PATTERNS, AND THAT EACH RAM LOCATION IS UNIQUELY ADDRESSED (I.E., THAT ONE AND ONLY ONE LOCATION IS ACCESSED BY ANY PARTICULAR ADDRESS). THE BYPRODUCT OF THESE TESTS IS A VERIFICATION OF TWO REGISTERS IN THE 2901 AND THE CAPABILITY OF THE 2901 TO CORRECTLY PERFORM AN ADD.

TEST 5: SECOND INITIALIZATION TEST

THIS TEST VERIFIES THE SAME ELEMENTS AS DID INITIALIZATION TEST #1 AND ALSO CHECKS THAT CERTAIN PARTS OF RAM IS CLEARED TO ZERO AND THAT 2901 REGISTERS 10 AND 11 ARE ALSO CLEARED TO ZERO. THIS IS A CONFIDENCE CHECK OF A PART OF THE SELF-TEST SEQUENCE (I.E., THAT IT IS REALLY BEING EXECUTED). FOR EACH OF TWO SUBTESTS (ONE FOR INITIALIZING VIA A BUS INIT, THE OTHER FOR INITIALIZING BY WRITING INTO THE TSSR), THE FOLLOWING SEQUENCE IS PERFORMED:

1. EACH RAM LOCATION AND 2901 REGISTERS 10 AND 11 ARE SET TO ALL 1'S BY USING WRITES INTO THE TSDB REGISTER (LOW BYTE AND MAINTENANCE MODE WORD WRITES).
2. THE CONTROLLER IS INITIALIZED AND THE VARIOUS CHECKS ON THE TSSR DESCRIBED IN INITIALIZATION TEST #1 ARE PERFORMED.
3. #1'S (377 OCTAL) ARE WRITTEN INTO THE LOW BYTE OF TSDB, WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM ADDRESS, SHOULD BE 0. RAM LOCATION 0 IS VERIFIED BY WRITING A WORD OF ZEROS INTO THE TSDB. THE RESULTING LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0. DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM OR A TIMING PROBLEM.

TEST 6: COMMAND REJECT

THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC COMMAND DECODING AND DATI DMA HANDLING. THIS TEST CONTAINS TWO SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE

REJECTED COMMAND; SUBTEST 2 PERFORMS SIMILARLY TO SUBTEST 1 BUT SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED.

TEST 7: WRITE CHARACTERISTICS

THIS TEST VERIFIES BASIC OPERATION OF THE WRITE CHARACTERISTICS COMMAND. IT VERIFIES THAT THE COMMAND BLOCK AND CHARACTERISTICS DATA BLOCK ARE FETCHED PROPERLY FROM CPU MEMORY, THE NEED BUFFER ADDRESS (NBA) BIT IN TSSR IS HANDLED PROPERLY, AND THAT A PROPER MESSAGE PACKET IS STORED, WHERE APPROPRIATE. THIS TEST DOES NOT CHECK THAT THE VARIOUS FUNCTIONS ENABLED BY CHARACTERISTIC MODE DATA BITS OPERATE PROPERLY; THE FUNCTIONING OF THESE BITS IS VERIFIED IN SUBSEQUENT TESTS. ALL COMMANDS EXECUTED IN THIS TEST HAVE THE INTERRUPT ENABLE (IE) BIT CLEARED TO ZERO, SO NO INTERRUPTS SHOULD BE GENERATED. HOWEVER, THE PROGRAM RUNS AT PROCESSOR PRIORITY 0, WITH THE INTERRUPT SERVICE ROUTINE SET UP TO FLAG UNEXPECTED INTERRUPTS. IF AN INTERRUPT OCCURS, A PROBLEM EXISTS IN EITHER THE PDP-11 BUS INTERFACE SECTION OR IN THE ROM OR PIPELINE.

TEST 8: VOLUME CHECK

THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD WITHIN THE M7455 AND APPEARING IN XSTO, IS SET BY INITIALIZE AND CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF PREVENTING OR ALLOWING A TAPE MOTION COMMAND DEPENDING UPON WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF TAPE MOTION COMMANDS.

TEST 9: COMPLETION INTERRUPT

THIS TEST VERIFIES THAT AN INTERRUPT IS GENERATED AT THE COMPLETION OF THE WRITE CHARACTERISTICS COMMAND IF THE INTERRUPT ENABLE (IE) BIT IN THE COMMAND HEADER WORD IS SET. THIS TEST CHECKS THE FUNCTIONING OF THE INTERRUPT LOGIC AND BASIC PROCESSING OF THE IE BIT.

THE SEQUENCES OF TEST 7 ARE REPEATED, EXCEPT THAT THE INTERRUPT SERVICE ROUTINE IS SET UP TO EXPECT INTERRUPTS AND EACH WRITE CHARACTERISTICS COMMAND IS ISSUED WITH THE IE BIT SET (1). IT IS VERIFIED, WHERE APPROPRIATE, THAT THE IE STATUS BIT IN XSTO OF ANY MESSAGE PACKET IS SET AND THAT A COMPLETION INTERRUPT IS GENERATED. FINALLY, A SEQUENCE OF TWO COMMANDS ARE ISSUED, THE FIRST WITH IE=1 AND THE SECOND WITH IE=0. IT IS VERIFIED THAT NO INTERRUPT IS GENERATED AFTER THE SECOND COMMAND AND THAT THE IE BIT IN XSTO IS 0.

TEST 10: BASIC PACKET PROTOCOL

THIS TEST VERIFIES BASIC OPERATION OF THE MESSAGE BUFFER RELEASE COMMAND, THE FUNCTION OF THE ACK BIT IN THE COMMAND HEADER WORD.

AND THE REGISTER MODIFICATION REFUSED (RMR) LOGIC.

TEST 11: NON-TAPE MOTION COMMANDS

THIS TEST VERIFIES PROPER OPERATION OF THE INITIALIZE COMMAND. TWO SUBTESTS ARE USED. THE FIRST VERIFIES THAT THE COMMAND RUNS TO COMPLETION AND STORES A VALID MESSAGE PACKET. THE SECOND VERIFIES THAT NON-ZERO VALUES IN THE COMMAND MODE FIELD CAUSES COMMAND REJECT.

7.0 MAINTENANCE HISTORY

REVISION A - MARCH 1982

```

1
2
3
4          .TITLE   TSV2   PROGRAM HEADER
5 000000   .SBTTL   PROGRAM HEADER
6          .PSECT   ABS
12         .MCALL   SVC
13 000000   SVC
14         ; INITIALIZE SUPERVISOR MACROS
15         .ENABLE  LC
21         .MLIST   BEX,CND
22         .ENABL   AMA
23         .=      +2000
24 002000   ;
002000     .BGNMOD  TSV2
TSV2::
25
26         ;++
27         ; THE PROGRAM HEADER IS THE INTERFACE BETWEEN
28         ; THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
29         ;--
30
31
32 002000   POINTER BGNSW,BGNSFT,BGNAU,BGNDU,BGNRPT
33 002000   HEADER  CZTSA,A,0,655.,0
002000     L$NAME:: ;DIAGNOSTIC NAME
002000         .ASCII /C/
002001         .ASCII /Z/
002002         .ASCII /T/
002003         .ASCII /S/
002004         .ASCII /A/
002005         .BYTE  0
002006         .BYTE  0
002007         .BYTE  0
002010     L$REV:: ;REVISION LEVEL
002010         .ASCII /A/
002011     L$DEPO:: ;0
002011         .ASCII /0/
002012     L$UNIT:: ;NUMBER OF UNITS
002012         .WORD  0
002014     L$TJML:: ;LONGEST TEST TIME
002014         .WORD  655.
002016     L$HPCP:: ;POINTER TO H.W. QUES.
002016         .WORD  L$HARD
002020     L$SPCP:: ;POINTER TO S.W. QUES.
002020         .WORD  L$SOFT
002022     L$HPTP:: ;PTR. TO DEF. H.W. PTABLE
002022         .WORD  L$HW
002024     L$SPTP:: ;PTR. TO S.W. PTABLE
002024         .WORD  L$SW
002026     L$LADP:: ;DIAG. END ADDRESS
002026         .WORD  L$LAST
002030     L$STA:: ;RESERVED FOR APT STATS
002030         .WORD  0
002032     L$CO::
002032         .WORD  0
002034     L$DTP:: ;DIAGNOSTIC TYPE
002034         .WORD  0

```

002036		L\$APT::			;APT EXPANSION
002036	000000	.WORD	0		
002040		L\$DTP::			;PTR. TO DISPATCH TABLE
002040	002124'	.WORD	L\$DISPATCH		
002042		L\$PRIO::			;DIAGNOSTIC RUN PRIORITY
002042	000000	.WORD	0		
002044		L\$ENVI::			;FLAGS DESCRIBE HOW IT WAS SETUP
002044	000000	.WORD	0		
002046		L\$EXP1::			;EXPANSION WORD
002046	000000	.WORD	0		
002050		L\$MREV::			;SVC REV AND EDIT #
002050	003	.BYTE	C\$REVISION		
002051	003	.BYTE	C\$EDIT		
002052		L\$EF::			;DIAG. EVENT FLAGS
002052	000000	.WORD	0		
002054	000000	.WORD	0		
002056		L\$SPC::			
002056	000000	.WORD	0		
002060		L\$DEVP::			; POINTER TO DEVICE TYPE LIST
002060	003400'	.WORD	L\$DVTYP		
002062		L\$REPP::			;PTR. TO REPORT CODE
002062	022434'	.WORD	L\$RPT		
002064		L\$EXP4::			
002064	000000	.WORD	0		
002066		L\$EXP5::			
002066	000000	.WORD	0		
002070		L\$AUT::			;PTR. TO ADD UNIT CODE
002070	022122'	.WORD	L\$AU		
002072		L\$DUT::			;PTR. TO DROP UNIT CODE
002072	022220'	.WORD	L\$DU		
002074		L\$LUN::			;LUN FOR EXERCISERS TO FILL
002074	000000	.WORD	0		
002076		L\$DESP::			;POINTER TO DIAG. DESCRIPTION
002076	003406'	.WORD	L\$DESC		
002100		L\$LOAD::			;GENERATE SPECIAL AUTOLOAD EMT
002100	104035	EMT	E\$LOAD		
002102		L\$ETP::			;POINTER TO ERRTABL
002102	000000	.WORD	0		
002104		L\$ICP::			;PTR. TO INIT CODE
002104	021326'	.WORD	L\$INIT		
002106		L\$CCP::			;PTR. TO CLEAN-UP CODE
002106	022406'	.WORD	L\$CLEAN		
002110		L\$ACP::			;PTR. TO AUTO CODE
002110	022326'	.WORD	L\$AUTO		
002112		L\$PRT::			;PTR. TO PROTECT TABLE
002112	021316'	.WORD	L\$PROT		
002114		L\$TEST::			;TEST NUMBER
002114	000000	.WORD	0		
002116		L\$DLY::			;DELAY COUNT
002116	000000	.WORD	0		
002120		L\$HIME::			;PTR. TO HIGH MEM
002120	000000	.WORD	0		

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.SBTTL DISPATCH TABLE

TSV2 PROGRAM HEADER MACRO M1113 07-FEB-84 10:58
DISPATCH TABLE

SEQ 021

```

39
40
41
42
43
44 002122
    002122 000013
    002124
    002124 023216'
    002126 023436'
    002130 024134'
    002132 024626'
    002134 026162'
    002136 027266'
    002140 030544'
    002142 034132'
    002144 035036'
    002146 040162'
    002150 043274'

45
46
47
48
49
50
51
52
53
54
55 002152
    002152 000003
    002154
    002154

56
57 002154 172520
58 002156 000224
59 002160 000200
60 002162
    002162

61
62
63
64
65
66
67
68
69 002162
    002162 000004
    002164
    002164

70
71 002164 000000
72 002166 000000
73
74
75 002170 000017

; **
; THE DISPATCH TABLE CONTAINS THE STARTING ADDRESS OF EACH TEST.
; IT IS USED BY THE SUPERVISOR TO DISPATCH TO EACH TEST.
; --

DISPATCH 11
.WORD 11
; DISPATCH::
.WORD T1
.WORD T2
.WORD T3
.WORD T4
.WORD T5
.WORD T6
.WORD T7
.WORD T8
.WORD T9
.WORD T10
.WORD T11

.SBTTL DEFAULT HARDWARE P-TABLE

; **
; THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF
; THE TEST-DEVICE PARAMETERS. THE STRUCTURE OF THIS TABLE
; IS IDENTICAL TO THE STRUCTURE OF THE RUN-TIME P-TABLE.
; --

BGNHW DFPTBL ; DEFAULT HARD-P-TABLE
.WORD L10000-L$HW/2
L$HW::
DFPTBL::

.WORD 172520 ; 1ST (OF 2) REGISTERS.
.WORD 224 ; INTERRUPT VECTOR
.WORD PRI04 ; INTERRUPT PRIORITY.
ENDHW
L10000:

.SBTTL SOFTWARE P-TABLE

; **
; THE SOFTWARE P-TABLE CONTAINS THE VALUES OF THE PROGRAM
; PARAMETERS THAT CAN BE CHANGED BY THE OPERATOR.
; --

BGNPW SFPTBL
.WORD L10001-L$SW/2
L$SW::
SFPTBL::

TRANSTST:: .WORD 0 ; ENABLE TEST OF TRANSPORT(S) IF =1
NOITS:: .WORD 0 ; INHIBIT ITERATION OPTION.
; ... 0 = ITERATE.
; ... NZ = INHIBIT ITERATE.
LERRMAX:: .WORD 15. ; LOCAL (PER TEST) ERROR LIMIT

```

J2

TSV2 - PROGRAM HEADER MACRO M1113 07-FEB-84 10:58
SOFTWARE P-TABLE

SEQ 022

76 002172 000310
77 002174
 002174
78
79 002174
80
81
84
85

GERRMAX: .WORD 200. ; GLOBAL (PER UNIT) ERROR LIMIT
 ENDSW
L10001:
 ENDMOD

```

7          .TITLE  TSV3 - GLOBAL AREAS
8          .SBTTL  GLOBAL EQUATES SECTION
13
19
20 002174  BGNMOD  TSV3
      TSV3::
21 002174
22
23          .SBTTL  GLOBAL EQUATES SECTION
24
25          ;**
26          ; THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
27          ; ARE USED IN MORE THAN ONE TEST.
28          ;--
29
33 002174          EQUALS          ; GET STANDARD EQUATES.
          ;
          ; BIT DIFINITIONS
          ;
          100000    BIT15== 100000
          040000    BIT14== 40000
          020000    BIT13== 20000
          010000    BIT12== 10000
          004000    BIT11== 4000
          002000    BIT10== 2000
          001000    BIT09== 1000
          000400    BIT08== 400
          000200    BIT07== 200
          000100    BIT06== 100
          000040    BIT05== 40
          000020    BIT04== 20
          000010    BIT03== 10
          000004    BIT02== 4
          000002    BIT01== 2
          000001    BIT00== 1
          ;
          001000    BIT9==  BIT09
          000400    BIT8==  BIT08
          000200    BIT7==  BIT07
          000100    BIT6==  BIT06
          000040    BIT5==  BIT05
          000020    BIT4==  BIT04
          000010    BIT3==  BIT03
          000004    BIT2==  BIT02
          000002    BIT1==  BIT01
          000001    BIT0==  BIT00
          ;
          ; EVENT FLAG DEFINITIONS
          ;   EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION
          ;
          000040    EF.START== 32.          ; START COMMAND WAS ISSUED
          000037    EF.RESTART== 31.       ; RESTART COMMAND WAS ISSUED
          000036    EF.CONTINUE== 30.      ; CONTINUE COMMAND WAS ISSUED
          000035    EF.NEW== 29.          ; A NEW PASS HAS BEEN STARTED
          000034    EF.PWR== 28.          ; A POWER-FAIL/POWER-UP OCCURRED
          ;
          ;

```

```

; PRIORITY LEVEL DEFINITIONS
;
000340  PRI07== 340
000300  PRI06== 300
000240  PRI05== 240
000200  PRI04== 200
000140  PRI03== 140
000100  PRI02== 100
000040  PRI01== 40
000000  PRI00== 0

```

```

; OPERATOR FLAG BITS
;
000004  EVL== 4
000010  LOT== 10
000020  ADR== 20
000040  IDU== 40
000100  ISR== 100
000200  UAM== 200
000400  BOE== 400
001000  PNT== 1000
002000  PRI== 2000
004000  IXE== 4000
010000  IBE== 10000
020000  IER== 20000
040000  LOE== 40000
100000  HOE== 100000

```

34
35 002174

```

KT11
.SBTTL MEMORY MANAGEMENT DEFINITIONS ;DEFINE MEMORY MANAGEMENT REGISTERS
;*KT11 VECTOR ADDRESS
MMVEC= 250
;*KT11 STATUS REGISTER ADDRESSES
SR0= 177572
SR1= 177574
SR2= 177576
SR3= 172516
; IF NB
;*USER "I" PAGE DESCRIPTOR REGISTERS
UIPDR0= 177600
UIPDR1= 177602
UIPDR2= 177604
UIPDR3= 177606
UIPDR4= 177610
UIPDR5= 177612
UIPDR6= 177614
UIPDR7= 177616
; IF NB
;*USER "D" PAGE DESCRIPTOR REGISTORS
UDPDR0= 177620
UDPDR1= 177622
UDPDR2= 177624
UDPDR3= 177626
UDPDR4= 177630
UDPDR5= 177632
UDPDR6= 177634
UDPDR7= 177636

```

000250
177572
177574
177576
172516


```
.ENDC
;*USER "I" PAGE ADDRESS REGISTERS
UIPAR0= 177640
UIPAR1= 177642
UIPAR2= 177644
UIPAR3= 177646
UIPAR4= 177650
UIPAR5= 177652
UIPAR6= 177654
UIPAR7= 177656
.IF NB
;*USER "D" PAGE ADDRESS REGISTERS
UDPAR0= 177660
UDPAR1= 177662
UDPAR2= 177664
UDPAR3= 177666
UDPAR4= 177670
UDPAR5= 177672
UDPAR6= 177674
UDPAR7= 177676
.ENDC
.ENDC
.IF NB
;*SUPERVISOR "I" PAGE DESCRIPTOR REGISTERS
SIPDR0= 172200
SIPDR1= 172202
SIPDR2= 172204
SIPDR3= 172206
SIPDR4= 172210
SIPDR5= 172212
SIPDR6= 172214
SIPDR7= 172216
.IF NB
;*SUPERVISOR "D" PAGE DESCRIPTOR REGISTERS
SDPDR0= 172220
SDPDR1= 172222
SDPDR2= 172224
SDPDR3= 172226
SDPDR4= 172230
SDPDR5= 172232
SDPDR6= 172234
SDPDR7= 172236
.ENDC
;*SUPERVISOR "I" PAGE ADDRESS REGISTERS
SIPAR0= 172240
SIPAR1= 172242
SIPAR2= 172244
SIPAR3= 172246
SIPAR4= 172250
SIPAR5= 172252
SIPAR6= 172254
SIPAR7= 172256
.IF NB
;*SUPERVISOR "D" PAGE ADDRESS REGISTERS
SDPAR0= 172260
SDPAR1= 172262
SDPAR2= 172264
```

```

SDPAR3= 172266
SDPAR4= 172270
SDPAR5= 172272
SDPAR6= 172274
SDPAR7= 172276
.ENDC
.ENDC
;*KERNEL "I" PAGE DESCRIPTOR REGISTERS
172300 KIPDR0= 172300
172302 KIPDR1= 172302
172304 KIPDR2= 172304
172306 KIPDR3= 172306
172310 KIPDR4= 172310
172312 KIPDR5= 172312
172314 KIPDR6= 172314
172316 KIPDR7= 172316
.IF NB
;*KERNEL "D" PAGE
DESCRIPTOR REGISTERS
KDPDR0= 172320
KDPDR1= 172322
KDPDR2= 172324
KDPDR3= 172326
KDPDR4= 172330
KDPDR5= 172332
KDPDR6= 172334
KDPDR7= 172336
.ENDC
;*KERNEL "I" PAGE ADDRESS REGISTERS
172340 KIPAR0= 172340
172342 KIPAR1= 172342
172344 KIPAR2= 172344
172346 KIPAR3= 172346
172350 KIPAR4= 172350
172352 KIPAR5= 172352
172354 KIPAR6= 172354
172356 KIPAR7= 172356
.IF NB
;*KERNEL "D" PAGE ADDRESS REGISTERS
KDPAR0= 172360
KDPAR1= 172362
KDPAR2= 172364
KDPAR3= 172366
KDPAR4= 172370
KDPAR5= 172372
KDPAR6= 172374
KDPAR7= 172376
.ENDC

```

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000004

.SBTTL TSU05 REGISTER AND PACKET DEFINITIONS

;
; SOME GENERAL EQUATES.
;

ERRVEC=- 4 ; POINTER TO ERROR VECTOR FOR BUS TIME OUT.

TSU05 REGISTER AND PACKET DEFINITIONS

```

48      000060      TIIVC==      60      ; INTERRUPT VECTOR FOR CONSOLE INPUT
49      177560      TIICSR==     177560    ; BUS ADDRESS OF CONSOLE INPUT
50      177562      TIIBFR==     177562    ; CONSOLE INPUT DATA BUFFER
51      177520      BDVPCR==     177520    ; BDV11 PAGE CONTROL REGISTER
52
53      ;
54      ;BIT DEFINITIONS FOR TSSR REGISTER
55      ;
56
57      100000      SC=      BIT15      ;SPECIAL CONDITION
58      040000      BIE=      BIT14      ;BUS INTERFACE ERROR
59      020000      SCE=      BIT13      ;SANITY CHECK ERROR
60      010000      RMR=      BIT12      ;MODIFICATION REFUSED
61      004000      NXM=      BIT11      ;NONEXISTANT MEMORY ERROR
62      002000      NBA=      BIT10      ;NEED BUFFER ADDRESS
63      001400      MIADDR= BIT9:BIT8    ;EXTENDED ADDRESS BITS
64      000200      SSR=      BIT7       ;SUB SYSTEM READY
65      000100      OFL=      BIT6       ;OFF LINE BIT
66      000060      FATERR= BIT4:BIT5    ;FATAL TERMINATION ERROR CCDES
67      000016      TERCLS= BIT3:BIT2:BIT1 ;TERMINATION CODES
68
69
70      ;
71      ;
72      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 0
73      ;(XST0)
74      ;
75      ;
76
77      100000      XS0THK= BIT15      ;TAPE MARK DETECTED
78      040000      XS0RLS= BIT14      ;RECORD LENGTH SHORT
79      020000      XS0LET= BIT13      ;LOGICAL END OF TAPE
80      010000      XS0RL=  BIT12      ;RECORD LENGTH LONG
81      004000      XS0MLE= BIT11      ;WRITE LOCK ERROR
82      002000      XS0NEF= BIT10      ;NON EXECUTABLE FUNCTION
83      001000      XS0ILC= BIT9       ;ILLEGAL COMMAND
84      000400      XS0ILA= BIT8       ;ILLEGAL ADDRESS
85      000200      XS0MOT= BIT7       ;TAPE IN MOTION
86      000100      XS0OOL= BIT6       ;TRANSPORT ON LINE
87      000040      XS0IE=  BIT5       ;INTERRUPT ENABLE
88      000020      XS0VCK= BIT4       ;VOLUME CHECK BIT
89      000010      XS0PED= BIT3       ;PHASE ENCODED DRIVE
90      000004      XS0MLK= BIT2       ;WRITE LOCKED
91      000002      XS0BOT= BIT1       ;BEGINNING OF TAPE
92      000001      XS0EOT= BIT0       ;END OF TAPE
93
94
95      ;
96      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 1
97      ;(XST1)
98      ;
99      100000      X1.DLT = BIT15      ;DATA LATE
100     040000      X1.SPARE= BIT14     ;NOT USED
101     020000      X1.COR = BIT13     ;CORRECTABLE DATA ERROR
102     017375      X1.MBZ = BIT12:BIT11:BIT10:BIT9:BIT7:BIT6:BIT5:BIT4:BIT3:BIT2:BIT0 ;ALWAYS 0
103     000400      X1.RBP = BIT8       ;READ BUS PARITY ERROR
104     000002      X1.UNC = BIT1       ;UNCORRECTABLE DATA OR HARD ERROR

```

```

105
106
107      ;*
108      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 2
109      ;(XST2)
110      ;-
110      100000      X2.OPM = BIT15      ;OPERATION IN PROGRESS (TAPE MOVING)
111      040000      X2.RCE = BIT14      ;RAM CHECKSUM ERROR
112      035400      X2.SPARE= BIT13·BIT12·BIT11·BIT9·BIT8 ;NOT USED BY TSUOS (ALWAYS=0)
113      002000      X2.WCF = BIT10      ;WRITE CLOCK FAILURE (FIFO NOT EMPTIED BY TRANSPORT)
114      000200      X2.EXTF = BIT7      ;IF WRITE CHAR CMD THEN = EXTENDED FEATURES ENABLED
115      000100      X2.BUFE = BIT6      ;IF WRITE CHAR CMD THEN = BUFFERING ENABLED
116      000077      X2.REV = 000077    ;IF WRITE CHAR CMD THEN = MICROCODE REVISION LEVEL
117      000007      X2.UNIT = BIT2·BIT1·BIT0 ;IF GET STATUS THEN = CURRENTLY SELECTED UNIT NO.
118
119      ;*
120      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 3
121      ;(XST3)
122      ;-
123      177400      X3.MDE = 177400    ;MICRO-DIAGNOSTIC ERROR CODE
124      000200      X3.SPARE= BIT7      ;NOT USED BY TSUOS
125      000100      X3.OPI = BIT6      ;OPERATION INCOMPLETE
126      000040      X3.REV = BIT5      ;REVERSE
127      000020      X3.TRF = BIT4      ;TRANSPORT RESPONSE FAILURE
128      000010      X3.DCK = BIT3      ;DENSITY CHECK
129      000006      X3.MBZ =BIT2·BIT1    ;NOT USED ALWAYS 0
130      000001      X3.RIB = BIT0      ;REVERSE INTO BOT
131
132      ;*
133      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 4
134      ;(XST4)
135      ;-
136      100000      X4.HSP = BIT15      ;HIGH SPEED
137      040000      X4.RCE = BIT14      ;RETRY COUNT EXCEEDED
138      020000      X4.TSM = BIT13      ;TRANSPORT SPECIAL MODE
139      017400      X4.MBZ = BIT12·BIT11·BIT10·BIT9·BIT8 ;NOT USED ALWAYS 0
140      000377      X4.WRC = 000377    ;WRITE RETRY COUNT FIELD
141
142
143      ;*
144      ;
145      ;TSSR TERMINATION CODES (BIT 0-2)
146      ;
147      ;-
148
149      000006      TSREJ= 3·2          ;COMMAND REJECTED
150      000006      UNREC= 6           ;UNRECOVERABLE ERROR
151
152      ;*
153      ;
154      ;DEVICE REGISTER OFFSETS
155      ;
156      ;-
157
158      000000      TSBA== 0
159      000000      TSDB== 0          ;TSDB/TSBA REGISTER
160      000001      TSBAH== 1
161      000001      TSDBH== 1        ;TSDB/TSBA REGISTER HIGH BYTE

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162          000002      TSSR== 2          ;TSSR REGISTER
163          000003      TSSRH== 3         ;TSSR REGISTER HIGH BYTE
164
165          ;*
166          ; TSD8 ADDRESS BIT DEFINITIONS
167          ; -
168          000003      A1716 = BIT17:BIT0 ;ADDRESS BITS 17:16 ARE IN 1:0
169
170          ;*
171          ; COMMAND DEFINITIONS
172          ; -
173          000017      P.GETSTAT = 17      ;GET STATUS
174          000013      P.INIT = 13         ;INITIALIZE
175          000012      P.CONTROL = 12      ;CONTROL COMMANDS
176          000011      P.FORMAT = 11       ;FORMAT
177          000010      P.POSITION = 10     ;POSITION
178          000006      P.WRTSUB = 6        ;SUBSYSTEM WRITE
179          000005      P.WRITE = 5        ;WRITE
180          000004      P.WRTCHAR = 4       ;WRITE CHARACTERISTICS
181          000001      P.READ = 1         ;READ
182
183          ;*
184          ; COMMAND PACKET HEADER WORD BIT DEFINITIONS
185          ; -
186          100000      P.ACK = BIT15       ;BUFFER AVAIL FOR CONTROLLER
187          040000      P.CVC = BIT14       ;CLEAR VOLUME CHECK
188          020000      P.OPP = BIT13       ;REVERSE SEQUENCE OF DATA BITS
189          010000      P.SWB = BIT12       ;SWAP BYTES IN MEMORY
190          007400      P.MODE = BIT11:BIT10:BIT9:BIT8 ;EXTENDED COMMAND MODE FIELD
191          000200      P.IE = BIT7        ;INTERRUPT ENABLE
192          000140      P.FMT = BIT6:BIT5   ;PACKET HEADER TYPE (ALWAYS=0)
193          000037      P.CMD = 37         ;MAJOR COMMAND FIELD
194
195          ;*
196          ; CONTROL COMMAND MODE CODES
197          ; -
197          000000      PC.RELEASE = 0*256. ;RELEASE BUFFER
198          000400      PC.REWIND = 1*256.  ;REWIND
199          001000      PC.NOOP = 2*256.    ;NO-OP
200          002000      PC.IEREW = 4*256.  ;REWIND IMMEDIATE INTERRUPT
201          002400      PC.ERASE = 5*256.   ;SECURITY ERASE
202
203          ;*
204          ; CONTROLLER RAM DEFINITIONS
205          ; -
206          000167      RMCHBEG = 167       ;CHARACTERISTICS IO DATA BEGIN RAM ADDRESS
207          000200      RMCHEND = 200       ;CHARACTERISTICS IO DATA END RAM ADDRESS
208          000201      RMPKTBEG = 201      ;COMMAND PACKET BEGIN RAM ADDRESS
209          000210      RMPKTEND = 210      ;COMMAND PACKET END RAM ADDRESS
210          000215      RMSGBEG = 215      ;MESSAGE BUFFER BEGIN RAM ADDRESS
211          000234      RMSGEND = 234      ;MESSAGE BUFFER END RAM ADDRESS
212
213          ;*
214          ; REGISTER DEFINITIONS IN THE MESSAGE BUFFER
215          ;
216          ; -
217
218          000006      XSTO== 6           ;EXTENDED STATUS REGISTER 0 (WORD 4)

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219      000010      XST1== 8.          ;EXTENDED STATUS REGISTER 1 (WORD 5)
220      000012      XST2== 10.         ;EXTENDED STATUS REGISTER 2 (WORD 6)
221      000014      XST3== 12.         ;EXTENDED STATUS REGISTER 3 (WORD 7)
222      000016      XST4== 14.         ;EXTENDED STATUS REGISTER 4 (WORD 8)
223
224
225
226      ;
227      ;OFFSETS TO WORD LOCATIONS IN PACKET DEFINITIONS
228      ;
229      ;-
230
231      000002      PKLOW  = 2          ;LOW ORDER CHARACTERISTIC DATA POINTER
232      000004      PKHI   = 4          ;HIGH ORDER CHARACTERISTIC DATA POINTER
233      000006      PKBCNT = 6          ;NUMBER OF BYTES IN DATA PACKET
234
235      000010      EXBCNT=10          ;NUMBER OF BYTES IN EXTENDED DATA PACKET
236
237      ;
238      ;DATA PACKET OFFSETS FOR WRITE SUBSYSTEM COMMAND
239      ;-
240      000000      BSELO  = 0          ;BYTE 0
241      000001      BSEL1  = 1          ;BYTE 1
242      000002      SEL2   = 2          ;WORD 2
243      000004      SELDATA = 4          ;WORD 3
244
245      ;
246      ;BSELO SELECT CODES FOR WRITE SUBSYSTEM COMMAND
247      ;-
248      000000      PW.NOP   = 0          ;NO-OP
249      000001      PW.RDRAM = 1          ;READ RAM
250      000002      PW.WTRAM = 2          ;WRITE RAM
251      000003      PW.RFIFO = 3          ;READ FIFO
252      000004      PW.WFIFO = 4          ;WRITE FIFO
253      000005      PW.RDSTAT = 5         ;READ STATUS
254      000006      PW.WCTL  = 6          ;WRITE TAPE CONTROL
255      000007      PW.WFMT  = 7          ;WRITE TAPE FORMAT
256      000010      PW.WMISC = 10         ;WRITE MISCELLANEOUS
257      000011      PW.WNPR  = 11         ;WRITE NPR CONTROL
258      000020      PW.D22   = 20         ;DO MICROTTEST 22
259      000021      PW.D11   = 21         ;DO MICROTTEST 11
260      000022      PW.D13   = 22         ;DO MICROTTEST 13
261      000023      PW.NO1311 = 23        ;DISABLE MICROTTEST 11 AND 13
262      000024      PW.RDEXT = 24         ;READ EXT. TAPE STATUS (NOT SUPPORTED BY ALL TRANSPORTS)
263
264      ;
265      ;BSEL1 CODES FOR WRITE TAPE CONTROL
266      ;-
267      000200      WC.IFAD  = BIT7        ;IFAD - FORMATTER ADDRESS
268      000100      WC.IOTAD = BIT6        ;ITADO - TRANSPORT ADDRESS BIT 0
269      000040      WC.I1TAD = BIT5        ;ITAD1 - TRANSPORT ADDRESS BIT 1
270      000020      WC.ISRESV = BIT4        ;IRESV5 - RESERVED #5
271      000010      WC.IREW  = BIT3        ;IREW - REWIND
272      000004      WC.IRWU  = BIT2        ;IRWU - REWIND AND UNLOAD
273      000002      WC.IFEN  = BIT1        ;IFEN - FORMATTER ENABLE
274      000001      WC.IGO   = BIT0        ;GO
275

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```

276
277          ;+
278          ;BSEL1 CODES FOR WRITE FORMAT
279          ;-
280          WF.IMISP      = BIT7      ;IMISP - HIGH SPEED
281          WF.IWRT      = BIT6      ;IWRT  - WRITE
282          WF.IREV      = BIT5      ;IREV  - REVERSE
283          WF.IWFM      = BIT4      ;IWFM  - WRITE FILE MARK
284          WF.IEDIT     = BIT3      ;IEDIT - EDIT
285          WF.IERASE    = BIT2      ;IERASE - ERASE
286          WF.I3RESV    = BIT1      ;IRESV3 - RESERVED #3
287          WF.I4RESV    = BIT0      ;IRESV4 - RESERVED #4
288
289          ;+
290          ;BSEL1 CODES FOR WRITE MISCELLANEOUS SUBCOMMAND
291          ;-
292          MS.EXT        = BIT7      ;INVERT SENSE OF EXTENDED FEATURES SWITCH
293          MS.RSFIFO     = BIT4      ;RESET FIFO AND INPUT PARITY ERRORR
294          MS.RSTAPE     = BIT3      ;RESET TAPE STATUS IN 2 FLIP-FLOPS
295          MS.ATTN      = BIT2:BIT1 ;ATTENTION TRIGGER FIELD
296          MS.RSD        = BIT0      ;RESET TIMER A,B THEN DELAY TIMES IN SEL2
297
298          ;+
299          ; MS.ATTN SUBCODES
300          ;-
301          MSA.NOP       = 0*2      ;NO-OP (NOTHING TRIGGERED)
302          MSA.VOL       = 1*2      ;SIMULATE ON-LINE/OFF-LINE TRANSITION
303          MSA.NRAM      = 2*2      ;FORCE NON-FATAL RAM ERROR (FORCES ERRCODE 54)
304          MSA.FRAME     = 3*2      ;FORCE FATAL RAM ERROR (CAUSES SCE TO SET)
305
306          ;+
307          ; WRITE SUBSYSTEM WRITE NPR BSEL1 BIT DEFINITIONS
308          ;-
309          NP.IR         = BIT7      ;INTERRUPT REQUEST (0-1 TRANSITION)
310          NP.OUT        = BIT6      ;TAPE DATA DIRECTION OUT (0= IN)
311          NP.LOOP       = BIT5      ;ENABLE TRANSPORT LOOPBACK
312          NP.WRP        = BIT4      ;WRITE CORRECT PARITY (SET=0 TO WRITE WRONG)
313
314          ;+
315          ; READ STATUS MESSAGE BUFFER BIT DEFINITIONS
316          ;-
317          S2.DIM        = BIT7      ;WORD #9 BYTE 2 DATA IN MISS
318          S2.ILW        = BIT6      ;
319          S2.OUTRDY     = BIT5      ;
320          S2.INRDY      = BIT4      ;
321          S2.ATIMR      = BIT3      ;
322          S2.BTIMR      = BIT2      ;
323          S2.UNDEF      = BIT1:BIT0 ;(UNDEFINED)
324          S1.PARIN      = BIT15     ;WORD #8 BYTE 1 PARIN H
325          S1.I2RESV     = BIT14     ;
326          S1.I1RESV     = BIT13     ;
327          S1.IEOT       = BIT12     ;
328          S1.IIDENT     = BIT11     ;
329          S1.ICER       = BIT10     ;
330          S1.IFMK       = BIT9      ;
331          S1.IHER       = BIT8      ;
332          S0.ISPEED     = BIT7      ;WORD #8 BYTE 0 ISPEED H
333          S0.IRDY       = BIT6      ;
334          S0.IONL       = BIT5      ;

```

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333      000020      SO.ILDP      = BIT4      ;      ILDP L
334      000010      SO.IDBY      = BIT3      ;      IDBY L
335      000004      SO.IRWD      = BIT2      ;      IRWD L
336      000002      SO.IFBY      = BIT1      ;      IFBY L
337      000001      SO.IFPT      = BIT0      ;      IFPT L
338      ;+
339      ;UNIBUS MAP DEFINATIONS
340      ;-
341      170200      MMRO= 170200
342
343
344      .SBTTL SPECIAL MACROS AND OPDEFS.
345
346
347      ;+
348      ;SAVE GENERAL REGS 1 TO 5
349      ;-
350
351      .MACRO SAVREG
352      JSR      R5,REGSAV
353      .ENDM
354
355      ;+
356      ; MACRO TO FORCE AN ERROR
357      ;-
358      .MACRO FORCERROR TAG,NOTSSR
359      .NLIST
360      .IIF NOF LISTALL, .NLIST
361      .LIST
362      .IF B NOTSSR
363      MOV      TSSR(R5),R1      ;READ TSSR
364      .ENDC
365      MOV      FORCER,FORCER      ;IS FORCER SET? (LEAVE C BIT ALONE)
366      BNE      TAG      ;BR IF YES
367      .NLIST
368      .IIF NOF LISTALL, .LIST
369      .LIST
370      .ENDM
371
372      ;+
373      ; MACRO TO FORCE AN EXIT TO AVOID SECTION ITERATIONS
374      ; WILL EXIT TO A LABEL IF FORCER IS NEGATIVE
375      ; SO TO FORCE ERRORS AND EXIT ON 1 ERROR SET
376      ; FORCER TO 177777
377      ; TO FORCE ERRORS AND ITERATIONS SET FORCER TO 1.
378      ;-
379      .MACRO FORCEEXIT TAG
380      .NLIST
381      .IIF NOF LISTALL, .NLIST
382      .LIST
383      MOV      FORCER,FORCER      ;IS FORCER NEGATIVE?
384      BMI      TAG      ;BR IF YES
385      .NLIST
386      .IIF NOF LISTALL, .LIST
387      .LIST
388      .ENDM
389      ;+

```


TSV3 GLOBAL AREAS
GLOBAL DATA SECTION

MACRO M1113 07 FEB 84 10:58

SEQ 034

```

447 002224 000000 EXTFEA:: .WORD 0 ;EXTENDED FEATURES SOFTWARE SW 0-OFF;1-ON
448 002226 000000 BENBSW:: .WORD 0 ;BUFFER ENABLE SWITCH SW 0-OFF;1-ON
449 002230 000000 EXPD:: .WORD 0 ;EXPECTED RAM DATA FOR PRAMPKT ROUTINE
450 002232 000000 RECV:: .WORD 0 ;RECEIVED RAM DATA FOR PRAMPKT ROUTINE
451 002234 000000 ERRHI:: .WORD 0 ;HIGH ADDRESS MEMORY ERROR
452 002236 000000 ERRLO:: .WORD 0 ;LOW ADDRESS MEMORY ERROR
453 002240 RAMDATA:: .BLKW 16. ;DATA READ FROM RAM PACKET OR MESSAGE BUF AREA
454 002300 000000 RAMSIZ:: .WORD 0 ;RAM DATA SIZE FOR PRAMPKT ROUTINE
455 002302 000000 RCVHIADD:: .WORD 0 ;RECEIVED BUFFER HIGH ADDRESS
456 002304 000000 RCVLOAD:: .WORD 0 ;RECEIVED BUFFER LOW ADDRESS
457 002306 000000 COUNT:: .WORD 0 ;TEST COUNT PATTERN
458 002310 000000 DATA:: .WORD 0 ;TEST DATA
459 002312 000000 TSTFLAG:: .WORD 0 ;TEST FLAG WORD
460 002314 000000 TSTPTR:: .WORD 0 ;TSTBLK POINTER
461 002316 000000 PRMNO:: .WORD 0 ;PRINT ROUTINE TEMP
462 002320 EXPMSG:: .BLKB 100. ;EXPECTED MESSAGE BUFFER DATA
463 002464 RECMG:: .BLKB 100. ;RECEIVED MESSAGE BUFFER DATA
464 002630 TMPBFR:: .BLKB 80. ;TEMPORARY STORAGE FOR PRINT
465
466
467 .SBTTL TSTBLK - TEST DATA TABLE
468
469 ;*
470 ;
471 ;THIS TABLE CONTAINS TEST DATA USED IN SEVERAL TESTS
472 ;
473 ;IN SEQUENCE THE DATA IS:
474 ;
475 ; ALL ZEROS
476 ; ALL ONES
477 ; WALKING ONES
478 ; WALKING ZEROS
479 ; ALTERNATING ONES AND ZEROS
480 ;
481 ;-
482
483 002750 TSTBLK::
484 002750 000000 .WORD 0 ;ALL ZEROS
485 002752 177777 .WORD 177777 ;ALL ONES
486 002754 000001 .WORD BIT0 ;DATA FOR WALKING ONES
487 002756 000002 .WORD BIT1
488 002760 000004 .WORD BIT2
489 002762 000010 .WORD BIT3
490 002764 000020 .WORD BIT4
491 002766 000040 .WORD BIT5
492 002770 000100 .WORD BIT6
493 002772 000200 .WORD BIT7
494 002774 000400 .WORD BIT8
495 002776 001000 .WORD BIT9
496 003000 002000 .WORD BIT10
497 003002 004000 .WORD BIT11
498 003004 010000 .WORD BIT12
499 003006 020000 .WORD BIT13
500 003010 040000 .WORD BIT14
501 003012 100000 .WORD BIT15
502 003014 177776 .WORD ^CBIT0 ;DATA FOR WALKING ZEROS
503 003016 177775 .WORD ^CBIT1

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```

504 003020 177773 .WORD †CBIT2
505 003022 177767 .WORD †CBIT3
506 003024 177757 .WORD †CBIT4
507 003026 177737 .WORD †CBIT5
508 003030 177677 .WORD †CBIT6
509 003032 177577 .WORD †CBIT7
510 003034 177377 .WORD †CBIT8
511 003036 176777 .WORD †CBIT9
512 003040 175777 .WORD †CBIT10
513 003042 173777 .WORD †CBIT11
514 003044 167777 .WORD †CBIT12
515 003046 157777 .WORD †CBIT13
516 003050 137777 .WORD †CBIT14
517 003052 077777 .WORD †CBIT15
518 003054 125252 .WORD 125252 ;ALTERNATING ONES, ZEROS
519 003056 052525 .WORD 052525 ;ALTERNATING ONES, ZERO OPPOSITE FROM ABOVE
520 003060'
521
522
523 .SBTTL GLOBAL ENVIRONMENT STORAGE
524 ;
525 ;STORAGE FOR DEVICE REGISTERS
526 ;
527 003060 000000 100000 000000 DUMMY: 0,100000,0,0 ;DUMMY DEVICE REGISTERS...
528 003070 000000 000000 000000 0,0,0,0,0,0,0,0,0
529 ;...FOR MULTI-UNIT CHECKOUT.
530
531
532 003110 000000 DUFLG:: .WORD 0 ;"DROPPED UNIT" FLAG.
533 ;INHIBITS CODE IN "CLEAN-UP".
534 003112 000000 NODEV:: .WORD 0 ;FLAG TO SAY NO DEVICE.
535
536 003114 000000 TEMP1:: .WORD 0 ;SOME TEMP LOCATIONS.
537 003116 000000 TEMP2:: .WORD 0
538 003120 000000 XXCOMM:: .WORD 0 ;XXDP+ COMM BLOCK POINTER.
539 003122 000000 FREE:: .WORD 0 ;1ST FREE MEMORY ADDRESS...
540 003124 000000 FRESIZ:: .WORD 0 ;...AND SIZE (IN WORDS).
541 003126 000000 FREEMI: .WORD 0 ;LAST WORD IN FREE SPACE
542 003130 000000 KTF LG:: .WORD 0 ;KT11, MEM AVAIL FLAG -
543 ;- .WORD 0 = <24K OR NO KT -
544 ;- NZ = >24K AND KT.
545 003132 000000 KTENABLE:: .WORD 0 ;SET BY TEST ROUTINES TO FLAG >28K UNDER TEST
546 003134 000000 NXMFLG:: .WORD 0 ;SET IF WE CAN TEST CLEARED OTHERWISE
547 003136 000000 NXMLO:: .WORD 0 ;NXM LO ADDRESS BITS
548 003140 000000 NXMHI:: .WORD 0 ;NXM HI ADDRESS BITS FOR DAL'S 16-21
549 003142 000000 T23A:: .WORD 0 ;PROCESSOR TYPE FLAG
550 003144 000000 T23B:: .WORD 0 ;PROCESSOR TYPE FLAG B
551 003146 000000 T3BFLG:: .WORD 0 ;TEST 3B FLAG †0
552 003150 002000 PST32W:: .WORD 2000 ;32W BLOCK ADDRESS FOR 32K START
553 003152 000000 SIFLAG:: .WORD 0
554 003154 000000 BADDAT:: .WORD 0 ;ACTUAL DATA
555 003156 000000 GDDAT:: .WORD 0 ;EXPECTED DATA
556 003160 000000 LOOPFL:: .WORD 0
557 003162 CTAB:: .WORD 0 ;CONFIGURATION TABLES.
558 003162 000000 CTABM:: .WORD 0 ;CONFIG WORK.
559 003164 000000 .WORD 0
560 003166 000000 .WORD 0

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```

561 003170 000000 .WORD 0
562 003172 177777 .WORD -1 ;END OF MEM TABLE.
563 003174
CTABE::
;ERROR STATISTICS TABLE (1 WORD PER UNIT), 64 UNITS MAX:
;
; 0 UNIT NOT TESTED
; 100000 UNIT ONLINE, NO ERRORS
; 10XXXX UNIT ONLINE, ENCOUNTERED XXXX ERRORS
; 160000 UNIT DROPPED, NON-EXISTENT DEVICE REGISTER
; 160001 UNIT DROPPED, NOT IDLE AT START
; 14XXXX UNIT DROPPED, ENCOUNTERED XXXX ERRORS
;
573 003174 ERTABL: .BLKW 64.
574 003374 000000 ERTABE: .WORD 0
575
576 003376 000000 SKIPT: .WORD 0 ;1=SKIP SUBTEST 0=NO SKIP OF SUBTEST
577
578 .SBTTL GLOBAL TEXT MESSAGES
579
;+
; THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,
580 ; MESSAGES, AND ASCII INFORMATION THAT ARE USED IN
581 ; MORE THAN ONE TEST.
582 ;--
583
584
585
586
587 ;+
588 ;NAMES OF DEVICES SUPPORTED
589 ;-
590
591 003400 DEVTYP <TSU05>
003400 L$DVTYP::
003400 124 123 125 .ASCIZ /TSU05/
.EVEN
592
593 ;+
594 ;TEST DESCRIPTION
595 ;-
596
597 003406 DESCRIPT <**** TSU05 DIAG PART 1 - REPLACE M7455 IF ERROR ****>
003406 L$DESC::
003406 052 052 052 .ASCIZ /**** TSU05 DIAG PART 1 - REPLACE M7455 IF ERROR ****/
.EVEN
618
619
620
621 ;+
622 ;BIT TO ASCII CONVERSION FOR TSSR REGISTER
623 ;-
624
625 003474 003534' 003537' 003543' TSSRBIT:: .WORD 1$,2$,3$,4$,5$,6$,7$,8$
626 003514 003575' 003601' 003605' .WORD 9$,10$,11$,12$,13$,14$,15$,16$
627 003534 123 103 000 1$: .ASCIZ 'SC'
628 003537 102 111 105 2$: .ASCIZ 'BIE'
629 003543 123 103 105 3$: .ASCIZ 'SCE'
630 003547 122 115 122 4$: .ASCIZ 'RMR'
631 003553 116 130 115 5$: .ASCIZ 'NXM'
632 003557 116 102 101 6$: .ASCIZ 'NBA'

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633 003563      102      111      124  7#:      .ASCIZ  'BIT9'
634 003570      102      111      124  8#:      .ASCIZ  'BIT8'
635 003575      123      123      122  9#:      .ASCIZ  'SSR'
636 003601      117      106      114 10#:      .ASCIZ  'OFL'
637 003605      102      111      124 11#:      .ASCIZ  'BIT5'
638 003612      102      111      124 12#:      .ASCIZ  'BIT4'
639 003617      102      111      124 13#:      .ASCIZ  'BIT3'
640 003624      102      111      124 14#:      .ASCIZ  'BIT2'
641 003631      102      111      124 15#:      .ASCIZ  'BIT1'
642 003636      102      111      124 16#:      .ASCIZ  'BIT0'
643              .EVEN
644 003644      124      123      123 SFIERR: .ASCIZ  'TSSR ERROR AFTER SOFT INIT'
645 003677      124      123      123 SFHERR: .ASCIZ  'TSSR ERROR AFTER BUS RESET'
646 003732      040      040      116 NXR:    .ASCIZ  / NON-EXISTANT DEVICE REGISTER/
647 003771      045      101      040 NXRX:   .ASCIZ  /#A ADDRESS: #06/
648 004012      045      101      040 TSSX:   .ASCII  /#A TSBA,TSSR EXP'D: #06#A,#06#N/
649 004052      045      101      040       .ASCIZ  /#A TSBA,TSSR REC'D: #06#A,#06/
650 004111      045      116      045 FUSI:   .ASCII  /#N#A/
651 004115      040      040      125 JSI:   .ASCIZ  / UNEXPECTED INTERRUPT/
652 004144      040      040      111 NSI:   .ASCIZ  / INTERRUPT EXPECTED, NOT RECEIVED/
653 004207      045      116      045 FNOINTR: .ASCII  /#N#A/
654 004213      040      040      116 NOINTR: .ASCIZ  / NO INTERRUPT WAS GENERATED/
655 004250      040      040      111 IFAULT: .ASCIZ  / INTERRUPT FAULT/
656 004272      045      101      040 INTX:   .ASCIZ  /#A CPU PC: #06#A TSBA: #06/
657 004327      040      040      042 NOINIT: .ASCIZ  / "BUS-INIT" DIDN'T INITIALIZE CONTROLLER/
658 004401      040      040      042 NSINIT: .ASCIZ  / "SOFT-INIT" DIDN'T INITIALIZE THE DPU/
659 004451      040      040      042 BRINIT: .ASCIZ  / "BUS-RESET" DIDN'T INITIALIZE THE DPU/
660
661 004521      000              NUL:    .ASCIZ  //
662 004522      045      116      000 NULCR: .ASCIZ  /#N/
663 004525      045      101      040 EXPGOT: .ASCIZ  /#A EXP'D: #06#A, REC'D: #06/
664 004561      045      116      045 EXPGT2: .ASCIZ  /#N#A EXP'D: #06#A, #06#N#A REC'D: #0#A, #06/
665 004635      045      101      040 DUAD12: .ASCIZ  /#A REG(W) WRITTEN TO: #06#A REG(R) READ; EXP'D: #06#A, REC'D: #06/
666 004737      122      101      115 PKTRAM: .ASCIZ  'RAM Contents Do Not Match Packet Sent'
667 005005      040      040      103 SCME:   .ASCIZ  / CONFIG DOESN'T MATCH MFG. MASTER/
668 005050      127      122      111 WRTMSG: .ASCIZ  'WRITE CHARACTERISTICS Failed'
669 005105      124      123      123 WRTERR: .ASCIZ  'TSSR Incorrect After WRITE Command, More Bits Set Than SSR'
670 005200      124      123      123 RDERR:  .ASCIZ  'TSSR Incorrect After READ Command, More Bits Set Than SSR'
671 005272      106      101      124 SCHERR: .ASCIZ  'FATAL ERROR IN SUBTEST - CHECK TAPE,CABLES,TRANSPORT etc.'
672 005364      105      122      122 RETERR: .ASCIZ  'ERROR IN SUBTEST - WRITE DATA RETRY FIVE TIMES FAILED'
673 005452      045      116      045 NOMEM: .ASCIZ  '#N#A ***** NO NXM ADDRESS--CANNOT TEST NXM TIMEOUT. *****#N'
674              .EVEN
675
676              .SBTTL  GLOBAL ERROR REPORT SECTION
677
678
679      ;++
680      ; THE GLOBAL ERROR REPORT SECTION CONTAINS THE PRINTB AND PRINTX
681      ; CALLS THAT ARE USED IN MORE THAN ONE TEST.
682      ; ASCII TEXT STRINGS ARE FOUND IN THE GLOBAL TEXT SECTION.
683      ;--
684 005546      BGNMSG  NXRERR              ;NON-EXISTANT DEVICE REGISTER.
005546      NXRERR:
685 005546      PRINTX  #NXRX,NODEV      ;NODEV = NEXM ADDRESS.
005546 013746 003112'      MOV      NODEV,-(SP)
005552 012746 003771'      MOV      #NXRX,-(SP)
005556 012746 000002      MOV      #2,-(SP)

```

```

005562 010600          MOV     SP,RO
005564 104415          TRAP   C#PNTX
005566 062706 000006    ADD    #6,SP
686 005572 004737 005600' JSR    PC,EXTEND      ; PRINT EXTENSION IF REQUIRED.
687 005576          ENDMSG
005576          L10002:
005576 104423          TRAP   C#MSG
688
689
690
691
692
693
694 005600 005727    EXTEND: TST     (PC)+
695 005602 000000    EXTA:   0          ; 0 = NO EXTENSION.
696 005604 001402    BEQ    1$
697 005606 004777 177770 JSR    PC,EXTA      ; APPEND EXTENSION TEXT.
698 005612          1$: PRINTX #NULCR      ; PRINT A BLANK LINE
005612 012746 004522'   MOV    #NULCR,-(SP)
005616 012746 000001   MOV    #1,-(SP)
005622 010600          MOV    SP,RO
005624 104415          TRAP   C#PNTX
005626 062706 000004   ADD    #4,SP
699 005632 000207   RTS    PC
700
701          .SBTTL PRITSSR - PRINT TSSR CONTENTS
702
703
704
705
706
707
708
709
710
711
712
713
714
715
716
717
718
719 005634          ;+
720 005634          ; ROUTINE TO DISPLAY THE CONTENTS, AND BIT DEFINITIONS, OF
721 005640 010104          ; THE TSSR REGISTER. THIS ROUTINE IS NORMALLY CALLED ONLY
722 005642          ; BY A MESSAGE PRINTING ROUTINE
005642 010446          ;
005644 012746 006225'   ; INPUTS:
005650 012746 000002   ;
005654 010600          ; R1      CONTENTS OF TSSR
005656 104414          ;
005660 062706 000006   ; SUBORDINATE ROUTINES:
723 005664 010400          ;
724 005666 004737 015654' ; CHKAMB CHECK FOR AMBIGUOUS CONTENTS
725 005672 103410          ;
726 005674          ; -
PRITSSR:
SAVREG          ;SAVE GENERAL REGISTERS
MOV     R1,R4   ;SAVE THE TSSR CONTENTS
PRINTB #TSSRFOR,R4 ;PRINT THE CONTENTS OF TSSR
MOV     R4,-(SP)
MOV     #TSSRFOR,-(SP)
MOV     #2,-(SP)
MOV     SP,RO
TRAP   C#PNTB
ADD    #6,SP
MOV     R4,RO   ;GET TSSR BACK FOR CHKAMB
JSR    PC,CHKAMB ;ARE CONTENTS AMBIGUOUS ?
BCS    5$      ;BRANCH IF NOT
PRINTX #AMBTSSR ;SHOW CONTENTS ARE AMBIGUOUS

```

```

005674 012746 006445'      MOV      #AMBTSSR,-(SP)
005700 012746 000001      MOV      #1,-(SP)
005704 010600      MOV      SP,RO
005706 104415      TRAP     C#PNTX
005710 062706 000004      ADD      #4,SP
727 005714 010403      5$:     MOV      R4,R3          ;CONTENTS OF TSSR
728 005716 042703 001476      BIC      #HIADDR!FATERR!TERCLS,R3 ;CLEAR ALL MULTIPLE BIT FIELDS
729 005722 001434      BEQ      20$          ;NO BITS ARE SET
730 005724 012702 002630'      MOV      #TMPBFR,R2    ;TEMPORARY ASCII BUFFER
731 005730 012701 003474'      MOV      #TSSRBIT,R1  ;ASCII EQUIVALENT OF BITS
732 005734 005703      10$:    TST      R3          ;REMAINING BITS TO CONVERT
733 005736 001413      BEQ      15$          ;BRANCH WHEN ALL ARE DONE
734 005740 000241      CLC      ;CLEAR CARRY FOR SHIFT
735 005742 006103      ROL      R3          ;SHIFT NEXT BIT TO CARRY
736 005744 103006      BCC      13$          ;BRANCH IF BIT NOT SET
737 005746 011100      MOV      (R1),RO      ;POINTER TO BIT DEFINITION
738 005750 112022      11$:    MOVB    (RO)+,(R2)+  ;MOVE ASCII TO BUFFER
739 005752 001376      BNE      11$          ;MOVE ALL BITS
740 005754 112762 000054 177777      MOVB    #' ,'-1(R2)  ;INSERT A COMMA TO TERMINATE
741 005762 005721      13$:    TST      (R1)+      ;POINT TO NEXT DESCRIPTION
742 005764 000763      BR       10$          ;GET THE REMAINING BITS
743 005766 105042      15$:    CLRB    -(R2)       ;TERMINATE THE LINE
744 005770      PRINTX  #TSSDEF,#TMPBFR ;PRINT THE BIT DEFINITIONS
005770 012746 002630'      MOV      #TMPBFR,-(SP)
005774 012746 006416'      MOV      #TSSDEF,-(SP)
006000 012746 000002      MOV      #2,-(SP)
006004 010600      MOV      SP,RO
006006 104415      TRAP     C#PNTX
006010 062706 000006      ADD      #6,SP
745
746 006014 010403      20$.    MOV      R4,R3          ;GET THE TSSR CONTENTS
747 006016 042703 177761      BIC      #+CTERCLS,R3 ;CLEAR ALL BUT TERMINATION
748 006022 016303 006506'      MOV      TCOCOD(R3),R3 ;GET THE TERMINATION CODE MEANING
749 006026      PRINTX  #TCOASC,R3    ;PRINT THE TERMINATION CODE
006026 010346      MOV      R3,-(SP)
006030 012746 006306'      MOV      #TCOASC,-(SP)
006034 012746 000002      MOV      #2,-(SP)
006040 010600      MOV      SP,RO
006042 104415      TRAP     C#PNTX
006044 062706 0C0006      ADD      #6,SP
750 006050 010403      MOV      R4,R3          ;TSSR CONTENTS AGAIN
751 006052 042703 177717      BIC      #+CFATERR,R3 ;CLEAR ALL BUT FATAL TERMINATION
752 006056 001416      BEQ      25$          ;DON'T PRINT IF ZERO
753 006060 006203      ASR      R3
754 006062 006203      ASR      R3
755 006064 006203      ASR      R3          ;ALINE TERMINATION CODE FOR INDEX
756 006066 016303 007046'      MOV      TSFCOD(R3),R3 ;GET THE FATAL TERMINATION CODE
757 006072      PRINTX  #TFCASC,R3    ;PRINT THE FATAL TERMINATION CODE
006072 010346      MOV      R3,-(SP)
006074 012746 006347'      MOV      #TFCASC,-(SP)
006100 012746 000002      MOV      #2,-(SP)
006104 010600      MOV      SP,RO
006106 104415      TRAP     C#PNTX
006110 062706 000006      ADD      #6,SP
758 006114 042704 176377      25$:    BIC      #+CHIADDR,R4 ;CLEAR ALL BUT EXTENDED ADDRESS
759 006120 001411      BEQ      30$          ;DON'T PRINT IF ZERO
760 006122      PRINTX  #TEXASC,R4    ;PRINT THE EXTENDED ADDRESS BITS
    
```

```

006122 010446
006124 012746 006245'
006130 012746 000002
006134 010600
006136 104415
006140 062706 000006
761 006144 013703 002176'      301:
762 006150
006150 010346
006152 012746 000001
006156 010600
006160 104415
006162 062706 000004
763 006166 000207
764
766 006170
767 006170      045      116      045
782 006225      045      116      045
783 006245      045      116      045
784 006306      045      116      045
785 006347      045      116      045
786 006416      045      116      045
787 006445      045      116      045
788
789 006506 006526' 006551' 006577'
790 006526      116      157      162
791 006551      124      145      162
792 006577      124      141      160
793 006621      106      165      156
794 006641      122      145      143
795 006723      122      145      143
796 006772      125      156      162
797 007016      106      141      164
798
799
800 007046 007056' 007112' 007123'
801 007056      111      156      164
802 007112      122      145      163
803 007123      102      165      163
804 007167      122      145      163
805
806
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820
821

```

```

MOV      R4, -(SP)
MOV      @TEXASC, -(SP)
MOV      @2, -(SP)
MOV      SP, R0
TRAP     C@PNTX
ADD      @6, SP
301:     MOV      EPRTSW, R3      ;PRINT MEASGE BUFFER ADDRESS
PRINTX   R3                    ;PRINT PROPER MESSAGE
MOV      R3, -(SP)
MOV      @1, -(SP)
MOV      SP, R0
TRAP     C@PNTX
ADD      @4, SP
RTS      PC                    ;RETURN TO CALLER

EPRT2:
EPRT1:   .ASCIZ  'ANMA *****REPLACE M7455*****'
TSSRFOR: .ASCIZ  'ANMA TSSR = #06'
TEXASC:  .ASCIZ  'ANMA Extended Address Bits = #06'
TCOASC:  .ASCIZ  'ANMA Termination Class Code = #T'
TFCASC:  .ASCIZ  'ANMA Fatal Termination Class Code = #T'
TSSDEF:  .ASCIZ  'ANMA TSSR Bits Set: #T'
AMBTSSR: .ASCIZ  'ANMA TSSR Contents Are Ambiguous'

.EVEN
TCOCOD: .WORD   1#,2#,3#,4#,5#,6#,7#,8#
1#:     .ASCIZ  'Normal Termination'
2#:     .ASCIZ  'Termination Condition'
3#:     .ASCIZ  'Tape Status Alert'
4#:     .ASCIZ  'Function Reject'
5#:     .ASCIZ  'Recoverable Error - Tape Position One Record Down'
6#:     .ASCIZ  'Recoverable Error - Tape Was Not Moved'
7#:     .ASCIZ  'Unrecoverable Error'
8#:     .ASCIZ  'Fatal Controller Error'
.EVEN

TSFCOD: .WORD   1#,2#,3#,4#
1#:     .ASCIZ  'Internal Diagnostic Failure'
2#:     .ASCIZ  'Reserved'
3#:     .ASCIZ  'Bus Interface or Sanity Check Error'
4#:     .ASCIZ  'Reserved'
.EVEN

.SBTL   PRIPKT - PRINT THE ADDRESS/CONTENTS OF COMMAND PACKET

;
;THIS ROUTINE PRINTS THE ADDRESS AND CONTENTS OF A COMMAND PACKET.
;THIS ROUTINE IS NORMALLY ONLY CALLED FROM A PRINT ROUTINE.
;
;INPUT:
;
;      R0      NUMBER OF WORDS IN PACKET
;      R3      HIGH ORDER COMMAND PACKET ADDRESS
;      R4      ADDRESS OF COMMAND PACKET
;
;      NOTE:   R3 IS IGNORED IF THE KENABLE FLAG IS CLEAR.
;

```



```

822 007200          PRIPKT:
823 007200          SAVREG
824 007204 010005    MOV      R0,R5          ;SAVE THE REGISTERS
825 007206 005737 003132'  TST      KTENABLE      ;SAVE NO. OF WORDS IN PACKET
826 007212 001001    BNE      10#           ;ABOVE 28K UNDER TEST?
827 007214 005003    CLR      R3            ;BR IF YES
828 007216 010301    10#:  MOV      R3,R1          ;SET HIGH ORDER ADDRESS TO 0
829 007220 010400    MOV      R4,R0          ;COPY HIGH ORDER ADDRESS
830 007222 006100    ROL      R0            ;GET LOWER ADDRESS
831 007224 006101    ROL      R1            ;SHIFT BIT 15 INTO C BIT
832 007226          PRINTB #PKTADD,R1,R4 ;AND INTO HIGH ORDER.
                        MOV      R4,-(SP) ;PRINT PACKET ADDRESS
                        MOV      R1,-(SP)
                        MOV      #PKTADD,-(SP)
                        MOV      #3,-(SP)
                        MOV      SP,R0
                        TRAP     C:PNTB
                        ADD      #10,SP
833 007252 010300    15#:  MOV      R3,R0          ;GET HIGH ORDER ADDRESS
834 007254 001404    BEQ     20#           ;BR IF NOT ABOVE 28K.
835 007256 010401    MOV      R4,R1          ;GET LOW ORDER ADDRESS
836 007260 004737 017130'  JSR     PC,SETMAP      ;SETUP PAR6 MAPPING FOR 18 BIT ADDRESS
837 007264 010004    MOV      R0,R4          ;GET RETURNED PAR6 ADDRESS BIAS
838 007266 005001    20#:  CLR      R1            ;SAVE WORD NUMBER
839 007270 012402    25#:  MOV      (R4)+,R2      ;GET PACKET CONTENTS
840 007272          PRINTB #PKTFRM,R1,R2 ;PRINT THE DATA
                        MOV      R2,-(SP)
                        MOV      R1,-(SP)
                        MOV      #PKTFRM,-(SP)
                        MOV      #3,-(SP)
                        MOV      SP,R0
                        TRAP     C:PNTB
                        ADD      #10,SP
841 007316 005201    INC     R1            ;NEXT WORD NUMBER
842 007320 020105    CMP     R1,R5          ;DONE ALL PACKET WORDS?
843 007322 002762    BLT    25#           ;LOOP TILL ALL DONE
844 007324 000207    RTS     PC            ;RETURN

```

```

845
846 007326 045 116 045 PKTFRM: .ASCIZ '##A Packet Word #D1#A - #06'
847 007364 045 116 045 PKTADD: .ASCIZ '##A Packet Address - #01#05'
848
849
850

```

851 .SBTTL PRIBXOR - PRINT EXPD, RECV AND XOR BYTE

```

852
853 ;*
854 ;
855 ;PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE DATA BYTE
856 ;THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
857 ;
858 ;INPUTS:
859 ;
860 ; R1 RECEIVED DATA
861 ; R2 EXPECTED DATA
862 ;
863 ;OUTPUT:
864 ;

```

```

865          ;      R0      XOR OF EXPECTED/RECEIVED DATA
866          ;
867          ;-
868
869 007422    PRIBXOR::
870 007422    SAVREG          ;SAVE THE REGISTERS
871 007426    010203        MOV     R2,R3      ;EXPECTED DATA
872 007430    XOR          R1,R3      ;FORM THE EXCLUSIVE OR
873 007440    012700    177400    MOV     #C<377>,R0  ;BYTE MASK
874 007444    040001        BIC     R0,R1      ;SAVE LOW BYTE RECV
875 007446    040002        BIC     R0,R2      ;SAVE LOW BYTE EXPD
876 007450    040003        BIC     R0,R3      ;SAVE LOW BYTE XOR
877 007452    PRINTB     #XORFOR,R2,R1,R3 ;PRINT THE MESSAGE
          007452    010346        MOV     R3,-(SP)
          007454    010146        MOV     R1,-(SP)
          007456    010246        MOV     R2,-(SP)
          007460    012746    007504'  MOV     #XORFOR,-(SP)
          007464    012746    000004    MOV     #4,-(SP)
          007470    010600        MOV     SP,R0
          007472    104414        TRAP    C#PNTB
          007474    062706    000012    ADD     #12,SP
878 007500    010300        MOV     R3,R0      ;R0 HAS XOR ON RETURN
879 007502    000207        RTS          ;RETURN TO CALLER
880
881 007504    045      116      045 XORBFOR: .ASCIZ '#N#A EXPD: #03#A RECV: #03#A XOR: #03'
882          .EVEN
883
884
885          .SBTTL PRIXOR - PRINT EXPD, RECV AND XOR
886
887
888 ;*
889 ;PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE TWO
890 ;THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
891 ;
892 ;INPUTS:
893 ;
894 ;      R1      RECEIVED DATA
895 ;      R2      EXPECTED DATA
896 ;
897 ;OUTPUT:
898 ;
899 ;      R0      XOR OF EXPECTED/RECEIVED DATA
900 ;
901 ;-
902
903 007552    PRIXOR::
904 007552    SAVREG          ;SAVE THE REGISTERS
905 007556    010203        MOV     R2,R3      ;EXPECTED DATA
906 007560    XOR          R1,R3      ;FORM THE EXCLUSIVE OR
907 007570    PRINTB     #XORFOR,R2,R1,R3 ;PRINT THE MESSAGE
          007570    010346        MOV     R3,-(SP)
          007572    010146        MOV     R1,-(SP)
          007574    010246        MOV     R2,-(SP)
          007576    012746    007622'  MOV     #XORFOR,-(SP)
          007602    012746    000004    MOV     #4,-(SP)
          007606    010600        MOV     SP,R0

```

TSV3 - GLOBAL AREAS MACRO M1113 07 FEB 84 10:58
 PRI XOR PRINT EXPD, RECV AND XOR

SEQ 043

```

007610 104414          TRAP  C:PNTB
007612 062706 000012  ADD   #12,SP
908 007616 010300          MOV   R3,R0          ;RO HAS XOR ON RETURN
909 007620 000207          RTS   PC            ;RETURN TO CALLER
910
911 007622 045 116 045 XORFOR: .ASCIZ '##A EXPD: #06#A RECV: #06#A XOR: #06'
912 .EVEN
913
914 .SBTTL PRIEQU - PRINT BIT NUMBERS AS ASCII EQUIVALENT
915
916 ;*
917 ;
918 ;ROUTINE TO CONVERT BIT VALUES TO ASCII AND PRINT THE STRING
919 ;THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE
920 ;
921 ;INPUTS:
922 ;
923 ; R0 OCTAL VALUE TO CONVERT
924 ; R1 TABLE OF POINTERS TO ASCII EQUIVALENT
925 ;
926 ;-
927
928 007670          PRIEQU:
929 007670          SAVREG          ;SAVE THE REGISTERS
930 007674 000207  RTS   PC            ;RETURN TO CALLER
931
932
933
934
935 .SBTTL PRIRAM - PRINT RAM ADDRESS
936
937 ;*
938 ;
939 ;PRINT CONTROLLER RAM ADDRESS.
940 ;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
941 ;
942 ;INPUTS:
943 ;
944 ; R4 RAM ADDRESS
945 ;
946 ;-
947 007676          PRIRAM:
948 007702          SAVREG          ;SAVE R1-R5 UNTIL NEXT RETURN
007702 010446          PRINTB #RAMFOR,R4 ;PRINT RAM ADDRESS IN ERROR
007704 012746 007726' MOV   R4,-(SP)
007710 012746 000002 MOV   #RAMFOR,-(SP)
007714 010600 MOV   #2,-(SP)
007716 104414 MOV   SP,R0
007720 062706 000006 TRAP  C:PNTB
949 007724 000207  ADD   #6,SP
950          RTS   PC            ;RETURN
951 007726 045 116 045 RAMFOR: .ASCIZ '##A CONTROLLER RAM ADDRESS = #06'
952 .EVEN
953
954
955 .SBTTL PRIADD - PRINT MEMORY ERROR ADDRESS
956 ;*

```

TSV3 GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 PRIADD PRINT MEMORY ERROR ADDRESS

SEQ 044

```

957
958 ;PRINT MEMORY ADDRESS
959 ;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
960
961 ; IMPLICIT INPUTS
962
963 ;          ERRHI - HIGH ORDER ADDRESS
964 ;          ERRLO - LOW ORDER ADDRESS
965
966 ;-
967 007770 PRIADD: SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
968 007770 MOV ERRHI,R0 ;GET HIGH ADDRESS
969 007774 013700 002234' MOV ERRLO,R1 ;GET LOW ADDRESS
970 010000 013701 002236' MOV R1,R2 ;COPY LOW ADDRESS
971 010004 010102 ROL R1 ;SHIFT BIT 15 TO C BIT
972 010006 006101 ROL R0 ;SHIFT INTO HIGH ORDER
973 010010 006100 PRINTB #PRIA0,R0,R2 ;PRINT MEMORY ADDRESS IN ERROR
974 010012 MOV R2,-(SP)
010014 010046 MOV R0,-(SP)
010016 012746 010040' MOV #PRIA0,-(SP)
010022 012746 000003 MOV #3,-(SP)
010026 010600 MOV SP,R0
010030 104414 TRAP C#PNTB
010032 062706 000010 ADD #10,SP
975 010036 000207 RTS PC ;RETURN
976
977 010040 045 116 045 PRIA0: .ASCIZ 'MMA MEMORY ERROR ADDRESS = #01#05'
978 .EVEN
979
980
981 .SBTTL PRITADD - PRINT MEMORY TEST ADDRESS
982
983 ;*
984 ;PRINT MEMORY ADDRESS
985 ;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
986
987 ; IMPLICIT INPUTS
988
989 ;          ERRHI - HIGH ORDER ADDRESS
990 ;          ERRLO - LOW ORDER ADDRESS
991
992 ;-
993 010104 PRITADD: SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
994 010104 MOV ERRHI,R2 ;GET HIGH ADDRESS
995 010110 013702 002234' MOV ERRLO,R1 ;GET LOW ADDRESS
996 010114 013701 002236' ;MOV R1,R2 ;COPY LOW ADDRESS
997 ;ROL R1 ;SHIFT BIT 15 TO C BIT
998 ;ROL R0 ;SHIFT INTO HIGH ORDER
999 PRINTB #PRIT0,R1 ;PRINT MEMORY ADDRESS LOW IN ERROR
1000 010120 MOV R1,-(SP)
010122 012746 010166' MOV #PRIT0,-(SP)
010126 012746 000002 MOV #2,-(SP)
010132 010600 MOV SP,R0
010134 104414 TRAP C#PNTB
010136 062706 000006 ADD #6,SP

```

```

1001 010142          PRINTB #PRIT1,R2          ;PRINT MEMORY ADDRESS HIGH IN ERROR
      010142 010246  MOV      R2,-(SP)
      010144 012746 010231' MOV      #PRIT1,-(SP)
      010150 012746 000002  MOV      #2,-(SP)
      010154 010600  MOV      SP,RO
      010156 104414  TRAP    C#PNTB
      010160 062706 000006  ADD      #6,SP
1002 010164 000207  RTS      PC          ;RETURN
1003
1004 010166      045      116      045  PRIT0: .ASCIZ '###A MEMORY TEST ADDRESS LOW = #06'
1005 010231      045      116      045  PRIT1: .ASCIZ '###A MEMORY TEST ADDRESS HIGH = #06'
1006          .EVEN
1007
1008
1009          .SBTTL SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND
1010
1011          ;+
1012          ;
1013          ;ROUTINE TO ISSUE A SPACE RECORDS
1014          ;COMMAND (FORWARD OR REVERSE)
1015          ;
1016          ;INPUT:
1017          ;
1018          ; R3      NUMBER OF RECORDS TO BE SPACED OVER
1019          ;          BIT15 CONTROLS DIRECTION
1020          ;          BIT15 = 0 IS FORWARD
1021          ;          BIT15 = 1 IS REVERSE
1022          ; R5      FIRST DEVICE UNIBUS ADDRESS
1023          ;
1024          ;          REQUIRES A WRITE CHARACTERISTICS DONE PREVIOUSLY
1025          ;
1026          ;OUTPUT:
1027          ;
1028          ; CARRY  SET - SPACE RECORDS COMMAND OK
1029          ;          CLR - SPACE RECORDS FAILED
1030          ;
1031          ;
1032          ; R0      THE CONTENTS OF R4 IS MOVED TO R0
1033          ;
1034          ;
1035          ;IMPLICIT OUTPUT:
1036          ;
1037          ;          TAPE HAS BEEN MOVED
1038          ;
1039          ;SIDE EFFECTS:
1040          ;
1041          ;
1042          ;-
1043
1044 010276          SPACE::
1045 010276          SAVREG          ;SAVE THE GENERAL REGISTERS
1046 010302 012737 000764 010470'  MOV      #500.,SDELAY          ;SET UP DELAY
1047 010310 012737 140010 010460'  MOV      #140010,80#          ;SET UP COMMAND, SPACE FORWARD
1048 010316 005703          TST      R3          ;CHECK FOR DIRECTION
1049 010320 100403          BMI      5#          ;BR, IF REVERSE INDICATED
1050 010322 010337 010462'  MOV      R3,90#          ;LOAD UP NUMBER OF RECORDS TO SPACE
1051 010326 000407          BR       10#          ;GO DO COMMAND
    
```

TSV3 GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND

```

1052 010330 042703 100000      5#: BIC    #BIT15,R3      ;CLEAR DIRECTION BIT
1053 010334 010337 010462'    MOV    R3,90#        ;LOAD UP NUMBER OF RECORDS TO SPACE
1054 010340 052737 000400 010460'  BIS    #BIT8,80#     ;SET REVERSE BIT IN COMMAND PACKET
1055 010346 012704 010460'    10#: MOV    #80#,R4     ;SET UP R4 WITH PACKET ADDRESS
1056 010352 010465 000000    MOV    R4,TSDB(R5)  ;SEND OUT COMMAND
1057 010356 004737 016060'    15#: JSR    PC,WAITF  ;WAIT FOR SSR
1058 010362 103420          BCS    20#          ;BR, IF SSR IS SET AND OK
1059 010364          DELAY  250        ;DELAY ABOUT .25 SECONDS
      010364 012727 000250    MOV    #250,(PC)+
      010370 000000          .WORD  0
      010372 013727 002116'    MOV    L#DLY,(PC)+
      010376 000000          .WORD  0
      010400 005367 177772    DEC    -6(PC)
      010404 001375          BNE    -.4
      010406 005367 177756    DEC    -22(PC)
      010412 001367          BNE    .-20
1060 010414 005337 010470'    DEC    SDELAY
1061 010420 001356          BNE    15#          ;BUMP DELAY COUNTER DOWN
1062 010422 000411          BR     60#          ;BR, IF MORE DELAY
1063 010424 016501 000002    20#: MOV    TSSR(R5),R1 ;READ TSSR
1064 010430 012702 000200    MOV    #SSR,R2     ;SET UP EXPECTED
1065 010434 020201    25#: CMP    R2,R1     ;ARE THEY OK
1066 010436 001401          BEQ   40#          ;BR, IF EQUAL = OK
1067 010440 000402          BR     60#          ;TROUBLE EXIT
1068 010442 000261    40#: SEC
1069 010444 000401          BR     70#          ;SET CARRY NO TROUBLE
1070 010446 000241    60#: CLC
1071 010450    70#:
1072 010450 010400          MOV    R4,R0
1073 010452 000207          RTS    PC          ;PASS PACKET ADDRESS
1074
1075
1076
1077
1078          ;
1079          ;PACKET FOR SPACE COMMAND
1081 010454          .BLKB  10-<.-TSV2&7>
1083
1084          ;
1085 010460 000000    80#: .WORD
1086          ;NUMBER OF RECORDS TO BE SPACED OVER WORD
1087 010462 000000    90#: .WORD
1088 010464 000000          .WORD
1089 010466 000000          .WORD
1090 010470 000000    SDELAY: .WORD  0          ;DELAY COUNTER
1091          .EVEN
1092
1093
1094          .SBTTL  WRCHR  - WRITE CHARACTERISTICS COMMAND
1095
1096
1097          ;+
1098          ;
1099          ;ROUTINE TO ISSUE A WRITE CHARACTERISTICS
1100          ;COMMAND SO THAT OTHER COMMANDS WILL BE ACCEPTED
1101          ;
1102          ;INPUT:

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 WRTCHR - WRITE CHARACTERISTICS COMMAND

SEQ 047

```

1103 ; R4 ADDRESS OF PACKET FROM TEST
1104 ; R5 FIRST DEVICE UNIBUS ADDRESS
1105 ; REQUIRES A CALL TO SOFINIT BE DONE PREVIOUSLY
1106 ;
1107 ;OUTPUT:
1108 ;
1109 ; R0 TSSR CONTENTS
1110 ; CARRY SET - WRITE CHARACTERISTICS COMMAND OK
1111 ; CLR - WRITE CHARACTERISTICS FAILED
1112 ;
1113 ;IMPLICIT OUTPUT:
1114 ;
1115 ; MESSAGE BUFFER AND OTHER BUFFERS ALL SET UP
1116 ; SOFTWARE SWITCHES SET AS FOLLOWS:
1117 ; EXTFEA = EXTENDED FEATURES PRESENT
1118 ; BENBSW = BUFFER ENABLE SWITCH ON OR OFF
1119 ;
1120 ;
1121 ;SIDE EFFECTS:
1122 ;
1123 ;
1124 ;-
1125
1126 010472 WRTCHR:: SAVREG ;SAVE THE GENERAL REGISTERS
1127 010472 CLR BENBSW ;CLEAR BUFFER ENABLE SWITCH
1128 010476 005037 002226' CLR EXTFEA ;CLEAR EXTENDED FEATURES SW SWITCH
1129 010502 005037 002224' 10$: MOV R4,TSDB(R5) ;SEND OUT COMMAND
1130 010506 010465 000000 JSR PC,CHKTSSR ;WAIT FOR SSR
1131 010512 004737 016146' BCS 20$ ;BR, IF SSR IS SET AND OK
1132 010516 103401 BR 60$ ;BR IF TROUBLE CARRY = CLEAR
1133 010520 000435 20$: MOV TSSR(R5),R1 ;READ TSSR
1134 010522 016501 000002 MOV #SSR,R2 ;SET UP EXPECTED
1135 010526 012702 000200 BIT #OFL,R1 ;WAS OFF LINE SET IN TSSR
1136 010532 032701 000100 BEQ 25$ ;BR, IF NO OFL SET
1137 010536 001402 BIS #OFL,R2 ;MAKE THEM LOOK ALIKE
1138 010540 052702 000100 CMP R2,R1 ;ARE THEY OK
1139 010544 020201 25$: BEQ 40$ ;BR, IF EQUAL = OK
1140 010546 001401 BR 60$ ;TROUBLE EXIT
1141 010550 000421 40$: ADD #8,R4 ;POINT TO WRT CHARA DATA PACKET
1142 010552 062704 000010 MOV (R4),R3 ;GET ADDRESS OF MESSAGE BUFFER
1143 010556 011403 BIT #X2.EXTF,XST2(R3) ;EXTENDED FEATURES BIT SET?
1144 010560 032763 000200 000012 BEQ 45$ ;BR IF NO
1145 010566 001402 45$: INC EXTFEA ;SET EXTENDED FEATURES SW SWITCH
1146 010570 005237 002224' BIT #X2.BUFE,XST2(R3) ;BUFFER ENABLE SWITCH SET
1147 010574 50$: BEQ 50$ ;BR, IF SWITCH NOT SET
1148 010576 032763 000100 000012 INC BENBSW ;SET SOFTWARE SWITCH FOR ENABLED
1149 010602 001402 50$: SEC ;SET CARRY NO TROUBLE
1150 010604 005237 002226' BR 70$ ;EXIT
1151 010610 50$: CLC ;CARRY CLEAR = ERROR
1152 010610 000261 60$: MOV TSSR(R5),R0 ;RETURN TSSR CONTENTS
1153 010612 000401 70$: RTS PC ;RETURN
1154 010614 000241
1155 010616 016500 000002
1156 010622 000207
1157
1158
1159 .SBTTL REWIND - POSITION TAPE (REWIND) COMMAND

```

1160
 1161
 1162
 1163
 1164
 1165
 1166
 1167
 1168
 1169
 1170
 1171
 1172
 1173
 1174
 1175
 1176
 1177
 1178
 1179
 1180
 1181
 1182
 1183
 1184
 1185
 1186
 1187 010624
 1188 010624
 1189 010630 012704 010720'
 1190 010634 010465 000000
 1191 010640 012703 000550
 1192 010644 004737 016060'
 1193 010650 103417
 1194 010652
 010652 012727 000372
 010656 000000
 010660 013727 002116'
 010664 000000
 010666 005367 177772
 010672 001375
 010674 005367 177756
 010700 001367
 1195 010702 005303
 1196 010704 001357
 1197 010706 000241
 1198 010710 010400
 1199 010712 000207
 1200
 1201
 1203 010714
 1205 010720
 1206 010720 102010
 1207 010722 000000
 1208
 1209
 1210

```

;+
; THIS ROUTINE WILL REWIND THE SELECTED TAPE.
;
; CAUTION: THE ROUTINE DOES NOT WAIT FOR BOT
; TO ARRIVE. ALSO THE CALLER MUST CHECK FOR
; SSR TO SET IN THE TSSR
;
; CALLING SEQUENCE:
;
; DO A SOFT INIT
; DO A WRITE CHARACTERISTICS
; JSR PC,REWIND
;
; INPUT:
;
; R5 FIRST DEVICE UNIBUS ADDRESS
;
; OUTPUT
;
; R0 THE CONTENTS OF R4 IS PASSED TO R0
;
; -
REWIND::
    SAVREG                                ;SAVE R1-R5 UNTIL NEXT RETURN
    MOV #RMPACK,R4                        ;GET PACKET ADDRESS
    MOV R4,TSD8(R5)                       ;SEND PACKET ADDRESS TO EXECUTE
    MOV #360.,R3                          ;ENOUGH TIME FOR 2400' REEL TO REWIND
10$: JSR PC,WAITF                          ;WAIT FOR SSR TO SET
    BCS 20$                               ;LEAVE WHEN SSR IS SET
    DELAY 250.                            ;WAIT FOR .25 SECONDS
    MOV #250.,(PC)
    .WORD 0
    MOV L#DL,(PC)
    .WORD 0
    DEC -6(PC)
    BNE -.4
    DEC -22(PC)
    BNE -.20
    DEC R3                                ;BUMP COUNTER DOWN
    BNE 10$                               ;KEEP GOING
    CLC                                  ;CLEAR CARRY TO SET ERROR
20$: MOV R4,R0                          ;PASS THE PACKET ADDRESS
    RTS PC                                ;RETURN

RMPACK: .BLKB 10-<.-TSV2E7>
    .WORD 102010                        ;POSTION COMMAND (REWIND)
    .WORD 0                            ;NOT USED

.SBTTL CKRAM - COMPARE RAM TO I/O PACKET
    
```



```

1211
1212
1213
1214 ;ROUTINE TO READ THE FIRST 8 BYTES FROM RAM
1215 ;MEMORY AND COMPARE THIS DATA TO A COMMAND PACKET.
1216
1217 ;INPUT:
1218
1219 ;        R4        ADDRESS OF THE COMMAND PACKET
1220 ;        R5        FIRST DEVICE UNIBUS ADDRESS
1221
1222 ;OUTPUT:
1223
1224 ;        CARRY     SET - RAM MATCHES PACKET
1225 ;        CLR       RAM DOES NOT MATCH PACKET
1226
1227 ;IMPLICIT OUTPUT:
1228
1229 ;        THE TABLE RAMDATA IS FILLED WITH THE
1230 ;        DATA HELD IN RAM.
1231 ;        RAMSIZ IS SET TO 8. FOR PRAMPKT ROUTINE
1232
1233 ;SIDE EFFECTS:
1234
1235 ;        THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE
1236
1237 ;-
1238
1239 010724    CKRAM::
1240 010724    SAVREG                            ;SAVE THE GENERAL REGISTERS
1241 010730    MOV        #RAMDATA,R1        ;ADDRESS TO SAVE THE RAM DATA
1242 010734    MOV        #RMPKTBEG,R2       ;BYTE ADDRESS OF FIRST RAM DATA
1243 010740    CLR        R3                    ;CLEAR THE ERROR FLAG
1244 010742    JSR        PC,CHKTSSR           ;WAIT FOR SSR
1245 010746    MOVB     #0,TSDB(R5)            ;SET MAINTENANCE MODE
1246 010754    JSR        PC,CHKTSSR           ;WAIT FOR SSR TO SET
1247 010760    MOV        R2,TSDB(R5)           ;SELECT NEXT RAM ADDRESS
1248 010764    JSR        PC,CHKTSSR           ;WAIT FOR SSR TO SET
1249 010770    MOVB     TSBA(R5),(R1)        ;READ THE RAM DATA
1250 010774    CMPB     (R1)*,(R4)*            ;COMPARE TO EXPECTED
1251 010776    BEQ        20$                    ;BRANCH IF OK
1252 011000    INC        R3                    ;SET ERROR FLAG
1253 011002    INC        R2                    ;ADDRESS OF NEXT RAM LOCATION
1254 011004    CMP        R2,#RMPKTEND        ;REACHED END YET ?
1255 011010    BLE        10$                    ;BRANCH TILL ALL READ
1256 011012    TST        R3                    ;WAS AN ERROR FOUND ?
1257 011014    BEQ        30$                    ;BRANCH IF NOT
1258 011016    CLC                            ;CLEAR CARRY TO SHOW ERROR
1259 011020    BR        50$                    ;AND EXIT
1260 011022    SEC                            ;SHOW GOOD COMPARE
1261 011024    MOV        #8.,RAMSIZ            ;SETUP RAMSIZ FOR PRAMPKT ROUTI
1262 011032    RTS        PC                    ;RETURN
1263
1264
1265            .SBTTL CKRAM2 - COMPARE RAM TO I/O CHARACTERISTICS DATA
1266
1267 ;

```

```

1268 ;ROUTINE TO READ THE FIRST 8 OR 10 BYTES FROM RAM
1269 ;MEMORY AND COMPARE THIS DATA TO A CHARACTERISTICS DATA BLOCK.
1270 ;
1271 ;INPUT:
1272 ;
1273 ; R4 ADDRESS OF THE CHARACTERISTICS DATA
1274 ; R5 FIRST DEVICE UNIBUS ADDRESS
1275 ;
1276 ;OUTPUT:
1277 ;
1278 ; CARRY SET - RAM MATCHES PACKET
1279 ; CLR - RAM DOES NOT MATCH PACKET
1280 ;
1281 ;IMPLICIT OUTPUT:
1282 ;
1283 ; THE TABLE RAMDATA IS FILLED WITH THE
1284 ; DATA HELD IN RAM.
1285 ; RAMSIZ IS SET TO 8. OR 10. FOR PRAMPKT ROUTINE
1286 ;
1287 ;SIDE EFFECTS:
1288 ;
1289 ; THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE
1290 ;
1291 ;-
1292
1293 CKRAM2::
1294 ; SAVREG ;SAVE THE GENERAL REGISTERS
1295 MOV #RAMDATA,R1 ;ADDRESS TO SAVE THE RAM DATA
1296 MOV #RMCHBEG,R2 ;BYTE ADDRESS OF FIRST RAM DATA
1297 CLR R3 ;CLEAR THE ERROR FLAG
1298 JSR PC,CHKTSSR ;WAIT FOR SSR
1299 MOVB #0,TSDB(R5) ;SET MAINTENANCE MODE
1300 JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
1301 MOV R2,TSDB(R5) ;SELECT NEXT RAM ADDRESS
1302 JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
1303 MOVB TSBA(R5),(R1) ;READ THE RAM DATA
1304 CMPB (R1)+,(R4)+ ;COMPARE TO EXPECTED
1305 BEQ 20$ ;BRANCH IF OK
1306 INC R3 ;SET ERROR FLAG
1307 INC R2 ;ADDRESS OF NEXT RAM LOCATION
1308 MOV #8.,RAMSIZ ;ASSUME EXTFEA NOT SET
1309 TST EXTFEA ;IS THE SOFTWARE EXTENDED FEATURES SET
1310 BEQ 25$ ;BR, IF NOT SET
1311 MOVB #10.,RAMSIZ ;SET RAMSIZ FOR EXTEND FEATURES
1312 CMP R2,#RMCHEND ;AT END OF EXTENDED BUFFER
1313 BLE 10$ ;BR, IF NOT AT END YET
1314 BR 27$ ;AT END BRANCH
1315 CMP R2,#RMCHEND-2 ;REACHED END YET ?
1316 BLE 10$ ;BRANCH TILL ALL READ
1317 TST R3 ;WAS AN ERROR FOUND ?
1318 BFC 30$ ;BRANCH IF NOT
1319 CLC ;CLEAR CARRY TO SHOW ERROR
1320 BR 50$ ;AND EXIT
1321 SEC ;SHOW GOOD COMPARE
1322 RTS PC ;RETURN
1323
1324

```

```

1325                                     .SBTTL CKMSG - COMPARE WRITE CHAR. MESSAGE BUFFERS
1326                                     ;+
1327                                     ;
1328                                     ;ROUTINE TO COMPARE A WRITE CHARACTERISTICS EXPD AND RECV
1329                                     ;BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
1330                                     ;ERROR PRINT ROUTINES.
1331                                     ;
1332                                     ;INPUT:
1333                                     ;
1334                                     ;       R0      RECV MESSAGE BUFFER HIGH ORDER ADDRESS
1335                                     ;       R1      RECV MESSAGE BUFFER LOW ORDER ADDRESS
1336                                     ;       R2      EXPD MESSAGE BUFFER ADDRESS
1337                                     ;OUTPUT:
1338                                     ;
1339                                     ;       CARRY   SET - MESSAGE BUFFERS MATCH
1340                                     ;               CLR -MESSAGE BUFFERS DON'T MATCH
1341                                     ;
1342                                     ;IMPLICIT OUTPUT:
1343                                     ;
1344                                     ;       EXPMSG   BUFFER IS SET TO EXPD DATA
1345                                     ;       RECMMSG   BUFFER IS SET TO RECV DATA
1346                                     ;       RCVHIADD  SET TO HIGH ORDER ADDRESS OF RECV
1347                                     ;       RCVLOADD  SET TO LOW ORDER ADDRESS OF RECV
1348                                     ;
1349                                     ;-
1350 011170 CKMSG::
1351 011170 SAVREG                                ;SAVE R1-R5 UNTIL NEXT RETURN
1352 011174 010037 002302' MOV R0,RCVHIADD ;SAVE RECV HIGH ADDRESS
1353 011200 010137 002304' MOV R1,RCVLOAD ;SAVE RECV LOW ADDRESS
1354 011204 005737 003132' TST KTENABLE ;TESTING ABOVE 28K?
1355 011210 001403 BEQ 10$ ;BR IF NO
1356 011212 004737 017130' JSR PC,SETMAP ;RETURN ADDRESS BIASED TO PAR6 IN R0
1357 011216 010001 MOV R0,R1 ;GET RETURNED ADDRESS BIASED TO PAR6
1358 011220 005004 10$: CLR R4 ;WORD IN BUFFER
1359 011222 005003 CLR R3 ;CLEAR ERROR SEEN FLAG
1360 011224 010205 MOV R2,R5 ;GET EXPD BUFFER ADDRESS
1361 011226 011264 002320' 15$: MOV (R2),EXPMSG(R4) ;SAVE EXPD FOR ERROR REPORT
1362 011232 011164 002464' MOV (R1),RECMMSG(R4) ;SAVE RECV FOR ERROR REPORT
1363 011236 022221 CMP (R2)+,(R1)+ ;EXPD EQUAL RECV?
1364 011240 001401 BEQ 25$ ;BR IF YES
1365 011242 005203 INC R3 ;SET ERROR SEEN FLAG
1366 011244 062704 000002 25$: ADD #2,R4 ;POINT TO NEXT WORD ADDRESS
1367 011250 020427 000014 CMP R4,#14 ;DONE FIRS; 7 WORDS?
1368 011254 003764 BLE 15$ ;BR IF NO
1369 011256 032765 000200 000012 BIT #X2.EXTF,XST2(R5) ;IS EXTENDED FEATURES SET IN EXPD?
1370 011264 001403 BEQ 50$ ;BR IF NO
1371 011266 020427 000016 CMP R4,#16 ;DONE EXTENDED FEATURES WORD?
1372 011272 003755 BLE 15$ ;BR IF NO
1373 011274 005703 50$: TST R3 ;ANY ERRORS SEEN?
1374 011276 001402 BEQ 55$ ;BR IF NO
1375 011300 000241 CLC ;SET FAILURE
1376 011302 000401 BR 60$ ;
1377 011304 000261 55$: SEC ;SET SUCCESS
1378 011306 000207 60$: RTS PC ;RETURN
1379
1380
1381                                     .SBTTL CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS

```

```

1382          ;*
1383          ;
1384          ;ROUTINE TO COMPARE AN EXPECTED AND RECEIVED MESSAGE
1385          ;BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
1386          ;ERROR PRINT ROUTINES.
1387          ;
1388          ;INPUT:
1389          ;
1390          ;      R0      RECV MESSAGE BUFFER HIGH ORDER ADDRESS
1391          ;      R1      RECV MESSAGE BUFFER LOW ORDER ADDRESS
1392          ;      R2      EXPD MESSAGE BUFFER ADDRESS
1393          ;      R3      NUMBER OF BYTES TO COMPARE
1394          ;
1395          ;OUTPUT:
1396          ;
1397          ;      CARRY   SET - MESSAGE BUFFERS MATCH
1398          ;              CLR - MESSAGE BUFFERS DON'T MATCH
1399          ;
1400          ;IMPLICIT OUTPUT:
1401          ;
1402          ;      EXPMSG   BUFFER IS SET TO EXPD DATA
1403          ;      RECMSG   BUFFER IS SET TO RECV DATA
1404          ;      RCVHIADD SET TO HIGH ORDER ADDRESS OF RECV
1405          ;      RCVLOADD SET TO LOW ORDER ADDRESS OF RECV
1406          ;
1407          ;-
1408          CKMSG2::
1409          SAVREG          ;SAVE R1-R5 UNTIL NEXT RETURN
1410          CMP            R3,#RECVMSG-EXPMSG,#60 IS COUNT ABOVE MAX ALLOWED?
1411          BLE            5# ;60 BR IF NO
1412          MOV            #RECVMSG-EXPMSG,R3;60
1413          PRINTF         #DEBUGMSG ;60
1414          MOV            #DEBUGMSG,-(SP)
1415          MOV            #1,-(SP)
1416          MOV            SP,R0
1417          TRAP          C#PNTF
1418          ADD            #4,SP
1419          MOV            R0,RCVHIADD ;SAVE RECV HIGH ADDRESS
1420          MOV            R1,RCVLOADD ;SAVE RECV LOW ADDRESS
1421          TST            KTNABLE ;TESTING ABOVE 28K?
1422          BEQ            10# ;BR IF NO
1423          JSR            PC,SETMAP ;RETURN ADDRESS BIASED TO PAR6 IN R0
1424          MOV            R0,R1 ;GET RETURNED ADDRESS BIASED TO PAR6
1425          CLR            R4 ;WORD IN BUFFER
1426          CLR            R5 ;CLEAR ERROR SEEN FLAG
1427          MOVB           (R2),EXPMSG(R4) ;SAVE EXPD FOR ERROR REPORT
1428          MOVB           (R1),RECVMSG(R4) ;SAVE RECV FOR ERROR REPORT
1429          CMPEB          (R2)+,(R1)+ ;EXPD EQUAL RECV?
1430          BEQ            25# ;BR IF YES
1431          INC            R5 ;SET ERROR SEEN FLAG
1432          ADD            #1,R4 ;POINT TO NEXT BYTE
1433          CMP            R4,R3 ;DONE ALL BYTES?
1434          BGE            50# ;BR IF YES
1435          BR             15# ;DO NEXT BYTE
1436          TST            R5 ;ANY ERRORS SEEN?
1437          BEQ            55# ;BR IF NO
1438          CLC            ;SET FAILURE
    
```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 CKMSG2 COMPARE EXPD RECV MESSAGE BUFFERS

SEQ 053

```

1434 011434 000401          BR      601          ;
1435 011436 000261          55: SEC          ;SET SUCCESS
1436 011440 000207          60: RTS      PC          ;RETURN
1437
1438 011442      120      122      117 DEBUGMSG: .ASCIZ 'PROGRAM INTERNAL ERROR -CKMSG2 MESSAGE BUFFER EXCEEDED ' ;000
1439 011532      045      116      045 FERCM: .ASCII /RNSA ***/
1440 011543      040      040      124 ERCM: .ASCIZ / TSSR ERROR CODE REC'D . /
1441 011576      056      056      056 SIMSG: .ASCIZ /.... AFTER DOING SOFT INIT/
1442 011631      124      105      123 TINERR: .ASCIZ /TEST: .../
1443
1444
1445
1446
1447
1448 ;
1449 ;PRINT ROUTINE TO FATAL SOFT INIT ERRORS
1450 ;
1451 ;INPUT:
1452 ;
1453 ;      R1      CONTENTS OF TSSR AT ERROR
1454 ;
1455 ;SIDE EFFECTS:
1456 ;
1457 ;      EXECUTES DROP UNIT TO CEASE TESTING
1458 ;
1459 ;-
1460
1461 011644          BGNMSG SFIMSG
1462 011644          SFIMSG:: JSR      PC,PRITSSR      ;PRINT CONTENTS OF TSSR REGISTER
1463 011654          JSR      PC,CKDROP      ;DROP UNIT, IF ALLOWED
1464 011654          ENDMMSG
1465 011654          L10003: TRAP      CMSG
1466
1467 ;
1468 ;PRINT ROUTINE TO PRINT THE CONTENTS OF
1469 ;TSSR AND A COMMAND PACKET OTHER THAN GET STATUS COMMAND PACKET.
1470 ;
1471 ;INPUTS:
1472 ;
1473 ;      R1      TSSR CONTENTS
1474 ;      R4      ADDRESS OF COMMAND PACKET
1475 ;
1476 ;-
1477 011656          BGNMSG PKTSSR
1478 011656          PKTSSR:: JSR      PC,PRITSSR      ;PRINT THE CONTENTS OF TSSR REGISTER
1479 011662          MOV      #4,R0      ;NO. OF WORDS IN PACKET
1480 011666          JSR      PC,PRIPKT      ;PRINT THE CONTENTS OF COMMAND PACKET
1481 011672          ENDMMSG
1482 011672          L10004: TRAP      CMSG
1483
1484 ;
1485 ;PRINT ROUTINE TO PRINT THE CONTENTS OF
1486 ;TSSR AND A GET STATUS COMMAND PACKET.

```

```

1485
1486      ; INPUTS:
1487      ;
1488      ;     R1     TSSR CONTENTS
1489      ;     R4     ADDRESS OF COMMAND PACKET
1490      ;
1491      ;-
1492
1493      BGNMSG  PKTGETS
1494      PKTGETS:
1495      JSR     PC,PRITSSR      ;PRINT THE CONTENTS OF TSSR REGISTER
1496      MOV     @2,R0          ;NO. OF WORDS IN GET STATUS PACKET
1497      JSR     PC,PRIPKT      ;PRINT THE CONTENTS OF COMMAND PACKET
1498      ENDMMSG
1499
1500      L10005:
1501      TRAP    CMSG
1502
1503      ;*
1504      ;PRINT TSSR ERRORS FOR INITIALIZATION TESTS
1505      ;
1506      ; INPUTS:
1507      ;
1508      ;     R1     TSSR CONTENTS
1509      ;     R4     ADDRESS OF COMMAND PACKET
1510      ;
1511      ;-
1512      BGNMSG  SFFMSG
1513      SFFMSG:
1514      JSR     PC,PRITSSR      ;PRINT CONTENTS OF TSSR REGISTER
1515      ENDMMSG
1516      L10006:
1517      TRAP    CMSG
1518
1519      .SBTTL  PKTMES  - PRINT TSSR AND MESSAGE BUFFER
1520      ;*
1521      ;PRINT ROUTINE TO PRINT THE CONTENTS OF TSSR AND MESSAGE
1522      ;BUFFER FOR ERROR REPORTS
1523      ;
1524      ; INPUTS:
1525      ;
1526      ;     R1     CONTENTS OF TSSR
1527      ;     R2     LOW ORDER MESSAGE BUFFER
1528      ;     R3     HIGH ORDER MESSAGE BUFFER ADDRESS
1529      ;     NOTE: R3 IS IGNORED IF KTENABLE FLAG IS CLEAR
1530      ;-
1531      BGNMSG  PKTMES
1532      PKTMES:
1533      JSR     PC,PRITSSR      ;PRINT CONTENTS OF TSSR
1534      MOV     R2,R0          ;LOW ORDER ADDRESS
1535      MOV     R3,R1          ;HIGH ORDER ADDRESS
1536      JSR     PC,PRMESS      ;PRINT THE MESSAGE BUFFER
1537      ENDMMSG
1538      L10007:
1539      TRAP    CMSG

```

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 1547 011736
 011736
 1548 011736 004737 010104'
 1549 011742 016501 0G0002
 1550 011746 004737 005634'
 1551 011752
 011752
 011752 104423
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 1553
 1554
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 1564
 1565
 1566 011754
 011754
 1567 011754 012700 000007
 1568 011760 005737 002224'
 1569 011764 001402
 1570 011766 012700 000010
 1571 011772 004737 014362'
 1572 011776
 011776
 011776 104423
 1573
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 1575
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```

        .SBTTL  ADDSSR  - PRINT TEST ADDRESS AND TSSR
;
;PRINT ROUTINE TO PRINT THE CONTENTS OF
;TSSR AND A MEMORY TEST ADDRESS
;
;INPUTS:
;
;      R5      FIRST DEVICE UNIBUS ADDRESS
;      ERRHI   HIGH ORDER MEMORY TEST ADDRESS
;      ERRLO   LOW ORDER MEMORY TEST ADDRESS
;
;
        BGNMSG  ADDSSR
ADDSSR::
        JSR    PC,PRITADD      ;PRINT MEMORY TEST ADDRESS
        MOV    TSSR(R5),R1     ;GET CURRENT TSSR
        JSR    PC,PRITSSR     ;PRINT THE CONTENTS OF TSSR REGISTER
        ENDMSG
L10010:
        TRAP   C:MSG
;
        .SBTTL  MSGEXP - PRINT WRITE CHAR. EXPD-RCV MESSAGE BUFFERS
;
;PRINT ROUTINE TO PRINT WRITE CHARACTERISTIC MESSAGE BUFFER
;
;IMPLICIT INPUTS:
;
;      EXPMSG  - EXPECTED MESSAGE BUFFER
;      RECMG   - RECEIVED MESSAGE BUFFER
;      RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
;      RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
;
;
        BGNMSG  MSGEXP
MSGEXP::
        MOV    #7,R0           ;ASSUME NO EXT FEATURES
        TST    EXTFEA         ;EXT FEATURES SET?
        BEQ    S:             ;BR IF NO
        MOV    #8,R0         ;EXT FEATURE BUFFER IS 8 WORDS
        JSR    PC,PRMSGEXP    ;PRINT EXPD/RCV MESSAGE BUFFERS
        ENDMSG
S:
L10011:
        TRAP   C:MSG
;
        .SBTTL  FIFEXP  - PRINT FIFO EXP/RCV DATA
;
;PRINT ROUTINE TO PRINT FIFO EXP/RCV DATA
;
;      R1      - BYTE COUNT
;
;IMPLICIT INPUTS:
;
;      EXPMSG  - EXPECTED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY
    
```

TSV3 GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 FIFEXP PRINT FIFO EXP/RECV DATA

SEQ 056

```

1584          |      RECMGX - RECEIVED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY)
1585          |:-
1586 012000      BGNMSG FIFEXP
1587 012000      FIFEXP::
          |      PRINTX #FIF1MSG,R1      ;PRINT BYTES TRANSFERRED
          |      MOV R1,-(SP)
          |      MOV #FIF1MSG,-(SP)
          |      MOV #2,-(SP)
          |      MOV SP,RO
          |      TRAP C#PNTX
          |      ADD #6,SP
1588 012022      PRINTX #FIF2MSG      ;PRINT HEADER MSG
          |      MOV #FIF2MSG,-(SP)
          |      MOV #1,-(SP)
          |      MOV SP,RO
          |      TRAP C#PNTX
          |      ADD #4,SP
1589 012042      MOV R1,RO      ;GET BYTE COUNT
1590 012044      JSR PC,PRBYTEXP ;PRINT FIFO BYTES IN ERROR
1591 012050      ENDMSG
          |
          |      L10012:
          |      TRAP C#MSG
1592 012052      .ASCIZ 'NUMBER OF BYTES TRANSFERRED = #D2
1593 012121      .ASCIZ 'NUMBER FIFO DATA BYTES IN ERROR:'
1594          |      .EVEN
1595          |
1596          |      .SBTTL MSGSTAT - PRINT STATUS HEADER AND MESSAGE BUFFERS
1597          |
1598          |      ;*
1599          |      ;PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RECV
1600          |
1601          |      ;
1602          |      ;IMPLICIT INPUTS:
1603          |
1604          |      ;      EXPMSG - EXPECTED MESSAGE BUFFER
1605          |      ;      RECMGX - RECEIVED MESSAGE BUFFER
1606          |      ;      RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1607          |      ;      RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1608          |      ;:-
1609 012160      BGNMSG MSGSTAT
          |      MSGSTAT::
1610 012160      MOV #STATCOD,R1      ;ASCII ADDRESS TABLE
1611 012164      MOV (R1)+,RO      ;DONE ALL MSG LINES?
1612 012166      BEQ 20$          ;BR IF YES
1613 012170      PRINTX RO      ;PRINT STATUS BIT NAMES
          |      MOV RO,-(SP)
          |      MOV #1,-(SP)
          |      MOV SP,RO
          |      TRAP C#PNTX
          |      ADD #4,SP
1614 012202      BR 10$          ;DO ANOTHER MSG LINE
1615 012210      MOV #10,RO      ;NUMBER OF WORDS IN A READ STATUS BUFFER
1616 012214      JSR PC,PRMSGEXP ;PRINT EXPD/RECV MESSAGE BUFFERS
1617 012220      ENDMSG
          |
          |      L10013:
          |      TRAP C#MSG
1618 012220      .ASCIZ 'NUMBER OF WORDS IN A READ STATUS BUFFER'
          |      .ASCIZ 'PRINT EXPD/RECV MESSAGE BUFFERS'

```



```

1619 012222 012240' 012302' 012373' STATCOD: .WORD 1#,2#,3#,4#,5#,6#,0
1620 012240 045 116 045 1#:.ASCIZ 'MNSA Tape Bus Signals in Word #8:'
1621 012302 045 116 045 2#:.ASCIZ 'MNSA PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>'
1622 012373 045 116 045 3#:.ASCIZ 'MNSA IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>'
1623 012464 045 116 045 4#:.ASCIZ 'MNSA IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>'
1624 012555 045 116 045 5#:.ASCIZ 'MNSA Tape Bus Signals in Word #9:'
1625 012617 045 116 045 6#:.ASCIZ 'MNSA DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>'
1626 .EVEN
1627
1628
1629

```

```

1630 .SBTTL MSGLOOP - PRINT LOOPBACK HEADER AND MESSAGE BUFFERS
1631

```

```

1632 ;*
1633 ;PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
1634 ;
1635 ;IMPLICIT INPUTS:
1636 ;
1637 ; EXPMSG - EXPECTED MESSAGE BUFFER
1638 ; RCVMSG - RECEIVED MESSAGE BUFFER
1639 ; RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1640 ; RCVLOAD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1641 ;-
1642 012674 BGNMSG MSGLOOP
012674 MSGLOOP:
1643 012674 012701 012736' MOV #LOOPCOD,R1 ;ASCII ADDRESS TABLE
1644 012700 012100 10#: MOV (R1),R0 ;DONE ALL MSG LINES?
1645 012702 001410 BEQ 20# ;BR IF YES
1646 012704 PRINTX R0 ;PRINT STATUS BIT NAMES
012704 010046 MOV R0,-(SP)
012706 012746 000001 MOV #1,-(SP)
012712 010600 MOV SP,R0
012714 104415 TRAP C#PNTX
012716 062706 000004 ADD #4,SP
1647 012722 000766 BR 10# ;DO ANOTHER MSG LINE
1648 012724 012700 000012 20#: MOV #10,R0 ;NUMBER OF WORDS IN A READ STATUS BUFFER
1649 012730 004737 014362' JSR PC,PRMSGEXP ;PRINT EXPD/RCV MESSAGE BUFFERS
1650 012734 ENDMMSG
012734 L10014:
012734 104423 TRAP C#MSG

```

```

1651
1652 012736 012756' 013031' 013130' LOOPCOD: .WORD 1#,2#,3#,4#,5#,6#,7#,0
1653 012756 045 116 045 1#:.ASCIZ 'MNSA Tape Bus Loopback Signals in Word #8:'
1654 013031 045 116 045 2#:.ASCIZ 'MNSA PARERR<15> IRESV2<14> IRESV1<13>'
1655 013130 045 116 045 3#:.ASCIZ 'MNSA IHISP=>IEOT<12> IWRT=>IIDENT<11> IREV =>ICER <10>'
1656 013227 045 116 045 4#:.ASCIZ 'MNSA IWFM =>IFMK<09> IEDIT=>IHER <08> IFAD =>ISPEED<07>'
1657 013326 045 116 045 5#:.ASCIZ 'MNSA ITADO=>IRDY<06> ITAD1=>IONL <05> IERASE=>ILDP <04>'
1658 013425 045 116 045 6#:.ASCIZ 'MNSA IREW =>IDBY<03> IRWU =>IRWD <02> IFEN =>IFBY <01>'
1659 013524 045 116 045 7#:.ASCIZ 'MNSA IGO =>IFPT<00>'
1660 .EVEN
1661

```

```

1662 .SBTTL MSGSUB - PRINT WRITE SUBSYSTEM MESSAGE BUFFER
1663

```

```

1664 ;*
1665 ;PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
1666 ;
1667 ;

```

```

1668 ;IMPLICIT INPUTS:
1669 ;
1670 ;        EXPMSG - EXPECTED MESSAGE BUFFER
1671 ;        RECMMSG - RECEIVED MESSAGE BUFFER
1672 ;        RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1673 ;        RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1674 ; -
1675 013552                    BGNMSG    MSGSUB
         013552
1676 013552    012700    000012
1677 013556    004737    014362'
1678 013562
         013562
         013562    104423
1679
1680
1681
1682
1683
1684                    .SBTTL    MEMADD - PRINT MEMORY ADDRESS DATA ERROR
1685 ;*
1686 ;
1687 ;PRINT ROUTINE TO PRINT MEMORY ADDRESS DATA COMPARE ERROR
1688 ;
1689 ;IMPLICIT INPUTS:
1690 ;
1691 ;        ERRHI    - MEMORY ERROR HIGH ORDER ADDRESS
1692 ;        ERRLO    - MEMORY ERROR LOW ORDER ADDRESS
1693 ;        EXP        - EXPECTED DATA
1694 ;        RECV      - RECEIVED DATA
1695 ; -
1696 013564                    BGNMSG    MEMADD
         013564
1697 013564    004737    007770'
1698 013570    013701    002230'
1699 013574    013702    002232'
1700 013600    004737    007552'
1701 013604
         013604
         013604    104423
1702
1703                    .SBTTL    PRAMPKT - PRINT RAM AND PACKET DATA
1704 ;*
1705 ;
1706 ;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
1707 ;WHEN THE RAM DATA DOES NOT MATCH.
1708 ;
1709 ;INPUTS:
1710 ;
1711 ;        R4        POINTER TO COMMAND PACKET
1712 ;
1713 ;IMPLICIT INPUTS:
1714 ;
1715 ;        RAMDATA    DATA AS READ FROM THE RAM
1716 ;        RAMSIZ    NUMBER OF BYTES IN PACKET
1717 ;                    IF RAMSIZ=0 THEN DEFAULT TO 6.
1718 ;

```

```

1719                                     ;IMPLICIT OUTPUTS:
1720                                     ;
1721                                     ;     RAMSIZ  SET TO 0
1722                                     ;-
1723
1724 013606                                PRAMPKT:
1725 013606                                SAVREG
1726 013612 012701 002240'                MOV     #RAMDATA,R1          ;SAVE R1-R5 UNTIL NEXT RETURN
1727 013616 005002                        CLR     R2                   ;DATA FROM THE RAM
1728 013620 122124                        5#:    CMPB  (R1)+,(R4)+      ;INIT BYTE NUMBER
1729 013622 001005                        BNE    7#                    ;COMPARE EXPECTED, RECEIVED
1730 013624                                FORCERROR 7#,NOTSSR         ;BR IF NO MATCH
1731 013634 000436                        BR     10#                   ;BBD
1732 013636 116105 177777                7#:    MOVB  -1(R1),R5        ;GET RECV RAM DATA
1733 013642 116403 177777                MOVB  -1(R4),R3            ;GET EXPD PACKET DATA
1734 013646                                XOR    R5,R3                ;XOR EXPD/RECV
1735 013656 042703 177400                BIC   #177400,R3          ;LOW BYTE ONLY
1736 013662 116137 177777 002232'      MOVB  -1(R1),RECV         ;GET RECEIVED RAM DATA
1737 013670 116437 177777 002230'      MOVB  -1(R4),EXPD         ;GET EXPECTED RAM DATA
1738 013676                                PRINTB #RAMASC,R2,RECV,EXPD,R3
1739 013676 010346                        MOV    R3,-(SP)
1740 013700 013746 002230'                MOV    EXPD,-(SP)
1741 013704 013746 002232'                MOV    RECV,-(SP)
1742 013710 010246                        MOV    R2,-(SP)
1743 013712 012746 013766'                MOV    #RAMASC,-(SP)
1744 013716 012746 000005                MOV    #5,-(SP)
1745 013722 010600                        MOV    SP,R0
1746 013724 104414                        TRAP  C#PNTB
1747 013726 062706 000014                ADD    #14,SP
1748 013732 005202                        10#:   INC    R2              ;UPDATE BYTE COUNT
1749 013734 005737 002300'                TST   RAMSIZ              ;DEFAULT TO 8.?
1750 013740 001404                        BEQ   15#                  ;BR IF YES
1751 013742 020237 002300'                CMP   R2,RAMSIZ           ;DONE ALL BYTES?
1752 013746 003724                        BLE   5#                    ;BR IF NO
1753 013750 000403                        BR    25#                  ;
1754 013752 020227 000010                15#:   CMP   R2,#8          ;DONE DEFAULT NUMBER OF BYTES?
1755 013756 002720                        20#:   BLT   5#              ;BR IF NO
1756 013760 005037 002300'                25#:   CLR   RAMSIZ        ;SET DEFAULT RAMSIZ
1757 013764 000207                        RTS    PC                  ;RETURN
1758
1759 013766 045 116 045 RAMASC: .ASCIZ '##A BYTE; #02##A RAM: #03##A Packet: #03##A XOR:#03'
1760 .EVEN
1761
1762                                     .SBTTL PRMESS - PRINT CONTENTS OF MESSAGE BUFFER
1763
1764                                     ;*
1765                                     ;
1766                                     ;THIS ROUTINE PRINTS THE CONTENTS OF
1767                                     ;THE 7 OR 8 WORD MESSAGE BUFFER RETURNED BY THE
1768                                     ;TSV-05.
1769                                     ;
1770                                     ;INPUT:
1771                                     ;
1772                                     ;     R0     LOW ORDER ADDRESS OF MESSAGE BUFFER
1773                                     ;     R1     HIGH ORDER ADDRESS OF MESSAGE BUFFER
1774                                     ;     NOTE: R1 IS IGNORED IF KTENABLE FLAG IS CLEAR
1775                                     ;
1776                                     ;THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE
    
```

```

1767
1768
1769
1770 014052          PRMESS:
1771 014052          SAVREG
1772 014056 010005  MOV      RO,R5          ;SAVE THE REGISTERS
1773 014060 005737 003132' TST      KTENABLE        ;SAVE LOW ORDER ADDRESS
1774 014064 001001          BNE      10$            ;ADDRESS ABOVE 28K?
1775 014066 005001          CLR      R1             ;BR IF YES
1776 014070 010103 10$:  MOV      R1,R3          ;SET HIGH ORDER ADDRESS TO 0
1777 014072 006100          ROL      RO             ;SAVE HIGH ORDER ADDRESS
1778 014074 006101          ROL      R1             ;SHIFT BIT15 TO C BIT
1779 014076          PRINTX  #PROASC,R1,R5 ;SHIFT TO HIGH ORDER FOR PRINTOUT
1779 014076          MOV      R5,-(SP) ;PRINT MESSAGE BUFFER ADDRESS
1779 014100 010146          MOV      R1,-(SP)
1779 014102 012746 014230'  MOV      #PROASC,-(SP)
1779 014106 012746 000003  MOV      #3,-(SP)
1779 014112 010600          MOV      SP,RO
1779 014114 104415          TRAP    C$PNTX
1779 014116 062706 000010  ADD      #10,SP
1780 014122          PRINTX  #PRIASC          ;PRINT HEADER FOR CONTENTS
1780 014122 012746 014275'  MOV      #PRIASC,-(SP)
1780 014126 012746 000001  MOV      #1,-(SP)
1780 014132 010600          MOV      SP,RO
1780 014134 104415          TRAP    C$PNTX
1780 014136 062706 000004  ADD      #4,SP
1781 014142 005004          CLR      R4
1782 014144 010501          MOV      R5,R1          ;NUMBER OF THE NEXT WORD
1783 014146 010300          MOV      R3,RO          ;COPY LOW ORDER ADDRESS
1784 014150 001403          BEQ     20$            ;COPY HIGH ORDER ADDRESS
1785 014152 004737 017130'  JSR     PC,SETMAP      ;BR IF NOT ABOVE 28K
1786 014156 010005          MOV      RO,R5          ;SETUP PAR ADDRESS IN RO
1787 014160          PRINTX  #PRASC,R4,(R5)+ ;GET PAR FORMAT ADDRESS ABOVE 28K
1787 014160 012546          MOV      (R5),-(SP)    ;PRINT THE CONTENTS OF MEMORY BUFFER
1787 014162 010446          MOV      R4,-(SP)
1787 014164 012746 014333'  MOV      #PRASC,-(SP)
1787 014170 012746 000003  MOV      #3,-(SP)
1787 014174 010600          MOV      SP,RO
1787 014176 104415          TRAP    C$PNTX
1787 014200 062706 000010  ADD      #10,SP
1788 014204 005204          INC      R4
1789 014206 020427 000007  CMP     R4,#7          ;NUMBER OF THE NEXT
1790 014212 003005          BGT     50$            ;DONE ALL YET ?
1791 014214 002761          BLT     20$            ;BRANCH IF ALL DONE
1792 014216 032763 000200 000012  BIT     #X2.EXTF,XST2(R3);PRINT FIRST 7 WORDS
1793 014224 001355          BNE     20$            ;EXTENDED FEATUTES ON ?
1794 014226 000207          RTS     PC             ;PRINT EXTENDED STATUS WORD
1795
1796 014230 045 116 045 PROASC: .ASCIZ '##A Message Buffer Address - #01#05'
1797 014275 045 116 045 PRIASC: .ASCIZ '##A Message Buffer Contents:'
1798 014333 045 116 045 PRASC: .ASCIZ '##A Word#D1#A: #0'
1799
1800
1801          .SBTTL  PRMSGEXP - PRINT EXPD/RECV MESSAGE BUFFERS
1802
1803          ;
1804          ;ROUTINE TO PRINT EXPECTED AND RECEIVED MESSAGE BUFFERS
    
```

```

1805 ;
1806 ; RO - NUMBER OF WORDS IN BUFFER
1807 ;
1808 ;IMPLICIT INPUTS:
1809 ;
1810 ; EXPMSG - EXPECTED MESSAGE BUFFER
1811 ; RECMMSG - RECEIVED MESSAGE BUFFER
1812 ; RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1813 ; RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1814 ;
1815 PRMSGEXP::
1816 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1817 MOV RO,R5 ;SAVE NUMBER OF WORDS
1818 MOV RCVLOADD,RO ;GET RCV LOW ADDRESS
1819 MOV RO,R4 ;COPY LOW ADDRESS
1820 MOV RCVHIADD,R1 ;GET RCV HIGH ADDRESS
1821 ROL RO ;SHIFT BIT15 TO C BIT
1822 ROL R1 ;SHIFT TO HIGH ORDER FOR PRINTOUT
1823 PRINTX #PRMSG0,R1,R4 ;PRINT MESSAGE BUFFER ADDRESS
1824 MOV R4,-(SP)
1825 MOV R1,-(SP)
1826 MOV #PRMSG0,-(SP)
1827 MOV #3,-(SP)
1828 MOV SP,RO
1829 TRAP C:PNTX
1830 ADD #10,SP
1831 PRINTX #PRMSG1 ;PRINT HEADER FOR CONTENTS
1832 MOV #PRMSG1,-(SP)
1833 MOV #1,-(SP)
1834 MOV SP,RO
1835 TRAP C:PNTX
1836 ADD #4,SP
1837 CLR R4 ;NUMBER OF THE CURRENT WORD
1838 MOV #EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
1839 MOV #RECMMSG,R2 ;GET RCV BUFFER ADDRESS
1840 20$: MOV (R1),RO ;GET EXPD
1841 MOV (R2),R3 ;GET RCV
1842 XOR RO,R3 ;XOR EXPD/RCV
1843 PRINTX #PRMSG2,R4,(R1)+,(R2)+,R3
1844 MOV R3,-(SP)
1845 MOV (R2)+,-(SP)
1846 MOV (R1)+,-(SP)
1847 MOV R4,-(SP)
1848 MOV #PRMSG2,-(SP)
1849 MOV #5,-(SP)
1850 MOV SP,RO
1851 TRAP C:PNTX
1852 ADD #14,SP
1853 INC R4 ;NUMBER OF THE NEXT
1854 CMP R4,R5 ;DONE ALL YET?
1855 BGE 50$ ;BR IF YES
1856 BR 20$ ;DO ANOTHER
1857 50$: RTS PC ;RETURN
1858 045 PRMSG0: .ASCIZ '##A Message Buffer Address = #01#05'
1859 045 PRMSG1: .ASCIZ '##A Message Buffer Contents:'
1860 045 PRMSG2: .ASCIZ '##A WORD #D2#A EXPD: #06#A RCV: #06#A XOR: #06#A'
    
```

```

1841 .EVEN
1842 .SBTTL PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER
1843
1844 ;*
1845 ;
1846 ;ROUTINE TO PRINT ERROR BYTES IN MESSAGE BUFFERS
1847 ; ONLY THE FIRST 8 ERRORS ENCOUNTERED ARE PRINTED DUE TO SCREEN SPACE
1848 ;
1849 ; RO - NUMBER OF BYTES IN BUFFER
1850 ;
1851 ;IMPLICIT INPUTS:
1852 ;
1853 ; EXPMSG - EXPECTED MESSAGE BUFFER
1854 ; RECMG - RECEIVED MESSAGE BUFFER
1855 ;
1856 014732 PRBYTEXP::
1857 014732 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1858 014736 010005 MOV RO,R5 ;SAVE NUMBER OF BYTES
1859 014740 005037 002316' CLR PRMNO ;INIT ERROR COUNT
1860 014744 005004 CLR R4 ;NUMBER OF THE CURRENT BYTE
1861 014746 012701 002320' MOV #EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
1862 014752 012702 002464' MOV #RCMSG,R2 ;GET RCV BUFFER ADDRESS
1863 014756 111100 20$: MOVB (R1),RO ;GET EXPD BYTE
1864 014760 042700 177400 BIC #C<377>,RO ;CLEAR UPPER BYTE
1865 014764 110037 015300' MOVB RO,PRBEXP ;SAVE FOR ERROR REPORT
1866 014770 111203 MOVB (R2),R3 ;GET RCV BYTE
1867 014772 042703 177400 BIC #C<377>,R3 ;CLEAR UPPER BYTE
1868 014776 110337 015302' MOVB R3,PRBREC ;FOR ERROR REPORT
1869 015002 XOR RO,R3 ;XOR EXPD/RCV
1870 015012 122122 CMPB (R1)+,(R2)+ ;EXPD = RCV?
1871 015014 001431 BEQ 30$ ;BR IF YES
1872 015016 005237 002316' INC PRMNO ;UPDATE ERROR COUNT
1873 015022 023727 002316' 000010 CMP PRMNO,#8. ;PRINTED 8?
1874 015030 101023 BHI 30$ ;BR IF YES
1875 015032 27$: PRINTX #PRMSG,R4,PRBEXP,PRBREC,R3
015032 010346 MOV R3,-(SP)
015034 013746 015302' MOV PRBREC,-(SP)
015040 013746 015300' MOV PRBEXP,-(SP)
015044 010446 MOV R4,-(SP)
015046 012746 015146' MOV #PRMSG,-(SP)
015052 012746 000005 MOV #5,-(SP)
015056 010600 MOV SP,RO
015060 104415 TRAP C:PNTX
015062 062706 000014 ADD #14,SP
1876 015066 FORCEEXIT 50$ ;$D
1877 015076 000404 BR 35$ ;$D
1878 015100 30$: FORCERRR 27$,NOTSSR ;$D
1879 015100 35$: ;$D
1880 015110 ;$D
1881 015110 005204 INC R4 ;NUMBER OF THE NEXT
1882 015112 020405 CMP R4,R5 ;DONE ALL YET?
1883 015114 002001 BGE 50$ ;BR IF YES
1884 015116 000717 BR 20$ ;DO ANOTHER
1885 015120 50$: PRINTX #PRBTOT,PRMNO ;PRINT TOTAL ERROR COUNT
015120 013746 002316' MOV PRMNO,-(SP)
015124 012746 015233' MOV #PRBTOT,-(SP)
015130 012746 000002 MOV #2,-(SP)

```

TSV3 GLOBAL AREAS MACRO M1113 07-FEB 84 10:58
PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER

```

015134 010600      MOV      SP,R0
015136 104415      TRAP    C#PNTX
015140 062706      ADD     #6,SP
1886 015144 000207      RTS     PC           ;RETURN
1887
1888 015146      045      116      045  PRBMSG: .ASCIZ '##N#A BYTE #D2#A EXPD: #03#A RECV: #03#A XOR: #03#
1889 015233      045      116      045  PRBTOT: .ASCIZ '##N#A NUMBER OF BYTES IN ERROR = #D2'
1890
1891 015300 000000      PRBEXP: .WORD 0           ;EXPD
1892 015302 000000      PRBREC: .WORD 0           ;RECV
1893
1894                      .SBTTL  EXPREC - PRINT EXPD/RECV WORD DATA
1895                      ;*
1896                      ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
1897                      ;
1898                      ;INPUTS:
1899                      ;
1900                      ;
1901                      ;      R1      RECEIVED DATA
1902                      ;      R2      EXPECTED DATA
1903                      ;
1904                      ;-
1905
1906 015304      BGNMSG  EXPREC
015304      EXPREC:: JSR     PC,PRI XOR           ;PRINT THE DATA
1907 015304 004737 007552'  ENDMSG
1908 015310      L10017: TRAP    C#MSG
015310      104423
1909
1910
1911
1912                      .SBTTL  EXPBREC - PRINT EXPD/RECV BYTE DATA
1913                      ;*
1914                      ;PRINT ROUTINE TO DISPLAY BYTE EXPD/RECV DATA
1915                      ;
1916                      ;INPUTS:
1917                      ;
1918                      ;
1919                      ;      R1      RECEIVED DATA BYTE
1920                      ;      R2      EXPECTED DATA BYTE
1921                      ;
1922                      ;-
1923
1924
1925
1926 015312      BGNMSG  EXPBREC
015312      EXPBREC:: JSR     PC,PRI BXOR           ;PRINT THE DATA
1927 015312 004737 007422'  ENDMSG
1928 015316      L10020: TRAP    C#MSG
015316      104423
1929
1930
1931
1932                      .SBTTL  RAMERR - PRINT RAM AND PACKET DATA
1933

```

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 1953 015320
 015320
 1954 015320 004737 013606'
 1955 015324
 015324
 015324 104423
 1956
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 1963
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 1965
 1966
 1967
 1968
 1969
 1970
 1971
 1972
 1973
 1974
 1975
 1976
 1977
 1978
 1979
 1980 015326
 015326
 1981 015326 004737 010104'
 1982 015332 004737 013606'
 1983 015336
 015336
 015336 104423
 1984

```

; *
;
; PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
;
; INPUTS:
;
;     R4     POINTER TO COMMAND PACKET
;
; IMPLICIT INPUTS:
;
;     RAMDATA   DATA AS READ FROM THE RAM
;     RAMSIZ    NUMBER OF BYTES IN PACKET
;               IF RAMSIZ=0 THEN DEFAULT TO 8.
;
; IMPLICIT OUTPUTS:
;
;     RAMSIZ    SET TO 0
;
; -
;
;     BGNMSG    RAMERR
RAMERR::      JSR     PC,PRAMPKT      ;PRINT RAM/PACKET DATA
;             ENDMSG
L10021:      TRAP    C#MSG

;
; .SBTTL RAMTADD - PRINT TEST ADDRESS, RAM AND PACKET DATA
; *
;
; PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
;
; INPUTS:
;
;     R4     POINTER TO COMMAND PACKET
;
; IMPLICIT INPUTS:
;
;     RAMDATA   DATA AS READ FROM THE RAM
;     RAMSIZ    NUMBER OF BYTES IN PACKET
;               IF RAMSIZ=0 THEN DEFAULT TO 8.
;     ERRHI     HIGH ORDER TEST ADDRESS
;     ERRLO     LOW ORDER TEST ADDRESS
;
; IMPLICIT OUTPUTS:
;
;     RAMSIZ    SET TO 0
;
; -
;
;     BGNMSG    RAMTADD
RAMTADD::    JSR     PC,PRITADD      ;PRINT TEST ADDRESS
;             JSR     PC,PRAMPKT      ;PRINT RAM/PACKET DATA
;             ENDMSG
L10022:      TRAP    C#MSG
  
```



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1998 015340
1999 015340
2000 015344
2001 015350
2002 015354
2003 015360
2004 015360
2005 015360
2006
2007
2008
2009
2010
2011
2012
2013
2014
2015
2016
2017 015362
2018 015362
2019 015402
2020 015406
2021 015406
2022
2023 015410
2024
2025
2026
2027
2028
2029
2030

                                .SBTTL  RAMEXP    PRINT RAM EXPD/RCV DATA
;+
;
;PRINT ROUTINE TO DISPLAY EXPD/RCV DATA
;
;INPUTS:
;
;      R1      RECEIVED DATA
;      R2      EXPECTED DATA
;      R4      CONTROLLER RAM ADDRESS
;-

                                BGNMSG  RAMEXP
RAMEXP::
BIC      #+C<377>,R1          ;SAVE EXPD RAM DATA BYTE
BIC      #+C<377>,R2          ;SAVE EXPD RAM DATA BYTE
JSR      PC,PRIRAM           ;PRINT THE RAM ADDRESS
JSR      PC,PRIXOR           ;PRINT THE DATA
ENDMSG

L10023:
TRAP     C#MSG

                                .SBTTL  TIMEXP    - PRINT TIMER A,B AND EXP/REC
;+
;
;PRINT ROUTINE TO DISPLAY EXPD/RCV DATA
;AND TIMER A,B HEADER MESSAGE
;
;INPUTS:
;
;      R1      RECEIVED DATA
;      R2      EXPECTED DATA
;-

                                BGNMSG  TIMEXP
TIMEXP::
PRINTX   #TIMSGO              ;PRINT HEADER
MOV      #TIMSGO,-(SP)
MOV      #1,-(SP)
MOV      SP,R0
TRAP     C#PNTX
ADD      #4,SP
JSR      PC,PRIXOR           ;PRINT THE DATA
ENDMSG

L10024:
TRAP     C#MSG

045 TIMSGO: .ASCIZ '###A TIMER A STATUS IS IN BIT 3###A TIMER B STATUS IS IN BIT 2'
.EVEN

                                .SBTTL  BADSSR    - PRINT TSSR ERRORS ON DATA TRANSFERS
;+
;

```

```

2031          ;PRINT ROUTINE FOR TSSR ERRORS ON DATA TRANSFERS
2032          ;
2033          ;*****TS:
2034          ;
2035          R1      CONTENTS OF TSSR
2036          R2      DATA WRITTEN (8 BITS)
2037          ;
2038          ;-
2039
2040 015510      BGNMSG  BADSSR
2041 015510      BADSSR:
2042 015510      MOV     R2,-(SP)          ;SAVE DATA TRANSFERRED
2043 015512      BIC     #177400,R2      ;GET JUST ONE BYTE
2044 015516      PRINTB @XFERASC,R2
2045 015516      MOV     R2,-(SP)
2046 015520      MOV     @XFERASC,-(SP)
2047 015524      MOV     #2,-(SP)
2048 015530      MOV     SP,R0
2049 015532      TRAP   C:PNTB
2050 015534      ADD     #6,SP
2051 015540      MOV     (SP),R2         ;RESTORE R2
2052 015542      JSR    PC,PRITSSR      ;DECODE TSSR CONTENTS
2053 015546      ENDMSG
2054
2055 L10025:      TRAP   C:MSG
2056 015550      .ASCIIZ 'DMA Data Transferred = #03'
2057
2058          .SBTTL  GLOBAL SUBROUTINES SECTION
2059
2060          ;**
2061          ; THE GLOBAL SUBROUTINES SECTION CONTAINS THE SUBROUTINES
2062          ; THAT ARE USED IN MORE THAN ONE TEST.
2063          ;**
2064
2065          .SBTTL  SOFINIT - SOFT INITIALIZE OF CONTROLLER
2066
2067          ;*
2068          ;
2069          ;ROUTINE TO DO A SOFT INITIALIZE OF THE CONTROLLER
2070          ;BY WRITING INTO THE TSSR REGISTER. AFTER THE INIT.
2071          ;THE TSSR REGISTER IS TESTED FOR ERRORS. ANY ERRORS
2072          ;DETECTED SHOULD BE TREATED AS DEVICE FATAL ERRORS.
2073          ;
2074          ;INPUTS:
2075          ;
2076          ;      R5      ADDRESS OF FIRST REGISTER
2077          ;
2078          ;OUTPUTS:
2079          ;
2080          ;      R0      CONTENTS OF TSSR, IF ERROR
2081          ;      CARRY   SET IF INIT WAS OKAY
2082          ;              CLEAR IF FATAL ERROR
2083          ;
2084          ;CALLING SEQUENCE:
2085          ;
2086          ;      MOV     #ADDRESS,R5

```

```

2079          :      JSR      PC,SOFINIT
2080          :      BCS      CONTINUE
2081          :      ERRDF          ;REPORT FATAL ERROR
2082          :
2083          :
2084          :
2085          :      SOFINIT::
2086          :      SAVREG          ; SAVE THE REGISTERS
2087          :      MOV      #0,TSSR(R5) ; DO THE INIT.
2088          :      JSR      PC,WAITF      ; WAIT FOR SSR
2089          :      MOV      TSSR(R5),R0    ; GET THE TSSR REGISTER
2090          :      MOV      R0,R4          ; TSSR CONTENTS
2091          :      BIC      #C<HIADDR!OFL>,R4
2092          :      BIS      @SSR!NBA,R4   ;R4 HAS EXPECTED CONTENTS
2093          :      CMP      R4,R0          ;ONLY EXPECTED BITS SET ?
2094          :      BEQ      5$           ;BRANCH IF OKAY
2095          :      CLC          ;CLEAR THE CARRY FOR ERROR
2096          :      BR      10$          ;GO TO EXIT
2097          :      SEC          ;SET THE CARRY BIT
2098          :      RTS      PC      ;RETURN TO CALLER
2099          :
2100          :      .SBTTL  CHKAMB - CHECK TSSR FOR AMBIGUITY
2101          :
2102          :
2103          :
2104          :      ;THIS ROUTINE TESTS THE CONTENTS OF THE TSSR REGISTER
2105          :      ;FOR AMBIGUITY
2106          :
2107          :      ;INPUT:
2108          :
2109          :      ;      RO      CONTENTS OF TSSR
2110          :
2111          :      ;OUTPUT:
2112          :
2113          :      ;      RO      CONTENTS OF TSSR
2114          :
2115          :      ;      CARRY   SET - NO AMBIGUITY
2116          :      ;      CLR    - AMBIGUOUS CONTENTS
2117          :
2118          :
2119          :
2120          :      CHKAMB:
2121          :      SAVREG          ;SAVE THE GENERAL REGISTERS
2122          :      MOV      RO,R4          ;CONTENTS OF TSSR
2123          :      BIT      #SC,RO         ;IS BIT 15 SET ?
2124          :      BNE      5$           ;BRANCH IF YES
2125          :      BIT      #C<NBA!OFL!SSR!HIADDR>,RO ;ANY OTHER BITS SET ?
2126          :      BNE      40$          ;MUST BE AN ERROR
2127          :      BR      45$          ;RETURN WITH SUCCESS
2128          :      5$:  BIT      @SSR,RO     ;IS READY BIT SET ?
2129          :      BNE      10$          ;BRANCH IF READY BIT IS SET.
2130          :      BIT      @BITS,RO      ;IS FATAL ERROR BIT SET ?
2131          :      BEQ      40$          ;ERROR IF NOT
2132          :      BIC      #C<TERCLS,R4   ;CLEAR ALL BUT TERMINATION CODE
2133          :      CMP      R4,#16        ;ALL THREE BITS MUST BE SET
2134          :      BNE      40$          ;ERROR IF NOT SET
2135          :      BR      45$          ;OK IF ALL ARE SET

```

```

2136 015730 032700 000040      10#: BIT      #BIT5,RO      ;IS FATAL ERROR BIT SET ?
2137 015734 001405              BEQ      45#           ;ERROR IF BIT IS SET WITH SSR
2138 015736 032700 000006      BIT      #BIT2!BIT1,RO  ;IS THIS A FUNCTION REJECT
2139 015742 001002              BNE     45#           ;BR, IF TSSR IS OK
2140 015744 000241      40#: CLC              ;AMBIGUOUS CONTENTS
2141 015746 000401              BR      50#
2142 015750 000261      45#: SEC              ;SHOW SUCCESS - NO AMBIGUITY
2143 015752 000207      50#: RTS      PC      ;RETURN TO CALLER
2144
2145              .SBTTL ENAINT,DSBINT - ENABLE/DISABLE INTERRUPTS
2146
2147 ;
2148 ; DEFAULT DISPLAY INTERRUPT HANDLERS.
2149 ; IF DISPLAY TIME-OUT, REPORT DEV FATAL, AND ABORT PASS.
2150 ; OTHERWISE, SAVE DPU REGISTERS AND DISMISS.
2151 ;
2152 ; BIT DEFINITIONS FOR "INTMASK" AND "INTFLAG" BYTES:
2153 ;
2154 ;           IOKCKIN=BIT7      ; DON'T CHECK FOR BAD INTERRUPTS -- TEST WILL.
2155 ;           IOKSTP=BIT0      ; EXPECT "STOP" INTERRUPT.
2156 ;
2157 ; INTERRUPT MASK -- SAYS EXPECTING INTERRUPTS
2158 015754      000      INTMASK: .BYTE 0
2159 ; INTERRUPT FLAG -- SAYS WE GOT ONE (IF POSITIVE)
2160 015755      000      INTFLAG: .BYTE 0
2161
2162 ; SAVED INTERRUPT VECTOR:
2163 015756 000000      INTVEC: .WORD 0
2164 ; SAVE CPU PC
2165 015760 000000      INTCP: .WORD 0
2166
2167 ; SUBROUTINE TO ENABLE INTERRUPTS:
2168 015762 010046      ENAINT: MOV      RO,-(SP)      ;SAVE RO
2169 015764 013700      MOV      IVEC,RO      ;GET POINTER TO VECTORS
2170 015770 012720 016026'  MOV      #INTR,(RO)    ;SET UP INTERRUPT VECTOR
2171 015774 012720 000340  MOV      #PRIORITY,(RO)
2172 016000 012600      MOV      (SP),RO      ;RESTORE RO
2173 016002 011646      MOV      (SP),-(SP)
2174 016004 012766 000000 000002  MOV      #0,2(SP)      ;SET CPU TO LEVEL 0
2175 016012 000002      RTI
2176
2177 ; SUBROUTINE TO DISABLE INTERRUPTS (RAISE PRIORITY TO LEVEL 7)
2178 016014 011646      DSBINT: MOV      (SP),-(SP)
2179 016016 012766 000340 000002  MOV      #PRIORITY,2(SP)
2180 016024 000002      RTI
2181
2182              .SBTTL INTR - INTERRUPT HANDLERS
2183
2184 016026      BGNDRV INTR      ;DEFINE INTERRUPT ENTRY
2185 016026 012737 000001 002222'  INTR:: MOV      #1,INTRECV  ;SET FLAG TO SHOW INTERRUPT RECEIVED
2186 016034 105037 015755'  CLR     INTFLAG      ;CLEAR FLAG TO SAY WE GOT INTERRUPT
2187 016040 132737 000001 015754'  BIT     #IOKSTP,INTMASK ;EXPECTING STOP INTERRUPT?
2188 016046 001003      BNE     1#           ;BR IF YES
2189 016050 152737 000001 015755'  BISE   #IOKSTP,INTFLAG ;NO. SET THE ERROR FLAG.
2190
2191 ;SAVE REGISTERS, MSG BUFFER, ETC.

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
INTR INTERRUPT HANDLERS

SEQ 069

```

2192 016056          1$:
2193 016056          ENDSRV
      016056          L10026:
      016056 000002          RTI

2194
2195          .SBTTL WAITF - WAIT FOR SUBSYSTEM READY
2196          ;
2197          ; SUBROUTINE TO WAIT FOR THE SUBSYSTEM READY FLAG
2198          ;
2199          ; INPUTS:
2200          ;
2201          ; R5 ADDRESS OF FIRST DEVICE REGISTER
2202          ;
2203          ; OUTPUTS:
2204          ;
2205          ; R0 CONTENTS OF LAST TSSR READ
2206          ; CARRY SET - READY BIT SET
2207          ; CLR - TIMEOUT WAITING FOR READY
2208          ;
2209 016060 000401 WAITF:: BR 1$ ;NOP WHEN SUPER FIXED
2210 016062          BREAK ; DO A SUPVSR BREAK FIRST.
      016062 104422 TRAP C#BRK
2211 016064 012746 011000 1$: MOV #11000,-(SP) ;25-APRIL-83 REV B - 1100 MSEC TIMER
2212 016070 016500 000002 2$: MOV TSSR(R5),R0 ;READ THE TSSR REGISTER
2213 016074 105700 TSTB R0 ;TEST FOR READY BIT SET
2214
2215 016076 100420 BMI 3$ ; EXIT ON STOP FLAG.
2216 016100          DELAY 1 ; WAIT 100 USEC
      016100 012727 000001 MOV #1,(PC)+
      016104 000000 .WORD 0
      016106 013727 002116' MOV L#DLY,(PC)+
      016112 000000 .WORD 0
      016114 005367 177772 DEC -6(PC)
      016120 001375 BNE .-4
      016122 005367 177756 DEC -22(PC)
      016126 001367 BNE .-20
2217 016130 005316 DEC (SP) ;REDUCE DELAY COUNT
2218 016132 001356 BNE 2$ ;RETRY UNTIL TIMER EXPIRES
2219 016134 000241 CLC ; C = 0, CONTROLLER STILL RUNNING...
2220 016136 000401 BR 4$ ;...OR HUNG-UP AFTER 300 MSEC.
2221 016140 000261 3$: SEC ; C = 1, CONTROLLER IS STOPPED.
2222 016142 005326 4$: DEC (SP)+ ;RESTORE STACK WITHOUT CHANGING CARRY BIT
2223 016144 000207 RTS PC
2224
2225          .SBTTL CHK TSSR - CHECK TSSR FOR READY
2226
2227          ;*
2228          ;
2229          ; THIS ROUTINE WAITS FOR READY IN THE TSSR
2230          ; AND TESTS FOR AMBIGUOUS BIT SETTINGS IN TSSR.
2231          ;
2232          ; INPUT:
2233          ;
2234          ; R5 ADDRESS OF CSR REGISTERS
2235          ;
2236          ; OUTPUT:
2237          ;

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 CHKTSSR - CHECK TSSR FOR READY

SEQ 070

```

2238      ;      RO      CONTENTS OF TSSR
2239      ;      CARRY   SET - OKAY
2240      ;              CLR - NOT READY AMBIGUOUS, OR SC SET
2241      ;
2242      ;
2243      ;
2244      ;
2244 016146      CHKTSSR:
2245 016146      JSR      PC, WAITF      ; WAIT FOR READY
2246 016152      BCC      20$           ; BRANCH IF TIME OUT
2247 016154      JSR      PC, CHKAMB     ; TSSR AMBIGUOUS?
2248 016160      BCC      10$           ; BR IF YES
2249 016162      BIT      @SC, R0       ; SPECIAL CONDITION SET?
2250 016166      BEQ      15$           ; BR IF NO
2251 016170      BIT      @<SCE!BIE!RMR!NXM>, R0 ; ANY ERROR BITS SET?
2252 016174      BEQ      15$           ; BR IF NO
2253 016176      10$:    CLC              ; SET FAILURE
2254 016200      BR       20$           ;
2255 016202      15$:    SEC              ; SET SUCCESS
2256 016204      20$:    RTS      PC     ; RETURN TO CALLER
2257
2258      ;
2258      .SBTTL  NXNM      - CHECK FOR NONEXISTENT MEMORY
2259
2260      ;
2260      ; * ROUTINE TO TEST FOR A NEXM IN THE RANGE (R1) THRU (R2).
2261      ; ON RETURN, IF "C" = 1, (R1) = NEXM ADDRESS.
2262      ; "C" = 0, ALL ADDRESSES OK.
2263      ;
2264      ;
2264      ; CALL:  MOV  ADR1, R1
2265      ;        MOV  ADR2, R2
2266      ;        JSR  PC, NXM
2267      ;        RETURN      ; TEST "C" AND PROCEED.
2268      ;
2269 016206      012737      016242' 000004  ;
2270 016214      012737      000200 000006  ;
2271 016222      005003      ;
2272 016224      000241      ;
2273 016226      005711      ;
2274
2275 016230      020102      ;
2276 016232      001407      ;
2277 016234      062701      000002      ;
2278 016240      000772      ;
2279
2280 016242      005103      2$:    COM      R3      ; GOT ONE, SET FLAG...
2281 016244      012716      016252'      MOV      @3$, (SP)
2282 016250      000002      ;
2283 016252      3$:    CLRVEC  @4      ; ...AND DISMISS INTERRUPT...
2284      016252      012700      000004      ; ...AND GIVE BACK THE VECTOR.
2285      016256      104436      ;
2284 016260      005703      ;
2285 016262      001401      ;
2286 016264      000261      ;
2287 016266      000207      ;
2288
2289
2290
2291
2292      ;
2292      .SBTTL  TSTLOOP - CHECK ITERATION COUNT

```

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2293
2294
2295
2296
2297
2298
2299
2300 016270
2301 016270 005737 002166'
2302 016274 001006
2303 016276 005737 002202'
2304 016302 100403
2305 016304 005337 002214'
2306 016310 001002
2307 016312 000241
2308 016314 000401
2309 016316 000261
2310 016320 000207
2311
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2336
2337
2338 016322
2339 016322 010046
2340 016324 005037 003152'
2341 016330 005037 016570'
2342 016334 005037 005602'
2343 016340 105037 015754'
2344 016344 013700 002200'
2345 016350 006300
2346 016352 005737 003112'
2347 016356 001430
2348 016360 100010
2349 016362 052760 160000 003174'

;
; SUBROUTINE TO EXECUTE TEST ITERATIONS.
; EXIT WITH "C" SET IF LOOPS ALLOWED AND LOOP COUNT NON-ZERO.
; LOOP COUNTER IS SET BY "BEGIN.TEST" MACRO.
;
; CALL: LOOPTO ARG
;
TSTLOOP::
    TST    NOITS          ; ITERATIONS INHIBITED?
    BNE    1$            ; YES.
    TST    QVP            ; NO.
    BMI    1$            ; LOOPS DISALLOWED IN QUICK PASS.
    DEC    LOOPCNT       ; BUMP LOOP COUNTER.
    BNE    2$            ;
1$:      CLC              ; LOOP DISALLOWED, OR DONE.
    BR     3$            ;
2$:      SEC              ; LOOP ENABLED.
3$:      RTS             PC

        .SBTTL TSTSETUP - PRINT TEST NAME AND INIT ERROR COUNTS
;
; PRINT THE NUMBER AND NAME OF EACH TEST AS WE GO ALONG.
; INCREMENT "TESTK" TO INDICATE THE NUMBER OF TESTS
; IN THE CURRENT RUN SEQUENCE.
; CLEAR THE ERROR COUNTER AND SIGNATURE EXTENSION FLAGS.
;
; INPUT:
;
;     R0      POINTER TO TEST ID ASCIZ STRING
;
; OUTPUT:
;
;     R5      ADDRESS OF FIRST DEVICE REGISTER
;
; IMPLICIT OUTPUTS:
;
;     TSTCNT  UPDATED TO COUNT TESTS PERFORMED SINCE START OR RESTART
;
; SIDE EFFECTS:
;
;     INTERRUPT LEVEL IS RAISED TO LEVEL OF
;     THE DEVICE UNDER TEST
;
; -
TSTSETUP::
    MOV    R0, -(SP)     ; SAVE THE TEST ID MESSAGE
    CLR    SIFLAG        ; CLEAR "SOFT INIT" FLAG
    CLR    ERRK          ; CLEAR LOCAL ERROR COUNTER.
    CLR    EXTA          ; CLEAR ERROR EXTENSION FLAG.
    CLR    INTMASK       ; CLEAR INTERRUPT MASK (CHECK ERROR)
    MOV    UNITN, R0     ; GET THE UNIT NUMBER.
    ASL    R0             ; ... AND MAKE IT A WORD OFFSET.
    TST    NODEV         ; DID STARTUP FIND THE DEVICE?
    BEQ    4$            ; BR IF YES
    BPL    3$            ; BR IF NOT IDLE
    BIS    #160000, ERTABL(R0) ; FLAG ERROR IN THE ERROR TABLE

```

```

2350 016370          ERRDF 1,NXR,NXRERR ; NO DEVICE HERE -- PRINT IT
      016370 104455  TRAP  C$ERDF
      016372 000001  .WORD 1
      016374 003732' .WORD NXR
      016376 005546' .WORD NXRERR
2351 016400 000407  BR 2$
2352 016402 052760 160001 003174' 3$: BIS #160001,ERTABL(RO) ; FLAG ERROR IN THE ERROR TABLE
2353 016410          ERRDF 2,NOINIT ; DEVICE NOT IDLE
      016410 104455  TRAP  C$ERDF
      016412 000002  .WORD 2
      016414 004327' .WORD NOINIT
      016416 000000  .WORD 0
2354 016420 012737 177777 003110' 2$: MOV #-1,DUFLG ; DROP THE UNIT
2355 016426          DODU UNITN
      016426 013700 002200' MOV UNITN,RO
      016432 104451  TRAP  C$DODU
2356 016434          DOCLN ; ABORT THE PASS
      016434 104444  TRAP  C$DCLN
2357 016436 000423  BR 5$
2358
2359 016440          RFLAGS RO ; GET THE OPERATOR FLAGS.
      016440 104421  TRAP  C$RFLA
2360 016442 032700 001000  BIT #PNT,RO ; PRINT THE TEST NUMBERS?
2361 016446 001412  BEQ 1$ ; BR IF NO
2362 016450 011600  MOV (SP),RO ; GET THE ID MESSAGE
2363 016452          PRINTF #TNAM,RO ; DISPLAY THE TEST ID
      016452 010046  MOV RO,-(SP)
      016454 012746 016516' MOV #TNAM,-(SP)
      016460 012746 000002  MOV #2,-(SP)
      016464 010600  MOV SP,RO
      016466 104417  TRAP  C$PNTF
      016470 062706 000006  ADD #6,SP
2364 016474 005237 002212' 1$: INC TSTCNT ; BUMP TEST COUNTER.
2365 016500          SETPRI IPRI ; PRIORITY THAT OF DEVICE
      016500 C13700 002210' MOV IPRI,RO
      016504 104441  TRAP  C$SPRI
2366 016506 005726 5$: TST (SP); ; FIX UP THE STACK
2367 016510 013705 002204' MOV CSRADDR,R5 ; ADDRESS OF TSV REGISTERS ON UNIBUS
2368 016514 000207  RTS PC
2369 016516 045 123 045 TNAM: .ASCIZ '#S#T#A Test'
2370
2371
2372          .SBTTL TSTEND - PRINT ERRORS RECEIVED
2373
2374 ; AT END OF EACH TEST, PRINT THE NUMBER OF ERRORS RECEIVED
2375 ; IF NORMAL ERROR REPORTING IS DISABLED (FLA:IER).
2376
2377 TSTEND: RFLAGS RO
      016532 104421  TRAP  C$RFLA
2378 016534 030027 020000  BIT RO,#IER
2379 016540 001412  BEQ 1$ ; BR IF "IER" NOT SET.
2380 016542          PRINTF #ESUM,ERRK ; PRINT ERROR COUNT.
      016542 013746 016570' MOV ERRK,-(SP)
      016546 012746 016572' MOV #ESUM,-(SP)
      016552 012746 000002  MOV #2,-(SP)
      016556 010600  MOV SP,RO
      016560 104417  TRAP  C$PNTF
    
```


TSV3 GLOBAL AREAS MACRO M1113 07-FEB 84 10:58
 TSTEND - PRINT ERRORS RECEIVED

SEQ 073

```

2381 016562 062706 000006          1$: ADD    #6,SP
2382 016566 000207                   RTS    PC
2383 016570 000000                   ERK:   0          ; LOCAL ERROR COUNT.
2384 016572      045      101      040 ESUM:   .ASCIZ  /#A #D#A ERRORS/
2385 016611      105      122      122 EMAXDU: .ASCIZ  /ERROR LIMIT REACHED -- DROPPING UNIT/
2386                                     .EVEN
2387
2388                                     .SBTTL  INCERK - INCREMENT LOCAL ERROR COUNT
2389
2390                                     ;
2391                                     ; ROUTINES TO INCREMENT LOCAL ERROR COUNT AND CHECK FOR LIMIT:
2392                                     ;
2392 016656 005237 016570'          INCERK: INC    ERRK          ; INCREMENT LOCAL ERROR COUNT
2393 016662 010046                   MOV    RO,-(SP)         ; SAVE RO
2394 016664 013700 002200'          MOV    UNITN,RO        ; GET UNIT NUMBER,
2395 016670 006300                   ASL    RO              ; ... AND MAKE IT A WORD OFFSET.
2396 016672 062700 003174'          ADD    #ERTABL,RO      ; RO GETS ADDRESS OF ERROR TABLE ENTRY.
2397 016676 005210                   INC    (RO)            ; INCREMENT THE DEVICE ERROR COUNT
2398 016700 032710 007777          BIT    #7777,(RO)     ; DID WE OVERFLOW THE FIELD?
2399 016704 001001                   BNE    1$             ; BR IF NO.
2400 016706 005310                   DEC    (RO)            ; YES -- BACK IT UP TO 7777.
2401 016710 012600                   1$:  MOV    (SP)+,RO    ; RESTORE RO
2402 016712 000207                   RTS    PC              ; RETURN TO CALLER.
2403
2404 016714 010046                   CKEMAX: MOV   RO,-(SP)   ; SAVE RO
2405 016716 013700 002200'          MOV   UNITN,RO        ; GET UNIT NUMBER
2406 016722 006300                   ASL   RO              ; ... AND MAKE IT A WORD OFFSET
2407 016724 016000 003174'          MOV   ERTABL(RO),RO   ; GET ERROR TABLE ENTRY
2408 016730 042700 170000          BIC   #170000,RO      ; EXTRACT ERROR COUNT FIELD
2409 016734 020037 002172'          CMP   RO,GERRMAX      ; IS GLOBAL LIMIT EXCEEDED FOR THIS UNIT?
2410 016740 103004                   BHIS  1$              ; BR IF YES
2411 016742 023737 016570' 002170' CMP   ERRK,LERRMAX    ; IS LOCAL LIMIT EXCEEDED FOR THIS TEST?
2412 016750 103417                   BLO  2$              ; BR IF NO
2413 016752                   1$:  RFLAGS RO          ; GET OPERATOR FLAGS
2414 016752 104421                   TRAP  C#RFLA
2415 016754 032700 000040          BIT   #IDU,RO         ; IS DROPPING INHIBITED?
2416 016760 001013                   BNE  2$              ; BR IF YES.
2417 016762 012737 177777 003110' MOV   #-1,DUFLG       ; NO - DROP THE UNIT
2418 016770 104455                   ERDF  4,EMAXDU
2419 016772 000004                   TRAP  C#ERDF
2420 016774 016611'                   .WORD 4
2421 016776 000000                   .WORD EMAXDU
2422 017000                   .WORD 0
2423 017000 013700 002200'          DODU  UNITN
2424 017004 104451                   MOV   UNITN,RO
2425 017006                   TRAP  C#DODU
2426 017006 104444                   DOCLN
2427 017010 012600                   2$:  TRAP  C#DCLN
2428 017012 000207                   MOV   (SP)+,RO        ; RESTORE RO
2429                                     RTS    PC              ; RETURN TO CALLER
2430
2431                                     .SBTTL  CKDROP - CHECK IF UNIT SHOULD BE DROPPED
2432                                     ;
2433                                     ; CHECK IF UNIT SHOULD BE DROPPED
2434                                     ;
2435 017014 010046                   CKDROP: MOV   RO,-(SP)
2436 017016                   FORCERROR 1$,NOTSSR

```

```

2429 017026                                RFLAGS RO
      017026 104421                        TRAP  C#RFLA
2430 017030 032700 000040                  BIT   #IDU,RO
2431 017034 001010                        BNE   1#
2432 017036 011600                        MOV   (SP),RO
2433 017040 012737 177777 003110'         MOV   #-1,DUFLG
2434 017046                                DODU  UNITN
      017046 013700 002200'               MOV   UNITN,RO
      017052 104451                        TRAP  C#DODU
2435 017054                                DOCLN                                ;ABORT THE PASS
      017054 104444                        TRAP  C#DCLN
2436 017056 012600                        1#:  MOV   (SP)+,RO
2437 017060 000207                        RTS   PC
2438
2439                                .SBTTL  CONFIG - DETERMINE CONFIGURATION OF SYSTEM
2440
2441                                ;
2442                                ; SUBROUTINE - DETERMINE CONFIGURATION OF TSU05 SYSTEM.
2443                                ;
2443 017062                                ; CONFIG:
2444 017062 004737 015604'                 JSR   PC,SOFINIT
2445 017066 000207                        RTS   PC
2446
2447                                .SBTTL  KTON,KTOFF - ENABLE/DISABLE MEMORY MANAGEMENT
2448
2449                                ;
2450                                ; SUBROUTINE - ENABLE MEM MGT.
2451                                ;
2451 017070 005737 003130'                 KTON: TST   KFLG                                ; GOT KT?
2452 017074 001403                        BEQ   1#                                ; NO.
2453 017076 012737 000001 177572         MOV   #1,SRO                                ; YES. ENABLE KT11.
2454 017104 000207                        1#:  RTS   PC
2455
2456
2457
2458
2459                                ;
2460                                ; SUBROUTINE - DISABLE MEM MGT.
2461                                ;
2461 017106 005737 003130'                 KTOFF: TST  KFLG                                ; GOT KT11?
2462 017112 001403                        BEQ   1#                                ; NO.
2463 017114 000240                        NOP
2464 017116 000240                        NOP
2465 017120 012737 000000 177572         MOV   #0,SRO                                ; DISABLE KT.
2466 017126 000207                        1#:  RTS   PC
2467
2468                                .SBTTL  SETMAP - SETUP PAR6 MAPPING
2469
2470
2471                                ;
2472                                ; THIS ROUTINE SETS UP KERNEL PAR6 TP HANDLE
2473                                ; AN 22 BIT ADDRESS. THE OFFSET INTO THE PAGE
2474                                ; IS RETURNED BIASED TO PAR6.
2475                                ;
2476                                ; INPUTS:
2477                                ;
2478                                ;     RO  HIGH ORDER ADDRESS BITS
2479                                ;     R1  LOW ORDER ADDRESS BITS
2480                                ;
2481                                ; OUTPUTS:

```

```

2482
2483
2484
2485
2486
2487 017130
2488 017130
2489 017134 005737 003130'
2490 017140 001433
2491 017142 010102
2492 000006
2493
2494
2495
2496 017174 042701 000177
2497 017200 020137 003130'
2498 017204 103011
2499 017206 010137 172354
2500 017212 042702 160000
2501 017216 062702 140000
2502 017222 010200
2503 017224 000261
2504 017226 000401
2505 017230 000241
2506 017232 000207
2507
2508
2509
2510
2511
2512
2513
2514
2515
2516
2517
2518
2519
2520
2521
2522
2523
2524 017234
2525 017234
2526 017240 004737 017106'
2527 017244 010003
2528 017246 013701 003122'
2529 017252 013702 003124'
2530 017256 010321
2531 017260 005302
2532 017262 003375
2533 017264 005737 003130'
2534 017270 001502
2535 017272 004737 017070'
2536 017276 005000
2537 017300 013701 003150'
2538 000006
    ;
    ; RO OFFSET INTO BLOCK WITH PAR6 BIAS (I.E. THE ADDRESS)
    ; CARRY SET IF SUCCESS
    ; CLR IF ERROR
    ;-
    SETMAP:
    SAVREG ;SAVE R1-R4 UNTIL NEXT RETURN
    TST KTF LG ;SYSTEM HAVE ABOVE 28K?
    BEQ 10$ ;BR IF NO
    MOV R1,R2 ;SAVE LOW ORDER BITS
    .REPT 6
    ASR R0 ;CONVERT WORD ADDRESS TO 32W BLOCKS
    ROR R1 ;MAKE IT DOUBLE PRECISION
    .ENDR
    BIC #177,R1 ;ALINE FOR LOWER 4K BOUNDARY
    CMP R1,KTF LG ;HIGHER THAN EXISTING MEMORY?
    BHIS 10$ ;BR IF YES
    MOV R1,#KIPAR6 ;SETUP MAPPING REGISTER PAR6
    BIC #160000,R2 ;SETUP DISPLACEMENT IN PAGE
    ADD #140000,R2 ;ADD IN PAR6 BIAS
    MOV R2,R0 ;RETURN IN R0
    SEC ;SET SUCCESS
    BR 15$
    10$: CLC ;SET FAILURE
    15$: RTS PC ;RETURN

    .SBTTL FILLMEM - FILL MEMORY WITH BACKGROUND PATTERN
    ;*
    ; FILL MEMORY WITH A BACKGROUND PATTERN
    ;
    ; INPUTS:
    ;
    ; RO = BACKGROUND PATTERN
    ; FREE = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
    ; KTF LG = SET TO HIGHEST MEMORY LOCATION IF > 28K.
    ;
    ; OUTPUTS:
    ;
    ; NONE
    ;-
    ;
    FILLMEM:
    SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
    JSR PC,KTOFF ;DISABLE KT.
    MOV R0,R3 ;COPY TEST PATTERN
    MOV FREE,R1 ;GET FIRST FREE LOCATION
    MOV FRESIZ,R2 ;SIZE OF FREE SPACE BELOW 28K.
    10$: MOV R3,(R1)+ ;STORE A BACKGROUND WORD
    DEC R2 ;DONE ALL MEMORY IN FREE SPACE?
    BGT 10$ ;BR IF NO
    TST KTF LG ; GOT KT?
    BEQ 55$ ; NO. GET OUT.
    JSR PC,KTON ; YES. ENABLE KT.
    CLR R0 ;HIGH ORDER ADDRESS START
    MOV PST32W,R1 ;GET >28K START ADDRESS (IN 32W BLOCKS)
    .REPT 6
    
```

```

2539          CLC          ;CLEAR C BIT
2540          ROL          R1      ;CONVERT BLOCKS TO WORDS
2541          ROL          R0      ;MAKE IT DOUBLE PRECISION
2542          .ENDR
2543 017350 004737 017130'
2544 017354 010320          30$: MOV R3,(R0) ;SETUP PAR6 MAPPING REGISTER
2545 017356 020027 160000    ;STORE TEST PATTERN IN >28K ADDRESS
2546 017362 103774          ;END OF PAR6 MAPPING AREA?
2547 017364 162700 020000    ;BR IF NO
2548 017370 062737 000200 172354 ;BACKUP INTO PAR6 MAPPING BEGIN
2549 017376 013705 003130'   ;POINT TO NEXT 4K BLOCK >28K.
2550 017402 042705 170000    ;GET VALUE FROM MEMORY SIZER
2551 017406 023705 172354    ;ONLY 18 BITS PASS
2552 017412 001427          ;END OF MEMORY?
2553 017414 005737 003142'   ;BR IF YES
2554 017420 001407          ;PROCESSOR TYPE A
2555 017422 013704 177572    ;NO KEEP GOING
2556 017426 042704 177761    ;GET SRO CONTENTS
2557 017432 022704 000016    ;CLEAR ALL BUT PAGE NUMBER
2558 017436 001415          ;SEE IF PAGE 7
2559 017440 005737 003144'   ;EXIT IF THERE
2560 017444 001410          ;PROCESSOR TYPE B
2561 017446 023727 172354 007600 ;NO KEEP GOING
2562 017454 103001          ;REACHED 18 BITS?
2563 017456 000403          ;YES
2564 017460 012737 000020 172516 40$: BR 45$ ;NO KEEP GOING
2565 017466 000137 017354'   45$: MOV #20,SR3 ;SET MMU RELOCATION
2566 017472 004737 017106'   50$: JMP 30$ ;KEEP GOING ON ETC.
2567 017476 000207          55$: JSR PC,KTOFF ;DISABLE KT.
2568          RTS          PC
2569          .SBTTL CMPMEM - COMPARE MEMORY TO BACKGROUND PATTERN
2570          ;*
2571          ; COMPARE MEMORY WITH A BACKGROUND PATTERN
2572          ;
2573          ; INPUTS:
2574          ;
2575          ; RO = BACKGROUND PATTERN
2576          ; FREE = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
2577          ; KTFLG = SET TO HIGHEST MEMORY LOCATION IF > 28K.
2578          ;
2579          ; OUTPUTS:
2580          ;
2581          ; CARRY - SET IF NO ERROR
2582          ; CARRY - CLR IF ERROR
2583          ;
2584          ; IMPLICIT OUTPUTS:
2585          ;
2586          ; ERRHI - ERROR HIGH ADDRESS
2587          ; ERRLO - ERROR LOW ADDRESS
2588          ; EXPD - EXPECTED DATA
2589          ; RECV - RECEIVED DATA
2590          ;-
2591 017500          CMPMEM:
2592 017500          SAVREG
2593 017504 010003          MOV RO,R3 ;SAVE R1-R5 UNTIL NEXT RETURN
2594 017506 004737 017106' JSR PC,KTOFF ;COPY TEST PATTERN
2595 017512 013701 003122' MOV FREE,R1 ;DISABLE KT.
                ;GET FIRST FREE LOCATION
    
```

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2596 017516 013702 003124'      MOV      FRESIZ,R2      ;SIZE OF FREE SPACE BELOW 28K.
2597 017522 020311      10$:    CMP      R3,(R1)    ;FREE SPACE LOCATION EQUAL TO EXPD?
2598 017524 001411      BEQ      15$          ;BR IF YES
2599 017526 010137 002236'      MOV      R1,ERRLO     ;SAVE ADDRESS IN ERROR
2600 017532 005037 002234'      CLR      ERRHI        ;NO HIGH ADDRESS
2601 017536 010337 002230'      MOV      R3,EXPD      ;SAVE EXPD FOR ERROR REPORT
2602 017542 011137 002232'      MOV      (R1),RECV    ;SAVE RECV FOR ERROR REPORT
2603 017546 000474      BR       50$          ;
2604 017550 005721      15$:    TST      (R1)+       ;POINT TO NEXT ADDRESS
2605 017552 005302      DEC      R2           ;DONE ALL MEMORY IN FREE SPACE?
2606 017554 003362      BGT      10$          ;BR IF NO
2607 017556 005737 003130'      TST      KTFLG        ; GOT KT?
2608 017562 001472      BEQ      55$          ; NO. GET OUT.
2609 017564 004737 017070'      JSR      PC,KTON      ; YES. ENABLE KT.
2610 017570 005000      CLR      R0           ;HIGH ORDER ADDRESS START
2611 017572 013701 003150'      MOV      PST32W,R1    ;GET >28K START ADDRESS (IN 32W BLOCKS)
2612                000006      .REPT      6
2613                ROL      R1           ;CONVERT BLOCKS TO WORDS
2614                ROL      R0           ;MAKE IT DOUBLE PRECISION
2615                .ENDR
2616 017626 042701 000177      BIC      #177,R1       ;ALINE 4K BOUNDARY
2617 017632 010046      MOV      R0,-(SP)     ;SAVE HIGH ORDER
2618 017634 010146      MOV      R1,-(SP)     ;SAVE LOW ORDER
2619 017636 004737 017130'      JSR      PC,SETMAP    ;SETUP PAR6 MAPPING REGISTER
2620 017642 010004      MOV      R0,R4        ;COPY ADDRESS BIASED TO PAR6
2621 017644 012601      MOV      (SP)+,R1     ;RESTORE LOW ORDER IN NON PAR6 FORMAT
2622 017646 012600      MOV      (SP)+,R0     ;RESTORE HIGH ORDER IN NON PAR6 FORMAT
2623 017650 020314      30$:    CMP      R3,(R4)     ;ABOVE 28K LOCATION EQUAL EXPD?
2624 017652 001411      BEQ      32$          ;BR IF YES
2625 017654 010037 002234'      MOV      R0,ERRHI    ;SAVE HIGH ORDER IN ERROR
2626 017660 010137 002236'      MOV      R1,ERRLO    ;SAVE LOW ORDER IN ERROR
2627 017664 010337 002230'      MOV      R3,EXPD     ;SAVE EXPD FOR ERROR REPORT
2628 017670 011437 002232'      MOV      (R4),RECV   ;SAVE RECV FOR ERROR REPORT
2629 017674 000421      BR       50$          ;
2630 017676 062701 000002      32$:    ADD      #2,R1        ;UPDATE NON PAR6 ADDRESS
2631 017702 005500      ADC      R0           ;MAKE IT DOUBLE PRECISION ADD
2632 017704 062704 000002      ADD      #2,R4        ;UPDATE PAR FORMAT ADDRESS
2633 017710 020427 160000      CMP      R4,#160000   ;END OF PAR6 MAPPING AREA?
2634 017714 103755      BLO      30$          ;BR IF NO
2635 017716 162704 020000      SUB      #20000,R4    ;BACKUP INTO PAR6 MAPPING BEGIN
2636 017722 062737 000200 172354      ADD      #200,#KIPAR6 ;POINT TO NEXT 4K BLOCK >28K.
2637 017730 023737 172354 003130'      CMP      #KIPAR6,KTFLG ;END OF MEMORY?
2638 017736 101744      BLOS    30$          ;BR IF NO
2639 017740 004737 017106'      50$:    JSR      PC,KTOFF    ;TURN OFF MEMORY MAPPING
2640 017744 000241      CLC                    ;SET FAILURE
2641 017746 000403      BR       60$          ;
2642 017750 004737 017106'      55$:    JSR      PC,KTOFF    ;TURN OFF MEMORY MAPPING
2643 017754 000261      SEC                    ;SET SUCCESS
2644 017756 000207      60$:    RTS      PC
2645
2646                .SBTTL  REGSAV  -  SAVE R1-R5 ON STACK
2647                ;+
2648                ;
2649                ;ROUTINE TO
2650                ;SAVE R1 THROUGH R5 ON THE STACK
2651                ;
2652                ;CALLING SEQUENCE:

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2653      ;
2654      ;      JSR      R5,REGSAV
2655      ;
2656      ; THIS IS A COOROUTINE WHICH TF NSFER CONTROL BACK TO
2657      ; THE CALLING ROUTINE. AT THE END OF THE CALLING ROUTINE,
2658      ; THE RTS PC RETURNS CONTROL TO THIS ROUTINE TO RESTORE
2659      ; REGISTERS.
2660      ;
2661      ; THIS ROUTINE SHOULD ONLY BE CALLED FROM ROUTINES WHICH ARE
2662      ; CALLED VIA A JSR PC INSTRUCTION
2663      ;
2664      ;
2665      ;
2666 017760 REGSAV:
2667 017760 010446      MOV      R4,-(SP)
2668 017762 010346      MOV      R3,-(SP)
2669 017764 010246      MOV      R2,-(SP)
2670 017766 010146      MOV      R1,-(SP)
2671 017770 010546      MOV      R5,-(SP)
2672 017772 016605      MOV      10.(SP),R5
2673 017776 004736      JSR      PC,@(SP)+
2674 020000 012601      MOV      (SP)+,R1
2675 020002 012602      MOV      (SP)+,R2
2676 020004 012603      MOV      (SP)+,R3
2677 020006 012604      MOV      (SP)+,R4
2678 020010 012605      MOV      (SP)+,R5
2679 020012 000207      RTS      PC
2680
2681      .SBTTL  GETPAT  - GET 8 BIT PATTERN FROM OPERATOR
2682      ;+
2683      ;
2684      ; ROUTINE TO REQUEST AN 8 BIT DATA PATTERN FROM THE OPERATOR
2685      ;
2686      ; INPUTS:
2687      ;
2688      ;      NONE.
2689      ;
2690      ; OUTPUTS:
2691      ;
2692      ;      R0      OCTAL NUMBER FROM THE OPERATOR
2693      ;
2694      ; CALLING SEQUENCE:
2695      ;
2696      ;      JSR      PC,GETPAT
2697      ;
2698      ;-
2699
2700 020014 GETPAT::
2701 020014      SAVREG      ;SAVE THE GENERAL REGISTERS
2702 020020 1$:      GMANID   DATASC,PATDAT,0,377,0,377,NO
                TRAP     C$GMAN
                BR      10000$
                .WORD   PATDAT
                .WORD   T$CODE
                .WORD   DATASC
                .WORD   377
                .WORD   T$LOLIM
    
```

```

020036 000377          .WORD  T#HILIM
020040          100001:  BNCOMPLETE  1#          ;RETRY IF ERROR
2703 020040          BCC  1#
020040 103367          MOV  PATDAT,RO          ;DATA PATTERN FROM OPERATOR
2704 020042 013700 020050'  RTS  PC          ;RETURN TO CALLER
2705 020046 000207
2706
2707          ;*
2708          ;LOCAL DATA AREA
2709          ;-
2710
2711 020050 000000          PATDAT: .WORD  0          ;TEMPORARY STORAGE FOR DATA
2712 020052 105 116 124  DATASC: .ASCIZ 'ENTER DATA PATTERN'
2713          .EVEN
2714
2715          .SBTTL  GETSEL - ISSUE MENU AND GET OPERATOR RESPONSE
2716          ;*
2717          ;
2718          ;ROUTINE TO ISSUE A MENU AND GET
2719          ;THE OPERATOR'S RESPONSE.
2720          ;
2721          ;INPUTS:
2722          ;
2723          ; R0 ADDRESS OF ASCIZ STRING OF MENU
2724          ; R1 MAXIMUM ALLOWABLE OPERATOR RESPONSE
2725          ;
2726          ;OUTPUTS:
2727          ;
2728          ; R0 NUMBER OF THE OPERATOR'S SELECTION
2729          ;
2730          ;-
2731
2732          GETSEL::
2733 020076          SAVREG          ;SAVE GENERAL REGISTERS
2734 020102 010002          MOV  R0,R2          ;SAVE THE MENU ADDRESS
2735 020104 010203          MOV  R2,R3          ;START OF MENU STRING
2736 020106 005713          TST  (R3)          ;END OF ASCII ?
2737 020110 001412          BEQ  3#          ;BRANCH IF ALL LINES DISPLAYED
2738 020112          PRINTF  #SELASC,(R3),          ;DISPLAY THE MENU
          020112 012346          MOV  (R3),-(SP)
          020114 012746 020262'  MOV  #SELASC,-(SP)
          020120 012746 000002  MOV  #2,-(SP)
          020124 010600          MOV  SP,RO
          020126 104417          TRAP C#PNTF
          020130 062706 000006  ADD  #6,SP
2739 020134 000764          BR  2#
2740 020136          3#:  G#ANID  MENASC,MENRES,D,-1,0, 1,NO
          020136 104443          TRAP C#GMAN
          020140 000406          BR  10001#
          020142 020316'          .WORD  MENRES
          020144 000042          .WORD  T#CODE
          020146 020267'          .WORD  MENASC
          020150 177777          .WORD  -1
          020152 000000          .WORD  T#LOLIM
          020154 177777          .WORD  T#HILIM
          100011:
2741 020156          BNCOMPLETE  1#          ;RETRY IF ERROR
    
```

```

020156 103352
2742 020160 013700 020316'
2743 020164 020001
2744 020166 101411
2745 020170
020170 012746 020214'
020174 012746 000001
020200 010600
020202 104417
020204 062706 000004
2746 020210 000735
2747 020212 000207
2748 020214 045 116 045 MENERR: .ASCIZ '###A *** Menu Selection Too Large ***'
2749 020262 045 116 045 SELASC: .ASCIZ '###T'
2750 020267 105 156 164 MENASC: .ASCIZ 'Enter Menu Selection: '
2751
2752 020316 000000 MENRES: .EVEN .WORD 0
2753
2754 .SBTTL CHKMAN - CHECK MANUAL INTERVENTION LEGALITY
2755 ;*
2756 ;
2757 ;ROUTINE TO TEST FOR MANUAL INTERVENTION LEGALITY.
2758 ;
2759 ;INPUT:
2760 ;
2761 ; NONE.
2762 ;
2763 ;OUTPUT:
2764 ;
2765 ; CARRY 0 MANUAL INTERVENTION NOT ALLOWED
2766 ; 1 MANUAL INTERVENTION IS OK
2767 ;
2768 ;SIDE EFFECTS:
2769 ;
2770 ; A MESSAGE IS DISPLAYED WARNING THAT TEST IS
2771 ; NOT EXECUTED IF MANUAL INTERVENTION IS NOT
2772 ; ALLOWED.
2773 ;
2774 ;-
2775
2776 020320
2777 020320
2778 020324
020324 104450
2779 020326
020326 103411
2780 020330
020330 012746 020354'
020334 012746 000001
020340 010600
020342 104417
020344 062706 000004
2781 020350 000241
2782 020352 000207
2783
2784 020354 045 116 045 NOMAN: .ASCIZ '###A *** Manual Intervention not Allowed - Test Aborted ***'
2785 .even
BCC 1#
MOV MENRES,RO ;GET THE OPERATOR'S REPLY
CMP RO,R1 ;COMPARE TO MAXIMUM ALLOWED
BLOS S# ;BRANCH IF OK
PRINTF #MENERR ;DISPLAY ERROR MESSAGE
MOV #MENERR,-(SP)
MOV #1,-(SP)
MOV SP,RO
TRAP C:PNTF
ADD #4,SP
BR 1# ;RETRY
S#: RTS PC ;RETURN TO CALLER

```



```

2786
2787
2788
2789
2790
2791 020450
020450 104431
2792 020452 010037 003122'
2793 020456 062737 000002 003122'
2794 020464 011037 003124'
2795 020470 162737 000004 003124'
2796 020476 013702 002012'
2797 020502 162737 000007 003124' 10#:
2798 020510 005302
2799 020512 001373
2800 020514 013700 003122'
2801 020520 063700 003124'
2802 020524 162700 000002
2803 020530 010037 003126'
2804 020534 000207 40#:
2805
2806
2807
2808
2809
2810
2811
2812
2813 020536
2814 020536 005037 003130'
2815 020542 005037 003132'
2816 020546 023727 002120' 001577
2817 020554 101453
2818 020556 023727 002120' 001777
2819 020564 101447
2820 020566 013700 000004
2821 020572 012737 020664' 000004
2822 020600 005737 177572
2823 020604 000240
2824 020606 013737 002120' 003130'
2825 020614 042737 000177 003130'
2826 020622 010037 000004
2827 020626 005000
2828 020630 012701 172340
2829 020634 012761 077406 177740 1#:
2830 020642 010021
2831 020644 062700 000200
2832 020650 020027 002000
2833 020654 001367
2834 020656 012741 177600
2835 020662 000410
2836
2837 020664 012716 020700' 2#:
2838 020670 000002
2839
2840
2841 020672 012716 020726' 3#:

```

.SBTTL ENVIRN - SETUP FREE DIAGNOSTIC SPACE

```

;
; SUBROUTINE TO SET-UP VARIOUS ENVIRONMENTAL PARAMETERS.
;
ENVIRN: MEMORY RO
TRAP C:MEM
MOV RO,FREE ; GET 1ST FREE ADDRESS...
ADD #2,FREE
MOV (RO),FRESIZ ; ...AND WORD COUNT.
SUB #4,FRFSIZ
MOV L:UNIT,R2 ; GET NUMBER OF UNITS
SUB #7,FRESIZ ; TAKE AWAY 7 WORDS PER UNIT
DEL R2
BNE 10#
MOV FREE,RO ;GET FIRST FREE ADDRESS
ADD FRESIZ,RO ;POINT TO LAST FREE ADDRESS
SUB #2,RO ;BACKUP 1 WORD
MOV RO,FREEHI ;STORE LAST FREE ADDRESS
RTS PC ;RETURN
40#:

```

.SBTTL KINIT - SETUP KT11 MEMORY MANAGEMENT REGISTERS

```

;
; ROUTINE TO INIT KT-11
;
;
KINIT:
CLR KFLAG ; INIT >28K MEMORY FLAG
CLR KENABLE ; INIT TEST >28K FLAG
CMP L:HIME,#1577 ; GOT ENOUGH MEMORY (>28K)?
BLOS 9# ; NO.
CMP L:HIME,#1777 ; GOT ENOUGH MEMORY (>32K)?
BLOS 9# ; NO.
MOV @ERRVEC,RO ; SAVE OLD ERR VEC PTR.
MOV #2,@ERRVEC ; SET ERR VEC PTR.
TST @SRO ; GOT KT11?
NOP ; (TRAP IF NO).
MOV L:HIME,KFLAG ; YES. SET KT FLAG.
BIC #177,KFLAG
MOV RO,@ERRVEC ; RESTORE OLD ERR VEC PTR.
CLR RO ; RO = AR DATA.
MOV #KIPAR0,R1 ; R1 = KI REGS PTR.
MOV #77406,-40(R1) ; SET DESCRIPTOR REG.
MOV RO,(R1) ; SET KIPAR REG.
ADD #200,RO ; BUMP AR DATA BY "4K".
CMP RO,#2000 ; AT "I/O"?
BNE 1# ; NO.
MOV #177600,-(R1) ; YES. SET KIPAR7 FOR I/O.
BR 9#
2#: MOV #6,(SP) ; SET UP RETURN
RTI ; RTI TO NEXT LOCATION
3#: MOV #10,(SP) ; SET UP RETURN

```

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 KTINIT SETUP KT11 MEMORY MANAGEMENT REGISTERS

SEQ 082

```

2842 020676 000002          RTI          ; RTI TO NEXT LOCATION
2843
2844 020700 010037 000004    6$:    MOV      RO,#ERRVEC    ; RESTORE OLD ERR VEC PTR.
2845
2846 020704          9$:
2847 020704 013700 000004    MOV      @ERRVEC,RO    ; SAVE OLD ERR VEC PTR.
2848 020710 012737 020672' 000004    MOV      #3,@ERRVEC    ; SET ERR VEC PTR.
2849 020716 042737 000001 170200    BIC      @BIT0,@MMRO    ;BE SURE UNIBUS MAP IS OFF
2850 020724 000240          NOP
2851 020726 010037 000004    10$:   MOV      RO,@ERRVEC    ; RESET VECTOR BACK TO ERROR POINTER
2852 020732 000207          RTS      PC
2853
2854
2855
2856          ;*
2857          ; SUBROUTINE TO SET EXTENDED FEATURES SWITCH
2858          ;
2859          ; Requires that SOFINIT and WRTCHR have been done previous to call.
2860          ;
2861          ; INPUTS:
2862          ; R5      CURRENT UNIT NUMBER
2863          ; OUTPUTS:
2864          ; The Extended Features Switch is set.
2865          ;
2866          ;-
2867
2868 020734          INVERT::
2869
2870 020734 005737 002224'    TST      EXTFEA        ; IS SWITCH SET?
2871 020740 001020          BNE      1$            ; YES,EXIT STAGE RIGHT!(or the next one outa town!)
2872 020742 012737 100206 021010'  MOV      @100206,CMDPKT ; WRT SUB-SYS MEM CMD
2873 020750 012737 021020' 021012'  MOV      @WSMBK,CMDPKT+2 ; MSG BUF ADDR
2874 020756 012737 000006 021016'  MOV      #6,CMDPKT+6    ; BYTE COUNT
2875 020764 012737 100010 021020    MOV      @100010,WSMBK  ; INVERT THE SWITCH
2876 020772 012704 021010'  MOV      @CMDPKT,R4     ; SET CMDPKT INTO R4
2877 020776 004737 010472'  JSR      PC,WRTCHR     ; DO IT
2878 021002 000207          1$:    RTS      PC        ; RETURN
2879
2880
2881          ; COMMAND PACKET.
2882
2884 021004          .BLKB 10-<.-TSV267>
2885
2887 021010 000000          CMDPKT:: 0          ;1ST WORD IS TS05 COMMAND.
2888 021012 000000          0          ;2ND WORD IS THE BUFFER LOW ADDRESS.
2889 021014 000000          0          ;3RD WORD IS THE BUFFER HIGH ADDRESS.
2890 021016 000000          0          ;4TH WORD IS THE BYTE/RECORD/FILE COUNT.
2891
2892
2893          ; WRITE SUB-SYSTEM MEMORY CHARACTERISTIC BLOCK.
2894
2895 021020 000000          WSMBK:: 0          ;1ST WORD:: SEL 0
2896 021022 000000          0          ;2ND WORD:: SEL 2
2897 021024 000000          0          ;3RD WORD:: SEL 4
2898          .EVEN
2899
2900          ;*
```

```

2901          ;          SUBROUTINE TO CHECK WETHER OR NOT WE'LL TEST NXM
2902          ;
2903          ;
2904          ;INPUTS:
2905          ;OUTPUTS:
2906          ;          The NXMFLG is set if we can test.
2907          ;          The NXMLO and NXMMHI addresses are setup.
2908          ;
2909          ;-
2910 021026      MEMCK::
2911
2912          SAVREG
2913 021026      CLR      NXMFLG          ;SAVE THE REGISTERS
2914 021032 005037 003134'    CLR      NXMLO          ;CLEAR THE FLAG
2915 021036 005037 003136'    CLR      NXMLO          ;CLEAR THE TEST ADDRESS LO
2916 021042 005037 003140'    CLR      NXMMHI         ;CLEAR THE TEST ADDRESS HI
2917 021046 032737 170000 002120' BIT      #170000,L#HIME      ;CHECK FOR MORE THAN 18 BITS INDICATED
2918          ;FROM THE SUPERVISOR
2919 021054 001050          BNE      14$          ;BR, IF MAP BOX ETC.
2920 021056 005737 003144'    TST      T23B         ;IS IT A PROCESSOR TYPE B?
2921 021062 001407          BEQ      1$          ;NO
2922 021064 023727 002120' 007777    CMP      L#HIME,#7777      ; GREATER THAN 128K
2923 021072 103406          BLO      2$          ; NO
2924 021074 004737 021222'    JSR      PC,NXMTST        ;SETUP THE ADDRESS
2925 021100 000427          BR       13$         ;SET THE FLAG AND EXIT
2926 021102 005737 003142'    1$:    TST      T23A         ;IS IT A PROCESSOR TYPE A?
2927 021106 001413          BEQ      4$          ;NO
2928 021110 023727 002120' 005777    2$:    CMP      L#HIME,#5777      ;GREATER THAN 96K
2929 021112 101027          BHI      14$         ;YES,23A/23B WITH 128K MEMORY
2930 021114 023727 002120' 003777    CMP      L#HIME,#3777      ;GREATER THAN 64K BUT LESS THAN 92K?
2931 021116 103403          BLO      4$          ;NO, CHECK 24K
2932 021118 004737 021222'    JSR      PC,NXMTST        ;SETUP THE ADDRESS
2933 021120 000411          BR       13$         ;SET THE FLAG AND EXIT
2934 021122 023727 002120' 001577    4$:    CMP      L#HIME,#1577      ;GREATER THAN 24K BUT LESS THAN 64K?
2935 021124 103414          BLO      14$         ;NO, TELL THEM AND EXIT WITH FLAG CLEAR
2936 021126 004737 021222'    JSR      PC,NXMTST        ;SETUP THE ADDRESS
2937 021128 062737 000077 003140' 13$:  ADD      #77,NXMMHI        ;FOOL THE 11/02 & 11/03
2938 021130 032737 177774 003140' 13$:  BIT      #177774,NXMMHI    ;ANY MORE THAN 18 BITS SET?
2939 021132 001014          BNE      15$         ;BR, IF MORE THAN 18 BITS SET
2940 021134 005237 003134'    INC      NXMFLG         ;SET THE FLAG
2941 021136 000411          BR       15$         ;EXIT
2942 021138 000410          14$:  BR       15$         ;NOP FOR PRINTOUT
2943 021200          PRINTF  #NOMEM          ;TELL THEM & EXIT ***NO PRINT*****
2944 021200 012746 005452'    MOV      #NOMEM,-(SP)
2945 021204 012746 000001'    MOV      #1,-(SP)
2946 021210 010600          MOV      SP,RO
2947 021212 104417          TRAP    C#PNTF
2948 021214 062706 00C004'    ADD      #4,SP
2949 021220 000207          15$:  RTS      PC          ;RETURN
2950
2951          ;*
2952          ;          SUBROUTINE TO SETUP THE NXM ADDRESS FOR TESTING
2953          ;
2954          ;
2955          ;OUTPUTS:NXMLO,NXMMHI          ;SETUP WITH NXM ADDRESS
2956          ;
2957          ;-

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 KTINIT - SETUP KT11 MEMORY MANAGEMENT REGISTERS

SEQ 084

2953	021222	013701	002120'	NXMTST: MOV	L#HIME,R1	;GET TOP OF MEMORY
2954	021226	062701	000200	ADD	#200,R1	;MAKE IT I/O BLOCK OR OTHER NXM
2955	021232	042701	000177	BIC	#177,R1	
2956	021236	010102		MOV	R1,R2	;RESAVE RESULTS
2957		000006		.REPT	6	
2958				ASL	R1	;PUT IN PLACE FOR XFER
2959				.ENDR		
2960	021254	010137	003136'	MOV	R1,NXML0	;SAVE TEST ADDRESS LOW
2961		000012		.REPT	10.	
2962				ASR	R2	;PUT IN PLACE FOR XFER
2963				.ENDR		
2964	021304	042702	177700	BIC	#177700,R2	;DON'T WANT ILA!
2965	021310	010237	003140'	MOV	R2,NXMH1	;SAVE TEST ADDRESS HIGH
2966	021314	000207		RTS	PC	;RETURN
2967						
2968						
2969						
2970						
2971	021316			ENDMOD		

```

6          .TITLE  TSV4 - MISCELLANEOUS SECTIONS
7
8 021316   BGNMOD  TSV4
9 021316   TSV4::
10
11
12
13
14
15
16          .SBTTL  PROTECTION TABLE
17 021316   BGNPROT
18 021316   L$PROT::
19 021326   177777 177777 177777 .WORD  -1, -1, -1, -1          ;NO DEVICE PROTECTION REQUIRED.
20          ENDPROT
21          .SBTTL  INITIALIZE SECTION
22
23          ;**
24          ;THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED
25          ;AT THE BEGINNING OF EACH PASS.
26          ;
27          ;IF "START" OR "RESTART", SET QUICK-PASS FLAG AND BUS-INIT.
28          ;IF "CONTINUE", NOTHING IS REQUIRED.
29          ;
30          ;--
31          ;*
32          ;INSERT TEMPORARY JUMP TO ODT
33          ;-
34 021326   BGNINIT
35 021326   L$INIT::
36 021326   005037 002224' 40$: CLR      EXTFEA
37 021332   005037 003134' CLR      NXMFLG
38 021336   012737 006170' 002176' MOV     #EPRT1,EPRTSW          ;SET UP PRIMARY MESSAGE FOR REPLACEMENT
39 021344   005037 003152' CLR      SIFLAG          ;CLEAR "SOFT INIT" FLAG
40 021350   005037 003132' CLR      KTENABLE          ;CLEAR TEST ABOVE 28K FLAG
41 021354   005037 002300' CLR      RAMSIZ          ;CLEAR RAM SIZE FOR RAMERR ROUTINE
42 021360   021360 012700 000036 READEF  #EF.CONTINUE
43 021364   021364 104447 MOV     #EF.CONTINUE,RO
44 021366   021366 103023 TRAP   C$REFG
45 021370   023737 002200' 002012' BNCOMPLETE 1$
46 021376   103070 BCC    1$
47 021400   005737 003110' CMP     UNITN,L$UNIT          ;UNIT IN RANGE?
48 021404   100472 BHIS   4$          ;BR IF NO.
49 021412   006301 TST    DUFFLG          ;DROPPED UNIT?
50 021414   005761 003174' BMI     NXTU          ;BR IF YES
51 021420   001516 MOV     UNITN,R1
52 021422   032761 040000 003174' ASL    R1
53 021432   001060 TST    ERTABL(R1)
54 021436   000416 BEQ    SETU
55 021442   104432 BIT    #BIT14,ERTABL(R1)          ;DROPPED?
56 021444   103052 BNE    NXTU
57 021446   000035 EXIT   INIT          ;DO NOTHING IF "CONTINUE".
58          TRAP   C$EXIT
59          .WORD  L10030-.
60          1$: READEF  #EF.NEW
61          MOV     #EF.NEW,RO
62          TRAP   C$REFG
63          BNCOMPLETE NXTU          ;TAKE NEXT UNIT IF NOT NEW PASS.
64          BCC    NXTU
65          READEF  #EF.START
    
```

TSV4 - MISCELLANEOUS SECTIONS MACRO M1113 07-FEB-84 10:58
INITIALIZE SECTION

SEQ 086

```

021446 012700 000040      MOV     #EF.START,RO
021452 104447      TRAP   C#REFG
57 021454      BCOMPLETE 2#
021454 103404      BCS    2#
58 021456      READEF #EF.RESTART
021456 012700 000037      MOV     #EF.RESTART,RO
021462 104447      TRAP   C#REFG
59 021464      BNCOMPLETE 31#
021464 103031      BCC    31#
60 021466      2#:
61 021466      BRESET ;1ST PASS, BUS-INIT...
021466 104433      TRAP   C#RESET ;BUS RESET.
62 021470 005037 002212'      CLR     TSTCNT ;NUMBER OF TESTS RUN IN PASS
63 021474 005037 002220'      CLR     FATFLG ;CLEAR FATAL ERROR COUNT
64 021500 005037 003142'      CLR     T23A ;CLEAR PROCSSOR TYPE A FLAG
65 021504 005037 003144'      CLR     T23B ;CLEAR PROCSSOR TYPE B FLAG
66 ;
67 ;
68 ;
69 021510 005037 003376'      MOV     #340,-(SP) ;RETURN TO DEBUGGER
70 021514      MOV     #20,-(SP) ;ENTER THE DEBUGGER
71 021514 012737 177777 002202'      JMP     0.00T ;CLEAR THE SUBTEST "SKIPPER"
72 021522 004737 020450'      CLR     SKIPT
71 021514 012737 177777 002202'      20#:
72 021522 004737 020450'      MOV     #-1,QVP ;...QUICK VERIFY...
73 021526 004737 020536'      JSR     PC,ENVIRN ;SET ENVIRONMENT.
74 021532 012700 003174'      JSR     PC,KTINIT ;INITIALIZE KT MEMORY MANAGEMENT
75 021536 005020      MOV     #ERTABL,RO
76 021540 020027 003374'      CLR     (RO) ;CLEAR THE ERROR TABLE
77 021544 103774      CMP     RO,#ERTABE
78 021546 000404      BLO    30#
79 021550 005037 002202'      BR     4#
80 021554 000137 021624'      CLR     QVP
81 ;
82 021560      JMP     PASRPT ;GO REPORT THE STATUS
83 021560 012737 177777 002200'      4#:
84 021566 005037 002216'      NEWPAS: MOV     #-1,UNITN ;INIT UNIT NUMBER...
85 021572      NXTU: CLR     DEVCNT ;CLEAR COUNT OF DEVICES RUNNING
021572 104422      BREAK
86 021574 005237 002200'      TRAP   C#BRK
87 021600 023737 002200' 002012'      INC     UNITN ;...AND SET NEXT UNIT NUMBER.
88 021606 103423      CMP     UNITN,L#UNIT
89 021610 012737 177777 003110'      BLO    SETU
90 021616 000401      MOV     #-1,DUFLG
91 021620      BR     11#
021620 104444      DOCLN
92 021622 000240      TRAP   C#DCLN
93 021624      11#:
94 021624 023727 002012' 000001      PASRPT: CMP     L#UNIT,#1 ;HOW MANY UNITS SELECTED?
95 021632 101752      BLOS   NEWPAS ;BR IF ONLY 1
96 021634 005737 002216'      TST     DEVCNT ;ARE ANY STILL RUNNING?
97 021640 001747      BEQ    NEWPAS ;BR IF NO
98 021642      RFLAGS RO
021642 104421      TRAP   C#RFLA
99 021644 032700 000100      BIT     #ISR,RO ;SHOULD WE PRINT STATISTICS
100 021650 001343      BNE    NEWPAS ;BR IF NO
101
102 021652      DORPT
021652 104424      TRAP   C#DRPT

```

```

103 021654 000741          BR      NEWPAS
104 021656                10$:
105
106 021656                SETU:  GPHARD  UNITN,RO      ;GET UNIT N P-TABLE POINTER.
    021656 013700 002200'   MOV      UNITN,RO
    021662 104442          TRAP    C#GPHRD
107 021664                BNCOMPLETE NXTU      ;BR IF UNIT NOT AVAILABLE.
    021664 103342          BCC     NXTU
108 021666 005037 003110'   CLR     DUFLG      ;CLEAR "DROPPED" FLAG.
109 021672 005237 002216'   INC     DEVCNT
110 021676 012001          MOV     (RO),R1      ;GET 1ST REGISTER ADDRESS.
111 021700 010137 002204'   MOV     R1,CSRADDR  ;ADDRESS OF REGISTERS OF UNIT UNDER TEST
112
113 021704 012001          MOV     (RO),R1      ;GET VECTOR ADDRESS.
114                        ;MOV     (RO),R2      ;GET INTERRUPT PRIORITY
115                        ;MOV     R2,IPRI      ;SET INTERRUPT PRIORITY.
116 021706 010137 002206'   MOV     R1,IVEC     ;SET INTERRUPT VECTOR POINTER...
117 021712 012721 016026'   MOV     #INTR,(R1)  ;...VECTOR...
118 021716 013721 002210'   MOV     IPRI,(R1)  ;...AND PRIORITY.
119
120 021722                1$:
121                        ; TST     QVP          ;1ST PASS ??
122                        ; BEQ     5$          ;NO, SKIP THE PASS 1 STUFF.
123
124                        ;
125                        ;1ST PASS, CHECK THAT DEVICE ADDRESSES ARE VALID, AND
126                        ;THAT THE DISPLAY STATUS IS PROPERLY INITIALIZED.
127                        ;
128 021722 013701 002200'   MOV     UNITN,R1
129 021726 006301          ASL     R1
130 021730 052761 100000 003174' BIS     #BIT15,ERTABL(R1) ;SAY DEVICE RUNNING
131 021736 005037 005602'   CLR     EXTA      ;CLEAR ERROR EXTENSION FLAG.
132 021742 023727 002012' 000001   CMP     L#UNIT,#1   ;ARE WE TESTING MULTIPLE UNITS?
133 021750 101416          BLOS   10$        ;BR IF NO.
134 021752                RFLAGS  RO      ;YES -- GET OPERATOR FLAGS.
    021752 104421          TRAP    C#RFLA
135 021754 032700 001000   BIT     #PNT,RO     ;SHOULD WE PRINT UNIT #?
136 021760 001412          BEQ    10$        ;BR IF NOT.
137 021762                PRINTF  #PUNIT,UNITN ;PRINT THE UNIT #
    021762 013746 002200'   MOV     UNITN,-(SP)
    021766 012746 022054'   MOV     #PUNIT,-(SP)
    021772 012746 000002   MOV     #2,-(SP)
    021776 010600          MOV     SP,RO
    022000 104417          TRAP    C#PNTF
    022002 062706 000006   ADD     #6,SP
138 022006                10$:
139 022006 005037 003112'   CLR     NODEV
140 022012 013701 002204'   MOV     CSRADDR,R1 ;ADDRESS OF FIRST REGISTER
141 022016 010102          MOV     R1,R2      ;START OF REGISTERS
142 022020 062702 000002   ADD     #TSSR,R2   ;ADDRESS OF TSSR REGISTER
143 022024 004737 016206'   JSR    PC,XNXM     ;TEST BOTH CONTROLLER REGISTERS...
144 02203C 103005          BCC    2$          ;...AND BR IF ALL OK.
145 022032 010137 003112'   MOV     R1,NODEV   ;FLAG DEVICE AS NON-EXISTENT
146 022036 012737 177777 003110' MOV     #-1,DUFLG  ;DROP THIS UNIT.
147 022044                2$:
148                        ;
149                        ;FINALLY, SET CPU PRIORITY AND WE'RE DONE.

```

```

150
151 022044          ;
      022044 012700 000000      5$:  SETPRI  #PRI00          ;ENABLE INTERRUPTS.
      022050 104441          MOV    #PRI00,R0
      022052          TRAP   C$SPRI
152 022052          L10030:    ENDINIT
      022052 104411          TRAP   C$INIT
153
154 022054          045      116      045 PUNIT: .ASCIZ  /##### TESTING UNIT #D2#A #####/
155          .EVEN
156
157          .SBTTL  ADD AND DROP UNITS SECTIONS
158
159          ;**
160          ; THE ADD-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
161          ; TO BE (A) ADDED TO THE TEST LIST FOR THE FIRST TIME,
162          ; OR (B) RE-INSERTED IF IT HAD BEEN PREVIOUSLY DROPPED.
163          ;--
164 022122          BGNUA
      022122          L$AU::
165 022122 010001          MOV    R0,R1          ; GET UNIT TO BE ADDED (R0)
166 022124 006301          ASL    R1          ; MAKE IT A WORD INDEX
167 022126 052761 100000 003174'  BIS    #100000,ERTABL(R1)  ; SET THE "ACTIVE" BIT
168 022134 042761 040000 003174'  BIC    #40000,ERTABL(R1)  ; CLEAR THE "DROPPED" BIT
169 022142          PRINTF  #1$,R0
      022142 010046          MOV    R0,-(SP)
      022144 012746 022170'  MOV    #1$,-(SP)
      022150 012746 000002          MOV    #2$,-(SP)
      022154 010600          MOV    SP,R0
      022156 104417          TRAP   C$PNTF
      C22160 062706 000006          ADD    #6,SP
170 022164          EXIT    AU
      022164 000167          .WORD  J$JMP
      022166 000026          .WORD  L10031-2-
171 022170          045      116      045 1$: .ASCIZ  /##### UNIT #D#A ADDED/
172          .EVEN
173
174 022216          ENDAU          ; UNUSED.
      022216          L10031:
      022216 104452          TRAP   C$AU
175
176          ;**
177          ; THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
178          ; TO BE REMOVED FROM THE TEST LIST.
179          ;
180          ; SUPVSR DUEC THE "DROPPING". THIS IS JUST TO TELL THE MAN.
181          ; "DROPPED" UNITS ARE RE-SELECTED ON OPERATOR "STA" OR "ADD"
182          ; COMMAND, OTHERWISE REMAIN INACTIVE. THE "DISPLAY" COMMAND
183          ; WILL PRINT ALL DROPPED UNITS, AND THE P-TABLES OF THOSE
184          ; WHICH ARE STILL ACTIVE.
185          ; UPON ENTRY, R0 CONTAINS THE UNIT TO BE DROPPED.
186 022220          BGN DU
      022220          L$DU::
187 022220 012737 177777 003110'  MOV    #-1,DUFLG
188 022226 010001          MOV    R0,R1
189 022230 006301          ASL    R1
190 022232 052761 140000 003174'  BIS    #140000,ERTABL(R1)  ; SAY DROPPED

```



```

191 022240 000240 000240 000240      240,240,240      ; ??????????
192 022246      PRINTF #1$,RO
      022246 010046      MOV RO,-(SP)
      022250 012746 022274'      MOV #1$,-(SP)
      022254 012746 000002      MOV #2$,-(SP)
      022260 010600      MOV SP,RO
      022262 104417      TRAP C#PNTF
      022264 062706 000006      ADD #6,SP
193 022270      EXIT DU
      022270 000167      .WORD J$JMP
      022272 000030      .WORD L10032-2-
194 022274      045      116      045 1$: .ASCIZ /%N%A UNIT %D%A DROPPED/
195      .EVEN
196 022324      ENDDU
      022324      L10032:
      022324 104453      TRAP C#DU
197      ;**
198      ; AUTO-DROP CODE SECTION.
199      ;--
200 022326      BGNAUTO
      022326      L$AUTO::
201 022326 013705 002204'      MOV CSRADDR,R5      ;POINT TO DEVICE REGISTER
202 022332 012703 000550      MOV #360.,R3      ;ENOUGH TIME FOR 2400' REEL TO REWIND
203 022336 004737 016060'      10$: JSR PC,WAITF      ;WAIT FOR SSR TO SET
204 022342 103420      BCS 20$      ;LEAVE WHEN SSR IS SET
205 022344      DELAY 250.      ;WAIT FOR .25 SECONDS
      022344 012727 000372      MOV #250.,(PC)+
      022350 000000      .WORD 0
      022352 013727 002116'      MOV L$DLY,(PC)+
      022356 000000      .WORD 0
      022360 005367 177772      DEC -6(PC)
      022364 001375      BNE .-4
      022366 005367 177756      DEC -22(PC)
      022372 001367      BNE .-20
206 022374 005303      DEC R3      ;BUMP COUNTER DOWN
207 022376 001357      BNE 10$      ;KEEP GOING
208 022400 004737 017014'      JSR PC,CKDROP      ;TRY AND DROP UNIT
209 022404
210 022404      20$: ENDAUTO      ; UNUSED.
      022404      L10033:
      022404 104461      TRAP C$AUTO
211
212      .SBTTL CLEAN-UP AND REPORT CODING SECTIONS
213
214      ;**
215      ; THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS
216      ; EXECUTED AT THE END OF EACH PASS (OR SUB-PASS).
217      ; USE TO RETURN DEVICE UNDER TEST TO A NEUTRAL STATE.
218      ;--
219 022406      BGNCLN
      J22406      L$CLEAN::
220 022406 013705 002204'      MOV CSRADDR,R5      ;POINT TO DEVICE REGISTER
221 022412 005737 003110'      TST DUFLG      ;"DROPPED" FLAG IS SET ON...
222 022416 100405      BMI 1$      ;...AND GROSS CONTROLLER FAULT...
223      ;...DON'T TRY TO XCT CLEANUP CODE.
224
225 022420 012765 000000 000002      MOV #0,TSSR(R5)      ;DO SOFT INIT

```

226	022426	004737	016060'		JSR	PC, WAITF	
227	022432			1\$:			
228	022432			2\$:	ENDCLN		
	022432			L10034:			
	022432	104412			TRAP	C\$CLEAN	
229					***		
230					; THE REPORT CODING SECTION CONTAINS THE		
231					; "PRINTS" CALLS THAT GENERATE STATISTICAL REPORTS.		
232					---		
233	022434				BGNRPT		
	022434			L\$RPT::			
234	022434				PRINTS	#DEVSUM	
	022434	012746	022676'		MOV	#DEVSUM, -(SP)	
	022440	012746	000001		MOV	#1, -(SP)	
	022444	010600			MOV	SP, R0	
	022446	104416			TRAP	C\$PNTS	
	022450	062706	000004		ADD	#4, SP	
235	022454	010246			MOV	R2, -(SP)	
236	022456	010346			MOV	R3, -(SP)	
237	022460	010446			MOV	R4, -(SP)	
238	022462	012704	003174'		MOV	#ERTABL, R4	; GET START OF ERROR TABLE.
239	022466	005003			CLR	R3	; CLEAR UNIT NUMBER
240	022470	011402		1\$:	MOV	(R4), R2	; GET ERROR TABLE ENTRY & TEST IT.
241	022472	001467			BEQ	4\$; ZERO IF UNIT NOT RUN
242	022474	100066			BPL	4\$	
243	022476	032702	040000		BIT	#BIT14, R2	; WAS UNIT DROPPED?
244	022502	001015			BNE	2\$; BR IF YES
245	022504	042702	170000		BIC	#C7777, R2	; GET ERROR COUNT FIELD
246	022510				PRINTS	#DEVONL, R3, R2	; PRINT
	022510	010246			MOV	R2, -(SP)	
	022512	010346			MOV	R3, -(SP)	
	022514	012746	022733'		MOV	#DEVONL, -(SP)	
	022520	012746	000003		MOV	#3, -(SP)	
	022524	010600			MOV	SP, R0	
	022526	104416			TRAP	C\$PNTS	
	022530	062706	000010		ADD	#10, SP	
247	022534	000446			BR	4\$	
248	022536	020227	160000	2\$:	CMP	R2, #160000	; WAS UNIT NON-EXISTENT?
249	022542	001012			BNE	3\$; BR IF NO
250	022544				PRINTS	#DEVNXR, R3	
	022544	010346			MOV	R3, -(SP)	
	022546	012746	023003'		MOV	#DEVNXR, -(SP)	
	022552	012746	000002		MOV	#2, -(SP)	
	022556	010600			MOV	SP, R0	
	022560	104416			TRAP	C\$PNTS	
	022562	062706	000006		ADD	#6, SP	
	022566	000431			BR	4\$	
251	022566	000431					
252	022570	020227	160001	3\$:	CMP	R2, #160001	; WAS UNIT NOT READY AT STARTUP?
253	022574	001012			BNE	30\$; BR IF NO.
254	022576				PRINTS	#DEVNRD, R3	
	022576	010346			MOV	R3, -(SP)	
	022600	012746	023065'		MOV	#DEVNRD, -(SP)	
	022604	012746	000002		MOV	#2, -(SP)	
	022610	010600			MOV	SP, R0	
	022612	104416			TRAP	C\$PNTS	
	022614	062706	000006		ADD	#6, SP	
255	022620	000414			BR	4\$	

```

256 022622 042702 170000      30$: BIC      #C7777,R2
257 022626      PRINTS    #DEVDR0,R3,R2
      022626 010246      MOV      R2,-(SP)
      022630 010346      MOV      R3,-(SP)
      022632 012746 023146'  MOV      #DEVDR0,-(SP)
      022636 012746 000003   MOV      #3,-(SP)
      022642 010600      MOV      SP,R0
      022644 104416      TRAP     C#PNTS
      022646 062706 000010   ADD      #10,SP
258 022652 062704 000002      4$: ADD      #2,R4
259 022656 005203      INC      R3
260 022660 020427 003374'  CMP      R4,#ERTABE
261 022664 103701      BLO     1#
262 022666 012604      MOV     (SP)+,R4
263 022670 012603      MOV     (SP)+,R3
264 022672 012602      MOV     (SP)+,R2
265 022674      ENDRPT ; UNUSED.
      022674      L10035:
      022674 104425      TRAP     C#RPT
266
267
268 022676      045      116      045 DEVSUM: .ASCIZ /#MADEVICE STATUS SUMMARY:#N/
269 022733      045      101      040 DEVONL: .ASCIZ /#A UNIT #D3#A ONLINE, ERRORS = #D#N/
270 023003      045      101      040 DEVNXR: .ASCIZ /#A UNIT #D3#A DROPPED, NON-EXISTENT REGISTER#N/
271 023065      045      101      040 DEVNRD: .ASCIZ /#A UNIT #D3#A DROPPED, NOT READY AT STARTUP#N/
272 023146      045      101      040 DEVDR0: .ASCIZ /#A UNIT #D3#A DROPPED, ERRORS = #D#N/
273
274
275 023216      ENDMOD
276
277
278
    
```

```

1          .TITLE TSV5A - HARDWARE TESTS
2
9
10 023216  BGNMOD TSV5
    023216  TSV5::
16
24
25          .SBITL TEST 1: BUS RESET TEST
26
27
28          ;
29          ; THIS TEST VERIFIES THAT THE M7455 MODULE'S DEVICE REGISTERS ARE
30          ; ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE
31          ; BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND
32          ; ANY BASIC PROBLEMS IN THE MODULE. AREAS OF LOGIC TESTED BY THE
33          ; SELF-TEST SEQUENCE ARE AS FOLLOWS: ROM AND PIPELINE REGISTER,
34          ; SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, AND, RAM. THIS
35          ; TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INIT SIGNAL
36          ; VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER,
37          ; WAITS A PERIOD OF TIME (TO ALLOW THE CONTROLLER'S INITIALIZATION
38          ; MICRODIAGNOSTIC SEQUENCE TO BE COMPLETED), AND THEN CHECKS THE
39          ; CONTENTS OF THE TSSR REGISTER. SUCCESSFUL INITIALIZATION IS
40          ; INDICATED BY SUBSYSTEM READY (SSR) AND NEED BUFFER ADDRESS (NBA)
41          ; BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17 AND A16 AND
42          ; OFL, WHICH ARE IGNORED FOR THIS TEST) BEING CLEAR (0). IF THE
43          ; CONTENTS OF TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED
44          ; LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES.
45          ; THE ERROR REPORT ANALYZES THE TSSR CONTENTS AND DISCERNS AND
46          ; REPORTS ONE OF THREE POSSIBILITIES:
47          ;
48          ;
49          ; 1. TSSR CONTENTS ARE AMBIGUOUS (ANY OF BITS 11-14 ARE SET,
50          ; OR STATES OF SSR AND SC BITS DO NOT CORRESPOND TO THE
51          ; APPARENT ERROR CODE IN BITS 0-5): INDICATES THAT THE
52          ; TSSR CONTENT CANNOT BE TRUSTED. INDICATES A
53          ; CATASTROPHIC CONTROLLER MALFUNCTION. THIS IS A FATAL
54          ; ERROR (EXECUTION IS ABORTED). FIELD ACTION WOULD BE TO
55          ; REPLACE THE M7455. IF THE M7455 ITSELF IS BEING
56          ; DEBUGGED, THE PROGRAM SHOULD BE RESTARTED WITH LOOP ON
57          ; ERROR ENABLED IN ORDER TO PROBE FOR THE PROBLEM.
58          ;
59          ; 2. SSR = 0, SC = 0 AND THE ERROR CODE IN BITS 0-5 IS IN
60          ; THE RANGE 17-13: THIS IS A FATAL ERROR. THE ERROR
61          ; CODE IS DECODED AND THE APPROPRIATE DESCRIPTION GIVEN.
62          ; INDICATES THAT A SERIOUS PROBLEM EXISTS.
63          ;
64 023216  BGNTEST
    023216
69 023216  012700 023414' MOV #TST1ID,RO ;ASCII MESSAGE TO IDENTIFY TEST
70 023222  004737 016322' JSR PC,TSTSETUP ;DO INITIAL TEST SETUP
71 023226  012737 000024 002214' MCV #2C.,LOOPCNT ;PERFORM 20 ITERATIONS
72 023234  T1LOOP:
73 023234  005003 CLR R3 ;USE R3 AS FATAL ERROR FLAG
74
75 023236  BGNSSUB ;////.////. BEGIN SUBTEST ////.////.
    023236  T1.1:
    023236  104402 TRAP C#BSUB
    
```

```

76
77 023240          BRESET          ;ISSUE A BUS RESET
    023240 104433          ;                               TRAP      C#RESET
78 023242 004737 016060'   JSR      PC,WAITF          ;WAIT FOR READY
79 023246 016501 000002   MOV      TSSR(R5),R1      ;GET THE CONTENTS OF TSSR
80 023252 010102          MOV      R1,R2           ;CONTENTS OF TSSR
81 023254 042702 176277   BIC      #1C<MIADDR!OFL>,R2 ;THESE BITS MAY BE SET
82 023260 052702 002200   BIS      #SSR!NBA,R2     ;READY AND NEW DATA SHOULD BE SET
83 023264 020102          CMP      R1,R2           ;COMPARE EXPECTED TO RECEIVED
84 023266 001405          BEQ      10$             ;BRANCH IF COMPARE
88 023270          ERROF      ERRNO,SFERR,SFFMSG ;REPORT A FATAL ERROR
    023270 104455          ;                               TRAP      C#ERDF
    023272 000145          ;                               .WORD    101
    023274 003677'        ;                               .WORD    SFERR
    023276 011712'        ;                               .WORD    SFFMSG
89 023300          INC      R3
90 023302          10$:
91 023302          ENDSUB
    023302          ;////////// END SUBTEST ////////////
    023302 104403          ;                               L10037:
    023302          ;                               TRAP      C#ESUB
92
93 023304 005703          TST      R3
94 023306 001402          BEQ      20$
95 023310 004737 017014'   JSR      PC,CKDROP
96 023314 005003          CLR      R3
97
98
99 023316          BGNSUB
    023316          ;////////// BEGIN SUBTEST ////////////
    023316 104402          ;                               T1.2:
    023316          ;                               TRAP      C#BSUB
100
101 023320 005065 000002   CLR      TSSR(R5)
102 023324 004737 016060'   JSR      PC,WAITF          ;WRITE TO ISSUE A SOFT RESET
103 023330 016501 000002   MOV      TSSR(R5),R1      ;WAIT FOR READY TO SET
104 023334 010102          MOV      R1,R2           ;GET REGISTER TSSR DATA
105 023336 042702 176277   BIC      #1C<MIADDR!OFL>,R2 ;CONTENTS OF TSSR
106 023342 052702 002200   BIS      #SSR!NBA,R2     ;THESE BITS MAY BE SET
107 023346 020102          CMP      R1,R2           ;READY AND NEW DATA SHOULD BE SET
108 023350 001405          BEQ      10$             ;COMPARE EXPECTED TO RECEIVED
112 023352          ERROF      ERRNO,SFIERR,SFFMSG ;BRANCH IF COMPARE
    023352 104455          ;REPORT A FATAL ERROR
    023354 000146          ;                               TRAP      C#ERDF
    023356 003644'        ;                               .WORD    102
    023360 011712'        ;                               .WORD    SFIERR
    023362 005203          ;                               .WORD    SFFMSG
113 023362          INC      R3
114 023364          10$:
115 023364          ENDSUB
    023364          ;////////// END SUBTEST ////////////
    023364 104403          ;                               L10040:
    023364          ;                               TRAP      C#ESUB
116
117
118 023366 005703          TST      R3
119 023370 001402          BEQ      20$
120 023372 004737 017014'   JSR      PC,CKDROP
121 023376 004737 016270'   JSR      PC,TSTLOOP
122 023402 103002          BCC     40$
123 023404 000137 023234'   JMP      T1LOOP
    ;FATAL ERROR DETECTED ?
    ;BRANCH IF NOT
    ;SEE IF TIME TO DROP UNIT
    ;SHOULD WE DO ITERATIONS ?
    ;BRANCH IF NOT
    ;LOOP UNTIL COUNT EXPIRED

```

```

124 023410          401:  EXIT   TST           ;ALL DONE THIS TEST
      023410 104432
      023412 000022          TRAP   C0EXIT
                                .WORD  L10036 .
125
126
127          ;*
128          ;LOCAL TEXT MESSAGES FOR TEST
129          ;-
130 023414      111      156      151  TST1ID: .ASCIZ  'Initialization'
131          .EVEN
132 023434          ENDTST
      023434
      023434 104401          L10036:  TRAP   C0ETST
133
134          .SBTTL  TEST  2:  WRAP DATA - HIGH BYTE
135
136
137          ;          THIS TEST VERIFIES OPERATION OF:
138          ;
139          ;
140          ;          1.  PART OF THE PDP-11 BUS INTERFACE SECTION OF THE M7455
141          ;          MODULE; PART OF THE INPUT FILE (TSDB HIGH BYTE), PART
142          ;          OF THE OUTPUT FILE (TSSR HIGH BYTE AND TSBA, BOTH
143          ;          BYTES), PART OF THE DCO05 TRANSCEIVER CIRCUITS (ADDRESS
144          ;          DECODER, BDAL DRIVERS, HIGH BYTE OF INTERNAL DAL BUS
145          ;          DRIVERS), AND BASIC PROGRAMMED I/O CONTROL SEQUENCES
146          ;          AND LOGIC;
147          ;
148          ;          2.  PART OF 2901 MICROPROCESSOR ELEMENTS (Q-REGISTER,
149          ;          REGISTER 0, ROTATE AND NEGATE FUNCTIONS;
150          ;
151          ;          3.  Y AND SOURCE BUSES;
152          ;
153          ;          4.  BASIC MICROPROGRAM SEQUENCES.
154          ;
155          ;
156          ;
157          ;          THE PROGRAM WRITES A TEST DATA BYTE INTO THE HIGH BYTE OF TSDB.
158          ;          WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS
159          ;          OF BOTH TSBA AND TSSR.  THE MODULE IS FUNCTIONING CORRECTLY IF
160          ;          DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT
161          ;          OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS
162          ;          8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN
163          ;          WRITTEN.  AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN
164          ;          IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED.  THE ANALYSIS
165          ;          LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED
166          ;          ABOVE.  THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA
167          ;          BYTES (0-377 OCTAL).
168          ;
169 023436          BGNTST
      023436
174 023436 012700 024104'  MOV     #TST2ID,R0          T2::
175 023442 004737 016322'  JSR     PC,TSTSETUP      ;ASCII MESSAGE TO IDENTIFY TEST
176 023446 012737 000024 002214'  MOV     #20.,LOOPCNT    ;DO INITIAL TEST SETUP
177 023454 005004          MOV     #20.,LOOPCNT    ;PERFORM 20 ITERATIONS
178 023456 012703 177777  T2LOOP: CLR     R4          ;STARTING DATA PATTERN
179 023462 005703          MOV     #-1,R3         ;DO INIT ON FIRST TIME THROUGH
                                TST     R3          ;DO WE NEED SOFT INIT
    
```

180	023464	001412		BEQ	10#			;	BRANCH IF NOT
181	023466	005003		CLR	R3			;	DON'T NEED INIT NEXT TIME THRU
182	023470	004737	015604'	JSR	PC,SOFINIT			;	DO SOFT INIT OF CONTROLLER
183	023474	103406		BCS	10#			;	BR IF SOFT INIT = OK
187	023476	010001		MOV	R0,R1			;	SAVE CONTENTS OF TSSR
188	023500			ERRDF	ERRNO,SFIERR,SFIMSG			;	DEVICE FATAL ERROR DURING INIT
	023500	104455							TRAP C#ERDF
	023502	000311							.WORD 201
	023504	003644'							.WORD SFIERR
	023506	011644'							.WORD SFIMSG
189	023510	005203		INC	R3			;	FORCE SOFT INIT ON NEXT PASS
190	023512	005037	002220'	CLR	FATFLG		10#:	;	CLEAR FATAL ERROR FLAG
191									
192	023516			BGNSEG				;	>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>
	023516	104404							TRAP C#BSEG
193									
194	023520	110465	000001	MOVB	R4,TSDBH(R5)			;	SET MAINT MODE + WRITE DATA
195	023524	004737	016060'	JSR	PC,WAITF			;	WAIT FOR SSR TO SET
196	023530	103411		BCS	15#			;	BR IF CARRY SET (GOOD RETURN)
197	023532	010001		MOV	R0,R1			;	SAVE CONTENTS OF TSSR
198	023534	010402		MOV	R4,R2			;	DATA THAT WAS WRITTEN
202	023536			ERRDF	ERRNO,T2SSR,EXPREC			;	DEVICE FATAL SSR FAILED TO SET
	023536	104455							TRAP C#ERDF
	023540	000312							.WORD 202
	023542	024032'							.WORD T2SSR
	023544	015304'							.WORD EXPREC
203	023546	005203		INC	R3			;	FORCE SOFT INIT ON NEXT PASS
204	023550	005237	002220'	INC	FATFLG			;	SET FATAL ERROR FLAG
205	023554			CKLOOP			15#:	;	LOOP ON ERROR, IF FLAG SET
	023554	104406							TRAP C#CLP1
206	023556	005737	002220'	TST	FATFLG			;	WAS FATAL ERROR RECEIVED ?
207	023562	001402		BEQ	20#			;	BRANCH IF NOT
208	023564	004737	017014'	JSR	PC,CKDROP			;	SEE IF TIME TO DROP UNIT
209	023570	010402		MOV	R4,R2		20#:	;	DATA PATTERN WRITTEN
210	023572	042702	177774	BIC	#C<BIT0!BIT1>,R2			;	CLEAR ALL BUT LOW 2 BITS
211	023576	000302		SWAB	R2			;	BITS 8 AND 9 HAVE LOW DATA BITS
212	023600	052702	002200	BIS	#SSR!NBA,R2			;	THESE BITS MUST BE SET ALSO
213	023604	016501	000002	MOV	TSSR(R5),R1			;	GET THE CONTENTS OF TSSR
214	023610	032701	000100	BIT	#OFL,R1			;	IS OFF-LINE BIT SET ?
215	023614	001402		BEQ	25#			;	BRANCH IF NOT OFF-LINE
216	023616	052702	000100	BIS	#OFL,R2			;	SET OFF-LINE IN EXPECTED DATA
217	023622	020201		CMP	R2,R1		25#:	;	DOES EXPECTED MATCH RECEIVED ?
218	023624	001405		BEQ	30#			;	OKAY IF MATCH
222	023626			ERRHRD	ERRNO,T2TSSR,EXPREC			;	TSSR WASN'T CORRECT
	023626	104456							TRAP C#ERHRD
	023630	000313							.WORD 203
	023632	023765'							.WORD T2TSSR
	023634	015304'							.WORD EXPREC
223	023636	005203		INC	R3			;	FORCE SOFT INIT ON NEXT PASS
224	023640			CKLOOP			30#:	;	LOOP ON ERROR ?
	023640	104406							TRAP C#CLP1
225	023642	016501	000000	MOV	TSBA(R5),R1			;	GET TSBA REGISTER CONTENTS
226	023646	005002		CLR	R2			;	
227	023650	150402		BISB	R4,R2			;	DATA PATTERN WRITTEN
228	023652	000302		SWAB	R2			;	MOVE INTO TOP BYTE
229	023654	150402		BISB	R4,R2			;	BOTH HALVES SHOULD BE SAME
230	023656	020102		CMP	R1,R2			;	COMPARE EXPECTED TO RECEIVED

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 TEST 4: RAM TEST

SEQ 102

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537
538 025206 005304          35$: DEC R4          ;SET BACK TO 7777
539 025210 005002          CLR R2          ;SET TO ALL ZEROS
540 025212 004737 016146' 40$: JSR PC,CHKTSSR ;WAIT FOR READY (SSR) TO SET
541 025216 010465 000000   MOV R4,TSDB(R5) ;LOAD UP THE ADDRESS FOR RAM
542 025222 004737 016146'   JSR PC,CHKTSSR ;WAIT FOR READY (SSR) TO SET
543 025226 016501 000000   MOV TSBA(R5),R1 ;READ THE RAM CONTENTS BACK
544 025232 005002          CLR R2          ;LOOKING FOR 000000 (EXPECTED)
545 025234 120102          CMPB R1,R2      ;BOTH SHOULD BE 00000000 BINARY
546 025236 001404          BEQ 43$        ;BR, IF DATA IS GOOD
550 025240          ERRHRD ERRNO,TSBAM3,EXPREC ;CHARACTERISTICS DATA NOT CORRECT
      025240 104456          TRAP C$ERHRD
      025242 000627          .WORD 407
      025244 026056'        .WORD TSBAM3
      025246 015304'        .WORD EXPREC
551 025250 012702 000377 43$: MOV #000377,R2 ;SET ALL ONES WORD
552 025254 010465 000000   MOV R4,TSDB(R5) ;LOAD UP RAM ADDRESS POINTER
553 025260 004737 016146'   JSR PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
554 025264 110265 000000   MOVB R2,TSDB(R5) ;WRITE DATA INTO RAM
555 025270 004737 016146'   JSR PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
556 025274 016501 000000   MOV TSBA(R5),R1 ;READ RAM CONTENTS BACK
557 025300 120102          CMPB R1,R2      ;CHECK WITH DATA WRITTEN
558 025302 001404          BEQ 45$        ;BR IF OK, DATA IN = DATA OUT
562 025304          ERRHRD ERRNO,TSBAM2,EXPREC ;WRITTEN DATA NOT = TO READ
      025304 104456          TRAP C$ERHRD
      025306 000630          .WORD 408
      025310 025774'        .WORD TSBAM2
      025312 015304'        .WORD EXPREC
563 025314          45$: CKLOOP ;SCOPE LOOP
      025314 104406          TRAP C$CLP1
564 025316 004737 016146'   JSR PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
565 025322 010465 000000   MOV R4,TSDB(R5) ;WORD WRITE TO SET UP ADDRESS
566 025326 004737 016146'   JSR PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
567 025332 116501 000001   MOVB TSBAH(R5),R1 ;HIGH BYTE READ OF TSBA
568 025336 010403          MOV R4,R3      ;DATA PATTERN WRITTEN
569 025340 000303          SWAB R3       ;HIGH TO LOW
570 025342 060403          ADD R4,R3     ;TOTAL OF BYTES IN LOW BYTE
571 025344 120103          CMPB R1,R3    ;SUM OF BYTES WRITTEN TO TSDB = TSBAH
572 025346 001404          BEQ 50$      ;BR IF OK, THEY SHOULD BE
576 025350          ERRHRD ERRNO,M2901,EXPREC ;2901 PROBLEM ADDER
      025350 104456          TRAP C$ERHRD
      025352 000631          .WORD 409
      025354 025704'        .WORD M2901
      025356 015304'        .WORD EXPREC
577 025360          50$: CKLOOP ;SCOPE LOOP
      025360 104406          TRAP C$CLP1
578 025362 005304          DEC R4         ;DROP RAM ADDRESS POINTER
579 025364 002312          BGE 40$      ;NOT AT LOC. ZERO YET
580
581 025366          ENDSUB ;////////////////// END SUBTEST ////////////////////
      025366 104403          L10045: TRAP C$ESUB
582
583
584 025370          BGNSUB ;////////////////// BEGIN SUBTEST ////////////////////
      025370 104402          T4.3: TRAP C$BSUB
      025370 104402

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025550 104456                                TRAP      C$ERHRD
025552 000634                                .WORD    412
025554 026056'                               .WORD    TSBAM3
025556 015304'                               .WORD    EXPREC
638 025560 005002          43$: CLR      R2                ;SET UP NEW EXPECTED
639 025562 010465 000000    MOV      R4,TSDB(R5)        ;LOAD UP RAM ADDRESS POINTER
640 025566 004737 016146'   JSR      PC,CHKTSSR        ;WAIT FOR READY, NON-AMBIGUOUS
641 025572 110265 000000    MOVB    R2,TSDB(R5)        ;WRITE DATA INTO RAM
642 025576 004737 016146'   JSR      PC,CHKTSSR        ;WAIT FOR READY, NON-AMBIGUOUS
643 025602 016501 000000    MOV      TSBA(R5),R1       ;READ RAM CONTENTS BACK
644 025606 120102          CMPB    R1,R2              ;CHECK WITH DATA WRITTEN
645 025610 001404          BEQ     45$                ;BR IF OK, DATA IN = DATA OUT
649 025612          ERRHRD  ERRNO,TSBAM2,EXPREC ;WRITTEN DATA NOT = TO READ
                                TRAP      C$ERHRD
                                .WORD    413
                                .WORD    TSBAM2
                                .WORD    EXPREC
025612 104456
025614 000635
025616 025774'
025620 015304'
650 025622          45$: CKLOOP          ;SCOPE LOOP
                                TRAP      C$CLP1
                                .WORD    EXPREC
025622 104406
651 025624 004737 016146'   JSR      PC,CHKTSSR        ;WAIT FOR READY, NON-AMBIGUOUS
652 025630 116501 000001    MOVB    TSBAH(R5),R1      ;HIGH BYTE READ OF TSBA
653 025634 010203          MOV     R2,R3              ;DATA PATTERN WRITTEN
654 025636 000303          SWAB   R3                  ;HIGH TO LOW
655 025640 060203          ADD    R2,R3              ;TOTAL OF BYTES IN LOW BYTE
656 025642 120103          CMPB    R1,R3              ;SUM OF BYTES WRITTEN TO TSDB = TSBAH
657 025644 001404          BEQ     50$                ;BR IF OK, THEY SHOULD BE
661 025646          ERRHRD  ERRNO,M2901,EXPREC ;2901 PROBLEM ADDER
                                TRAP      C$ERHRD
                                .WORD    414
                                .WORD    M2901
                                .WORD    EXPREC
025646 104456
025650 000636
025652 025704'
025654 015304'
662 025656          50$: CKLOOP          ;SCOPE LOOP
                                TRAP      C$CLP1
                                .WORD    EXPREC
025656 104406
663 025660 005304          DEC     R4                  ;DROP RAM ADDRESS POINTER
664 025662 001315          BNE    40$                 ;NOT AT LOC. ZERO YET
665
666 025664          ENDSUB          ;////////////////// END SUBTEST ////////////////////
025664          L10046:          TRAP      C$ESUB
025664 104403
667
668 025666 004737 016270'   JSR      PC,TSTLOOP        ;DO WE NEED TO ITERATE TEST ?
669 025672 103002          BCC    63$                 ;BRANCH IF NOT
670 025674 000137 024646'   JMP     T4LOOP             ;EXECUTE AGAIN
671 025700          63$: EXIT      TST        ;ALL DONE THIS TEST
025700 104432                                TRAP      C$EXIT
025702 000256                                .WORD    L10043-.

672
673          ;*
674          ;LOCAL TEXT MESSAGES FOR TEST
675          ;-
676 025704 040 124 123 M2901: .ASCIZ ' TSBA High Byte Not Sum of Last TSDB Write (2901 Error)'
677 025774 040 127 162 TSBAM2: .ASCIZ ' Write to TSDB Not Equal to Read of TSBA Low Byte'
678 026056 127 162 151 TSBAM3: .ASCIZ ' Write To RAM Location Modified Another Location'
679 026136 122 101 115 TST4ID: .ASCIZ 'RAM Verification'
680          .EVEN
681 026160          ENDTST
026160
                                L10043:
    
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026160 104401

TRAP C#ETST

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.SBTTL TEST 5: SECOND INITIALIZATION TEST

THIS TEST VERIFIES THE SAME ELEMENTS AS DID INITIALIZATION TEST #1 AND ALSO CHECKS THAT CERTAIN PARTS OF RAM IS CLEARED TO ZERO AND THAT 2901 REGISTERS 10 AND 11 ARE ALSO CLEARED TO ZERO. THIS IS A CONFIDENCE CHECK OF A PART OF THE SELF-TEST SEQUENCE (I.E., THAT IT IS REALLY BEING EXECUTED). FOR EACH OF TWO SUBTESTS (ONE FOR INITIALIZING VIA A BUS INIT, THE OTHER FOR INITIALIZING BY WRITING INTO THE TSSR), THE FOLLOWING SEQUENCE IS PERFORMED:

1. EACH RAM LOCATION AND 2901 REGISTERS 10 AND 11 ARE SET TO ALL 1'S BY USING WRITES INTO THE TSDB REGISTER (LOW BYTE AND MAINTENANCE MODE WORD WRITES).
2. THE CONTROLLER IS INITIALIZED AND THE VARIOUS CHECKS ON THE TSSR DESCRIBED IN INITIALIZATION TEST #1 ARE PERFORMED.
3. #1'S (377 OCTAL) ARE WRITTEN INTO THE LOW BYTE OF TSDB, WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM ADDRESS, SHOULD BE 0. RAM LOCATION 0 IS VERIFIED BY WRITING A WORD OF ZEROS INTO THE TSDB. THE RESULTING LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0. DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM OR A TIMING PROBLEM.

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026162
026162
026162 012700 027134'
026166 004737 016322'
026172 012737 000024 002214'
026200
026200 005037 002220'
026204
026204
026204 104402
026206 004737 015604'
026212 103404
026214
026214 104455
026216 000765
026220 003644'

TSLOOP:

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BGNTS1
                                TS::
MOV    #TSTSID,RO                ;ASCII MESSAGE TO IDENTIFY TEST
JSR    PC,TSTSETUP                ;DO INITIAL TEST SETUP
MOV    #20,,LOOPCNT              ;PERFORM 20 ITERATIONS
CLR    FATFLG                     ;CLEAR THE FATAL ERROR FLAG
BGNSUB                            ;////////// BEGIN SUBTEST ////////////
                                TS.1:
                                TRAP    C#BSUB
JSR    PC,SOFINIT                 ;DO A SOFT TO START
BCS    10#                         ;BRANCH IF O.K.
ERRDF  ERRNO,SFIERR,SFIMSG        ;REPORT ERROR AND DROP DRIVE
                                TRAP    C#ERDF
                                .WORD  501
                                .WORD  SFIERR

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026222 011644'                                     .WORD SFIMSG
739 026224 012702 177777 101: MOV      #1,R2           ;ALL ONE DATA PATTERN
740 026230 005004                                     ;STARTING RAM ADDRESS
741 026232 004737 016146'                            ;WAIT FOR READY, NON-AMBIGUOUS
742 026236 105065 000000 151: CLR      TSDB(R5)        ;SET MAINTENANCE MODE
743 026242 004737 016146'                            ;WAIT FOR READY, NON-AMBIGUOUS
744 026246 010465 000000                            ;SET THE NEXT RAM ADDRESS
745 026252 004737 016146'                            ;WAIT FOR READY, NON-AMBIGUOUS
746 026256 110265 000000                            ;LOAD TEST DATA
747 026262 005204                                     ;NEXT ADDRESS TO TEST
748 026264 020427 007777                            ;COMPARE TO LAST ADDRESS
749 026270 003762                                     ;BRANCH TILL ALL DATA WRITTEN
750 026272                                     ;ISSUE A BUS RESET
                                TRAP          C#RESET
026272 104433                                     ;WAIT FOR READY, NON-AMBIGUOUS
751 026274 004737 016146' JSR      PC,CHKTSSR        ;GET THE CONTENTS OF TSSR
752 026300 016501 000002                            ;CONTENTS OF TSSR
753 026304 010102                                MOV      R1,R2
754 026306 042702 176277 BIC      #C<HIADDR!OFL>,R2 ;THESE BITS MAY BE SET
755 026312 052702 002200 BIS      #SSR!NBA,R2       ;READY AND NEW DATA SHOULD BE SET
756 026316 020102                                CMP      R1,R2
757 026320 001406 BEQ     201                ;COMPARE EXPECTED TO RECEIVED
761 026322 ERRDF  ERRNO,SFHERR,SFFMSG              ;BRANCH IF COMPARE
                                ;REPORT A FATAL ERROR
                                TRAP          C#ERDF
                                .WORD        502
                                .WORD        SFHERR
                                .WORD        SFFMSG
026322 104455                                     ;SET FATAL ERROR FLAG
026324 000766                                     ;LOOP ON ERROR IF FLAG SET
026326 003677'                                     ;EXIT IF FATAL ERROR DETECTED
026330 011712'                                     ;NO MORE CHECKS IF FATAL ERROR
762 026332 005237 002220' 201: INC      FATFLG
763 026336 CKLOOP
764 026340 ESCAPE SUB
                                TRAP          C#CLP1
                                TRAP          C#ESCAPE
                                .WORD        L10050-.
026342 000170
765 026344 004737 016146' JSR      PC,CHKTSSR        ;WAIT FOR SSR TO SET
766 026350 105065 000000 CLR      TSDB(R5)        ;PUT BACK INTO MAINTENANCE MODE
767 026354 004737 016146' JSR      PC,CHKTSSR        ;WAIT FOR READY, NON-AMBIGUOUS
768 026360 005065 000000 CLR      TSDB(R5)        ;SET ADDRESS BACK TO 0000
769 026364 012702 000377 MOV      #377,R2
770 026370 004737 016146' JSR      PC,CHKTSSR        ;WAIT FOR READY, NON-AMBIGUOUS
771 026374 110265 000000 MOV      R2,TSDB(R5)      ;SHOULD POINT TO RAM 0
772 026400 004737 016146' JSR      PC,CHKTSSR        ;WAIT FOR READY, NON-AMBIGUOUS
773 026404 005065 000000 CLR      TSDB(R5)        ;SELECT LOCATION 0
774 026410 004737 016146' JSR      PC,CHKTSSR        ;WAIT FOR READY, NON-AMBIGUOUS
775 026414 116501 000000 MOV      TSBA(R5),R1     ;READ RAM LOCATION SPECIFIED
776 026420 120102 CMP      R1,R2           ;LOCATION SHOULD BE 377 OCTAL
777 026422 001406 BEQ     251                ;BR IF OK
778 026424 ERRDF  ERRNO,TSADDR,EXPREC            ;WASN'T POINTING TO CORRECT LOC.
                                TRAP          C#ERDF
                                .WORD        502
                                .WORD        TSADDR
                                .WORD        EXPREC
026424 104455                                     ;SET THE FATAL ERROR FLAG
026426 000766                                     ;SCOPE LOOP
026430 027222'                                     ;NO MORE CHECKS IF FATAL ERROR
026432 015304'                                     ;START WITH LOC 310
779 026434 005237 002220' 251: INC      FATFLG
780 026440 CKLOOP
781 026442 ESCAPE SUB
782 026444 000066 MOV      #310,R4
026444 012704 000310

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783	026452	005002			CLR	R2			;MEMORY EXPECTED SHOULD BE 000000		
784	026454	004737	016146'		JSR	PC,CHKTSSR			;WAIT FOR READY, NON-AMBIGUOUS		
785	026460	010465	000000	30#:	MOV	R4,TSDB(R5)			;SELECT LOCATION SPECIFIED		
786	026464	004737	016146'		JSR	PC,CHKTSSR			;WAIT FOR READY, NON-AMBIGUOUS		
787	026470	116501	000000		MOVB	TSBA(R5),R1			;READ LOC CONTENTS		
788	026474	120102			CMPS	R1,R2			;CHECK MEMORY FOR 000000		
789	026476	001406			BEQ	40#			;BRANCH IF DATA OKAY		
790	026500				ERRDF	ERRNO,TSMEM,SFFMSG			;MEMORY NOT ZERO AFTER INIT.		
	026500	104455								TRAP	C#ERDF
	026502	000766								.WORD	502
	026504	027164'								.WORD	TSMEM
	026506	011712'								.WORD	SFFMSG
791	026510	005237	002220'		INC	FATFLG			;SET THE FATAL ERROR FLAG		
792	026514			40#:	CKLOOP						
793	026516	104406			ESCAPE	SUB			;EXIT ON FATAL ERROR	TRAP	C#CLP1
	026516	104410									
	026520	000012								TRAP	C#ESCAPE
	026522	000012								.WORD	L10050 .
794	026522	005204			INC	R4			;LOOK AT NEXT RAM LOC.		
795	026524	020427	000400		CMP	R4,#400			;AT TOP OF RAM ADDRESS SPACE		
796	026530	001353			BNE	30#			;BRANCH TILL ALL MEMORY TESTED		
797											
798	026532				ENDSUB				;////////// END SUBTEST //////////		
	026532										
	026532	104403								L10050:	
										TRAP	C#ESUB
799											
800	026534	005737	002220'		TST	FATFLG			;IS FATAL ERROR FLAG SET ?		
801	026540	001404			BEQ	50#			;BRANCH IF NOT		
802	026542	004737	017014'		JSR	PC,CKDROP			;NO LOOP, TRY TO DROP DEVICE		
803	026546	005037	002220'		CLR	FATFLG			;CLEAR THE FATAL ERROR FLAG		
804	026552			50#:							
805											
806	026552				BGNSUB				;////////// BEGIN SUBTEST //////////		
	026552										
	026552	104402								TS.2:	
										TRAP	C#BSUB
807											
808	026554	004737	015604'		JSR	PC,SOFINIT			;DO A SOFT TO START		
809	026560	103404			BCS	10#			;BRANCH IF O.K.		
813	026562				ERRDF	ERRNO,SFIERR,SFIMSG			;REPORT ERROR AND DROP DRIVE		
	026562	104455								TRAP	C#ERDF
	026564	000767								.WORD	503
	026566	003644'								.WORD	SFIERR
	026570	011644'								.WORD	SFIMSG
814	026572	012702	177777	10#:	MOV	#-1,R2			;ALL ONE DATA PATTERN		
815	026576	005004			CLR	R4			;STARTING RAM ADDRESS		
816	026600	004737	016146'		JSR	PC,CHKTSSR			;WAIT FOR READY, NON-AMBIGUOUS		
817	026604	105065	000000	15#:	CLRB	TSDB(R5)			;SET MAINTENANCE MODE		
818	026610	004737	016146'		JSR	PC,CHKTSSR			;WAIT FOR READY, NON-AMBIGUOUS		
819	026614	010465	000000		MOV	R4,TSDB(R5)			;SET THE NEXT RAM ADDRESS		
820	026620	004737	016146'		JSR	PC,CHKTSSR			;WAIT FOR READY, NON-AMBIGUOUS		
821	026624	110265	000000		MOVB	R2,TSDB(R5)			;LOAD TEST DATA		
822	026630	005204			INC	R4			;NEXT ADDRESS TO TEST		
823	026632	020427	007777		CMP	R4,#777			;COMPARE TO LAST ADDRESS		
824	026636	003762			BLE	15#			;BRANCH TILL ALL DATA WRITTEN		
825	026640	005065	000002		CLR	TSSR(R5)			;ISSUE A SOFT RESET		
826	026644	004737	016146'		JSR	PC,CHKTSSR			;WAIT FOR READY, NON-AMBIGUOUS		
827	026650	016501	000002		MOV	TSSR(R5),R1			;GET THE CONTENTS OF TSSR		

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TEST 5: SECOND INITIALIZATION TEST

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828	026654	010102		MOV	R1,R2		;CONTENTS OF TSSR		
829	026656	042702	'176277	BIC	@C<HIADDR!OFL>,R2		;THESE BITS MAY BE SET		
830	026662	052702	002200	BIS	@SSR!NBA,R2		;READY AND NEW DATA SHOULD BE SET		
831	026666	020102		CMP	R1,R2		;COMPARE EXPECTED TO RECEIVED		
832	026670	001406		BEQ	20:		;BRANCH IF COMPARE		
836	026672			ERRDF	ERRNO,SFHERR,SFFMSG		;REPORT A FATAL ERROR		
	026672	104455						TRAP	C#ERDF
	026674	000770						.WORD	504
	026676	003677'						.WORD	SFHERR
	026700	011712'						.WORD	SFFMSG
837	026702	005237	002220'	INC	FATFLG		;SET FATAL ERROR FLAG		
838	026706			20:	CKLOOP		;LOOP ON ERROR IF FLAG SET		
	026706	104406						TRAP	C#CLP1
839	026710			ESCAPE	SUB		;EXIT IF FATAL ERROR DETECTED		
	026710	104410						TRAP	C#ESCAPE
	026712	000170						.WORD	L10051--
840	026714	004737	016146'	JSR	PC,CHKTSSR		;WAIT FOR SSR TO SET		
841	026720	105065	000000	CLRB	TSDB(R5)		;PUT BACK INTO MAINTENANCE MODE		
842	026724	004737	016146'	JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
843	026730	005065	000000	CLR	TSDB(R5)		;SET ADDRESS BACK TO 0000		
844	026734	012702	000377	MOV	#377,R2				
845	026740	004737	016146'	JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
846	026744	110265	000000	MOVB	R2,TSDB(R5)		;SHOULD POINT TO RAM 0		
847	026750	004737	016146'	JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
848	026754	005065	000000	CLR	TSDB(R5)		;SELECT LOCATION 0		
849	026760	004737	016146'	JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
850	026764	116501	000000	MOVB	TSBA(R5),R1		;READ RAM LOCATION SPECIFIED		
851	026770	120102		CMPB	R1,R2		;LOCATION SHOULD BE 377 OCTAL		
852	026772	001406		BEQ	25:		;BR IF OK		
853	026774			ERRDF	ERRNO,TSADDR,EXPREC		;WASN'T POINTING TO CORRECT LOC.		
	026774	104455						TRAP	C#ERDF
	026776	000770						.WORD	504
	027000	027222'						.WORD	TSADDR
	027002	015304'						.WORD	EXPREC
854	027004	005237	002220'	INC	FATFLG		;SET THE FATAL ERROR FLAG		
855	027010			25:	CKLOOP		;SCOPE LOOP		
	027010	104406						TRAP	C#CLP1
856	027012			ESCAPE	SUB		;NO MORE CHECKS IF FATAL ERROR		
	027012	104410						TRAP	C#ESCAPE
	027014	000066						.WORD	L10051--
857	027016	012704	000310	MOV	#310,R4		;START WITH LOC 310		
858	027022	005002		CLR	R2		;MEMORY EXPECTED SHOULD BE 000000		
859	027024	004737	016146'	JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
860	027030	010465	000000	MOV	R4,TSDB(R5)		;SELECT LOCATION SPECIFIED		
861	027034	004737	016146'	JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
862	027040	116501	000000	MOVB	TSBA(R5),R1		;READ LOC CONTENTS		
863	027044	120102		CMPB	R1,R2		;CHECK MEMORY FOR 000000		
864	027046	001406		BEQ	40:		;BRANCH IF DATA OKAY		
865	027050			ERRDF	ERRNO,TSMEM,SFFMSG		;MEMORY NOT ZERO AFTER INIT.		
	027050	104455						TRAP	C#ERDF
	027052	000770						.WORD	504
	027054	027164'						.WORD	TSMEM
	027056	011712'						.WORD	SFFMSG
866	027060	005237	002220'	INC	FATFLG		;SET THE FATAL ERROR FLAG		
867	027064			40:	CKLOOP				
	027064	104406						TRAP	C#CLP1
868	027066			ESCAPE	SUB		;EXIT ON FATAL ERROR		

```

027066 104410
027070 000012
869 027072 005204
870 027074 020427 000400
871 027100 001353
872
873 027102
027102
027102 104403
874
875 027104 005737 002220'
876 027110 001402
877 027112 004737 017014'
878 027116 004737 016270'
879 027122 103002
880 027124 000137 026200'
881 027130
027130 104432
027132 000132
882
883
884
885
886
887 027134 105 170 164 TST5ID: .ASCIZ 'Extended Initialization'
888 027164 111 156 143 TSMEM: .ASCIZ 'Incorrect RAM Data After Init'
889 027222 111 156 143 TSADDR: .ASCIZ 'Incorrect RAM Address After Init'
890 .EVEN
891 027264
027264
027264 104401
892
893
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; LOCAL TEXT MESSAGES FOR TEST
; -
;
; THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE
; CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS
; (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR
; REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS
; REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC
; COMMAND DECODING AND DATI DMA HANDLING. THIS TEST CONTAINS TWO
; SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER
; THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT
; CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE
; REJECTED COMMAND; SUBTEST 2 PERFORMS SIMILARLY TO SUBTEST 1 BUT
; SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN
; INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED. SUBTEST 1
; SETS UP THE INTERRUPT SERVICE ROUTINE TO FLAG UNEXPECTED
; INTERRUPTS. THE COMMAND WORD IN THE COMMAND BUFFER IS
; INITIALIZED TO 100000 (OCTAL) AND THE REMAINING THREE WORDS IN
; THE COMMAND BUFFER ARE SET TO KNOWN UNIQUE PATTERNS. THEN THE
; FOLLOWING SEQUENCE IS PERFORMED:
;
; 1. INITIALIZE THE CONTROLLER BY WRITING INTO THE TSSR;
; PROPER INITIAL CONDITIONS ARE VERIFIED.

TRAP C$ESCAPE
.WORD L10051-.

;LOOK AT NEXT RAM LOC.
;AT TOP OF RAM ADDRESS SPACE
;BRANCH TILL ALL MEMORY TESTED

;////////////////////// END SUBTEST ////////////////////////
L10051:
TRAP C$ESUB

;IS FATAL ERROR FLAG SET ?
;BRANCH IF NOT
;NO LOOP, TRY TO DROP DEVICE
;SHOULD WE DO ITERATIONS ?
;BRANCH IF NOT
;LOOP UNTIL COUNT EXPIRED
;ALL DONE THIS TEST

TRAP C$EXIT
.WORD L10047-.

L10047:
TRAP C$ETST

.SBTTL TEST 6: COMMAND REJECT

```


976	027340			ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT		
	027340	104455					TRAP	C#ERDF
	027342	001131					.WORD	601
	027344	003644'					.WORD	SFIERR
	027346	011644'					.WORD	SFIMSG
977	027350	005037	002220'	10#:	CLR	FATFLG		
978	027354	005037	002222'		CLR	INTRECV		
979	027360	004737	016146'		JSR	PC,CHKTSSR		
980	027364	042714	000200		BIC	#BIT7,(R4)		
981	027370	010465	000000		MOV	R4,TSDB(R5)		
982	027374	004737	016060'		JSR	PC,WAITF		
983	027400	103407			BCS	15#		
984	027402	010001			MOV	R0,R1		
988	027404				ERRDF	ERRNO,T6SSR,PKTSSR		
	027404	104455					TRAP	C#ERDF
	027406	001132					.WORD	602
	027410	030235'					.WORD	T6SSR
	027412	011656'					.WORD	PKTSSR
989	027414	005237	002220'		INC	FATFLG		
990	027420			15#:	CKLOOP			
	027420	104406						
991	027422				ESCAPE	SUB		
	027422	104410					TRAP	C#CLP1
	027424	000170					.WORD	C#ESCAPE
992	027426	005737	002222'		TST	INTRECV		L10053-
993	027432	001404			BEQ	22#		
997	027434				ERRMRD	ERRNO,T6INT,PKTSSR		
	027434	104456					TRAP	C#ERMRD
	027436	001133					.WORD	603
	027440	030313'					.WORD	T6INT
	027442	011656'					.WORD	PKTSSR
998	027444	012702	102206	22#:	MOV	#SC!NBA!SSR!TSREJ,R2		
999	027450	004737	016146'		JSR	PC,CHKTSSR		
1000	027454	016501	000002		MOV	TSSR(R5),R1		
1001	027460	032701	000100		BIT	#OFL,R1		
1002	027464	001402			BEQ	25#		
1003	027466	052702	000100		BIS	#OFL,R2		
1004	027472	020201		25#:	CMP	R2,R1		
1005	027474	001404			BEQ	30#		
1009	027476				ERRMRD	ERRNO,T6NBA,PKTSSR		
	027476	104456					TRAP	C#ERMRD
	027500	001134					.WORD	604
	027502	030210'					.WORD	T6NBA
	027504	011656'					.WORD	PKTSSR
1010	027506			30#:	CKLOOP			
	027506	104406					TRAP	C#CLP1
1011	027510	004737	016146'		JSR	PC,CHKTSSR		
1012	027514	016501	000000		MOV	TSBA(R5),R1		
1013	027520	010402			MOV	R4,R2		
1014	027522	062702	000010		ADD	#10,R2		
1015	027526	020102			CMP	R1,R2		
1016	027530	001404			BEQ	35#		
1020	027532				ERRMRD	ERRNO,T6TSBA,EXPREC		
	027532	104456					TRAP	C#ERMRD
	027534	001135					.WORD	605
	027536	030451'					.WORD	T6TSBA
	027540	015304'					.WORD	EXPREC

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TEST 6: COMMAND REJECT

SEQ 112

1021							
1022							
1023	027542	004737	010724'	35:	JSR	PC,CKRAM	;SEE IF DATA IN RAM IS CORRECT
1024	027546	103404			BCS	40:	;BRANCH IF PACKET IN RAM IS CORRECT
1028	027550				ERRHRD	ERRNO,PKTRAM,RAMERR	;REPORT THE RAM ERROR(S)
	027550	104456					TRAP C#ERHRD
	027552	001136					.WORD 606
	027554	004737'					.WORD PKTRAM
	027556	015320'					.WORD RAMERR
1029	027560			40:	ENDSEG		;##### END SEGMENT #####
	027560						L10000:
	027560	104405					TRAP C#ESEG
1030	027562	011300			MOV	(R3),RO	;PACKET COMMAND WORD
1031	027564	042700	177740		BIC	#177740,RO	;GET BITS 0-4
1032	027570	020027	000004		CMF	RO,#4	;DON'T TEST WRITE CHARACTERISTICS
1033	027574	001002			BNE	45:	;BRANCH IF OK
1034	027576	062703	000002		ADD	#2,R3	;GET NEXT WORD FROM DATA TABLE
1035	027602	020327	003060'	45:	CMF	R3,@TBLEND	;REACHED END OF TABLE ?
1036	027606	103002			BHIS	50:	;BRANCH IF END OF TABLE
1037	027610	000137	027324'		JMP	5:	;CONTINUE TEST WITH NEW DATA
1038							
1039	027614			50:	ENDSUB		;##### END SUBTEST #####
	027614						L10053:
	027614	104403					TRAP C#ESUB
1040							
1041	027616	005737	002220'		TST	FATFLG	;ANY FATAL ERRORS ?
1042	027622	001402			BEQ	60:	;BRANCH IF NOT
1043	027624	004737	017014'		JSR	PC,CKDROP	;TRY TO DROP THE UNIT
1044							
1045	027630			60:	BGNSUB		;##### BEGIN SUBTEST #####
	027630						T6.2:
	027630	104402					TRAP C#BSUB
1046							
1047	027632				SETPRI	#PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	027632	012700	000000				MOV #PRI00,RO
	027636	104441					TRAP C#SPRI
1048	027640	012704	030200'		MOV	#T6PACKET,R4	;GET THE ADDRESS OF COMMAND PACKET
1049	027644	012703	002750'		MOV	#TSTBLK,R3	;START OF TEST DATA
1050	027650	012314		5:	MOV	(R3),.(R4)	;PLACE NEXT DATA WORD IN PACKET
1051	027652				BGNSEG		;##### BEGIN SEGMENT #####
	027652	104404					TRAP C#BSEG
1052	027654	004737	015604'		JSR	PC,SOFINIT	;DO SOFT INIT OF CONTROLLER
1053	027660	103405			BCS	10:	;BR IF SOFT INIT = OK
1057	027662	010001			MOV	RO,R1	;SAVE CONTENTS OF TSSR
1058	027664				ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT
	027664	104455					TRAP C#ERDF
	027666	001137					.WORD 607
	027670	003644'					.WORD SFIERR
	027672	011644'					.WORD SFIMSG
1059	027674	005037	002220'	10:	CLR	FATFLG	;CLEAR FATAL ERROR FLAG
1060	027700	005037	002222'		CLR	INTREC	;CLEAR INTERRUPT RECEIVED FLAG
1061	027704	004737	016146'		JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS
1062	027710	052714	000200		BIS	#BIT7,(R4)	;ENABLE INTERRUPTS
1063	027714	010465	000000		MOV	R4,TSDB(R5)	;SET THE PACKET ADDRESS
1064	027720	004737	016060'		JSR	PC,WAITF	;WAIT FOR SSR TO SET
1065	027724	103407			BCS	15:	;BR IF CARRY SET (GOOD RETURN)
1066	027726	010001			MOV	RO,R1	;SAVE CONTENTS OF TSSR

1070	027730			ERRDF	ERRNO,T6SSR,PKTSSR				;DEVICE FATAL SSR FAILED TO SET		
	027730	104455							TRAP	C\$ERDF	
	027732	001140							.WORD	608	
	027734	030235'							.WORD	T6SSR	
	027736	011656'							.WORD	PKTSSR	
1071	027740	005237	002220'	15#:	INC	FATFLG			;SET FATAL ERROR FLAG		
1072	027744				CKLOOP				;LOOP ON ERROR, IF FLAG SET		
	027744	104406							TRAP	C\$CLP1	
1073	027746				ESCAPE	SUB			;BY-PASS SUBTEST IF FATAL ERROR		
	027746	104410							TRAP	C\$ESCAPE	
	027750	000170							.WORD	L10054	
1074	027752	005737	002222'		TST	INTRECV			;DID AN INTERRUPT OCCUR ?		
1075	027756	001004			BNE	22#			;BRANCH IF YES		
1079	027760				ERRHRD	ERRNO,T6NINT,PKTSSR			;REPORT ERROR IF NO INTERRUPT		
	027760	104456							TRAP	C\$ERHRD	
	027762	001141							.WORD	609	
	027764	030371'							.WORD	T6NINT	
	027766	011656'							.WORD	PKTSSR	
1080	027770	012702	102206	22#:	MOV	#SC!NBA!SSR!TSREJ,R2			;EXPECTED CONTENTS OF TSSR		
1081	027774	004737	016146'		JSR	PC,CHKTSSR			;WAIT FOR READY, NON-AMBIGUOUS		
1082	030000	016501	000002		MOV	TSSR(R5),R1			;GET THE CONTENTS OF TSSR		
1083	030004	032701	000100		BIT	#OFL,R1			;IS OFF-LINE BIT SET ?		
1084	030010	001402			BEQ	25#			;BRANCH IF NOT OFF-LINE		
1085	030012	052702	000100		BIS	#OFL,R2			;SET OFF-LINE IN EXPECTED DATA		
1086	030016	020201		25#:	CMF	R2,R1			;DOES EXPECTED MATCH RECEIVED ?		
1087	030020	001404			BEQ	30#			;OKAY IF MATCH		
1091	030022				ERRHRD	ERRNO,T6NBA,PKTSSR			;NBA NOT SET TO REJECT		
	030022	104456							TRAP	C\$ERHRD	
	030024	001142							.WORD	610	
	030026	030210'							.WORD	T6NBA	
	030030	011656'							.WORD	PKTSSR	
1092	030032			30#:	CKLOOP				;LOOP ON ERROR ?		
	030032	104406							TRAP	C\$CLP1	
1093	030034	004737	016146'		JSR	PC,CHKTSSR			;WAIT FOR READY, NON-AMBIGUOUS		
1094	030040	016501	000000		MOV	TSBA(R5),R1			;GET TSBA REGISTER CONTENTS		
1095	030044	010402			MOV	R4,R2			;START OF THE PACKET		
1096	030046	062702	000010		ADD	#10,R2			;EXPECT TSBA TO PACKET + 10		
1097	030052	020102			CMF	R1,R2			;COMPARE EXPECTED TO RECEIVED		
1098	030054	001404			BEQ	35#			;ERROR IF NOT EQUAL		
1102	030056				ERRHRD	ERRNO,T6TSBA,EXPREC			;PRINT THE ERROR & EXPD/RCV		
	030056	104456							TRAP	C\$ERHRD	
	030060	001143							.WORD	611	
	030062	030451'							.WORD	T6TSBA	
	030064	015304'							.WORD	EXPREC	
1103											
1104											
1105	030066	004737	010724'	35#:	JSR	PC,CKRAM			;SEE IF DATA IN RAM IS CORRECT		
1106	030072	103404			BCS	40#			;BRANCH IF PACKET IN RAM IS CORRECT		
1110	030074				ERRHRD	ERRNO,PKTRAM,RAMERR			;REPORT THE RAM ERROR(S)		
	030074	104456							TRAP	C\$ERHRD	
	030076	001144							.WORD	612	
	030100	004737'							.WORD	PKTRAM	
	030102	015320'							.WORD	RAMERR	
1111	030104			40#:	ENDSEG				;***** END SEGMENT *****		
	030104								10000#:		
	030104	104405							TRAP	C\$ESEG	
1112	030106	011300			MOV	(R3),R0			;NEXT PACKET COMMAND WORD		

```

1113 030110 042700 177740           BIC      #177740,R0           ;GET BITS 0-4
1114 030114 020027 000004           CMP      R0,#4              ;DON'T TEST WRITE CHARACTERISTICS
1115 030120 001002                   BNE     45$                 ;BRANCH IF NOT WRITE CHARACTERISTICS
1116 030122 062703 000002           ADD     #2,R3              ;BY-PASS WRITE CHARACTERISTICS
1117 030126 020327 003060'         45$:   CMP      R3,#TBLEND     ;HAVE WE COMPLETED DATA TABLE ?
1118 030132 103002                   BHS    50$                 ;BRANCH IF ALL TESTED
1119 030134 000137 027650'         JMP     5$                  ;TEST WITH NEXT DATA
1120
1121 030140                   50$:   ENDSUB                ;////////////////////// END SUBTEST ////////////////////////
                                030140                               L10054:
                                030140 104403                               TRAP   C#ESUB
1122 030142 005737 002220'         TST     FATFLG             ;ANY FATAL ERRORS ?
1123 030146 001402                   BEQ    60$                 ;BRANCH IF NOT
1124 030150 004737 017014'         JSR    PC,CKDROP          ;TRY TO DROP THE UNIT
1125 030154 004737 016270'         60$:   JSR    PC,TSTLOOP      ;SHOULD WE DO ITERATIONS ?
1126 030160 103002                   BCC    62$                 ;BRANCH IF NOT
1127 030162 000137 027304'         JMP     T6LOOP            ;LOOP UNTIL COUNT EXPIRED
1128 030166 104432                   62$:   EXIT     TST           ;ALL DONE THIS TEST
                                030170 000352                               TRAP   C#EXIT
                                                .WORD  L10052-.
1129
1130
1131
1132
1133
1135 030172
1137 030200
1138 030200 000000
1139 030202 052525
1140 030204 125252
1141 030206 052525
1142
1143
1144
1145
1146
1147
1148 030210      103      157      155 T6NBA: .ASCIZ 'Command Not Rejected'
1149 030235      103      157      156 T6SSR: .ASCIZ 'Contents of TSSR Incorrect After Write Packet'
1150 030313      125      156      145 T6INT: .ASCIZ 'Unexpected Interrupt Received On Write Packet'
1151 030371      105      170      160 T6NINT: .ASCIZ 'Expected Interrupt Not Received On Write Packet'
1152 030451      111      156      143 T6TSBA: .ASCIZ 'Incorrect TSBA Address After Packet Write'
1153 030523      103      157      155 TST6ID: .ASCIZ 'Command Reject'
1154
1155 030542
                                .EVEN
                                030542 ENDTST
                                                L10052: TRAP   C#ETST
                                030542 104401
1156
1157
1158
1159
1160
1161
1162
1163
1164
1165
;
; .SBTTL TEST 7: WRITE CHARACTERISTICS
;
; THIS TEST VERIFIES BASIC OPERATION OF THE WRITE CHARACTERISTICS
; COMMAND. IT VERIFIES THAT THE COMMAND BLOCK AND CHARACTERISTICS
; DATA BLOCK ARE FETCHED PROPERLY FROM CPU MEMORY, THE NEED BUFFER
; ADDRESS (NBA) BIT IN TSSR IS HANDLED PROPERLY, AND THAT A PROPER
; MESSAGE PACKET IS STORED, WHERE APPROPRIATE. THIS TEST DOES NOT
; CHECK THAT THE VARIOUS FUNCTIONS ENABLED BY CHARACTERISTIC MODE

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1315 031214 011656'
031216 104406
1316 031220 104410
031222 000116
1317 031224 005737 002222'
1318 031230 001404
1322 031232
031232 104456
031234 001305
031236 033661'
031240 011656'
1323 031242 016501 000002
1324 031246 012702 102206
1325 031252 032701 000100
1326 031256 001402
1327 031260 052702 000100
1328 031264 020201
1329 031266 001414
1330 031270 010100
1331 031272
1332 031302 020027 002000
1333 031306 001404
1337 031310
031310 104456
031312 001306
031314 033113'
031316 011656'
1338 031320
031320 104406
1339 031322 032701 002000
1340 031326 001004
1344 031330
031330 104456
031332 001307
031334 032762'
031336 011656'
1345 031340
1346 031340
031340
031340 104405
1347
1348 031342 062703 000004
1349 031346 020327 032762'
1350 031352 103002
1351 031354 000137 031124'
1352
1353 031360
031360
031360 104403
1354
1355
1356
1357
1358
1359

```

```

151: CKLOOP
ESCAPE SEG
TST INTRECV
BEQ 221
ERRHRD ERRNO,T7INT,PKTSSR
221: MOV TSSR(R5),R1
MOV #SC!SSR!TSREJ!NBA,R2
BIT #OFL,R1
BEQ 251
9IS #OFL,R2
251: CMP R2,R1
BEQ 301
MOV R1,R0
XOR R2,R0
CMP R0,#NBA
BEQ 301
ERRHRD ERRNO,T72REJ,PKTSSR
301: CKLOOP
BIT #NBA,R1
BNE 351
ERRHRD ERRNO,T72NBA,PKTSSR
351: ENDSEG
571: ENDSUB

```

```

;LOOP ON ERROR, IF FLAG SET
;BY-PASS CHECKS IF FATAL ERROR
;DID AN INTERRUPT OCCUR ?
;BRANCH IF NOT
;GET THE CONTENTS OF TSSR
;EXPECTED CONTENTS OF TSSR
;IS OFF-LINE BIT SET ?
;BRANCH IF NOT OFF-LINE
;SET OFF-LINE IN EXPECTED DATA
;DOES EXPECTED MATCH RECEIVED ?
;OKAY IF MATCH
;DATA FROM TSSR
;FIND BITS IN ERROR
;IS NBA ONLY BIT IN ERROR ?
;DON'T PRINT ERROR IF NBA ONLY BAD BIT
;COMMAND NOT REJECTED
;LOOP ON ERROR ?
;IS NBA BIT SET ?
;OKAY IF NBA SET
;NBA NOT SET
;POINT TO NEXT DATA PAIR
;COMPARE TO END OF TEST DATA
;BRANCH IF ALL DATA TESTED
;BRANCH TILL BACK TO ZERO
;CHECK THE WRITE CHARACTERISTICS COMMAND IS REJECTED

```

```

.WORD PKTSSR
SET C1CLP1
TRAP C1ESCAPE
TRAP 100001-
TRAP C1ERHRD
.WORD 709
.WORD T7INT
.WORD PKTSSR
TRAP C1ERHRD
.WORD 710
.WORD T72REJ
.WORD PKTSSR
TRAP C1CLP1
TRAP C1ERHRD
.WORD 711
.WORD T72NBA
.WORD PKTSSR
;<<<<<<<<<<<<<<<< END SEGMENT
100001: TRAP C1ESEG
;///////////////// END SUBTEST
L10057: TRAP C1ESUB

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1360                                     ;IF ISSUED WITH AN INVALID DATA BLOCK BYTE COUNT
1361                                     ;
1362                                     ;
1363
1364 031362                                BGNSUB                                ;//////////////// BEGIN SUBTEST //////////////////
                                031362                                T7.3:                                TRAP      C#BSUB
                                031362 104402
1365
1366 031364                                SETPRI @PRI00                            ;LOWER PRIORITY TO ALLOW INTERRUPTS
                                031364 012700 000000                          MOV        @PRI00,R0
                                031370 104441                          TRAP      C#SPRI
1367 031372 012703 000001                            ;STARTING BYTE COUNT
1368 031376 012704 032700' 5#:  MOV        @T7PACKET,R4           ;GET THE ADDRESS OF COMMAND PACKET
1369 031402 004737 034062'  JSR        PC,T7REST          ;RESTORE PACKET TO STARTING VALUES
1370
1371 031406                                BGNSEGA                                ;>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>
                                031406 104404                                TRAP      C#BSEGA
1372
1373 031410 004737 015604'  JSR        PC,SOFINIT          ;DO SOFT INIT OF CONTROLLER
1374 031414 103405                          BCS       10#                  ;BR IF SOFT INIT - OK
1378 031416 010001                          MOV        R0,R1              ;SAVE CONTENTS OF TSSR
1379 031420                          ERDF       ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
                                031420 104455                                TRAP      C#ERDF
                                031422 001310                                .WORD    712
                                031424 003644'                            .WORD    SFIERR
                                031426 011644'                            .WORD    SFIMSG
1380 031430 005037 002222' 10#: CLR        INTRECV          ;CLEAR INTERRUPT RECEIVED FLAG
1381 031434 010364 000006                          MOV        R3,PKBCNT(R4)      ;INSERT THE BYTE COUNT FOR TEST
1382 031440 010465 000000                          MOV        R4,TSD8(R5)       ;SET THE PACKET ADDRESS
1383 031444 004737 016060'  JSR        PC,WAITF          ;WAIT FOR SSR TO SET
1384 031450 103405                          BCS       15#                  ;BR IF CARRY SET (GOOD RETURN)
1385 031452 010001                          MOV        R0,R1              ;SAVE CONTENTS OF TSSR
1389 031454                          ERDF       ERRNO,T7SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
                                031454 104455                                TRAP      C#ERDF
                                031456 001311                                .WORD    713
                                031460 033501'                            .WORD    T7SSR
                                031462 011656'                            .WORD    PKTSSR
1390 031464                          CKLOOP                            ;LOOP ON ERROR, IF FLAG SET
                                031464 104406                                TRAP      C#CLP1
1391 031466                          ESCAPE SEG                        ;BY-PASS SUBTEST IF FATAL ERROR
                                031466 104410                                TRAP      C#ESCAPE
                                031470 000116                                .WORD    10000#-
1392 031472 005737 002222'  TST        INTRECV          ;DID AN INTERRUPT OCCUR ?
1393 031476 001404                          BEQ       22#                  ;BRANCH IF NOT
1397 031500                          ERMRD      ERRNO,T7INT,PKTSSR
                                031500 104456                                TRAP      C#ERMRD
                                031502 001312                                .WORD    714
                                031504 033661'                            .WORD    T7INT
                                031506 011656'                            .WORD    PKTSSR
1398 031510 016501 000002 22#:  MOV        TSSR(R5),R1         ;GET THE CONTENTS OF TSSR
1399 031514 012702 102206                          MOV        #SC!SSR!TSREJ!NBA,R2 ;EXPECTED CONTENTS OF TSSR
1400 031520 032701 000100                          BIT        @OFL,R1           ;IS OFF-LINE BIT SET ?
1401 031524 001402                          BEQ       25#                  ;BRANCH IF NOT OFF-LINE
1402 031526 052702 000100                          BIS        @OFL,R2           ;SET OFF-LINE IN EXPECTED DATA
1403 031532 020201 25#:  CMP        R2,R1              ;DOES EXPECTED MATCH RECEIVED ?
1404 031534 001414                          BEQ       30#                  ;OKAY IF MATCH
1405 031536 010100                          MOV        R1,R0              ;DATA FROM TSSR

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031664 001315
031666 003644'
031670 011644'
1454 031672 005037 002222' 10$: CLR INTRECV ;CLEAR INTERRUPT RECEIVED FLAG
1455 031676 052737 000001 032710' BIS #1,T7DATA ;MAKE ADDRESS ODD
1456 031704 010465 000000 MOV R4,T5DB(R5) ;SET THE PACKET ADDRESS
1457 031710 004737 016060' JSR PC,WAITF ;WAIT FOR SSR TO SET
1458 031714 103405 BCS 15$ ;BR IF CARRY SET (GOOD RETURN)
1459 031716 010001 MOV R0,R1 ;SAVE CONTENTS OF TSSR
1463 031720 ERRDF ERRNO,T7SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
031720 104455 TRAP C#ERDF
031722 001316 .WORD 718
031724 033501' .WORD T7SSR
031726 011656' .WORD PKTSSR
1464 031730 15$: CKLOOP ;LOOP ON ERROR, IF FLAG SET
031730 104406 TRAP C#CLP1
1465 031732 ESCAPE SUB ;BY-PASS SUBTEST IF FATAL ERROR
031732 104410 TRAP C#ESCAPE
031734 000116 .WORD L10061-
1466 031736 005737 002222' TST INTRECV ;DID AN INTERRUPT OCCUR ?
1467 031742 001404 BEQ 22$ ;BRANCH IF NOT
1471 031744 ERRHRD ERRNO,T7INT,PKTSSR
031744 104456 TRAP C#ERHRD
031746 001317 .WORD 719
031750 033661' .WORD T7INT
031752 011656' .WORD PKTSSR
1472 031754 016501 000002 22$: MOV TSSR(R5),R1 ;GET THE CONTENTS OF TSSR
1473 031760 012702 102206 MOV #SC!SSR!TSREJ!NBA,R2 ;EXPECTED CONTENTS OF TSSR
1474 031764 032701 000100 BIT #OFL,R1 ;IS OFF-LINE BIT SET ?
1475 031770 001402 BEQ 25$ ;BRANCH IF NOT OFF-LINE
1476 031772 052702 000100 BIS #OFL,R2 ;SET OFF-LINE IN EXPECTED DATA
1477 031776 020201 25$: CMP R2,R1 ;DOES EXPECTED MATCH RECEIVED ?
1478 032000 001414 BEQ 30$ ;OKAY IF MATCH
1479 032002 010100 MOV R1,R0 ;DATA FROM TSSR
1480 032004 XOR R2,R0 ;FIND BITS IN ERROR
1481 032014 020027 002000 CMP R0,#NBA ;IS NBA ONLY BIT IN ERROR ?
1482 032020 001404 BEQ 30$ ;DON'T PRINT ERROR IF NBA ONLY BAD BIT
1486 032022 ERRHRD ERRNO,T74REJ,PKTSSR ;COMMAND NOT REJECTED
032022 104456 TRAP C#ERHRD
032024 001320 .WORD 720
032026 033305' .WORD T74REJ
032030 011656' .WORD PKTSSR
1487 032032 30$: CKLOOP ;LOOP ON ERROR ?
032032 104406 TRAP C#CLP1
1488 032034 032701 002000 BIT #NBA,R1 ;IS NBA BIT SET ?
1489 032040 001004 BNE 35$ ;OKAY IF NBA SET
1493 032042 ERRHRD ERRNO,T72NBA,PKTSSR ;NBA NOT SET
032042 104456 TRAP C#ERHRD
032044 001321 .WORD 721
032046 032762' .WORD T72NBA
032050 011656' .WORD PKTSSR
1494
1495 032052 35$: ENDSUB ;////////// END SUBTEST ////////////
032052 L10061: TRAP C#ESUB
032052 104403
1496
1497

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1507 032054          BGNSUB          ;/////////// BEGIN SUBTEST ////////////
      032054          ;              T7.5:
      032054 104402          TRAP      C#BSUB
1508
1509 032056          SETPRI #PRI00          ;LOWER PRIORITY TO ALLOW INTERRUPTS
      032056 012700 00C000          MOV      #PRI00,R0
      032052 104441          TRAP      C#SPRI
1510 032064 012703 000001          MOV      #1,R3          ;STARTING BUFFER LENGTH
1511 032070 012704 032700'        5$: MOV      #T7PACKET,R4          ;GET THE ADDRESS OF COMMAND PACKET
1512 032074 004737 034062'        JSR      PC,T7REST          ;RESTORE PACKET TO STARTING VALUES
1513
1514 032100          BGNSEG          ;>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
      032100 104404          TRAP      C#BSEG
1515
1516 032102 004737 015604'        JSR      PC,SOFINIT          ;DO SOFT INIT OF CONTROLLER
1517 032106 103405          BCS     10$
1521 032110 010001          MOV      R0,R1          ;SAVE CONTENTS OF TSSR
1522 032112          ERDF    ERRNO,SFIERR,SFIMSG          ;DEVICE FATAL ERROR DURING INIT
      032112 104455          TRAP      C#ERDF
      032114 001322          .WORD    722
      032116 003644'        .WORD    SFIERR
      032120 011644'        .WORD    SFIMSG
1523 032122 005037 002222'        10$: CLR      INTRECV          ;CLEAR INTERRUPT RECEIVED FLAG
1524 032126 010337 032714'        MOV      R3,T7DATA+4          ;INSERT THE BAD MESSAGE LENGTH
1525 032132 010465 000000'        MOV      R4,TSDB(R5)          ;SET THE PACKET ADDRESS
1526 032136 004737 016060'        JSR      PC,WAITF          ;WAIT FOR SSR TO SET
1527 032142 103405          BCS     15$
1528 032144 010001          MOV      R0,R1          ;SAVE CONTENTS OF TSSR
1532 032146          ERDF    ERRNO,T7SSR,PKTSSR          ;DEVICE FATAL SSR FAILED TO SET
      032146 104455          TRAP      C#ERDF
      032150 001323          .WORD    723
      032152 033501'        .WORD    T7SSR
      032154 011656'        .WORD    PKTSSR
1533 032156          CKLOOP          ;LOOP ON ERROR, IF FLAG SET
      032156 104406          TRAP      C#CLP1
1534 032160          ESCAPE  SEG          ;BY-PASS SUBTEST IF FATAL ERROR
      032160 104410          TRAP      C#ESCAPE
      032162 000116          .WORD    10000$-.
1535 032164 005737 002222'        TST     INTRECV          ;DID AN INTERRUPT OCCUR ?
1536 032170 001404          BEQ     22$
1540 032172          ERHRD  ERRNO,T7INT,PKTSSR          ;BRANCH IF NOT
      032172 104456          TRAP      C#ERHRD
      032174 001324          .WORD    724
      032176 033661'        .WORD    T7INT
      032200 011656'        .WORD    PKTSSR
1541 032202 016501 000002        22$: MOV      TSSR(R5),R1          ;GET THE CONTENTS OF TSSR
1542 032206 012702 102206          MOV      #SC!SSR!TSREJ!NBA,R2          ;EXPECTED CONTENTS OF TSSR
1543 032212 032701 000100          BIT     #OFL,R1          ;IS OFF-LINE BIT SET ?

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1637	032536	104406							TRAP	C#CLP1
	032540	004737	016146'		JSR	PC,CHKTSSR				
	032544	016501	000000		MOV	TSBA(R5),R1				
	032550	012702	032726'		MOV	#T7BFR,R2				
	032554	032762	000200 000012		BIT	#BIT7,XST2(R2)				
	032562	001402			BEQ	32#				
	032564	062702	000002		ADD	#2,R2				
	032570	062702	000016	32#:	ADD	#14.,R2				
	032574	020102			CMP	R1,R2				
	032576	001404			BEQ	35#				
	032600				ERRHRD	ERRNO,T7TSBA,EXPREC				
	032600	104456								
	032602	001333							TRAP	C#ERHRD
	032604	033750'							.WORD	731
	032606	015304'							.WORD	T7TSBA
									.WORD	EXPREC
1650										
1651										
1652	032610	012704	032710'	35#:	MOV	#T7DATA,R4				
1653	032614	004737	011034'		JSR	PC,CKRAM2				
1654	032620	103404			BCS	40#				
1658	032622				ERRHRD	ERRNO,PKTRAM,RAMERR				
	032622	104456								
	032624	001334							TRAP	C#ERHRD
	032626	004737'							.WORD	732
	032630	015320'							.WORD	PKTRAM
									.WORD	RAMERR
1659										
1660	032632	012704	032700'	40#:	MOV	#T7PACKET,R4				
1661	032636				ENDSEG					
	032636	104405								
	032636									
1662										
1663	032640	012364	000006		MOV	(R3)+,PKBCNT(R4)				
1664	032644	020327	003060'		CMP	R3,#TBLEND				
1665	032650	103002			BHIS	55#				
1666	032652	000137	032372'		JMP	5#				
1667										
1668	032656			55#:	ENDSUB					
	032656									
	032656	104403								
1669										
1670	032660	005737	002220'		TST	FATFLG				
1671	032664	001402			BEQ	60#				
1672	032666	004737	017014'		JSR	PC,CKDROP				
1673	032672			60#:						
1674	032672				EXIT	TST				
	032672	104432								
	032674	001234							TRAP	C#EXIT
									.WORD	L10055..
1675										
1676										
1677										
1678										
1679										
1681	032676									
1683	032700				.BLKB	10-<.-TSV2E7>				
1684	032700	100004		T7PACKET:						
1685	032702	032710'			.WORD	100004				
1686	032704	000000			.WORD	T7DATA				
					.WORD	0				

```

;WAIT FOR READY, NON-AMBIGUOUS
;GET TSBA REGISTER CONTENTS
;START OF THE DATA BUFFER
;IS EXTENDED FEATURES BIT SET ?
;BRANCH IF EXTENDED FEATURES OFF
;EXTRA WORD IF SPECIAL FEATURES
;EXPECTED CONTENTS OF TSBA
;COMPARE EXPECTED TO RECEIVED
;ERROR IF NOT EQUAL
;PRINT THE ERROR & EXPD/RECV

;SET POINTER FOR CHECKER
;SEE IF DATA IN RAM IS CORRECT
;BRANCH IF PACKET IN RAM IS CORRECT
;REPORT THE RAM ERROR(S)

;RESET PACKET POINTER
;***** END SEGMENT *****
10000#:
TRAP C#ESEG

;SET THE TEST WORD
;HAS ALL DATA BEEN TESTED ?
;BRANCH IF ALL DATA DONE
;BRANCH TILL BACK TO ZERO

;***** END SUBTEST *****
L10063:
TRAP C#ESUB

;ANY FATAL ERRORS ?
;BRANCH IF NOT
;TRY TO DROP THE UNIT

;ALL DONE THIS TEST

TRAP C#EXIT

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1687 032706 000010          .WORD 8.          ;STARTING VALUE OF BLOCK SIZE
1688
1689 032710          T7DATA:          ;CHARACTERISTICS DATA BLOCK
1690 032710 032726'      .WORD T7BFR      ;ADDRESS OF MESSAGE BUFFER
1691 032712 000000      .WORD 0          ;
1692 032714 000016      .WORD 14.        ;LENGTH OF MESSAGE BUFFER
1693 032716 000000      .WORD 0          ;
1694 032720 000000      T7SP: .WORD 0          ;EXTFEA EXTRA WORD
1695
1696 032722 000000 000000 .WORD 0,0        ;SPACE
1697 032726          T7BFR: .BLKW 8.        ;MESSAGE BUFFER
1698
1699          ;*
1700          ;
1701          ;TEST DATA FOR SUBTEST TWO
1702          ;
1703          ;DATA HAS FORMAT:
1704          ;
1705          ;          1ST WORD          OFFSET TO TEST WORD IN PACKET
1706          ;          2ND WORD          BITS TO SET FOR TEST
1707          ;
1708          ;-
1709
1710 032746          T72DATA:
1711 032746 000000 037140 .WORD 0,BIT5!BIT6!BIT9!BIT10!BIT11!BIT12!BIT13
1712 032752 000002 000001 .WORD 2,BIT0
1713 032756 000004 100100 .WORD 4,BIT6!BIT15
1714          T72DONE=.
1715
1716
1717          ;*
1718          ;LOCAL TEXT MESSAGES FOR TEST
1719          ;-
1720
1721 032762          116          102          101 T72NBA: .ASCIZ 'NBA Not Set On Rejected WRITE CHARACTERISTICS'
1722 033040          127          122          111 T7NBA: .ASCIZ 'WRITE CHARACTERISTICS Command Not Accepted'
1723 033113          127          122          111 T72REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
1724 033212          127          122          111 T73REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
1725 033305          127          122          111 T74REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
1726 033403          127          122          111 T75REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
1727 033501          103          157          156 T7SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
1728 033570          105          170          160 T7NINT: .ASCIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
1729 033661          125          156          145 T7INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
1730 033750          111          156          143 T7TSBA: .ASCIZ 'Incorrect TSBA Address After WRITE CHARACTERISTICS'
1731 034033          127          162          151 TST7ID: .ASCIZ 'Write Characteristics'
1732          .EVEN
1733
1734
1735          ;*
1736          ;
1737          ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
1738          ;
1739          ;-
1740
1741 034062          T7REST:
1742 034062          SAVREG          ;SAVE THE REGISTERS
1743 034066 012701 032700' MOV #T7PACKET,R1 ;START OF THE PACKET
    
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 TEST 7: WRITE CHARACTERISTICS

SEQ 127

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1744 034072 012721 100004      MOV     #100004,(R1)+    ;WRITE CHARACTERISTICS WITH ACK
1745 034076 012721 032710'    MOV     #T7DATA,(R1)+  ;ADDRESS OF CHAR DATA BLOCK
1746 034102 005021              CLR     (R1)+          ;EXTENDED ADDRESS
1747 034104 012721 000010      MOV     #8.,(R1)+      ;SIZE OF DATA BLOCK IN BYTES
1748 034110 012721 032726'    MOV     #T7BFR,(R1)+  ;ADDRESS OF MESSAGE BUFFER
1749 034114 005021              CLR     (R1)+          ;
1750 034116 012721 000020      MOV     #16.,(R1)+    ;LENGTH OF MESSAGE BUFFER
1751 034122 005021              CLR     (R1)+          ;
1752 034124 005011              CLR     (R1)+          ;
1753 034126 000207              RTS     PC              ;RETURN
1754 034130      ENDTST

                                L10055:
                                TRAP    C#ETST

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1791 034132      BGNTST
      034132
1796 034132 012700 035017'    MOV     #T8ID,R0      ;ASCII MESSAGE TO IDENTIFY TEST
1797 034136 004737 016322'    JSR     PC,T8SETUP   ;DO INITIAL TEST SETUP
1798 034142 012737 000024 002214'  MOV     #20.,LOOPCNT ;PERFORM 20 ITERATIONS
1799 034150      T8LOOP:
1800
1801 034150 012704 034540'    MOV     #T8PACKET,R4 ;PACKET FOR WRITE CHARACTERISTICS

                                .SBTTL TEST 8: VOLUME CHECK

                                THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD
                                WITHIN THE M7455 AND APPEARING IN XSTO, IS SET BY INITIALIZE AND
                                CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE
                                CVC BIT SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS
                                COMMAND WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE
                                VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF
                                PREVENTING OR ALLOWING A TAPE MOTION COMMAND DEPENDING UPON
                                WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS
                                TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF
                                TAPE MOTION COMMANDS.

                                THE TEST PROCEEDS AS FOLLOWS:

                                1. THE CONTROLLER IS INITIALIZED BY WRITING INTO THE TSSR.
                                2. A WRITE CHARACTERISTICS COMMAND IS ISSUED (WITH CVC=0)
                                   AND XSTO IN THE RETURNED MESSAGE BUFFER IS EXAMINED;
                                   THE VCK BIT SHOULD BE CLEAR (0).
                                3. THE PREVIOUS STEP IS REPEATED TO VERIFY THAT VCK DOES
                                   NOT CHANGE (REMAINS AT 0).
                                4. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=1
                                   AND THE VCK BIT IN XSTO IN THE MESSAGE BUFFER IS
                                   EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
                                5. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=0
                                   AND THE VCK BIT IN XSTO IN THE MESSAGE BUFFER IS
                                   EXAMINED; THE VCK BIT SHOULD REMAIN CLEAR (0).

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 TEST 8: VOLUME CHECK

SEQ 128

1802	034154	004737	015604'	5#:	JSR	PC,SOFINIT		;DO SOFT INIT OF CONTROLLER
1803	034160	103405			BCS	10#		;BR IF SOFT INIT = OK
1807	034162	010001			MOV	R0,R1		;SAVE CONTENTS OF TSSR
1808	034164				ERRDF	ERRNO,SFIERR,SFIMSG		;DEVICE FATAL ERROR DURING INIT
	034164	104455						TRAP C#ERDF
	034166	001441						.WORD 801
	034170	003644'						.WORD SFIERR
	034172	011644'						.WORD SFIMSG
1809	034174	042714	040000	10#:	BIC	#BIT14,(R4)		;CLEAR THE CVC BIT
1810	034200	010465	000000		MOV	R4,TSDB(R5)		;SET THE PACKET ADDRESS FOR WRITE CHAR
1811	034204	004737	016146'		JSR	PC,CHKTSSR		;WAIT FOR SSR TO SET
1812	034210	103405			BCS	15#		;BR IF CARRY SET (GOOD RETURN)
1813	034212	010001			MOV	R0,R1		;SAVE CONTENTS OF TSSR
1817	034214				ERRDF	ERRNO,T8SSR,PKTSSR		;DEVICE FATAL SSR FAILED TO SET
	034214	104455						TRAP C#ERDF
	034216	001442						.WORD 802
	034220	034730'						.WORD T8SSR
	034222	011656'						.WORD PKTSSR
1818	034224			15#:	CKLOOP			;LOOP ON ERROR, IF FLAG SET
	034224	104406						TRAP C#CLP1
1819	034226				ESCAPE	TST		;EXIT IF FATAL ERROR
	034226	104410						TRAP C#ESCAPE
	034230	000604						.WORD L10064--
1820	034232	012702	034562'		MOV	#T8BFR,R2		;ADDRESS OF THE MESSAGE BUFFER
1821	034236	032762	000020 000006		BIT	#XSOVCK,XST0(R2)		;IS VOLUME CHECK SET IN XST0 ?
1822	034244	001406			BEG	20#		;OKAY IF VOLUME CHECK IS CLEAR
1826	034246	016501	000002		MOV	TSSR(R5),R1		;CONTENTS OF TSSR FOR ERROR REPORT
1827	034252				ERRHRD	ERRNO,T8NVCK,PKTMES		;VOLUME CHECK NOT CLEAR
	034252	104456						TRAP C#ERHRD
	034254	001443						.WORD 803
	034256	034637'						.WORD T8NVCK
	034260	011720'						.WORD PKTMES
1828	034262			20#:	CKLOOP			;LOOP ON ERROR ?
	034262	104406						TRAP C#CLP1
1829	034264	010465	000000		MOV	R4,TSDB(R5)		;SET THE PACKET ADDRESS FOR WRITE CHAR
1830	034270	004737	016146'		JSR	PC,CHKTSSR		;WAIT FOR SSR TO SET
1831	034274	103405			BCS	25#		;BR IF CARRY SET (GOOD RETURN)
1832	034276	010001			MOV	R0,R1		;SAVE CONTENTS OF TSSR
1836	034300				ERRDF	ERRNO,T8SSR,PKTSSR		;DEVICE FATAL SSR FAILED TO SET
	034300	104455						TRAP C#ERDF
	034302	001444						.WORD 804
	034304	034730'						.WORD T8SSR
	034306	011656'						.WORD PKTSSR
1837	034310			25#:	CKLOOP			;LOOP ON ERROR, IF FLAG SET
	034310	104406						TRAP C#CLP1
1838	034312				ESCAPE	TST		;EXIT IF FATAL ERROR
	034312	104410						TRAP C#ESCAPE
	034314	000520						.WORD L10064--
1839	034316	032762	000020 000006		BIT	#XSOVCK,XST0(R2)		;IS VOLUME CHECK SET IN XST0 ?
1840	034324	001406			BEG	30#		;OKAY IF VOLUME CHECK IS SET
1844	034326	016501	000002		MOV	TSSR(R5),R1		;CONTENTS OF TSSR FOR ERROR REPORT
1845	034332				ERRHRD	ERRNO,T8NVCK,PKTMES		;VOLUME CHECK NOT SET
	034332	104456						TRAP C#ERHRD
	034334	001445						.WORD 805
	034336	034637'						.WORD T8NVCK
	034340	011720'						.WORD PKTMES
1846	034342			30#:	CKLOOP			;LOOP ON ERROR ?

M10

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TEST 8: VOLUME CHECK

SEQ 129

1847	034342	104406			BIS	#BIT14,(R4)		TRAP	C\$CLP1
1848	034344	052714	040000		MOV	R4,TSDB(R5)			
1849	034350	010465	000000		JSR	PC,CHKTSSR			
1850	034354	004737	016146'		BCS	35\$			
1851	034360	103405			MOV	R0,R1			
1855	034362	010001			ERRDF	ERRNO,T8SSR,PKTSSR			
	034364	104455						TRAP	C\$ERDF
	034366	001446						.WORD	806
	034370	034730'						.WORD	T8SSR
	034372	011656'						.WORD	PKTSSR
1856	034374			35\$:	CKLOOP				
	034374	104406						SET	
1857	034376				ESCAPE	TST		TRAP	C\$CLP1
	034376	104410							
	034400	000434						TRAP	C\$ESCAPE
1858	034402	032762	000020	000006	BIT	#XSOVCK,XSTO(R2)		.WORD	L10064--
1859	034410	001406			BEQ	40\$			
1863	034412	016501	000002		MOV	TSSR(R5),R1			
1864	034416				ERRHRD	ERRNO,T8VCK,PKTMES			
	034416	104456						TRAP	C\$ERHRD
	034420	001447						.WORD	807
	034422	034602'						.WORD	T8VCK
	034424	011720'						.WORD	PKTMES
1865	034426			40\$:	CKLOOP				
	034426	104406							
1866	034430	042714	040000		BIC	#BIT14,(R4)		TRAP	C\$CLP1
1867	034434	010465	000000		MOV	R4,TSDB(R5)			
1868	034440	004737	016146'		JSR	PC,CHKTSSR			
1869	034444	103405			BCS	45\$			
1870	034446	010001			MOV	R0,R1			
1874	034450				ERRDF	ERRNO,T8SSR,PKTSSR			
	034450	104455						TRAP	C\$ERDF
	034452	001450						.WORD	808
	034454	034730'						.WORD	T8SSR
	034456	011656'						.WORD	PKTSSR
1875	034460			45\$:	CKLOOP				
	034460	104406						SET	
1876	034462				ESCAPE	TST		TRAP	C\$CLP1
	034462	104410							
	034464	000350						TRAP	C\$ESCAPE
1877	034466	032762	000020	000006	BIT	#XSOVCK,XSTO(R2)		.WORD	L10064--
1878	034474	001406			BEQ	50\$			
1882	034476	016501	000002		MOV	TSSR(R5),R1			
1883	034502				ERRHRD	ERRNO,T8VCK,PKTMES			
	034502	104456						TRAP	C\$ERHRD
	034504	001451						.WORD	809
	034506	034602'						.WORD	T8VCK
	034510	011720'						.WORD	PKTMES
1884	034512			50\$:	CKLOOP				
	034512	104406							
1885	034514	004737	016270'	60\$:	JSR	PC,TSTLOOP		TRAP	C\$CLP1
1886	034520	103002			BCC	62\$			
1887	034522	000137	034150'		JMP	T8LOOP			
1888	034526			62\$:	EXIT	TST			
	034526	104432						TRAP	C\$EXIT
	034530	000304						.WORD	L10064--

```

1889
1890
1891          ;+
1892          ;LOCAL STORAGE FOR THIS TEST
1893          ;
1895 034532          .BLKB  10-<.-TSV2&7>
1897 034540          T8PACKET:          ;COMMAND PACKET FOR TEST
1898 034540 100004          .WORD  100004          ;WRITE CHARACTERISTICS COMMAND
1899 034542 034550'          .WORD  T8DATA          ;ADDRESS OF CHARACTERISTICS BLOCK
1900 034544 000000          .WORD  0
1901 034546 000010          .WORD  10          ;STARTING VALUE OF COUNTER
1902
1903 034550          T8DATA:          ;CHARACTERISTICS DATA BLOCK
1904 034550 034562'          .WORD  T8BFR          ;ADDRESS OF MESSAGE BUFFER
1905 034552 000000          .WORD  0
1906 034554 000020          .WORD  16.          ;LENGTH OF MESSAGE BUFFER
1907 034556 000000 000000          .WORD  0,0
1908
1909 034562          T8BFR: .BLKW  8.          ;MESSAGE BUFFER
1910
1911          ;+
1912          ;LOCAL TEXT MESSAGES FOR TEST
1913          ;-
1914
1915
1916 034602          126      157      154  T8VCK: .ASCIZ  'Volume Check Bit Not Cleared'
1917 034637          126      157      154  T8NVCK: .ASCIZ  'Volume Check Bit (VCK) Not Clear After Initialize (XST0)'
1918 034730          103      157      156  T8SSR: .ASCIZ  'Contents of TSSR Incorrect After Write Characteristics'
1919 035017          126      157      154  T8T8ID: .ASCIZ  'Volume Check'
1920          .EVEN
1921 035034          .ENDTST
1921 035034          L10064:          TRAP  C#ETST
1921 035034 104401
1922
1923          .SBTTL  TEST 9: COMPLETION INTERRUPT
1924
1925
1926          ;
1927          ; THIS TEST VERIFIES THAT AN INTERRUPT IS GENERATED AT THE
1928          ; COMPLETION OF THE WRITE CHARACTERISTICS COMMAND IF THE INTERRUPT
1929          ; ENABLE (IE) BIT IN THE COMMAND HEADER WORD IS SET. THIS TEST
1930          ; CHECKS THE FUNCTIONING OF THE INTERRUPT LOGIC AND BASIC
1931          ; PROCESSING OF THE IE BIT.
1932          ;
1933          ; THE SEQUENCES OF TEST 7 ARE REPEATED, EXCEPT THAT THE INTERRUPT
1934          ; SERVICE ROUTINE IS SET UP TO EXPECT INTERRUPTS AND EACH WRITE
1935          ; CHARACTERISTICS COMMAND IS ISSUED WITH THE IE BIT SET (1). IT
1936          ; IS VERIFIED, WHERE APPROPRIATE, THAT THE IE STATUS BIT IN XST0
1937          ; OF ANY MESSAGE PACKET IS SET AND THAT A COMPLETION INTERRUPT IS
1938          ; GENERATED. FINALLY, A SEQUENCE OF TWO COMMANDS ARE ISSUED, THE
1939          ; FIRST WITH IE=1 AND THE SECOND WITH IE=0. IT IS VERIFIED THAT
1940          ; NO INTERRUPT IS GENERATED AFTER THE SECOND COMMAND AND THAT THE
1941          ; IE BIT IN XST0 IS 0.
1942          ;
1942 035036          .BGNST
1942 035036          T9::
1947 035036 005037 002224'          CLR  EXTFEA          ;CLEAR EXTENDED FEATURES SWITCH
1948 035042 012700 040061'          MOV  #TST9ID,R0          ;ASCII MESSAGE TO IDENTIFY TEST
    
```

1949	035046	004737	016322'		JSR	PC,TSTSETUP	;	DO INITIAL TEST SETUP		
1950	035052	012737	000024	002214'	MOV	#20.,LOOPCNT	;	PERFORM 20 ITERATIONS		
1951	035060									
1952	035060				T9LOOP:	BGNSUB		////////////////////////////////////		
	035060							T9.1:		
	035060	104402						TRAP	C#BSUB	
1953										
1954	035062	004737	040106'		JSR	PC,T9REST	;	SET PACKET TO INITIAL VALUES		
1955	035066				SETPRI	#PRI00	;	LOWER PRIORITY TO ALLOW INTERRUPTS		
	035066	012700	000000					MOV	#PRI00,R0	
	035072	104441						TRAP	C#SPRI	
1956	035074	012703	002762'		MOV	#TSTBLK*10.,R3	;	START OF TEST DATA		
1957	035100	012704	037010'		MOV	#T9PACKET,R4	;	GET THE ADDRESS OF COMMAND PACKET		
1958	035104	012764	000010	000006	MOV	#8.,PKBCNT(R4)	;	START WITH MINIMUM ALLOWABLE VALUE		
1959	035112				5#:					
1960	035112				BGNSEG		;	>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>		
	035112	104404						TRAP	C#BSEG	
1961										
1962	035114	004737	015604'		JSR	PC,SOFINIT	;	DO SOFT INIT OF CONTROLLER		
1963	035120	103405			BCS	10#	;	BP IF SOFT INIT = OK		
1967	035122	010001			MOV	R0,R1	;	SAVE CONTENTS OF TSSR		
1968	035124				ERRDF	ERRNO,SFIERR,SFIMSG	;	DEVICE FATAL ERROR DURING INIT		
	035124	104455						TRAP	C#ERDF	
	035126	001605						.WORD	901	
	035130	003644'						.WORD	SFIERR	
	035132	011644'						.WORD	SFIMSG	
1969	035134	005037	002220'	10#:	CLR	FATFLG	;	CLEAR FATAL ERROR FLAG		
1970	035140	005037	002222'		CLR	INTRECV	;	CLEAR INTERRUPT RECEIVED FLAG		
1971	035144	010465	000000		MOV	R4,T9DB(R5)	;	SET THE PACKET ADDRESS		
1972	035150	004737	016146'		JSR	PC,CHKTSSR	;	WAIT FOR SSR TO SET		
1973	035154	103407			BCS	15#	;	BR IF CARRY SET (GOOD RETURN)		
1974	035156	010001			MOV	R0,R1	;	SAVE CONTENTS OF TSSR		
1978	035160				ERRDF	ERRNO,T9SSR,PKTSSR	;	DEVICE FATAL SSR FAILED TO SET		
	035160	104455						TRAP	C#ERDF	
	035162	001606						.WORD	902	
	035164	037527'						.WORD	T9SSR	
	035166	011656'						.WORD	PKTSSR	
1979	035170	005237	002220'		INC	FATFLG	;	SET FATAL ERROR FLAG		
1980	035174			15#:	CKLOOP		;	LOOP ON ERROR, IF FLAG SET		
	035174	104406						TRAP	C#CLP1	
1981	035176				ESCAPE	SEG	;	BY-PASS SUBTEST IF FATAL ERROR		
	035176	104410						TRAP	C#ESCAPE	
	035200	000056						.WORD	10000#-. .	
1982	035202	005737	002222'		TST	INTRECV	;	DID AN INTERRUPT OCCUR ?		
1983	035206	001004			BNE	22#	;	BRANCH IF YES		
1987	035210				ERRHRD	ERRNO,T9NINT,PKTSSR				
	035210	104456						TRAP	C#ERHRD	
	035212	001607						.WORD	903	
	035214	037616'						.WORD	T9NINT	
	035216	011656'						.WORD	PKTSSR	
1988	035220	016501	000002	22#:	MOV	TSSR(R5),R1	;	GET THE CONTENTS OF TSSR		
1989	035224	012702	000200		MOV	#SSR,R2	;	EXPECTED CONTENTS OF TSSR		
1990	035230	032701	000100		BIT	#OFL,R1	;	IS OFF-LINE BIT SET ?		
1991	035234	001402			BEQ	25#	;	BRANCH IF NOT OFF-LINE		
1992	035236	052702	000100		BIS	#OFL,R2	;	SET OFF-LINE IN EXPECTED DATA		
1993	035242	020201		25#:	CMP	R2,R1	;	DOES EXPECTED MATCH RECEIVED ?		
1994	035244	001404			BtQ	30#	;	OKAY IF MATCH		

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 TEST 9: COMPLETION INTERRUPT

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1998 035246             ERRHRD  ERRNO,T9NBA,PKTSSR    ;NBA NOT ZERO
      035246 104456                                TRAP  CIERHRD
      035250 001610                                .WORD 904
      035252 037056'                                .WORD T9NBA
      035254 011656'                                .WORD PKTSSR
1999 035256             30$:
2000 035256             ENDSEG                    ;<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<
      035256                                10000$: TRAP  CIESEG
      035256 104405
2001
2002 035260 012364 000006  MOV      (R3),PKBCNT(R4)    ;SET THE TEST WORD
2003 035264 020327 003060' CMP       R3,#TBLEND        ;HAS ALL DATA BEEN TESTED ?
2004 035270 103002                 BHS     55$                ;BRANCH IF ALL DATA DONE
2005 035272 000137 035112'       JMP      5$                ;BRANCH TILL BACK TO ZERO
2006
2007 035276             55$: ENDSUB                    ;<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<
      035276                                L10066: TRAP  CIESUB
      035276 104403
2008
2009 035300 005737 002220'       TST     FATFLG             ;ANY FATAL ERRORS ?
2010 035304 001402                 BEQ     60$                ;BRANCH IF NOT
2011 035306 004737 017014'       JSR     PC,CKDROP         ;TRY TO DROP THE UNIT
2012 035312 033727 037044' 000200 60$: BIT     T9NBA+12,#BIT7  ;EXTENDED FEATURES SET?
2013 035320 001402                 BEQ     70$                ;BR IF NO
2014 035322 005237 002224'       INC     EXTFEA            ;SET EXT FEATURE FLAG
2015 035326             70$:
2016
2017
2018 ;
2019 ; TEST 9, SUBTEST 2
2020 ;
2021 ; CHECK THAT UNUSED BITS BEING SET CAUSES
2022 ; WRITE CHARACTERISTICS COMMAND TO BE REJECTED
2023 ;
2024 ;
2025 ;
2026 035326             BGNSUB                    ;<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<
      035326                                T9 2: TRAP  CIBSUB
      035326 104402
2027
2028 035330             SETPRI  #PRI00              ;LOWER PRIORITY TO ALLOW INTERRUPTS
      035330 012700 000000                                MOV     #PRI00,RO
      035334 104441                                TRAP  CISPRI
2029 035336 012703 037052'       MOV     #T92DATA,R3      ;START OF TEST DATA FOR SUBTEST
2030 035342 012704 037010'       MOV     #T9PACKET,R4    ;GET THE ADDRESS OF COMMAND PACKET
2031 035346 004737 040106'       JSR     PC,T9REST        ;RESTORE PACKET TO STARTING VALUES
2032
2033 035352             BGNSEGE                    ;>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>
      035352                                TRAP  CIBSEGE
      035352 104404
2034
2035 035354 004737 015604'       JSR     PC,SOFINIT       ;DO SOFT INIT OF CONTROLLER
2036 035360 103405                 BCS     10$              ;BR IF SOFT INIT = OK
2040 035362 010001                 MOV     RO,R1           ;SAVE CONTENTS OF TSSR
2041 035364             ERRDF  ERRNO,SFIERR,SFIMSG  ;DEVICE FATAL ERROR DURING INIT
      035364 104455                                TRAP  CIERDF
      035366 001611                                .WORD 905
      035370 003644'                                .WORD SFIERR

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2088 ;IF ISSUED WITH AN INVALID DATA BLOCK BYTE COUNT
2089 ;
2090 ;-
2091 ;-
2092 035540             BGNSUB             ;////////////////// BEGIN SUBTEST ////////////////////
      035540             T9.3:          TRAP      C1BSUB
      035540 104402
2093
2094 035542             SETPRI    #PRI00           ;LOWER PRIORITY TO ALLOW INTERRUPTS
      035542 012700 000000             MOV      #PRI00,R0
      035546 104441             TRAP      C1SPRI
2095 035550 012703 000001             ;STARTING BYTE COUNT
2096 035554 012704 037010'          5#:  MOV      #T9PACKET,R4           ;GET THE ADDRESS OF COMMAND PACKET
2097 035560 004737 040106'          JSR      PC,T9REST           ;RESTORE PACKET TO STARTING VALUES
2098
2099 035564             BGNSEG             ;>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>
      035564 104404             TRAP      C1BSEG
2100
2101 035566 004737 015604'          JSR      PC,SOFINIT           ;DO SOFT INIT OF CONTROLLER
2102 035572 103405             BCS     10#                    ;BR IF SOFT INIT = OK
2106 035574 010001             MOV      R0,R1                    ;SAVE CONTENTS OF TSSR
2107 035576             ERRDF    ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
      035576 104455             TRAP      C1ERDF
      035600 001615             .WORD    909
      035602 003644'          .WORD    SFIERR
      035604 011644'          .WORD    SFIMSG
2108 035606 005037 002222'          10#: CLR      INTRECV           ;CLEAR INTERRUPT RECEIVED FLAG
2109 035612 010364 000006          MOV      R3,PKBCNT(R4)         ;INSERT THE BYTE COUNT FOR TEST
2110 035616 010465 000000          MOV      R4,TSDB(R5)         ;SET THE PACKET ADDRESS
2111 035622 004737 016060'          JSR      PC,WAITF           ;WAIT FOR SSR TO SET
2112 035626 103405             BCS     15#                    ;BR IF CARRY SET (GOOD RETURN)
2113 035630 010001             MOV      R0,R1                    ;SAVE CONTENTS OF TSSR
2117 035632             ERRDF    ERRNO,T9SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
      035632 104455             TRAP      C1ERDF
      035634 001616             .WORD    910
      035636 037527'          .WORD    T9SSR
      035640 011656'          .WORD    PKTSSR
2118 035642             15#:  CKLOOP           ;LOOP ON ERROR, IF FLAG SET
      035642 104406             TRAP      C1CLP1
2119 035644             ESCAPE  SEG           ;BY-PASS SUBTEST IF FATAL ERROR
      035644 104410             TRAP      C1ESCAPE
      035646 000056             .WORD    10000#--
2120 035650 005737 002222'          TST     INTRECV           ;DID AN INTERRUPT OCCUR ?
2121 035654 001004             BNE     22#                    ;BRANCH IF YES
2125 035656             ERRHRD   ERRNO,T9NINT,PKTSSR
      035656 104456             TRAP      C1ERHRD
      035660 001617             .WORD    911
      035662 037616'          .WORD    T9NINT
      035664 011656'          .WORD    PKTSSR
2126 035666 016501 000002          22#: MOV      TSSR(R5),R1         ;GET THE CONTENTS OF TSSR
2127 035672 012702 102206          MOV      #SC!SSR!TSREJ!NBA,R2 ;EXPECTED CONTENTS OF TSSR
2128 035676 032701 000100          BIT     #OFL,R1              ;IS OFF-LINE BIT SET ?
2129 035702 001402             BEQ     25#                    ;BRANCH IF NOT OFF-LINE
2130 035704 052702 000100          BIS     #OFL,R2              ;SET OFF-LINE IN EXPECTED DATA
2131 035710 020201             25#: CMP      R2,R1              ;DOES EXPECTED MATCH RECEIVED ?
2132 035712 001404             BEQ     30#                    ;OKAY IF MATCH
2136 035714             ERRHRD   ERRNO,T93REJ,PKTSSR ;COMMAND NOT REJECTED
  
```



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2271
2272 ; THIS SUBTEST IS EXECUTED ONLY IF THE EXTENDED
2273 ; FEATURES MODE IS ENABLED (AS DETERMINED BY EXAMINING
2274 ; XST2 AFTER A PREVIOUS EXECUTION OF WRITE CHARACTERISTICS).
2275 ; IT VERIFIES THAT A FIFTH CHARACTERISTICS DATA WORD IS FETCHED
2276 ; IF THE BYTE COUNT PARAMETER IN THE COMMAND PACKET IS 10 DECIMAL
2277 ; OR GREATER.
2278 ;
2279 ;
2280 036336         ;-----
                BGNSUB                                ;/////////// BEGIN SUBTEST ////////////
                T9.6:                                  TRAP    C#BSUB
2281 036336 104402                                ;IS EXTENDED FEATURES SOFT. SW SET?
                TST    EXTFEA                          ;BR, IF SOFTWARE SWITCH IS SET (ON)
2282 036340 005737 002224'                       ;EXIT SUBTEST
                BNE    4$                               ;SET PACKET TO START-UP VALUES
2283 036346 000137 036566'                       ;
                JMP    5$
2284 036352 004737 040106'                       ;
                JSR    PC,T9REST
2285
2286 036356                                ;LOWER PRIORITY TO ALLOW INTERRUPTS
                SETPRI #PRI00                          TRAP    C#SPRI
                MOV    #PRI00,R0
                T9.6:                                  TRAP    C#SPRI
2287 036364 012700 000000                          ;START OF TEST DATA
                MOV    #T9PACKET,R4
                MOV    #10.,PKBCNT(R4)                 ;GET THE ADDRESS OF COMMAND PACKET
2288 036370 012704 037010'                          ;START WITH EXTENDED FEATURES VALUE
2289 036374 012764 000012 000006                   5$:
                MOV    #10.,PKBCNT(R4)
2290 036402
2291 036402 104404                                ;>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>
                T9.6:                                  TRAP    C#BSEG
2292
2293 036404 004737 015604'                          ;DO SOFT INIT OF CONTROLLER
                JSR    PC,SOFINIT                       ;BR IF SOFT INIT = OK
2294 036410 103405
                BCS    10$
2298 036412 010001
                MOV    R0,R1                            ;SAVE CONTENTS OF TSSR
2299 036414 104455
                ERRDF  ERRNO,SFIERR,SFIMSG             ;DEVICE FATAL ERROR DURING INIT
                TRAP    C#ERDF
                .WORD   921
                .WORD   SFIERR
                .WORD   SFIMSG
                .WORD
2300 036424 005037 002220'                          10$:
                CLR    FATFLG                           ;CLEAR FATAL ERROR FLAG
                CLR    INTRECV                          ;CLEAR INTERRUPT RECEIVED FLAG
                MOV    R4,T9SSR(R5)                    ;SET THE PACKET ADDRESS
                JSR    PC,CHKTSSR                       ;WAIT FOR SSR TO SET
2301 036430 005037 002222'                          ;BR IF CARRY SET (GOOD RETURN)
                JSR    PC,CHKTSSR                       ;SAVE CONTENTS OF TSSR
2302 036434 010465 000000
                BCS    15$
2303 036440 004737 016146'                          ;DEVICE FATAL SSR FAILED TO SET
                JSR    PC,CHKTSSR                       TRAP    C#ERDF
2304 036444 103407
                MOV    R0,R1                            .WORD   922
2305 036446 010001
                ERRDF  ERRNO,T9SSR,PKTSSR             .WORD   T9SSR
2309 036450 104455
                ERRDF  ERRNO,T9SSR,PKTSSR             .WORD   PKTSSR
                TRAP    C#ERDF
                .WORD   922
                .WORD   T9SSR
                .WORD   PKTSSR
                .WORD
2310 036460 005237 002220'                          ;SET FATAL ERROR FLAG
                INC    FATFLG                           ;LOOP ON ERROR, IF FLAG SET
2311 036464 104406                                ;BY-PASS SUBTEST IF FATAL ERROR
                CKLOOP                                TRAP    C#CLP1
2312 036466 104410
                ESCAPE  SEG                             TRAP    C#ESCAPE
                .WORD   10000$ .
                .WORD
2313 036472 005737 002222'                          ;DID AN INTERRUPT OCCUR ?
                TST    INTRECV                          ;BRANCH IF YES
2314 036476 001004
                BNE    22$
                ERRMRD  ERRNO,T9NINT,PKTSSR
2318 036500 104456
                ERRMRD  ERRNO,T9NINT,PKTSSR           TRAP    C#ERMRD
                .WORD   923
                .WORD
036502 001633

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2406      ; -
2407
2409 037000      .BLKB  10-<.-TSV2E7>
2411 037010      T9PACKET: ;COMMAND PACKET FOR TEST
2412 037010 100204      .WORD  100204      ;WRITE CHAR COMMAND, WITH IE, ACK
2413 037012 037020'    .WORD  T9DATA      ;ADDRESS OF CHARACTERISTICS BLOCK
2414 037014 000000      .WORD  0
2415 037016 000010      .WORD  8.          ;STARTING VALUE OF BLOCK SIZE
2416
2417 037020      T9DATA:      ;CHARACTERISTICS DATA BLOCK
2418 037020 037032'    .WORD  T9BFR      ;ADDRESS OF MESSAGE BUFFER
2419 037022 000000      .WORD  0
2420 037024 000016      .WORD  14.         ;LENGTH OF MESSAGE BUFFER
2421 037026 000000 000000 .WORD  0,0
2422
2423 037032      T9BFR: .BLKW  8.          ;MESSAGE BUFFER
2424
2425      ; +
2426      ;
2427      ;TEST DATA FOR SUBTEST TWO
2428      ;
2429      ;DATA HAS FORMAT:
2430      ;
2431      ;          1ST WORD      OFFSET TO TEST WORD IN PACKET
2432      ;          2ND WORD      BITS TO SET FOR TEST
2433      ;
2434      ; -
2435
2436 037052      T92DATA:
2437 037052 000000 037140      .WORD  0,BIT5:BIT6:BIT9:BIT10:BIT11:BIT12:BIT13
2438 037056 000002 000001      .WORD  2,BIT0
2439 037062 000004 100100      .WORD  4,BIT6:BIT15
2440 037066'    T92DONE=.
2441
2442
2443      ; +
2444      ;LOCAL TEXT MESSAGES FOR TEST
2445      ; -
2446
2447 037066      127      122      111 T9NBA: .ASCIZ  'WRITE CHARACTERISTICS Command Not Accepted'
2448 037141      127      122      111 T92REJ: .ASCIZ  'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
2449 037240      127      122      111 T93REJ: .ASCIZ  'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
2450 037333      127      122      111 T94REJ: .ASCIZ  'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
2451 037431      127      122      111 T95REJ: .ASCIZ  'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
2452 037527      103      157      156 T95SR: .ASCIZ  'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
2453 037616      105      170      160 T9NINT: .ASCIZ  'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
2454 037707      125      156      145 T9INT: .ASCIZ  'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
2455 037776      111      156      143 T9TSBA: .ASCIZ  'Incorrect TSBA Address After WRITE CHARACTERISTICS'
2456 040061      103      157      155 TST9ID: .ASCIZ  'Completion Interrupt'
2457      .EVEN
2458
2459
2460      ; +
2461      ;
2462      ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
2463      ;
2464      ;

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2465
2466 040106          T9REST:
2467 040106          SAVREG          ;SAVE THE REGISTERS
2468 040112 012701 037010'   MOV     #T9PACKET,R1     ;START OF THE PACKET
2469 040116 012721 100204'   MOV     #100204,(R1)+    ;WRITE CHARACTERISTICS WITH ACK, IE
2470 040122 012721 037020'   MOV     #T9DATA,(R1)+   ;ADDRESS OF CHAR DATA BLOCK
2471 040126 005021          CLR     (R1)+           ;EXTENDED ADDRESS
2472 040130 012721 000010'   MOV     #8,(R1)+       ;SIZE OF DATA BLOCK IN BYTES
2473 040134 012721 037032'   MOV     #T9BFR,(R1)+   ;ADDRESS OF MESSAGE BUFFER
2474 040140 005021          CLR     (R1)+           ;
2475 040142 012721 000016'   MOV     #14,(R1)+      ;LENGTH OF MESSAGE BUFFER
2476 040146 005021          CLR     (R1)+           ;
2477 040150 005011          CLR     (R1)           ;
2478 040152 005037 037032'   CLR     T9BFR          ;CLEAR 1ST LOC IN MESSAGE BUFFER
2479 040156 000207          RTS      PC            ;RETURN
2480 040160          ENDTST
      040160
      040160 104401          L10065:      TRAP     C#ETST

2481
2482                .SBTTL  TEST 10: BASIC PACKET PROTOCOL
2483
2484                ;
2485                ;   THIS TEST VERIFIES BASIC OPERATION OF THE MESSAGE BUFFER RELEASE
2486                ;   COMMAND, THE FUNCTION OF THE ACK BIT IN THE COMMAND HEADER WORD,
2487                ;   AND THE REGISTER MODIFICATION REFUSED (RMR) LOGIC.
2488                ;
2489                ;
2490                ;*
2491                ;TEST 10 SUBTEST 1
2492                ;
2493                ;CHECKS THAT THE MESSAGE BUFFER RELEASE COMMAND WORKS
2494                ;PROPERLY AND THAT NO INTERRUPT IS GENERATED EVEN
2495                ;IF THE "IE" BIT IS SET IN THE COMMAND PACKET
2496                ;
2497                ;-
      040162          BGNTST
      040162
2502 040162 012700 043117'   MOV     #TST10ID,R0     ;ASCII MESSAGE TO IDENTIFY TEST
2503 040166 004737 016322'   JSR     PC,TSTSETUP     ;DO INITIAL TEST SETUP
2504 040172 012737 000024 002214'   MOV     #20,LOOPCNT    ;PERFORM 20 ITERATIONS
2505 040200          T10LOOP:
2506
2507 040200          BGNSUB          ;//////////////// BEGIN SUBTEST //////////////////
      040200          T10.1:      TRAP     C#BSUB
      040200 104402
2508
2509 040202 004737 043146'   JSR     PC,T10RST       ;SET PACKET TO INITIAL VALUES
2510 040206 012700 000000'   SETPRI #PRIO           ;LOWER PRIORITY TO ALLOW INTERRUPTS
      040206          MOV     #PRIO0,R0
      040212 104441          TRAP     C#SPRI
2511 040214 012704 042310'   MOV     #T10PACKET,R4   ;GET THE ADDRESS OF COMMAND PACKET
2512 040220 012764 000010 000006'   MOV     #8,PKBCNT(R4)  ;START WITH MINIMUM ALLOWABLE VALUE
2513 040226          S#:
2514 040226          BGNSEG          ;>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>
      040226 104404          TRAP     C#BSEG
2515
2516 040230 004737 015604'   JSR     PC,SOFINIT      ;DO SOFT INIT OF CONTROLLER
2517 040234 103405          BCS     10#            ;BR IF SOFT INIT = OK

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2521	040236	010001		MOV	R0,R1								
2522	040240			ERRDF	ERRNO,SFIERR,SFIMSG								
	040240	104455									TRAP	C#ERDF	
	040242	001751									.WORD	1001	
	040244	003644'									.WORD	SFIERR	
	040246	011644'									.WORD	SFIMSG	
2523	040250	005037	002220'	10#:	CLR	FATFLG							
2524	040254	005037	002222'		CLR	INTRECV							
2525	040260	010465	000000		MOV	R4,TSDB(R5)							
2526	040264	004737	016146'		JSR	PC,CHKTSSR							
2527	040270	103407			BCS	15#							
2528	040272	010001			MOV	R0,R1							
2532	040274				ERRDF	ERRNO,T10SSR,PKTSSR							
	040274	104455									TRAP	C#ERDF	
	040276	001752									.WORD	1002	
	040300	042650'									.WORD	T10SSR	
	040302	011656'									.WORD	PKTSSR	
2533	040304	005237	002220'		INC	FATFLG							
2534	040310			15#:	CKLOOP								
	040310	104406											
2535	040312				ESCAPE	SEG							
	040312	104410											
	040314	000056									TRAP	C#CLP1	
2536	040316	005737	002222'		TST	INTRECV							
2537	040322	001004			BNE	22#							
2541	040324				ERRHRD	ERRNO,T10NINT,PKTSSR							
	040324	104456									TRAP	C#ERHRD	
	040326	001753									.WORD	1003	
	040330	042737'									.WORD	T10NINT	
	040332	011656'									.WORD	PKTSSR	
2542	040334	016501	000002	22#:	MOV	TSSR(R5),R1							
2543	040340	012702	000200		MOV	#SSR,R2							
2544	040344	032701	000100		BIT	#OFL,R1							
2545	040350	001402			BEQ	25#							
2546	040352	052702	000100		BIS	#OFL,R2							
2547	040356	020201		25#:	CMP	R2,R1							
2548	040360	001404			BEQ	30#							
2552	040362				ERRHRD	ERRNO,T10NBA,PKTSSR							
	040362	104456									TRAP	C#ERHRD	
	040364	001754									.WORD	1004	
	040366	042511'									.WORD	T10NBA	
	040370	011656'									.WORD	PKTSSR	
2553	040372			30#:	ENDSEG								
2554	040372												
	040372	104405											
2555	040374				BGNSEG								
	040374	104404											
2556											TRAP	C#ESEG	
2557	040376	005037	002222'		CLR	INTRECV							
2558	040402	012737	025252' 042332'		MOV	#025252,T10BFR							
2559	040410	012714	100212		MOV	#100212,(R4)							
2560	040414	010465	000000		MOV	R4,TSDB(R5)							
2561	040420	004737	016146'		JSR	PC,CHKTSSR							
2562	040424	103407			BCS	45#							
2563	040426	010001			MOV	R0,R1							
2567	040430				ERRDF	ERRNO,T10SSR,PKTSSR							

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040430 104455
040432 001755
040434 042650'
040436 011656'
2568 040440 005237 002220'
2569 040444 104406 45: INC FATFLG ;SET FATAL ERROR FLAG
                                CKLOOP ;LOOP ON ERROR, IF FLAG SET
040444 104406
2570 040446 005737 002222' TST INTRECV ;DID AN INTERRUPT OCCUR ?
2571 040452 001404 BEQ 52: ;BRANCH IF NO
2575 040454 ERRHRD ERRNO,T10INT,PKTSSR
040454 104456 TRAP C:ERHRD
040456 001756 .WORD 1006
040460 043030' .WORD T10INT
040462 011656' .WORD PKTSSR
2576 040464 016501 000002 52: MOV TSSR(R5),R1 ;GET THE CONTENTS OF TSSR
2577 040470 012702 000200 MOV #SSR,R2 ;EXPECTED CONTENTS OF TSSR
2578 040474 032701 000100 BIT #OFL,R1 ;IS OFF-LINE BIT SET ?
2579 040500 001402 BEQ 55: ;BRANCH IF NOT OFF-LINE
2580 040502 052702 000100 BIS #OFL,R2 ;SET OFF-LINE IN EXPECTED DATA
2581 040506 020201 55: CMP R2,R1 ;DOES EXPECTED MATCH RECEIVED ?
2582 040510 001404 BEQ 60: ;OKAY IF MATCH
2586 040512 ERRHRD ERRNO,T10NBA,PKTSSR ;NBA SET
040512 104456 TRAP C:ERHRD
040514 001757 .WORD 1007
040516 042573' .WORD T10NBA
040520 011656' .WORD PKTSSR
2587 040522 013701 042332' 60: MOV T10BFR,R1 ;PICK UP THE 1ST WORD OF MESSAGE BUFFER
2588 040522 012702 025252 MOV #025252,R2 ;SET UP EXPECTED DATA
2590 040532 020102 CMP R1,R2 ;WAS ANY MESSAGE REC'D
2591 040534 001404 BEQ 70: ;BR, IF OK (EQUAL)
2595 040536 ERRHRD ERRNO,T10MBF,EXPREC ;MESSAGE BUFFER WAS MODIFIED
040536 104456 TRAP C:ERHRD
040540 001760 .WORD 1008
040542 042414' .WORD T10MBF
040544 015304' .WORD EXPREC
2596 040546 005737 002220' 70: TST FATFLG ;ANY FATAL ERRORS
2597 040546 001403 BEQ 80: ;BR, IF NO FATAL ERRORS
2598 040552 004737 017014' JSR PC,CKDROP ;TRY TO DROP THE UNIT
2600 040560 ENDSEG ;***** END SEGMENT *****
040560 104405 10001: TRAP C:SEEG
2601 040562 80: ENDSUB ;////////// END SUBTEST ////////////
2602 040562 L10076: TRAP C:ESUB
040562 104403

2603
2604
2605 ;TEST 10 SUBTEST 2
2606
2607 ;CHECKS THAT THE MESSAGE BUFFER RELEASE COMMAND WORKS
2608 ;PROPERLY AND THAT THERE IS AN INTERRUPT IF THE "IE"
2609 ;BIT IS SET IN THE COMMAND PACKET AND THE 'ERI' BIT
2610 ;IS SET IN THE CHARACTERISTICS DATA PACKET
2611
2612

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040760 042511'
040762 011656'
2660 040764 30$: ENDFSEG
2661 040764
040764 104405
2662 040766 BGNSEG
040766 104404
2663
2664 040770 005037 002222' CLR INTRECV ;CLEAR INTERRUPT RECEIVED FLAG
2665 040774 012737 025252 042332' MOV #025252,T10BFR ;WIPE OUT MESSAGE BUFFER AREA
2666 041002 012714 100212 MOV #100212,(R4) ;SET COMMAND PACKET TO MESS BUF REI
2667 041006 010465 000000 MOV R4,TSD8(R5) ;SET THE PACKET ADDRESS
2668 041012 004737 016146' JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
2669 041016 103407 BCS 45$ ;BR IF CARRY SET (GOOD RETURN)
2670 041020 010001 MOV R0,R1 ;SAVE CONTENTS OF TSSR
2674 041022 ERROF ERRNO,T10SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
041022 104455 TRAP C#ERDF
041024 001765 .WORD 1013
041026 042650' .WORD T10SSR
041030 011656' .WORD PKTSSR
2675 041032 005237 002220' 45$: INC FATFLG ;SET FATAL ERROR FLAG
2676 041036 CKLOOP ;LOOP ON ERROR, IF FLAG SET
041036 104406 TRAP C#CLP1
2677 041040 005737 002222' TST INTRECV ;DID AN INTERRUPT OCCUR ?
2678 041044 001004 BNE 52$ ;BRANCH IF YES
2682 041046 ERMRD ERRNO,T10INT,PKTSSR
041046 104456 TRAP C#EMRD
041050 001766 .WORD 1014
041052 043030' .WORD T10INT
041054 011656' .WORD PKTSSR
2683 041056 016501 000002 52$: MOV TSSR(R5),R1 ;GET THE CONTENTS OF TSSR
2684 041062 012702 000200 MOV #SSR,R2 ;EXPECTED CONTENTS OF TSSR
2685 041066 032701 000100 BIT #OFL,R1 ;IS OFF-LINE BIT SET ?
2686 041072 001402 BEQ 55$ ;BRANCH IF NOT OFF-LINE
2687 041074 052702 000100 BIS #OFL,R2 ;SET OFF-LINE IN EXPECTED DATA
2688 041100 CMP R2,R1 ;DOES EXPECTED MATCH RECEIVED ?
2689 041102 001404 BEQ 60$ ;OKAY IF MATCH
2693 041104 ERMRD ERRNO,T10NBA,PKTSSR ;NBA NOT SET
041104 104456 TRAP C#EMRD
041106 001767 .WORD 1015
041110 042573' .WORD T10NBA
041112 011656' .WORD PKTSSR
2694 041114
2695 041114 013701 042332' 60$: MOV T10BFR,R1 ;PICK UP THE 1ST WORD OF MESSAGE BUFFER
2696 041120 012702 025252 MOV #025252,R2 ;SET UP EXPECTED DATA
2697 041124 020102 CMP R1,R2 ;WAS ANY MESSAGE REC'D
2698 041126 001404 BEQ 70$ ;BR, IF OK (EQUAL)
2702 041130 ERMRD ERRNO,T10MBF,EXPREC ;MESSAGE BUFFER WAS MODIFIED
041130 104456 TRAP C#EMRD
041132 001770 .WORD 1016
041134 042414' .WORD T10MBF
041136 015304' .WORD EXPREC
2703
2704 041140
2705 041140 005737 002220' 70$: TST FATFLG ;ANY FATAL ERRORS
2706 041144 001402 BEQ 80$ ;BR, IF NO FATAL ERRORS

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TSV5A - HARDWARE TESTS MACRO M1113 07 FEB 84 10:58
 TEST 10: BASIC PACKET PROTOCOL

SEQ 147

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2707 041146 004737 017014'          JSR      PC,CKDROP          ;TRY TO DROP THE UNIT
2708 041152                         80$:           ENDSEG          ;<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<
2709 041152                         ENDSEG          10001$:           TRAP      C#ESEG
                                041152         104405                   ;//////////////// END SUBTEST //////////////////
2710 041154                         ENDSUB          L10077:          TRAP      C#ESUB
                                041154         104403                   ;
                                041154         104403                   ;
2711                             ;*
2712                             ;TEST 10 SUBTEST 3
2713                             ;
2714                             ;CHECKS THAT THE CPU GIVES UP OWNERSHIP OF THE MESSAGE BUFFER
2715                             ;AFTER THE MESSAGE BUFFER RELEASE, AND THAT FOLLOWING COMMANDS
2716                             ;WORK CORRECTLY
2717                             ;
2718                             ;
2719                             ;-
2720 041156                         BGNSUB          ;//////////////// BEGIN SUBTEST //////////////////
                                041156         104402                   T10.3:          TRAP      C#BSUB
                                041156         104402                   ;
2721                             ;
2722 041160 004737 043146'          JSR      PC,T1ORST         ;SET PACKET TO INITIAL VALUES
2723 041164 SETPRI          #PRI00        ;LOWER PRIORITY TO ALLOW INTERRUPTS
                                041164         012700         000000          MOV      #PRI00,R0
                                041170         104441          TRAP      C#SPRI
2724 041172 012704 042310'          MOV      #T10PACKET,R4    ;GET THE ADDRESS OF COMMAND PACKET
2725 041176 012764 000010 000006   MOV      #8.,PKBCNT(R4)   ;START WITH MINIMUM ALLOWABLE VALUE
2726 041204                         5$:           BGNSEG          ;>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>
2727 041204         104404                   TRAP      C#BSEG
2728                             ;
2729 041206 004737 015604'          JSR      PC,SOFINIT        ;DO SOFT INIT OF CONTROLLER
2730 041212 103405                   BCS     10$             ;BR IF SOFT INIT = OK
2734 041214 010001                   MOV      R0,R1          ;SAVE CONTENTS OF TSSR
2735 041216 ERRDF          ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
                                041216         104455          TRAP      C#ERDF
                                041220         001771          .WORD    1017
                                041222         003644          .WORD    SFIERR
                                041224         011644          .WORD    SFIMSG
2736 041226 005037 002220'          CLR      FATFLG         ;CLEAR FATAL ERROR FLAG
2737 041232 005037 002222'          CLR      INTRECV        ;CLEAR INTERRUPT RECEIVED FLAG
2738 041236 010465 000000          MOV      R4,TSDB(R5)    ;SET THE PACKET ADDRESS
2739 041242 004737 016146'          JSR      PC,CHKTSSR       ;WAIT FOR SSR TO SET
2740 041246 103407                   BCS     15$             ;BR IF CARRY SET (GOOD RETURN)
2741 041250 010001                   MOV      R0,R1          ;SAVE CONTENTS OF TSSR
2745 041252 ERRDF          ERRNO,T10SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
                                041252         104455          TRAP      C#ERDF
                                041254         001772          .WORD    1018
                                041256         042650          .WORD    T10SSR
                                041260         011656          .WORD    PKTSSR
2746 041262 005237 002220'          INC      FATFLG         ;SET FATAL ERROR FLAG
2747 041266 CKLOOP          15$:           CKLOOP          ;LOOP ON ERROR, IF FLAG SET
                                041266         104406                   TRAP      C#CLP1
2748 041270                         ESCAPE          SEG          ;BY-PASS SUBTEST IF FATAL ERROR
                                041270         104410          TRAP      C#ESCAPE
                                041272         000056          .WORD    10000$-.
2749 041274 005737 002222'          TST      INTRECV        ;DID AN INTERRUPT OCCUR ?

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2750	041300	001004			BNE	22:							
2754	041302				ERRHRD		ERRNO.T10NINT,PKTSSR						
	041302	104456							TRAP	C#ERHRD			
	041304	001773							.WORD	1019			
	041306	042737'							.WORD	T10NINT			
	041310	011656'							.WORD	PKTSSR			
2755	041312	016501	000002			22:	MOV	TSSR(R5),R1					
2756	041316	012702	000200				MOV	#SSR,R2					
2757	041322	032701	000100				BIT	#OFL,R1					
2758	041326	001402					BEQ	25:					
2759	041330	052702	000100				BIS	#OFL,R2					
2760	041334	020201				25:	CMP	R2,R1					
2761	041336	001404					BEQ	30:					
2765	041340						ERRHRD	ERRNO.T10NBA,PKTSSR					
	041340	104456							TRAP	C#ERHRD			
	041342	001774							.WORD	1020			
	041344	042511'							.WORD	T10NBA			
	041346	011656'							.WORD	PKTSSR			
2766	041350					30:							
2767	041350						ENDSEG						
	041350												
	041350	104405											
2768	041352						BGNSEG						
	041352	104404											
2769	041354	004737	043146'				JSR	PC.T10RST					
2770	041360	005037	002222'				CLR	INTRECV					
2771	041364	012737	025252	042332'			MOV	#025252,T10BFR					
2772	041372	012714	100212				MOV	#100212,(R4)					
2773	041376	010465	000000				MOV	R4,TSD8(R5)					
2774	041402	004737	016146'				JSR	PC.CMKTSSR					
2775	041406	103407					BCS	45:					
2776	041410	010001					MOV	R0,R1					
2780	041412						ERRDF	ERRNO.T10SSR,PKTSSR					
	041412	104455											
	041414	001775							TRAP	C#ERDF			
	041416	042650'							.WORD	1021			
	041420	011656'							.WORD	T10SSR			
2781	041422	005237	002220'				INC	FATFLG					
2782	041426					45:	CKLOOP						
	041426	104406											
2783	041430	005737	002222'				TST	INTRECV					
2784	041434	001404					BEQ	52:					
2788	041436						ERRHRD	ERRNO.T10INT,PKTSSR					
	041436	104456											
	041440	001776							TRAP	C#ERHRD			
	041442	043030'							.WORD	1022			
	041444	011656'							.WORD	T10INT			
2789	041446	016501	000002			52:	MOV	TSSR(R5),R1					
2790	041452	012702	000200				MOV	#SSR,R2					
2791	041456	032701	000100				BIT	#OFL,R1					
2792	041462	001402					BEQ	55:					
2793	041464	052702	000100				BIS	#OFL,R2					
2794	041470	020201				55:	CMP	R2,R1					
2795	041472	001404					BEQ	60:					
2799	041474						ERRHRD	ERRNO.T10NBA,PKTSSR					
	041474	104456											
	041476	001777							TRAP	C#ERHRD			
									.WORD	1023			

		041500	042573'				.WORD	T10NNBA
		041502	011656'				.WORD	PKTSSR
2800	041504			60:				
2801	041504	013701	042332'		MOV	T10BFR,R1		;PICK UP THE 1ST WORD OF MESSAGE BUFFER
2802	041510	012702	025252'		MOV	#025252,R2		;SET UP EXPECTED DATA
2803	041514	020102			CMP	R1,R2		;WAS ANY MESSAGE REC'D
2804	041516	001404			BEQ	70:		;BR, IF OK (EQUAL)
2808	041520				ERRHRD	ERRNO,T10MBF,EXPREC		;MESSAGE BUFFER WAS MODIFIED
	041520	104456					TRAP	C#ERRHRD
	041522	002000					.WORD	1024
	041524	042414'					.WORD	T10MBF
	041526	015304'					.WORD	EXPREC
2809								
2810	041530			70:	CKLOOP			;LOOP ON ERROR IF FLAG SET
	041530	104406					TRAP	C#CLP1
2811								
2812	041532	005037	002222'		CLR	INTRECV		;CLEAR INTERRUPT RECEIVED FLAG
2813	041536	004737	043146'		JSR	PC,T10RST		;RESET THE PACKETS AND COMMANDS
2814	041542	042714	100000'		BIC	#100000,(R4)		;CLEAR THE ACK BIT
2815	041546	010465	000000'		MOV	R4,TSSDB(R5)		;SET THE PACKET ADDRESS
2816	041552	004737	016146'		JSR	PC,CHKTSSR		;WAIT FOR SSR TO SET
2817	041556	103407			BCS	75:		;BR IF CARRY SET (GOOD RETURN)
2818	041560	010001			MOV	R0,R1		;SAVE CONTENTS OF TSSR
2822	041562				ERRDF	ERRNO,T10SSR,PKTSSR		;DEVICE FATAL SSR FAILED TO SET
	041562	104455					TRAP	C#ERRDF
	041564	002001					.WORD	1025
	041566	042650'					.WORD	T10SSR
	041570	011656'					.WORD	PKTSSR
2823	041572	005237	002220'		INC	FATFLG		;SET FATAL ERROR FLAG
2824	041576			75:	CKLOOP			;LOOP ON ERROR, IF FLAG SET
	041576	104406					TRAP	C#CLP1
2825	041600				ESCAPE	SEG		;BY-PASS SUBTEST IF FATAL ERROR
	041600	104410					TRAP	C#ESCAPE
	041602	000062					.WORD	10001#-. .
2826	041604	005737	002222'		TST	INTRECV		;DID AN INTERRUPT OCCUR ?
2827	041610	001006			BNE	82:		;BRANCH IF YES
2831	041612	016500	000002'		MOV	TSSR(R5),R0		;GET TSSR FOR ERROR REPORT
2832	041616				ERRHRD	ERRNO,T10NINT,PKTSSR		
	041616	104456					TRAP	C#ERRHRD
	041620	002002					.WORD	1026
	041622	042737'					.WORD	T10NINT
	041624	011656'					.WORD	PKTSSR
2833	041626	016501	000002'	82:	MOV	TSSR(R5),R1		;GET THE CONTENTS OF TSSR
2834	041632	012702	000200'		MOV	#SSR,R2		;EXPECTED CONTENTS OF TSSR
2835	041636	032701	000100'		BIT	#OFL,R1		;IS OFF-LINE BIT SET ?
2836	041642	001402			BEQ	85:		;BRANCH IF NOT OFF-LINE
2837	041644	052702	000100'		BIS	#OFL,R2		;SET OFF-LINE IN EXPECTED DATA
2838	041650	020201		85:	CMP	R2,R1		;DOES EXPECTED MATCH RECEIVED ?
2839	041652	001404			BEQ	90:		;OKAY IF MATCH
2843	041654				ERRHRD	ERRNO,T10SSR,PKTSSR		;NBA NOT ZERO
	041654	104456					TRAP	C#ERRHRD
	041656	002003					.WORD	1027
	041660	042650'					.WORD	T10SSR
	041662	011656'					.WORD	PKTSSR
2844	041664			90:				
2845	041664				ENDSEG			;***** END SEGMENT *****
	041664							10001#;

042224	002011					.WORD	1033
042226	042737					.WORD	T10NINT
042230	011656					.WORD	PKTSSR
2940	042232	016501	000002	22:	MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR
2941	042236	012702	110200		MOV	#SSR!RMR!SC,R2	;EXPECTED CONTENTS OF TSSR
2942	042242	032701	000100		BIT	#OFL,R1	;IS OFF-LINE BIT SET ?
2943	042246	001402			BEQ	25:	;BRANCH IF NOT OFF-LINE
2944	042250	052702	000100		BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA
2945	042254	020201		25:	CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?
2946	042256	001404			BEQ	30:	;OKAY IF MATCH
2950	042260				ERRMRD	ERRNO,T10SSR,PKTSSR	;NBA NOT ZERO
	042260	104456					TRAP
	042262	002012					C#ERMRD
	042264	042650					.WORD
	042266	011656					1034
2951	042270			30:			.WORD
2952	042270				ENDSEG		T10SSR
	042270						PKTSSR
2953	042272	104405			ENDSUB		
	042272						
2954	042274	104403			EXIT	TST	
	042274	104432					
	042276	000774					
2955							
2956							
2957							
2958							
2959							
2961	042300				.BLKB	10-<.-TSV2&7>	
2963	042310			T10PACKET:	.WORD	100204	;COMMAND PACKET FOR TEST
2964	042310	100204			.WORD	T10DATA	;WRITE CHAR COMMAND, WITH IE, ACK
2965	042312	042320			.WORD	0	;ADDRESS OF CHARACTERISTICS BLOCK
2966	042314	000000			.WORD	8.	;STARTING VALUE OF BLOCK SIZE
2967	042316	000010			.WORD		
2968							
2969	042320			T10DATA:	.WORD	T10BFR	;CHARACTERISTICS DATA BLOCK
2970	042320	042332			.WORD	0	;ADDRESS OF MESSAGE BUFFER
2971	042322	000000			.WORD	14.	;LENGTH OF MESSAGE BUFFER
2972	042324	000016			.WORD	0.0	
2973	042326	000000	000000		.WORD		
2974							
2975	042332			T10BFR: .BLKW	8.		;MESSAGE BUFFER
2976							
2977							
2978							
2979							
2980							
2981	042352						
2982	042352	100204		T10PKT:	.WORD	100204	;COMMAND PACKET FOR TEST
2983	042354	042362			.WORD	T10DATA	;WRITE CHAR COMMAND, WITH IE, ACK
2984	042356	000000			.WORD	0	;ADDRESS OF CHARACTERISTICS BLOCK
2985	042360	000010			.WORD	8.	;STARTING VALUE OF BLOCK SIZE
2986							
2987	042362			T10DATA:	.WORD	T10BUFR	;CHARACTERISTICS DATA BLOCK
2988	042362	042374					;ADDRESS OF MESSAGE BUFFER


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2989 042364 000000          .WORD 0
2990 042366 000016          .WORD 14.          ;LENGTH OF MESSAGE BUFFER
2991 042370 000000 000000  .WORD 0,0
2992
2993 042374          T10BUFR: .BLKW 8.          ;MESSAGE BUFFER
2994
2995          ;+
2996          ;LOCAL TEXT MESSAGES FOR TEST
2997          ;-
2998
2999
3000 042414 115 145 163 T10MBF: .ASCIZ 'Message Buffer Modified after MESSAGE BUFFER RELEASE Command'
3001 042511 116 102 101 T10NBA: .ASCIZ 'NBA Not Clear After WRITE CHARACTERISTICS Command'
3002 042573 116 102 101 T10NNBA: .ASCIZ 'NBA Set After MESSAGE BUFFER RELEASE Command'
3003 042650 103 157 156 T10SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
3004 042737 105 170 160 T10INT: .ASCIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
3005 043030 125 156 145 T10INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
3006 043117 102 141 163 T10IOD: .ASCIZ 'Basic Packet Protocol'
3007          .EVEN
3008
3009
3010
3011          ;+
3012          ;
3013          ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
3014          ;
3015          ;-
3016
3017 043146          T10RST:
3018 043146          SAVREG          ;SAVE THE REGISTERS
3019 043152 012701 042310' MOV #T10PACKET,R1          ;START OF THE PACKET
3020 043156 012721 100204 MOV #100204,(R1)+          ;WRITE CHARACTERISTICS WITH ACK, IE
3021 043162 012721 042320' MOV #T10DATA,(R1)+          ;ADDRESS OF CHAR DATA BLOCK
3022 043166 005021 CLR (R1)+          ;EXTENDED ADDRESS
3023 043170 012721 000010 MOV #8,(R1)+          ;SIZE OF DATA BLOCK IN BYTES
3024 043174 012721 042332' MOV #T10BFR,(R1)+          ;ADDRESS OF MESSAGE BUFFER
3025 043200 005021 CLR (R1)+
3026 043202 012721 000016 MOV #14,(R1)+          ;LENGTH OF MESSAGE BUFFER
3027 043206 005021 CLR (R1)+
3028 043210 005011 CLR (R1)
3029 043212 005037 042332' CLR T10BFR          ;CLEAR 1ST LO^ IN MESSAGE BUFFER
3030 043216 000207 RTS PC          ;RETURN
3031
3032          ;+
3033          ;
3034          ;ROUTINE TO RESTORE COMMAND PACKET #2 TO START-UP (DEFAULT) VALUES
3035          ;
3036          ;-
3037 043220          T10RT2:
3038 043220          SAVREG          ;SAVE THE REGISTERS
3039 043224 012701 042352' MOV #T10PKT,R1          ;START OF THE PACKET
3040 043230 012721 100204 MOV #100204,(R1)+          ;WRITE CHARACTERISTICS WITH ACK, IE
3041 043234 012721 042362' MOV #T10DTA,(R1)+          ;ADDRESS OF CHAR DATA BLOCK
3042 043240 005021 CLR (R1)+          ;EXTENDED ADDRESS
3043 043242 012721 000010 MOV #8,(R1)+          ;SIZE OF DATA BLOCK IN BYTES
3044 043246 012721 042374' MOV #T10BUFR,(R1)+          ;ADDRESS OF MESSAGE BUFFER
3045 043252 005021 CLR (R1)+

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3097
3098 043376 005037 002220'        10$: CLR    FATFLG        ;CLEAR FATAL ERROR FLAG
3099 043402 005037 002222'        CLR    INTRECV       ;CLEAR INTERRUPT RECEIVED FLAG
3100 043406 010465 000000        MOV    R4,TSDB(R5)    ;SET THE PACKET ADDRESS
3101 043412 004737 016146'        JSR    PC,CHKTSSR     ;WAIT FOR SSR TO SET
3102 043416 103407                BCS    15$           ;BR IF CARRY SET (GOOD RETURN)
3103 043420 010001                MOV    R0,R1         ;SAVE CONTENTS OF TSSR
3107 043422                ERRDF   ERRNO,T11SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
                                     TRAP   C$ERDF
                                     .WORD  1103
                                     .WORD  T11SSR
                                     .WORD  PKTSSR
                                     .WORD
3108 043432 005237 002220'        15$: INC    FATFLG        ;SET FATAL ERROR FLAG
3109 043436                CKLOOP                 ;LOOP ON ERROR, IF FLAG SET
3110 043440                ESCAPE  SEG          ;BY-PASS SUBTEST IF FATAL ERROR
                                     TRAP   C$CLP1
                                     .WORD  C$ESCAPE
                                     .WORD  10000$-.
3111 043444 005737 002222'        TST    INTRECV       ;DID AN INTERRUPT OCCUR ?
3112 043450 001004                BNE    22$           ;BRANCH IF YES
3116 043452                ERRHRD  ERRNO,T11NINT,PKTSSR
                                     TRAP   C$ERHRD
                                     .WORD  1104
                                     .WORD  T11NINT
                                     .WORD  PKTSSR
3117 043462 016501 000002        22$: MOV    TSSR(R5),R1    ;GET THE CONTENTS OF TSSR
3118 043466 012702 000200        MOV    #SSR,R2       ;EXPECTED CONTENTS OF TSSR
3119 043472 032701 000100        BIT    #OFL,R1       ;IS OFF-LINE BIT SET ?
3120 043476 001402                BEQ    25$           ;BRANCH IF NOT OFF-LINE
3121 043500 052702 000100        BIS    #OFL,R2       ;SET OFF-LINE IN EXPECTED DATA
3122 043504 020201 000100        25$: CMP    R2,R1         ;DOES EXPECTED MATCH RECEIVED ?
3123 043506 001404                BEQ    30$           ;OKAY IF MATCH
3127 043510                ERRHRD  ERRNO,T11NBA,PKTSSR ;NBA NOT ZERO
                                     TRAP   C$ERHRD
                                     .WORD  1105
                                     .WORD  T11NBA
                                     .WORD  PKTSSR
3128 043520                30$:
3129 043520 004737 010724'        35$: JSR    PC,CKRAM       ;CHECK RAM TO MEMORY
3130 043524 103405                BCS    59$           ;RAM OK GO ON
3134 043526                ERRHRD  ERRNO,PKTRAM,RAMERR ;THEY DON'T MATCH
                                     TRAP   C$ERHRD
                                     .WORD  1106
                                     .WORD  PKTRAM
                                     .WORD  RAMERR
3135 043536                ENDSEG             ;<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<
                                     .WORD  10000$:
                                     TRAP   C$ESEG
3136 043536 104405                59$:
3137 043540                ENDSUB             ;////////// END SUBTEST ////////////
3138 043540                L10103:
3139 043540 104403                TRAP   C$ESUB
3140 043542 005737 002220'        TST    FATFLG        ;ANY FATAL ERRORS ?
3141 043546 001402                BEQ    60$           ;BRANCH IF NOT
3142 043550 004737 017014'        JSR    PC,CKDROP     ;TRY TO DROP THE UNIT

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3143 043554      60$:
3144
3145      ;*
3146      ;
3147      ;TEST 11, SUBTEST 2
3148      ;
3149      ;CHECK THAT NON-ZERO MODE BITS BEING SET CAUSES
3150      ;INITIALIZE COMMAND TO BE REJECTED
3151      ;
3152      ;-
3153
3154 043554      BGNSUB              ;////////// BEGIN SUBTEST ///////////
      043554                       T11.2:
      043554 104402                 TRAP      C#B$UB
3155
3156 043556      SETPRI #PRI00      ;LOWER PRIORITY TO ALLOW INTERRUPTS
      043556 012700 000000         MOV      #PRI00,R0
      043562 104441                 TRAP      C#SPRI
3157 043564      BGNSEG              ;>>>>>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>>>>>
      043564 104404                 TRAP      C#B$SEG
3158
3159
3160 043566 004737 015604'         JSR      PC,SOFINIT           ;DO SOFT INIT OF CONTROLLER
3161 043572 103405                 BCS      3$                  ;BR IF SOFT INIT = OK
3165 043574 010001                 MOV      R0,R1              ;SAVE CONTENTS OF TSSR
3166 043576      ERRDF             ERRNO,SFIERR,SFIMSG         ;DEVICE FATAL ERROR DURING INIT
      043576 104455                 TRAP      C#ERDF
      043600 002123                 .WORD    1107
      043602 003644'                .WORD    SFIERR
      043604 011644'                .WORD    SFIMSG
3167 043606
3168 043606 012704 044530'         MOV      #T11PK2,R4         ;WRITE CHARACTERISTICS PACKET
3169 043612 004737 010472'         JSR      PC,WRTCHR          ;ISSUE WRITE CHARACTERISTICS
3170 043616 103404                 BCS      4$                  ;BR, IF COMMAND ISSUED OK
3174 043620      ERRHRD             ERRNO,WRTMSG,SFIMSG         ;WRITE CHARACTERISTICSC FAILED
      043620 104456                 TRAP      C#ERHRD
      043622 002124                 .WORD    1108
      043624 005050'                .WORD    WRTMSG
      043626 011644'                .WORD    SFIMSG
3175 043630
3176 043630 004737 045334'         JSR      PC,T11REST         ;SET UP PACKET FOR COMMAND
3177 043634 012704 044460'         MOV      #T11PACKET,R4     ;GET THE ADDRESS OF COMMAND PACKET
3178 043640
3179 043640 005037 002222'         CLR      INTRECV           ;CLEAR INTERRUPT RECEIVED FLAG
3180 043644 052714 007400         BIS      #P.MODE,(R4)      ;NON-ZERO COMMAND MODE BITS
3181 043650 010465 000000         MOV      R4,TSDB(R5)       ;SET THE PACKET ADDRESS
3182 043654 004737 016146'         JSR      PC,CHKTSSR        ;WAIT FOR SSR TO SET
3183 043660 103405                 BCS      15$                ;BR IF CARRY SET (GOOD RETURN)
3184 043662 010001                 MOV      R0,R1              ;SAVE CONTENTS OF TSSR
3188 043664      ERRDF             ERRNO,T11SSR,PKTSSR        ;DEVICE FATAL SSR FAILED TO SET
      043664 104455                 TRAP      C#ERDF
      043666 002125                 .WORD    1109
      043670 045024'                .WORD    T11SSR
      043672 011656'                .WORD    PKTSSR
3189 043674      CKLOOP            15$:      CKLOOP           ;LOOP ON ERROR, IF FLAG SET
      043674 104406                 TRAP      C#CLP1
3190 043676      ESCAPE            SEG          ;BY-PASS CHECKS IF FATAL ERROR
    
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3237	044022			ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT		
	044022	104455					TRAP	C#ERDF
	044024	002131					.WORD	1113
	044026	003644'					.WORD	SFIERR
	044030	011644'					.WORD	SFIMSG
3238	044032			3#:				
3239	044032	012704	044530'		MOV #T11PK2,R4	;WRITE CHARACTERISTICS PACKET		
3240	044036	004737	010472'		JSR PC,WRTCHR	;ISSUE WRITE CHARACTERISTICS		
3241	044042	103404			BCS 4#	;BR, IF COMMAND ISSUED OK		
3245	044044				ERRHRD ERRNO,WRTMSG,SFIMSG	;WRITE CHARACTERISTIC FAILED		
	044044	104456					TRAP	C#ERHRD
	044046	002132					.WORD	1114
	044050	005050'					.WORD	WRTMSG
	044052	011644'					.WORD	SFIMSG
3246	044054			4#:				
3247	044054	004737	045334'		JSR PC,T11REST	;SET UP PACKET FOR COMMAND		
3248	044060	012704	044460'		MOV #T11PACKET,R4	;GET THE ADDRESS OF COMMAND PACKET		
3249	044064			5#:				
3250	044064	005037	002222'	10#:	CLR INTRECV	;CLEAR INTERRUPT RECEIVED FLAG		
3251	044070	010465	000000		MOV R4,TSDB(R5)	;SET THE PACKET ADDRESS		
3252	044074	004737	016146'		JSR PC,CHKTSSR	;WAIT FOR SSR TO SET		
3253	044100	103405			BCS 15#	;BR IF CARRY SET (GOOD RETURN)		
3254	044102	010001			MOV R0,R1	;SAVE CONTENTS OF TSSR		
3258	044104				ERRDF ERRNO,T11SR2,PKTSSR	;DEVICE FATAL SSR FAILED TO SET		
	044104	104455					TRAP	C#ERDF
	044106	002133					.WORD	1115
	044110	045100'					.WORD	T11SR2
	044112	011656'					.WORD	PKTSSR
3259	044114			15#:	CKLOOP	;LOOP ON ERROR, IF FLAG SET		
	044114	104406					TRAP	C#CLP1
3260	044116				ESCAPE SEG	;BY-PASS SUBTEST IF FATAL ERROR		
	044116	104410					TRAP	C#ESCAPE
	044120	000074					.WORD	10000#-
3261	044122	005737	002222'		TST INTRECV	;DID AN INTERRUPT OCCUR ?		
3262	044126	001004			BNE 22#	;BRANCH IF YES		
3266	044130				ERRHRD ERRNO,T11NINT,PKTSSR			
	044130	104456					TRAP	C#ERHRD
	044132	002134					.WORD	1116
	044134	045154'					.WORD	T11NINT
	044136	011656'					.WORD	PKTSSR
3267	044140	016501	000002	22#:	MOV TSSR(R5),R1	;GET THE CONTENTS OF TSSR		
3268	044144	012702	000200		MOV #SSR,R2	;EXPECTED CONTENTS OF TSSR		
3269	044150	032701	000100		BIT #OFL,R1	;IS OFF-LINE BIT SET ?		
3270	044154	001402			BEQ 25#	;BRANCH IF NOT OFF-LINE		
3271	044156	052702	000100		BIS #OFL,R2	;SET OFF-LINE IN EXPECTED DATA		
3272	044162	020201		25#:	CMP R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
3273	044164	001404			BEQ 30#	;OKAY IF MATCH		
3277	044166				ERRHRD ERRNO,T113REJ,PKTSSR	;COMMAND NOT ACCEPTED		
	044166	104456					TRAP	C#ERHRD
	044170	002135					.WORD	1117
	044172	044713'					.WORD	T113REJ
	044174	011656'					.WORD	PKTSSR
3278	044176			30#:				
3279	044176	004737	010724'	35#:	JSR PC,CKRAM	;CHECK RAM TO MEMORY		
3280	044202	103405			BCS 59#	;RAM OK GO ON		
3284	044204				ERRHRD ERRNO,PKTRAM,RAMERR	;THEY DON'T MATCH		
	044204	104456					TRAP	C#ERHRD

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044206 002136
044210 004737'
044212 015320'
3285 044214 104405      ENDSSEG
044214 104405
3286
3287
3288 044216 59$:      ENDSUB
044216 104403
044216 104403
3289
3290
3291
3292
3293
3294
3295
3296
3297
3298
3299 044220 3$:      BGNSUB
044220 104402
044220 104402
3300
3301 044222 3$:      SETPRI #PRI00
044222 012700 000000
044222 104441
3302 044230 3$:      BGNSSEG
044230 104404
3303 044232 004737 015604'
3304 044236 103405
3308 044240 010001
3309 044242 3$:      JSR PC,SOFINIT
044242 104455
044244 002137
044246 003644'
044250 011644'
3310 044252 3$:      MOV #T11PK2,R4
3311 044252 012704 044530'
3312 044256 004737 010472'
3313 044262 103404
3317 044264 3$:      JSR PC,WRTCHR
044264 104456
044266 002140
044270 005050'
044272 011644'
3318 044274 4$:      BCS 4$
3319 044274 004737 045334'
3320 044300 012704 044460'
3321 044304
3322 044304 005037 002222'
3323 044310 052714 007000
3324 044314 010465 000000
3325 044320 004737 016146'
3326 044324 103405
3327 044326 010001
3327 044326 10$:      JSR PC,T11REST
MOV #T11PACKET,R4
CLR INTRECV
BIS #007000,(R4)
MOV R4,TSDB(R5)
JSR PC,CHKTSSR
BCS 15$
MOV RO,R1

;***** END SEGMENT *****
10000$: TRAP C#ESEG

;////////// END SUBTEST ////////////
L10105: TRAP C#ESUB

;+
;
;TEST 11. SUBTEST 4
;
;SUBTEST TO VERIFY THAT A GET STATUS COMMAND IS
;REJECTED IF A NON-ZERO COMMAND MODE FIELD IS USED
;
;-

;////////// BEGIN SUBTEST ////////////
T11.4: TRAP C#BSUB

;LOWER PRIORITY TO ALLOW INTERRUPTS
MOV #PRI00,R0
TRAP C#SPRI
;***** BEGIN SEGMENT *****
TRAP C#BSEG
;DO SOFT INIT OF CONTROLLER
;BR IF SOFT INIT = OK
;SAVE CONTENTS OF TSSR
;DEVICE FATAL ERROR DURING INIT
TRAP C#ERDF
.WORD 1119
.WORD SFIERR
.WORD SFIMSG

;WRITE CHARACTERISTICS PACKET
;ISSUE WRITE CHARACTERISTICS
;BR, IF COMMAND ISSUED OK
;WRITE CHARACTERISTIC FAILED
TRAP C#ERMRD
.WORD 1120
.WORD WRTMSG
.WORD SFIMSG

;SET UP PACKET FOR COMMAND
;GET THE ADDRESS OF COMMAND PACKET

;CLEAR INTERRUPT RECEIVED FLAG
;SET TO NON-ZERO MODE
;SET THE PACKET ADDRESS
;WAIT FOR SSR TO SET
;BR IF CARRY SET (GOOD RETURN)
;SAVE CONTENTS OF TSSR
    
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3374 044462 044470'          .WORD  T11DATA          ;ADDRESS OF CHARACTERISTICS BLOCK
3375 044464 000000          .WORD  0              ;
3376 044466 000010          .WORD  8              ;STARTING VALUE OF BLOCK SIZE
3377
3378 044470          T11DATA:          ;CHARACTERISTICS DATA BLOCK
3379 044470 044502'          .WORD  T11BFR          ;ADDRESS OF MESSAGE BUFFER
3380 044472 000000          .WORD  0              ;
3381 044474 000016          .WORD  14             ;LENGTH OF MESSAGE BUFFER
3382 044476 000000 000000  .WORD  0,0           ;
3383
3384 044502          T11BFR: .BLKW  8              ;MESSAGE BUFFER
3385
3386
3388 044522          .BLKB  10-<.-TSV2&7>
3390 044530          T11PK2:          ;COMMAND PACKET FOR TEST
3391 044530 100204          .WORD  100204         ;WRITE CHAR COMMAND, WITH IE, ACK
3392 044532 044540'          .WORD  T11DTA         ;ADDRESS OF CHARACTERISTICS BLOCK
3393 044534 000000          .WORD  0              ;
3394 044536 000010          .WORD  8              ;STARTING VALUE OF BLOCK SIZE
3395
3396 044540          T11DTA:          ;CHARACTERISTICS DATA BLOCK
3397 044540 044552'          .WORD  T11BF2         ;ADDRESS OF MESSAGE BUFFER
3398 044542 000000          .WORD  0              ;
3399 044544 000016          .WORD  14             ;LENGTH OF MESSAGE BUFFER
3400 044546 000000 000000  .WORD  0,0           ;
3401
3402 044552          T11BF2: .BLKW  8              ;MESSAGE BUFFER
3403
3404
3405
3406
3407          ;*
3408          ;LOCAL TEXT MESSAGES FOR TEST
3409          ;-
3410 044572          111      116      111  T11NBA: .ASCIZ  'INITIALIZE Command Not Accepted'
3411 044632          111      116      111  T112REJ: .ASCIZ  'INITIALIZE Not Rejected With Non-Zero Mode Field'
3412 044713          107      105      124  T113REJ: .ASCIZ  'GET STATUS Not Accepted'
3413 044743          107      105      124  T114REJ: .ASCIZ  'GET STATUS Not Rejected With Non-Zero Mode Field'
3414 045024          103      157      156  T11SSR: .ASCIZ  'Contents of TSSR Incorrect After INITIALIZE'
3415 045100          103      157      156  T11SR2: .ASCIZ  'Contents of TSSR Incorrect After GET STATUS'
3416 045154          105      170      160  T11NINT: .ASCIZ  'Expected Interrupt Not Received On INITIALIZE'
3417 045232          111      156      143  T11TSBA: .ASCIZ  'Incorrect TSBA Address After INITIALIZE'
3418 045302          116      157      156  TST11ID: .ASCIZ  'Non-Tape Motion Commands'
3419
3420          .EVEN
3421
3422          ;*
3423          ;
3424          ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
3425          ;INITIALIZE COMMAND
3426          ;
3427          ;-
3428
3429 045334          T11REST:
3430 045334          SAVREG          ;SAVE THE REGISTERS
3431 045340 012701 044460'  MOV      #T11PACKET,R1  ;START OF THE PACKET
3432 045344 012721 100213  MOV      #100213,(R1)+ ;INITIALIZE WITH ACK, IE

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3433 045350 005021          CLR      (R1).      ;ADDRESS OF CHAR DATA BLOCK
3434 045352 005021          CLR      (R1).      ;EXTENDED ADDRESS
3435 045354 005021          CLR      (R1).      ;SIZE OF DATA BLOCK IN BYTES
3436 045356 005021          CLR      (R1).      ;ADDRESS OF MESSAGE BUFFER
3437 045360 005021          CLR      (R1).
3438 045362 005021          CLR      (R1).      ;LENGTH OF MESSAGE BUFFER
3439 045364 005021          CLR      (R1).
3440 045366 005011          CLR      (R1).
3441 045370 005037 044502'  CLR      T11BFR     ;CLEAR 1ST LOC IN MESSAGE BUFFER
3442 045374 000207          RTS       PC         ;RETURN
3443
3444
3445
3446
3447
3448
3449
3450 045376          T11RT2:
3451 045376          SAVREG
3452 045402 012701 044460'  MOV      #T11PACKET,R1 ;SAVE THE REGISTERS
3453 045406 012721 100217  MOV      #100217,(R1). ;START OF THE PACKET
3454 045412 005021          CLR      (R1).      ;GET STATUS WITH ACK, IE
3455 045414 005021          CLR      (R1).      ;ADDRESS OF CHAR DATA BLOCK
3456 045416 005021          CLR      (R1).      ;EXTENDED ADDRESS
3457 045420 005021          CLR      (R1).      ;SIZE OF DATA BLOCK IN BYTES
3458 045422 005021          CLR      (R1).      ;ADDRESS OF MESSAGE BUFFER
3459 045424 005021          CLR      (R1).      ;LENGTH OF MESSAGE BUFFER
3460 045426 005021          CLR      (R1).
3461 045430 005011          CLR      (R1).
3462 045432 005037 044502'  CLR      T11BFR     ;CLEAR 1ST LOC IN MESSAGE BUFFER
3463 045436 000207          RTS       PC         ;RETURN
3464 045440          ENDTST
3465 045442          L10102: TRAP      C$ETST
          ENDMOD

```

```

1          .TITLE  TSV6 - PARAMETER CODING
7
12
18
19 045442      BGNMOD  TSV6
    045442      TSV6::
20
21
22          .SBTTL  HARDWARE PARAMETER CODING SECTION
23
24          ;**
25          ; THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
26          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
27          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
28          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
29          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
30          ; WITH THE OPERATOR.
31          ;--
32 045442      BGNHRD
    045442      .WORD  L10107-L#HARD/2
    045444      L#HARD::
33
34 045444      GPRMA   HPM1,0,0,160010,177776,YES      ;GET TSBA/TSDB REGISTER ADDRESS.
    045444      .WORD  T#CODE
    045446      .WORD  HPM1
    045450      .WORD  T#LOLIM
    045452      .WORD  T#HILIM
35 045454      GPRMA   HPM2,2,0,0,776,YES              ;GET VECTOR ADDRESS.
    045454      .WORD  T#CODE
    045456      .WORD  HPM2
    045460      .WORD  T#LOLIM
    045462      .WORD  T#HILIM
36          ;GPRMD  HPM3,4,0,340,0,7,YES              ;GET INTERRUPT PRIORITY.
37 045464      ENDRD
    .EVEN
    045464      L10107:
38 045464      104     105     126  HPM1:  .ASCIZ  'DEVICE ADDRESS (TSBA/TSDB) '
39 045520      111     116     124  HPM2:  .ASCIZ  'INTERRUPT VECTOR '
40 045544      111     116     124  HPM3:  .ASCIZ  'INTERRUPT PRIORITY '
41          .EVEN
42
43          .SBTTL  SOFTWARE PARAMETER CODING SECTION
44
45          ;**
46          ; THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS
47          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
48          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
49          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
50          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
51          ; WITH THE OPERATOR.
52          ;--
53 045574      BGNSFT
    045574      .WORD  L10110-L#SOFT/2
    045576      L#SOFT::
54          ;
55 045576      GPRML   SPM1,0,-1,YES                    ; GET TRANSPORT TEST FLAG.
    045576      GPRML   SPM4,2,-1,YES                  ; GET ITERATION CONTROL.
    .WORD  T#CODE
    
```

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045600 045634' .WORD SPM4
045602 177777 .WORD -1
56 ; GPRMD SPM6,4,D,7777,0,7777,YES ; GET LOCAL ERROR LIMIT
57 ; GPRMD SPM7,6,D,7777,0,7777,YES ; GET GLOBAL ERROR LIMIT
58 045604 ENDSFT
.EVEN
045604 L10110:
59
60
61 045604 105 116 101 SPM1: .ASCIZ 'ENABLE TRANSPORT TESTS '
62 045634 111 116 110 SPM4: .ASCIZ 'INHIBIT ITERATIONS '
63 ;SPM6: .ASCIZ 'PER TEST ERROR LIMIT '
64 ;SPM7: .ASCIZ 'PER UNIT ERROR LIMIT '
65 .SBTTL PATCH AREA
66
67 ;
68 ; FINALLY A GENEROUS PATCH AREA.
69 ;
70 ; AND AN ADJUSTMENT TO ACCOUNT FOR THE "LASTAD BIT7" HACK
71 ; DESCRIBED IN "SUPPRG.MEM" (FOR REV C).
72 ;
73
74 045664 PATCH::
75
76 ; .BLKW 32.
77 045664 ; .BLKW 1.
78
79 ; .IF NZ,..&377
80 ; =.!377+1
81 ;
82 045666 LASTAD ;SET LAST USED ADDRESS.
.EVEN
045666 000000 .WORD 0
045670 000000 .WORD 0
045672
83 045672 L$LAST:: ENDMOD
84 .SBTTL HARD CODED P-TABLE
85
86 ;**
87 ;
88 045672 ;-- BGNSETUP 1
89 045672 BGNPTAB
045672 000000 .WORD 0
045674 000003 .WORD L10113-./2-1
045676
90 045676 172522 L10111: .WORD 172522
91 045700 000224 .WORD 224
92 045702 000240 .WORD PRI05
93 045704 ENDP TAB
045704 L10113:
94 045704 ENDSETUP
95
96 000001 .END

```

ADSSR 011736RG	002 C#AU = 000052	DEVDR0 023146R	002 FRESIZ 003124RG	002 INTFLA 015755R	002
ADR = 000020 G	C#AUTO= 000061	DEVNDR 023065R	002 FUSI 004111R	002 INTMAS 015754R	002
AMBTSS 006445R	002 C#BRK = 000022	DEVNDR 023003R	002 F#AU = 000015	INTR 016026RG	002
ASSEMB= 000010	C#BSEG= 000004	DEVONL 022733R	002 F#AUTO= 000020	INTREC 002222RG	002
A1716 = 000003	C#BSUB= 000002	DEVSUM 022676R	002 F#BGN = 000040	INTVEC 015756R	002
BADAT 003154RG	002 C#CEFG= 000045	DFPTBL 002154RG	002 F#CLEA= 000007	INTX 004272R	002
BADPCR 015510RG	002 C#CLCK= 000062	DIAGMC= 000000	F#DU = 000016	INVERT 020734RG	002
BDVPCR= 177520 G	C#CLEA= 000012	DICEA = 000001	F#END = 000041	IOCKI= 000200	
BENBSW 002226RG	002 C#CLOS= 000035	DSBINT 016014R	002 F#HARD= 000004	IOKSTP= 000001	
BIE = 040000	C#CLP1= 000006	DUAD12 004635R	002 F#HW = 000013	IPRI 002210RG	002
BIT0 = 000001 G	C#CVEC= 000036	DUFLG 003110RG	002 F#INIT= 000006	ISR = 000100 G	
BIT00 = 000001 G	C#DCLN= 000044	DUMMY 003060R	002 F#JMP = 000050	IVEC 002206RG	002
BIT01 = 000002 G	C#DODU= 000051	EF.CON= 000036 G	F#MOD = 000000	IXE = 004000 G	
BIT02 = 000004 G	C#DRPT= 000024	EF.NEW= 000035 G	F#MSG = 000011	I#AU = 000041	
BIT03 = 000010 G	C#DU = 000053	EF.PWR= 000034 G	F#PROT= 000021	I#AUTO= 000041	
BIT04 = 000020 G	C#EDIT= 000003	EF.RES= 000037 G	F#PWR = 000017	I#CLN = 000041	
BIT05 = 000040 G	C#ERDF= 000055	EF.STA= 000040 G	F#RPT = 000012	I#DU = 000041	
BIT06 = 000100 G	C#ERMR= 000056	EMAXDU 016611R	002 F#SEG = 000003	I#HRD = 000041	
BIT07 = 000200 G	C#ERRO= 000060	EN = 000000	F#SOFT= 000005	I#INIT= 000041	
BIT08 = 000400 G	C#ERSF= 000054	ENAINI 015762R	002 F#SRV = 000010	I#MOD = 000041	
BIT09 = 001000 G	C#ERSO= 000057	ENVIRN 020450R	002 F#SUB = 000002	I#MSG = 000041	
BIT1 = 000002 G	C#ESCA= 000010	EPRTSW 002176RG	002 F#SW = 000014	I#PROT= 000040	
BIT10 = 002000 G	C#ESEG= 000005	EPRT1 006170R	002 F#TEST= 000001	I#PTAB= 000041	
BIT11 = 004000 G	C#ESUB= 000003	EPRT2 006170R	002 GDDAT 003156RG	002 I#PWR = 000041	
BIT12 = 010000 G	C#ETST= 000001	ERRC 011543R	002 GERRMA 002172RG	002 I#RPT = 000041	
BIT13 = 020000 G	C#EXIT= 000032	ERRMI 002234RG	002 GETPAT 020014RG	002 I#SEG = 000041	
BIT14 = 040000 G	C#GETB= 000026	ERRK 016570R	002 GETSEL 020076RG	002 I#SETU= 000041	
BIT15 = 100000 G	C#GETW= 000027	ERRLO 002236RG	002 G#CNT0= 000200	I#SFT = 000041	
BIT2 = 000004 G	C#GMAN= 000043	ERRNO = 002144	G#DELM= 000372	I#SRV = 000041	
BIT3 = 000010 G	C#GPHR= 000042	ERRVEC= 000004 G	G#DISP= 000003	I#SUB = 000041	
BIT4 = 000020 G	C#GPLO= 000030	ERTABE 003374R	002 G#EXCP= 000400	I#TST = 000041	
BIT5 = 000040 G	C#GPRI= 000040	ERTABL 003174R	002 G#HLI= 000002	J#JMP = 000167	
BIT6 = 000100 G	C#INIT= 000011	ESUM 016572R	002 G#LOLI= 000001	KIPAR0= 172340	
BIT7 = 000200 G	C#INLP= 000020	EVL = 000004 G	G#NO = 000000	KIPAR1= 172342	
BIT8 = 000400 G	C#MANI= 000050	EXBCNT= 000010	G#OFFS= 000400	KIPAR2= 172344	
BIT9 = 001000 G	C#MEM = 000031	EXPBRE 015312RG	002 G#OSI= 000376	KIPAR3= 172346	
BOE = 000400 G	C#MSG = 000023	EXPD 002230RG	002 G#PRMA= 000001	KIPAR4= 172350	
BRINIT 004451R	002 C#OPEN= 000034	EXPOT 004525R	002 G#PRMD= 000002	KIPAR5= 172352	
BSELO = 000000	C#PNTB= 000014	EXPOT2 004561R	002 G#PRML= 000000	KIPAR6= 172354	
BSEL1 = 000001	C#PNTF= 000017	EXPMSG 002320RG	002 G#RADA= 000140	KIPAR7= 172356	
CHKAMB 015654R	002 C#PNTS= 000016	EXPREC 015304RG	002 G#RADB= 000000	KIPDR0= 172300	
CHKMAN 020320RG	002 C#PNTX= 000015	EXTA 005602R	002 G#RADD= 000040	KIPDR1= 172302	
CHKTSS 016146R	002 C#QIO = 000377	EXTEND 005600R	002 G#RADL= 000120	KIPDR2= 172304	
CKDROP 017014R	002 C#RDBU= 000007	EXTFEA 002224RG	002 G#RADO= 000020	KIPDR3= 172306	
CKEMAX 016714R	002 C#REFG= 000047	E#END = 002100	G#XFER= 000004	KIPDR4= 172310	
CKMSG 011170RG	002 C#RESE= 000033	E#LOAD= 000035	G#YES = 000010	KIPDR5= 172312	
CKMSG2 011310RG	002 C#REVI= 000003	FATERR= 000060	HIADDR= 001400	KIPDR6= 172314	
CKRAM 010724RG	002 C#RFLA= 000021	FATFLG 002220RG	002 HOE = 100000 G	KIPDR7= 172316	
CKRAM2 011034RG	002 C#RPT = 000025	FERCH 011532R	002 HPM1 045464R	002 KTENAB 003132RG	002
CMDPKT 021010RG	002 C#SEFG= 000046	FIFEXP 012000RG	002 HPM2 045520R	002 KTFLG 003130RG	002
CMPMEM 017500R	002 C#SPRI= 000041	FIFIMS 012052R	002 HPM3 045544R	002 KTINIT 020536R	002
CONF IG 017062R	002 C#SVEC= 000037	FIF2MS 012121R	002 IBE = 010000 G	KTOFF 017106R	002
COUNT 002306RG	002 C#TPRI= 000013	FILLME 017234R	002 IDU = 000040 G	KTON 017070R	002
CSRADD 002204RG	002 DATA 002310RG	FNOINT 004207R	002 IER = 020000 G	LERRMA 002170RG	002
CTAB 003162RG	002 DATASC 020052R	002 FORCER 002174RG	002 IFAULT 004250R	002 LISTAL= 000001	
CTABE 003174RG	002 DEBUGM 011442R	002 FREE 003122RG	002 INCERK 016656R	002 LOE = 040000 G	
CTABM 003162RG	002 DEVCNT 002216RG	002 FREEHI 003126R	002 INTCPC 015760R	002 LOOPCN 002214RG	002

RECMSG	002464RG	002	S1.IFM=	001000	TST7ID	034033R	002	T##SOF=	010110	T238	003144RG	002		
RECV	002232RG	002	S1.IHE=	000400	TST8ID	035017R	002	T##SRV=	010026	T3	024134RG	002		
REGSAV	017760R	002	S1.IID=	004000	TST9ID	040061R	002	T##SUB=	010106	T3BFLG	003146RG	002		
RETERR	005364R	002	S1.IIR=	020000	TSV2	002000RG	002	T##SW =	010001	T3LOOP	024152R	002		
REWIND	010624RG	002	S1.I2R=	040000	TSV3	002174RG	002	T##TES=	010102	T3SSR	024526R	002		
RMCHBE	000167		S1.PAR=	100000	TSV4	021316RG	002	T1	023216RG	002	T3TSBA	024416R	002	
RMCHEN	007200		S2.ATI=	000010	TSV5	023216RG	002	T1LOOP	023234R	002	T3TSSR	024462R	002	
RMMSCB	000215		S2.BTI=	000004	TSV6	045442RG	002	T1.1	023236R	002	T4	024626RG	002	
RMMSCZ	000234		S2.DIM=	000200	TTIBFR	177562 G		T1.2	023316R	002	T4LOOP	024646R	002	
RMPKIB	000201		S2.ILW=	000100	TTICSR	177560 G		T10	040162RG	002	T4.1	024626R	002	
RMPKTE	000210		S2.INR=	000020	TTIVFC	000060 G		T10BFR	042332R	002	T4.2	025070R	002	
RMR	010000		S2.OUT=	000040	T#ARGC	000003		T10BUF	042374R	002	T4.3	025370R	002	
RWPACK	010720R	002	S2.UND=	000003	T#CODE	001130		T10DAT	042320R	002	T5	026162RG	002	
SC	100000		TBLEND=	003060RG	002	T#ERRN=	002144	T10DTA	042362R	002	T5ADDR	027222R	002	
SCE	020000		TCOASC	006306R	002	T#EXCP=	000000	T10INT	043030R	002	T5LOOP	026200R	002	
SCHERR	005272R	002	TCOCOD	006506R	002	T#FLAG=	000040	T10LOO	040200R	002	T5MEM	027164R	002	
SCHE	005005R	002	TEMP1	003114RG	002	T#FREE=	045704R	002	T10MFB	042414R	002	T5.1	027304R	002
SELAY	010470R	002	TEMP2	003116RG	002	T#GMAN=	000000	T10NBA	042511R	002	T5.2	027352R	002	
SELASC	020262R	002	TERCLS	000016		T#HILI=	000776	T10NIN	042737R	002	T6	027266RG	002	
SELDAT	000004		TESTNO	000013		T#LAST=	000001	T10NMB	042573R	002	T6INT	030313R	002	
SEL2	000002		TEXASC	006245R	002	T#LOLI=	000000	T10PAC	042310R	002	T6LOOP	027304R	002	
SETMAP	017130R	002	TFCASC	006347R	002	T#LSYM=	010000	T10PKT	042352R	002	T6NBA	030210R	002	
SETU	021656R	002	TIMEXP	015362RG	002	T#LTNO=	000013	T10RST	043146R	002	T6NINT	030371R	002	
SFFMSG	011712RG	002	TIMSG0	015410R	002	T#NEST=	177777	T10RT2	043220R	002	T6PACK	030200R	002	
SFHERR	003677R	002	TINERR	011631R	002	T#NS0 =	000000	T10SSR	042650R	002	T6SSR	030235R	002	
SFIERR	003644R	002	TMPBFR	002630RG	002	T#NS1 =	000005	T10.1	040200R	002	T6TSBA	030451R	002	
SFMSG	011644RG	002	TNAM	016516R	002	T#NS2 =	000002	T10.2	040564R	002	T6.1	027304R	002	
SFPTBL	002164RG	002	TRANST	002164RG	002	T#NS3 =	000003	T10.3	041156R	002	T6.2	027630R	002	
SIFLAG	003152RG	002	TSBA =	000000 G		T#PCNT=	000000	T10.4	042046R	002	T7	030544RG	002	
SIMSG	011576R	002	TSBAH =	000001 G		T#PTAB=	010112	T11	043274RG	002	T7BFR	032726R	002	
SKIPT	003376R	002	TSBAM2	025774R	002	T#PTHV=	000001	T11BFR	044502R	002	T7DATA	032710R	002	
SOFINI	015604RG	002	TSBAM3	026056R	002	T#PTNU=	000001	T11BF2	044552R	002	T7INT	033661R	002	
SPACE	010276RG	002	TSDB =	000000 G		T#SAVL=	177777	T11DAT	044470R	002	T7LOOP	030562R	002	
SPM1	045604R	002	TSDBH =	000001 G		T#SEGL=	177777	T11DTA	044540R	002	T7NBA	033040R	002	
SPM4	045634R	002	TSFCOD	007046R	002	T#SEKO=	010000	T11LOO	043312R	002	T7NINT	033570R	002	
SRO	177572		TSREJ =	000006		T#SIZE=	000005	T11NBA	044572R	002	T7PACK	032700R	002	
SR1	177574		TSSDEF	006416R	002	T#SUBN=	000004	T11NIN	045154R	002	T7REST	034062R	002	
SR2	177576		TSSR	000002 G		T#TAGL=	177777	T11PAC	044460R	002	T7SP	032720R	002	
SR3	172516		TSSRBI	003474RG	002	T#TAGN=	010114	T11PK2	044530R	002	T7SSR	033501R	002	
SSR	000200		TSSRFO	006225R	002	T#TEMP=	000000	T11RES	045334R	002	T7TSBA	033750R	002	
STATCO	012222R	002	TSSRH =	000003 G		T#TEST=	000013	T11RT2	045376R	002	T7.1	030562R	002	
SVCGBL	000000		TSSX	004012R	002	T#TSTM=	177777	T11SR2	045100R	002	T7.2	031110R	002	
SVCINS	000000		TSTBLK	002750RG	002	T#TSTS=	000001	T11SSR	045024R	002	T7.3	031362R	002	
SVCSUB	000001		TSTCNT	002212RG	002	T#TAU =	010031	T11TSB	045232R	002	T7.4	031626R	002	
SVCAG	000000		TSTEND	016532R	002	T#TAUT=	010033	T11.1	043312R	002	T7.5	032054R	002	
SVCST	000001		TSTFLA	002312RG	002	T#TCLE=	010034	T11.2	043554R	002	T7.6	032320R	002	
S#LSYM	010000		TSTL00	016270RG	002	T#TDAT=	010113	T11.3	044000R	002	T72DAT	032746R	002	
SO.IDB	000010		TSTPTR	002314RG	002	T#TDU =	010032	T11.4	044220R	002	T72DON=	032762R	002	
SO.IFB	000002		TSTSET	016322RG	002	T#THAR=	010107	T112RE	044632R	002	T72NBA	032762R	002	
SO.IFP	000001		TST1ID	023414R	002	T#THW =	010000	T113RE	044713R	002	T72REJ	033113R	002	
SO.ILD	000020		TST10I	043117R	002	T#TINI=	010030	T114RE	044743R	002	T73REJ	033212R	002	
SO.ION	000040		TST11I	045302R	002	T#TMSG=	010025	T2	023436RG	002	T74REJ	033305R	002	
SO.IRD	000100		TST2ID	024104R	002	T#TPC =	000001	T2LOOP	023454R	002	T75REJ	033403R	002	
SO.IRW	000004		TST3ID	024577R	002	T#TPRO=	010027	T2SSR	024032R	002	T8	034132RG	002	
SO.ISP	000200		TST4ID	026136R	002	T#TPTA=	010112	T2TSBA	023720R	002	T8BFR	034562R	002	
S1.ICE	002000		TST5ID	027134R	002	T#TRPT=	010035	T2TSSR	023765R	002	T8DATA	034550R	002	
S1.IEO	010000		TST6ID	030523R	002	T#TSEG=	010000	T23A	003142RG	002	T8LOOP	034150R	002	

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SYMBOL TABLE

SEQ 168

T8NVCK	034637R	002	T92DAT	037052R	002	WF.IRE =	000040	XSOILC =	001000	X1.UNC =	000002		
T8PACK	034540R	002	T92DON =	037066R	002	WF.IWF =	000020	XSOLET =	020000	X2.BUF =	000100		
T8SSR	034730R	002	T92REJ	037141R	002	WF.IWR =	000100	XSOMDT =	000200	X2.EXT =	000200		
T8VCK	034602R	002	T93REJ	037240R	002	WF.I3R =	000002	XSONEF =	002000	X2.OPM =	100000		
T9	035036RG	002	T94REJ	037333R	002	WF.I4R =	000001	XSOONL =	000100	X2.RCE =	040000		
T9BFR	037032R	002	T95REJ	037431R	002	WRTCHR	010472RG	002	XSOPEL =	000010	X2.REV =	000077	
T9DATA	037020R	002	UAM =	000200 G		WRTERR	005105R	002	XSORLL =	010000	X2.SPA =	035400	
T9INT	037707R	002	UNITN	002200RG	002	WRTMSG	005050R	002	XSORLS =	040000	X2.UNI =	000007	
T9LOOP	035060R	002	UNREC =	000006		WSMBK	021020RG	002	XSOTMK =	100000	X2.WCF =	002000	
T9MBA	037066R	002	USI	004115R	002	XFERAS	015550R	002	XSOVCK =	000020	X3.DCK =	000010	
T9NINT	037616R	002	WAITF	016060RG	002	XNXM	016206R	002	XSOLE =	004000	X3.MBZ =	000006	
T9PACK	037010R	002	WC.IFA =	000200		XORBFO	007504R	002	XSOVW =	000004	X3.MDE =	177400	
T9REST	040106R	002	WC.IFE =	000002		XORFOR	007622R	002	XXCDPM	003120RG	002	X3.OPI =	000100
T9SSR	037527R	002	WC.IGO =	000001		XSTO =	000006 G		X#ALWA =	000000		X3.REV =	000040
T9TSBA	037776R	002	WC.IRE =	000010		XST1 =	000010 G		X#FALS =	000040		X3.RIB =	000001
T9.1	035060R	002	WC.IRW =	000004		XST2 =	000012 G		X#OFFS =	000400		X3.SPA =	000200
T9.2	035326R	002	WC.IOT =	000100		XST3 =	000014 G		X#TRUE =	000020		X3.TRF =	000020
T9.3	035540R	002	WC.IIT =	000040		XST4 =	000016 G		X1.COR =	020000		X4.HSP =	100000
T9.4	035744R	002	WC.ISR =	000020		XSOBOT =	000002		X1.DLT =	100000		X4.MBZ =	017400
T9.5	036132R	002	WF.IED =	000010		XSOEOT =	000001		X1.MBZ =	017375		X4.RCE =	040000
T9.6	036336R	002	WF.IER =	000004		XSOIE =	000040		X1.RBP =	000400		X4.TSM =	020000
T9.7	036570R	002	WF.IHI =	000200		XSOILA =	000400		X1.SPA =	040000		X4.WRC =	000377
. ABS.	000000	000											
	000000	001											
ABS	045704	002											
ERRORS DETECTED:	0												

VIRTUAL MEMORY USED: 28264 WORDS (111 PAGES)
DYNAMIC MEMORY: 20614 WORDS (79 PAGES)
ELAPSED TIME: 00:36:10
CZTSAA,CZTSAA.SEQ/-SP=SVC/ML,TSV1A,TSV22A,TSV3B,TSV4,TSV5A,TSV6