

.NLIST SEQ,BIN,LOC  
.REM 8

IDENTIFICATION

PRODUCT CODE: AC-S875A-MC  
PRODUCT NAME: CZKBAO KMC11-B STATIC PART1  
PROGRAM DATE: SEPTEMBER 1981  
MAINTAINER: CSS/NSG DIAGNOSTICS

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## 1.0 GENERAL INFORMATION

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THIS PROGRAM TESTS 1 TO 64 KMC11-B (M8206) MODULES. IT RUNS UNDER THE DIAGNOSTIC RUNTIME SERVICES (DRS).

## 1.1 PROGRAM ABSTRACT

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THIS PROGRAM CONSISTS OF A SET OF SEQUENTIAL LOGIC TESTS USED TO VERIFY MOST OF THE LOGIC OF THE KMC11-B. IT IS RUN BEFORE, AND IN CONJUNCTION WITH, CZKMC TO FULLY CHECK THE KMC11-B LOGIC.

### 1.1.1 STRUCTURE OF PROGRAM

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THIS DIAGNOSTIC OCCUPIES 14.5K WORDS OF MEMORY AND IS COMPATIBLE WITH BOTH XXDP+ AND ACT. IT CAN BE RUN STANDALONE UNDER XXDP+, AND CAN BE CHAINED UNDER XXDP+, ACT AND APT IN ACT MODE (SEE "CREATE CORE IMAGE" COMMAND BELOW FOR DETAILS OF CHAINING PROCEDURE).

WHEN THIS DIAGNOSTIC IS STARTED, CONTROL GOES FIRST TO THE SUPERVISOR. IT WILL THEN ENTER COMMAND MODE, INDICATED BY THE PROMPT 'DR>'. AT COMMAND MODE THE OPERATOR MAY THEN ENTER ANY OF SEVERAL COMMANDS AS DESCRIBED BELOW.

## 1.2 SYSTEM REQUIREMENTS

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### 1.2.1 HARDWARE REQUIREMENTS

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PDP-11 PROCESSOR WITH 16K OR MORE OF MEMORY  
CONSOLE DEVICE (LA36,LA120,LA34,VT100,ETC.)  
KMC11-B (M8206)

### 1.2.2 SOFTWARE REQUIREMENTS

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THE PROGRAM IS REVISION-D DIAGNOSTIC SUPERVISOR COMPATIBLE. CONSULT THE XXDP+ USERS MANUAL FOR OPERATING INSTRUCTIONS.

## 1.3 RELATED DOCUMENTS AND STANDARDS

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XXDP+ USERS MANUAL      CHQUS

## 1.4 DIAGNOSTIC HIERARCHY PREREQUISITES

NONE

1.5 ASSUMPTIONS  
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THE HARDWARE OTHER THAN THE SUBSYSTEM BEING TESTED IS ASSUMED TO WORK PROPERLY. FALSE ERRORS MAY BE REPORTED IF THE PROCESSOR, MEMORY, ETC., DO NOT FUNCTION PROPERLY.

2.0 OPERATING INSTRUCTIONS

2.1 LOADING AND STARTING PROCEDURES

2.1.1 LOADING PROCEDURES

THIS PROGRAM MAY BE LOADED FROM ANY XXDP+ LOAD MEDIA.

2.1.2 STARTING PROCEDURES

THE DIAGNOSTIC SUPERVISOR AUTOSTARTS ON LOADING. IT MAY BE RESTARTED AT 200(8).

A SAMPLE DIALOGUE IS LISTED BELOW:

DR>STA

---

CHANGE HW (L) ? Y

-----

# UNITS (D) ? 1

-----

UNIT 0

-----

CSR ADDRESS : (0) 174100 ? <CR>

-----

VECTOR ADDRESS : (0) 300 ? <CR>

-----

PRIORITY LEVEL : (0) 5 ? <CR>

-----

2.1.3 STEPS FOR QUICK AND SIMPLE EXECUTION

THE DIAGNOSTIC CAN BE EXECUTED STANDALONE WITHOUT READING THE REMAINDER OF THIS DOCUMENT, AS FOLLOWS:

- A) LOAD THE DIAGNOSTIC FROM THE RELEVANT XXDP+ MEDIUM.
- B) RECEIVE PROMPT 'DR>'
- C) ENTER STA<CR>
- D) ANSWER HARDWARE QUESTIONS
- E) GET END OF PASS MESSAGES OR ERROR MESSAGES
- F) TO END EXECUTION, ENTER CONTROL/C. THIS RETURNS THE 'DR>' PROMPT.

## 2.2 SPECIAL ENVIRONMENTS

NONE

## 2.3 PROGRAM OPTIONS

### 2.3.1 START COMMAND

\*\*\*\*\*  
STA(RT)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:<FLAG-LIST>/EOP:<INCR>  
\*\*\*\*\*

#### 2.3.1.1 TESTS SWITCH (/TESTS:<TEST-LIS > )

<TEST-LIST> IS A SEQUENCE OF DECIMAL NUMBERS (1:2 ETC.) OR RANGES OF DECIMAL NUMBERS (1-5:8-10 ETC.) THAT SPECIFY THE TESTS TO BE EXECUTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS RANGE FROM 1 TO THE LARGEST TEST NUMBER IN THE DIAGNOSTIC. THEY MAY BE SPECIFIED IN ANY ORDER. TESTS WILL BE EXECUTED IN NUMERICAL ORDER REGARDLESS OF THE ORDER OF SPECIFICATION. THE DEFAULT IS TO EXECUTE ALL TESTS. ON THIS AND ALL SWITCHES, THE ANGLE BRACKETS <> ARE PUNCTUATION USED IN THE DEFINITION ONLY, AND ARE NOT TO BE TYPED BY THE OPERATOR. SEE EXAMPLE AT END OF 2.3.1.

#### 2.3.1.2 PASS SWITCH (/PASS:<PASS-CNT>)

<PASS-CNT> IS A DECIMAL NUMBER INDICATING THE DESIRED NUMBER OF PASSES. A PASS IS DEFINED AS THE EXECUTION OF THE FULL DIAGNOSTIC (ALL SELECTED TESTS) AGAINST ALL UNITS SUBMITTED. THE DEFAULT IS NON-ENDING EXECUTION. IN THIS CASE EXIT FROM THE PROGRAM IS ACCOMPLISHED EITHER BY TYPING A CONTROL/C OR BY OCCURANCE OF AN ERROR WITH THE HALT ON ERROR FLAG BEING SET. THE EXIT IS A RETURN TO COMMAND MODE. SEE EXAMPLE AT END OF 2.3.1.

#### 2.3.1.3 FLAGS SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS A SEQUENCE OF ELEMENTS OF THE FORM <FLAG>, <FLAG=1>, OR <FLAG=0>, SEPARATED BY COLONS, WHERE <FLAG> HAS ONE OF THE FOLLOWING VALUES:

HOE	HALT ON ERROR, CAUSING COMMAND MODE TO BE ENTERED WHEN AN ERROR IS ENCOUNTERED
LOE	LOOP ON ERROR, CAUSING THE DIAGNOSTIC TO LOOP CONTINUOUSLY WITHIN THE SMALLEST DEFINED BLOCK OF CODING (SEGMENT, SUBTEST, OR TEST) CONTAINING THE ERROR
IER	INHIBIT ERROR REPORTING
IBE	INHIBIT BASIC ERROR REPORTS
IXE	INHIBIT EXTENDED ERROR REPORTS
PRI	DIRECT ALL MESSAGES TO A LINE PRINTER
PNT	PRINT NUMBER OF TEST BEING EXECUTED

BOE BELL ON ERROR  
UAM RUN IN UNATTENDED MODE, BYPASSING MANUAL INTERVENTION TESTS  
ISR INHIBIT STATISTICAL REPORTS  
IDR INHIBIT DROPPING OF UNITS BY DIAGNOSTIC

THE FLAGS NAMED OR EQUATED TO 1 ARE SET, THOSE EQUATED TO 0 ARE CLEARED. A FLAG NOT SPECIFIED IS CLEARED. IF THE FLAGS SWITCH IS NOT GIVEN ALL FLAGS ARE CLEARED.

2.3.1.4 END OF PASS SWITCH (/EOP:<INCR>)

<INCR> IS A DECIMAL NUMBER INDICATING HOW OFTEN (IN TERMS OF PASSES) IT IS DESIRED THAT THE END OF PASS MESSAGE BE PRINTED. THE DEFAULT IS AT THE END OF EVERY PASS. SEE EXAMPLE AT END OF 2.3.1.

2.3.1.5 EFFECT OF COMMAND

THE EFFECT OF THE START COMMAND IS TO INITIATE THE HARDWARE PARAMETER DIALOGUE, THE SOFTWARE PARAMETER DIALOGUE, AND THEN THE DIAGNOSTIC TESTS THEMSELVES.

THE HARDWARE PARAMETER DIALOGUE COMMENCES WITH THE QUESTION '# UNITS?' TO WHICH THE OPERATOR REPLIES WITH A DECIMAL NUMBER N FROM 1 TO 64. THE TERM 'UNIT' REFERS TO THE DEVICE TO WHICH THIS SERIES OF DIAGNOSTICS IS DEDICATED. FOLLOWING THIS ARE THE QUESTIONS WHEREBY THE P-TABLES THEMSELVES WILL BE BUILT. EACH P-TABLE IS A CORE-RESIDENT TABLE CONTAINING ALL THE HARDWARE INFORMATION FOR ONE UNIT. THE OPERATOR MUST SUPPLY N (NUMBER OF UNITS) VALUES FOR EACH QUESTION. HE MAY DO THIS BY GIVING ONE ANSWER TO EACH QUESTION (IN WHICH CASE THE SERIES OF QUESTIONS WILL BE POSED N TIMES) OR BY GIVING N VALUES, SEPARATED BY COMMAS, TO EACH QUESTION (SERIES WILL BE POSED ONCE). EACH QUESTION IS FOLLOWED BY THE RESPONSE RADIX (D FOR DECIMAL, B FOR BINARY, O FOR OCTAL, L FOR YES/NO) IN PARENTHESES AND THE DEFAULT VALUE AFTER THE PARENTHESES.

FOLLOWING THE HARDWARE QUESTIONS ARE THE SOFTWARE QUESTIONS TO BUILD THE SOFTWARE TABLES, WHICH DEFINE THE MODE (QUICK VERIFY ETC.) THAT THE DIAGNOSTIC WILL EXECUTE IN.

WHEN THE QUESTION '# UNITS?' IS ANSWERED, MEMORY STORAGE IS ALLOCATED FOR THE P-TABLES, AND IF THERE IS NOT ENOUGH TO ACCOMMODATE THEM THE MESSAGE 'TOO MANY UNITS' IS ISSUED. IN THIS CASE THE DIAGNOSTIC MUST BE EXECUTED MORE THAN ONCE TO TEST ALL UNITS.

EXAMPLE:

STA/TESTS:1:2-4:6:8-10/PASS:3/FLAGS:IER:HOE=1:UAM:LOE

THIS COMMAND WILL CAUSE THREE PASSES TO BE MADE, EACH PASS CONSISTING OF TESTS 1,2,3,4,6,8,9, AND 10 EXECUTED AGAINST ALL UNITS. THERE IS NO DIFFERENCE BETWEEN SAYING <FLAG> AND SAYING <FLAG=1>. THE NOTATION <FLAG=0> IS MEANINGFUL ONLY ON A COMMAND OTHER THAN START TO CLEAR A FLAG THAT WAS PREVIOUSLY SET. NOTE THAT ON ALL COMMANDS ONLY THE FIRST THREE LETTERS ARE SCANNED.

### 2.3.2 RESTART COMMAND

\*\*\*\*\*  
RES(TART)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:<FLAG-LIST>/UNITS:<UNIT-LIST>  
\*\*\*\*\*

#### 2.3.2.1 TESTS, PASS, AND FLAGS SWITCHES

<TEST-LIST>, <PASS-CNT>, AND <FLAG-LIST> ARE AS IN THE START COMMAND.

#### 2.3.2.2 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS A SEQUENCE OF DECIMAL NUMBERS (1,2 ETC.) OR RANGES OF DECIMAL NUMBERS (1-5, 8-10 ETC.) THAT SPECIFY THE UNITS TO BE TESTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS MAY RANGE FROM 1 THRU N (N IS THE NUMBER OF UNITS SPECIFIED IN THE PREVIOUS START COMMAND). THE NUMBER INDICATES THE POSITION OF THE P-TABLE AS THE DATA WAS ENTERED DURING THE HARDWARE DIALOGUE. THE UNITS WHICH ARE SELECTED MUST NOT HAVE BEEN DROPPED BY THE DROP COMMAND. SEE THE DISCUSSION OF ADD AND DROP COMMANDS BELOW. DEFAULT IS TO TEST ALL UNITS WHICH HAVE NOT BEEN DROPPED BY A DROP COMMAND.

#### 2.3.2.3 EFFECT OF COMMAND

THE RESTART COMMAND DIFFERS FROM THE START COMMAND IN THAT THE P-TABLES FROM THE PREVIOUS START COMMAND (THERE MUST HAVE BEEN ONE) ARE USED, INSTEAD OF NEW ONES BEING BUILT. THE UNITS SWITCH GIVES THE ABILITY TO SELECT A SUBSET OF THESE. THE SOFTWARE DIALOGUE MAY OPTIONALLY BE RE-EXECUTED (OPERATOR WILL BE ASKED). THE COMMAND CAN BE USED AFTER COMMAND MODE HAS BEEN REENTERED IN ANY OF THE THREE NORMAL WAYS: A) THE REQUESTED NUMBER OF PASSES HAVE BEEN MADE B) AN ERROR WAS ENCOUNTERED WITH THE HALT ON ERROR FLAG SET C) A CONTROL/C WAS ENTERED BY THE OPERATOR.

### 2.3.3 CONTINUE COMMAND

\*\*\*\*\*  
CON(TINUE)/PASS:<PASS-CNT>/FLAGS:<FLAG-LIST>  
\*\*\*\*\*

#### 2.3.3.1 PASS SWITCH (/PASS:<PASS-CNT>)

<PASS-CNT> IS SAME AS IN START COMMAND, BUT THE DEFAULT IS THE UNSATISFIED PASS-CNT FROM THE PREVIOUS START OR RESTART. IF NONE REMAINS, THE DEFAULT IS NON-ENDING EXECUTION.

#### 2.3.3.2 FLAG SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS SAME AS IN START COMMAND. BUT UNSPECIFIED FLAGS RETAIN THEIR CURRENT VALUE.

#### 2.3.3.3 EFFECT OF COMMAND

CONTINUE MUST FOLLOW A START OR RESTART, AND COMMAND MODE MUST HAVE BEEN ENTERED DUE TO A HALT ON ERROR OR A CONTROL/C. THE EFFECT OF THE COMMAND IS TO GO TO THE BEGINNING OF THE TEST THAT WAS BEING EXECUTED WHEN THE HALT OR CONTROL/C TOOK PLACE. SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED. HARDWARE PARAMETERS MAY NOT BE CHANGED.

#### 2.3.4 PROCEED COMMAND

\*\*\*\*\*  
PRO(CEED)/FLAGS:<FLAG-LIST>  
\*\*\*\*\*

##### 2.3.4.1 FLAGS SWITCH (/FLAGS:<FLAG-LIST>)

<FLAG-LIST> IS AS IN THE START COMMAND, BUT UNSPECIFIED FLAGS RETAIN THEIR CURRENT VALUE.

##### 2.3.4.2 EFFECT OF COMMAND

PROCEED MUST FOLLOW A START, RESTART, OR CONTINUE. COMMAND MODE MUST HAVE BEEN ENTERED VIA A HALT ON ERROR. THE EFFECT OF THE COMMAND IS TO BEGIN EXECUTION AT THE LOCATION FOLLOWING THE ERROR CALL. NEITHER HARDWARE NOR SOFTWARE PARAMETERS MAY BE ALTERED.

#### 2.3.5 CREATE CORE IMAGE COMMAND

\*\*\*\*\*  
CCI/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:<FLAG-LIST>  
\*\*\*\*\*

##### 2.3.5.1 TESTS, PASS, AND FLAGS SWITCHES

<TEST-LIST>, <PASS-CNT>, <FLAG-LIST>, AND ARE AS IN THE START COMMAND, EXCEPT THAT THE UAM (UNATTENDED MODE) FLAG DEFAULTS TO THE SET POSITION.

##### 2.3.5.2 EFFECT OF COMMAND

THE PURPOSE OF THIS COMMAND IS TO CREATE A BIC FILE SUITABLE FOR CHAIN MODE EXECUTION. THE XXDP+ PROCEDURE IS AS FOLLOWS:

```
INVOKE THE XXDP+ UTILITY UPD1 OR UPD2
LOAD XXN:FILE.BIN
START 200
<QUESTIONS AND ANSWERS>
RESTART UPD1 USING RESTART ADDRESS
HICORE ADDRESS (IF "PASSED 14.5K" MESSAGE CAME)
DUMP XXN:FILE.BIC
```

THE OPERATOR DIALOGUE (HARDWARE AND SOFTWARE) WILL BE EXECUTED AS IN THE START COMMAND, BUT AT THE END OF THE QUESTIONS



THE HALT STATE WILL BE ENTERED. THE OPERATOR SHOULD THEN DUMP THE PROGRAM TO THE XXDP+ LIBRARY USING A BIC EXTENSION TO INDICATE THAT THIS FILE IS CHAINABLE. HE SHOULD USE THE XXDP+ UTILITY UPD1 OR UPD2 TO DO THIS. IF THE P-TABLES EXTEND BEYOND 14.5K, A MESSAGE WILL BE ISSUED GIVING THE NEW UPPER CORE LIMIT, TO WHICH THE OPERATOR MUST ADJUST BEFORE DUMPING. HE MAY NOW DELETE THE NON-CHAINABLE BIN FILE IF DESIRED, SINCE THE BIC FILE HAS ALL THE CAPABILITIES OF IT.

WHEN THIS BIC FILE IS SUBSEQUENTLY EXECUTED IN CHAIN MODE, THE OPERATOR DIALOGUES WILL BE BYPASSED. HOWEVER, IF IT IS EXECUTED STANDALONE, THE DIALOGUE WILL BE REISSUED.

NOTE THAT IF THE MESSAGE 'TOO MANY UNITS' IS ISSUED, TWO OR MORE CORE IMAGES MUST BE CREATED (WITH DIFFERENT NAMES) TO TEST ALL UNITS.

NOTE THAT ALTHOUGH THE CHAINABLE IMAGE CAN BE EXECUTED ON A 16K MACHINE, THE ORIGINAL CCI CREATION MUST BE DONE ON A LARGE MACHINE, THE EXACT SIZE BEING DEPENDENT ON WHICH UPDATE UTILITY IS USED.

### 2.3.6 ADD COMMAND

\*\*\*\*\*  
ADD/UNITS:<UNIT-LIST>  
\*\*\*\*\*

#### 2.3.6.1 UNITS SWITCH (/UNITS:<UNIT-LIST>

<UNIT-LIST> IS AS IN THE RESTART COMMAND.

#### 2.3.6.2 EFFECT OF COMMAND

THE UNITS SPECIFIED ARE ADDED TO THE TEST SEQUENCE. EACH UNIT MUST HAVE A P-TABLE IN MEMORY DUE TO AN EARLIER HARDWARE DIALOGUE. THIS COMMAND MUST BE FOLLOWED BY A RESTART OR CONTINUE. THE UNITS SWITCH MUST BE SPECIFIED. THE ADD COMMAND IS MEANINGFUL ONLY FOR UNITS THAT WERE PREVIOUSLY DROPPED.

### 2.3.7 DROP COMMAND

\*\*\*\*\*  
DRO(P)/UNITS:<UNIT-LIST>  
\*\*\*\*\*

#### 2.3.7.1 UNITS SWITCH (/UNITS:<UNIT-LIST>

<UNIT-LIST> IS AS IN THE RESTART COMMAND.

#### 2.3.7.2 EFFECT OF COMMAND

THE UNITS SPECIFIED WILL BE DROPPED FROM TESTING. THE UNITS WILL BE RESELECTED ONLY BY THE EXECUTION OF AN ADD OR START COMMAND. THE UNITS SWITCH MUST BE ENTERED. THIS COMMAND MUST BE

FOLLOWED BY A RESTART OR A CONTINUE COMMAND.

2.3.8 PRINT COMMAND

\*\*\*\*\*  
PRI(NT)  
\*\*\*\*\*

2.3.8.1 EFFECT OF COMMAND

ALL STATISTICS TABLES ACCUMULATED BY THE DIAGNOSTIC ARE PRINTED. THE  
ISR (INHIBIT STATISTICAL REPORTING) FLAG IS CLEARED.

2.3.9 DISPLAY COMMAND

\*\*\*\*\*  
DIS(PLAY)/UNITS:<UNIT-LIST>  
\*\*\*\*\*

2.3.9.1 UNITS SWITCH (/UNITS:<UNIT-LIST>)

<UNIT-LIST> IS AS IN THE RESTART COMMAND.

2.3.9.2 EFFECT OF COMMAND

THE HARDWARE P-TABLES FOR ALL UNITS UNDER TEST ARE PRINTED OUT IN THE  
FORMAT IN WHICH THEY WERE ENTERED. ANY UNITS THAT WERE DROPPED BY THE  
OPERATOR 'DROP' COMMAND ARE SO DESIGNATED.

2.3.10 FLAGS COMMAND

\*\*\*\*\*  
FLA(GS)  
\*\*\*\*\*

2.3.10.1 EFFECT OF COMMAND

THE CURRENT SETTINGS OF ALL FLAGS ARE PRINTED.

2.3.11 ZFLAGS COMMAND

\*\*\*\*\*  
ZFL(AGS)  
\*\*\*\*\*

2.3.11.1 EFFECT OF COMMAND

ALL FLAGS ARE CLEARED.

2.3.12 CONTROL CHARACTERS

A CONTROL C (^C) ENTERED DURING THE EXECUTION OF A DIAGNOSTIC

CAUSES A RETURN TO COMMAND MODE.

A CONTROL Z (^Z) ENTERED DURING ONE OF THE THREE OPERATOR DIALOGUES (HARD CORE QUESTIONS (SEE 1.1.1), HARDWARE DIALOGUE (SEE 2.3.1.5), OR SOFTWARE DIALOGUE (SEE 2.3.1.5) CAUSES THE DEFAULTS TO BE TAKEN FOR THE REMAINDER OF THAT DIALOGUE.

A CONTROL O (^O) ENTERED DURING THE EXECUTION OF A DIAGNOSTIC CAUSES ALL TELETYPE OUTPUT TO BE SUPPRESSED FOR THE REMAINDER OF THE DIAGNOSTIC OR UNTIL ANOTHER ^O IS TYPED, WHICH RESTORES NORMAL TELETYPE OUTPUT.

### 2.3.13 HARDWARE PARAMETERS

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THE FOLLOWING QUESTIONS WILL BE ASKED ON A START COMMAND. THE VALUE LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN ON A CARRIAGE RETURN RESPONSE.

CSR ADDRESS	(O)	174100	?
VECTOR	(O)	300	?
PRIORITY LEVEL	(O)	5	?

### 2.3.14 SOFTWARE PARAMETERS

-----

NONE

### 2.3.15 EXTENDED DISCUSSION OF P-TABLE DIALOGUE

-----

THE FULL CAPABILITY OF THE HARDWARE DIALOGUE IS REVEALED BY THE FOLLOWING DISCUSSION OF WHAT HAPPENS INTERNALLY.

AS SOON AS THE QUESTION "# UNITS?" IS ANSWERED (WITH THE NUMBER N, SAY) SPACE IN CORE IS ALLOCATED FOR N P-TABLES. ALL OF THE P-TABLES ARE OF THE SAME FORMAT, AND THERE IS A ONE-TO ONE CORRESPONDENCE BETWEEN THE HARDWARE PARAMETER QUESTIONS AND THE SLOTS IN THE P-TABLE FORMAT.

ON THE FIRST TRIP THRU THE QUESTIONS, ALL OF THE SLOTS IN ALL OF THE P-TABLES ARE FILLED. IF THE OPERATOR TYPES IN LESS THAN N EXPLICIT VALUES IN RESPONSE TO A PARTICULAR QUESTION, THESE VALUES ARE PLACED IN THE P-TABLES (ONE VALUE GOING INTO THE PROPER SLOT OF EACH P-TABLE BEGINNING WITH THE FIRST P-TABLE) UNTIL THE STRING OF VALUES IS EXHAUSTED. THE LAST VALUE IN THE STRING BECOMES THE NEW DEFAULT AND IS USED TO FILL THAT SLOT IN THE REMAINING P-TABLES.

ON SUBSEQUENT TRIPS THRU THE QUESTIONS, THE SAME PROCESS IS CARRIED OUT, EXCEPT THAT THE EARLIEST P-TABLE NOT TO HAVE RECEIVED AN EXPLICIT VALUE IN ANY OF ITS SLOTS NOW ASSUMES THE ROLE THAT TABLE NUMBER ONE PLAYED IN THE FIRST TRIP.

THE SERIES OF QUESTIONS IS REISSUED UNTIL AT LEAST ONE QUESTION HAS RECEIVED  
N EXPLICIT VALUES FROM THE OPERATOR.

IN GIVING A STRING OF VALUES, COMMAS WITHOUT INTERVENING VALUES MAY BE USED  
TO INDICATE A REPETITION OF THE LAST NAMED VALUE.

A STRING OF VALUES MAY BE GIVEN AS A RANGE (6-10 FOR EXAMPLE). IF THE  
VALUES REPRESENT PURE NUMERICAL DATA, THIS SAMPLE RANGE TRANSLATES TO THE  
STRING 6,7,8,9,10 (AN INCREMENT OF 1). IF THE VALUES ARE ADDRESSES, THE  
SAMPLE RANGE TRANSLATES TO THE STRING 6,8,10 (AN INCREMENT OF 2).

NOW LET US SEE HOW WE COULD USE THESE CAPABILITIES TO CONSTRUCT A SET OF  
P-TABLES. ASSUME THAT WE HAVE 64 UNITS, AND THAT THERE ARE THREE HARDWARE  
PARAMETERS FOR EACH (THREE SLOTS IN THE P-TABLE, THREE HARDWARE QUESTIONS  
IN THE DIALOGUE). LET THE DESIRED VALUE FOR THE FIRST PARAMETER  
BE THE NUMBER 75 FOR ALL 64 TABLES. LET THE DESIRED VALUE FOR THE SECOND  
PARAMETER BE EQUAL TO THE UNIT NUMBER (1,2,3,...,64) EXCEPT FOR UNIT  
50, WHICH SHOULD RECEIVE THE VALUE 49. LET THE DESIRED VALUE FOR THE THIRD  
PARAMETER BE THE NUMBER 76 FOR THE FIRST 20 UNITS AND THE NUMBER 77 FOR THE  
LAST 44 UNITS.

THE FOLLOWING DIALOGUE WOULD ACCOMPLISH THIS GOAL:

```
# UNITS (D) ? 64

UNIT 1
<QUESTION 1> ? 75
<QUESTION 2> ? 1-20
<QUESTION 3> ? 76

UNIT 21
<QUESTION 1> ?
<QUESTION 2> ? 21-49,,51-64
<QUESTION 3> ? 77
```

THE FIRST TIME THE SERIES IS ASKED, SLOT ONE RECEIVES A 75 IN ALL  
64 TABLES. SLOT TWO RECEIVES THE VALUES 1,2,3,...,20 IN TABLES 1 THRU 20  
AND A CONSTANT 20 IN TABLES 21 THRU 64. SLOT THREE RECEIVES A CONSTANT 76  
IN ALL 64 TABLES.

THE SECOND TIME THRU THE SERIES, TABLES 21 THRU THE END ARE GOING TO  
BE AFFECTED (NOTE THAT THIS PIECE OF INFORMATION IS PRINTED OUT FOR THE  
THE OPERATOR IN THE FORM "UNIT XX" AT THE BEGINNING OF EACH SERIES).  
QUESTION 1 IS RESPONDED TO BY A <CR>, SO SLOT ONE STAYS  
AT CONSTANT 75 IN TABLES 21 THRU 64, SINCE NO NEW EXPLICIT VALUES ARE TYPED  
IN. SLOT TWO GETS THE VALUES 21,22,23,...,49 IN TABLES 21 THRU 49, AND  
GETS A 49 IN SLOT 50, AND GETS THE VALUES 51,52,53,...,64 IN TABLES  
51 THRU 64. SLOT THREE GETS THE VALUE 77 IN TABLES 21 THRU 64.

THE DIALOGUE IS TERMINATED WHEN THE SOFTWARE RECOGNIZES THAT 64  
EXPLICIT VALUES HAVE BEEN GIVEN FOR AT LEAST ONE QUESTION  
(NAMELY QUESTION 2).

#### 2.4 EXECUTION TIMES

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ONE PASS OF ONE UNIT TAKES APPROXIMATELY 10 SECONDS.

### 3.0 ERROR INFORMATION

#### 3.1 ERROR REPORTING

THE ERROR MESSAGES PRODUCED BY THIS DIAGNOSTIC HAVE THE FOLLOWING FORMAT:

CZKMB.A DEV[FTL OR HRD] ERROR NNNNN ON UNIT UU TST NN SUB NNN PC XXXXX  
ASCII ERROR MESSAGE

#### 3.2 ERROR HALTS

ERROR HALTS ARE SUPPORTED PER DESCRIBED IN THE PREVIOUS SECTION  
WITH /FLAG:HOE. THERE ARE NO OTHER HALTS.

### 4.0 PERFORMANCE AND PROGRESS REPORTS

#### 4.1 PERFORMANCE REPORTS

A CUMALATIVE ERROR COUNT IS GIVEN AFTER PASS.

#### 4.2 PROGRESS REPORTS

A PASS COUNT IS UPDATED AND PRINTED AFTER EVERY PASS.

### 5.0 DEVICE INFORMATION TABLES

DEVICE:	M8206
CSR:	FLOATING (174.00 IS THE DEFAULT)
VECTOR:	FLOATING (300 IS THE DEFAULT)
BR LEVEL:	5

### 6.0 TEST SUMMARIES

TEST SUMMARIES ARE GIVEN AT THE BEGINNING OF EACH  
TEST MODULE

```
681          002000          .TITLE CZKMBAO KMC11-B STATIC PART1
682                                     .=2000
683
684
685
686
687
688
689          .MCALL  SVC
690 002000          SVC                                     : INITIALIZE SUPERVISOR MACROS
691
692
693
694
695
696 002000          BGNMOD  CZKMB
697
698
699          000000          $LSTIN= 0
700          000000          $LSTTAG= 0
701          000000          SVCINS= 0          ; LIST INSTRUCTIONS, SHIFTED RIGHT
702          000000          SVCTST= 0         ; LIST TEST TAGS, SHIFTED RIGHT
703          000000          SVCSUB= 0        ; LIST SUBTEST TAGS, SHIFTED RIGHT
704          000000          SVCGBL= 0       ; LIST GLOBAL TAGS, SHIFTED RIGHT
705          000000          SVCTAG= 0       ; LIST OTHER TAGS, SHIFTED RIGHT
706
707          ;          CHANGE THE VALUES OF THE SVC... SYMBOLS TO BE ZERO IF YOU WISH
708          ;          TO ALIGN THE MACRO CALLS AND THEIR EXPANSIONS. CHANGE THE
709          ;          SYMBOLS TO BE MINUS-ONE TO NOT LIST THE EXPANSIONS. YOU MAY
710          ;          CHANGE THE SYMBOLS AT ANY POINT IN YOUR PROGRAM.
711
712
713          .ENABL  AMA
```

714  
715  
716  
717  
718  
719  
720 002000  
721  
722  
723 002000  
724 002000  
725 002000 103  
726 002001 132  
727 002002 113  
728 002003 115  
729 002004 102  
730 002005 000  
731 002006 000  
732 002007 000  
733 002010  
734 002010 101  
735 002011  
736 002011 060  
737 002012  
738 002012 000000  
739 002014  
740 002014 000360  
741 002016  
742 002016 041146  
743 002020  
744 002020 000000  
745 002022  
746 002022 002366  
747 002024  
748 002024 002412  
749 002026  
750 002026 041740  
751 002030  
752 002030 000000  
753 002032  
754 002032 000000  
755 002034  
756 002034 000000  
757 002036  
758 002036 000000  
759 002040  
760 002040 002124  
761 002042  
762 002042 000000  
763 002044  
764 002044 000000  
765 002046  
766 002046 000000  
767 002050  
768 002050 003  
769 002051 003

.SBTTL PROGRAM HEADER  
:++  
: THE PROGRAM HEADER IS THE INTERFACE BETWEEN  
: THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.  
:--  
  
POINTER BGNAU,BGNDU,BGNSW,BGNRPT  
  
HEADER CZKMB,A,0,240,0  
L\$NAME:: :DIAGNOSTIC NAME  
      .ASCII /C/  
      .ASCII /Z/  
      .ASCII /K/  
      .ASCII /M/  
      .ASCII /B/  
      .BYTE 0  
      .BYTE 0  
      .BYTE 0  
L\$REV:: :REVISION LEVEL  
      .ASCII /A/  
L\$DEPO:: :0  
      .ASCII /O/  
L\$UNIT:: :NUMBER OF UNITS  
      .WORD 0  
L\$TIML:: :LONGEST TEST TIME  
      .WORD 240.  
L\$HPCP:: :POINTER TO H.W. QUES.  
      .WORD L\$HARD  
L\$SPCP:: :POINTER TO S.W. QUES.  
      .WORD 0  
L\$HPTP:: :PTR. TO DEF. H.W. PTABLE  
      .WORD L\$HW  
L\$SPTP:: :PTR. TO S.W. PTABLE  
      .WORD L\$SW  
L\$LADP:: :DIAG. END ADDRESS  
      .WORD L\$LAST  
L\$STA:: :RESERVED FOR APT STATS  
      .WORD 0  
L\$CO::  
      .WORD 0  
L\$DTYP:: :DIAGNOSTIC TYPE  
      .WORD 0  
L\$APT:: :APT EXPANSION  
      .WORD 0  
L\$DTP:: :PTR. TO DISPATCH TABLE  
      .WORD L\$DISPATCH  
L\$PRIO:: :DIAGNOSTIC RUN PRIORITY  
      .WORD 0  
L\$ENV:: :FLAGS DESCRIBE HOW IT WAS SETUP  
      .WORD 0  
L\$EXP1:: :EXPANSION WORD  
      .WORD 0  
L\$MREV:: :SVC REV AND EDIT #  
      .BYTE C\$REVISION  
      .BYTE C\$EDIT



770 002052  
771 002052 000000  
772 002054 000000  
773 002056  
774 002056 000000  
775 002060  
776 002060 003116  
777 002062  
778 002062 012454  
779 002064  
780 002064 000000  
781 002066  
782 002066 000000  
783 002070  
784 002070 013174  
785 002072  
786 002072 013170  
787 002074  
788 002074 000000  
789 002076  
790 002076 003124  
791 002100  
792 002100 104035  
793 002102  
794 002102 000000  
795 002104  
796 002104 012472  
797 002106  
798 002106 013166  
799 002110  
800 002110 012470  
801 002112  
802 002112 012462  
803 002114  
804 002114 000000  
805 002116  
806 002116 000000  
807 002120  
808 002120 000000  
809  
810  
811  
812  
813  
814  
815

L\$EF:: .WORD 0 ;DIAG. EVENT FLAGS  
L\$SPC:: .WORD 0  
L\$DEVP:: .WORD 0 ; POINTER TO DEVICE TYPE LIST  
L\$REPP:: .WORD L\$DVTYP ;PTR. TO REPORT CODE  
L\$EXP4:: .WORD L\$SRPT  
L\$EXP5:: .WORD 0  
L\$AUT:: .WORD L\$SAU ;PTR. TO ADD UNIT CODE  
L\$DUT:: .WORD L\$SDU ;PTR. TO DROP UNIT CODE  
L\$LUN:: .WORD L\$DU ;LUN FOR EXERCISERS TO FILL  
L\$DESP:: .WORD 0 ;PTR. TO DIAG. DESCRIPTION  
L\$LOAD:: .WORD L\$DESC ;GENERATE SPECIAL AUTOLOAD EMT  
L\$ETP:: EMT E\$LOAD ;PTR. TO ERR TBL  
L\$ICP:: .WORD 0 ;PTR. TO INIT CODE  
L\$CCP:: .WORD L\$INIT ;PTR. TO CLEAN-UP CODE  
L\$ACP:: .WORD L\$CLEAN ;PTR. TO AUTO CODE  
L\$PRT:: .WORD L\$AUTO ;PTR. TO PROTECT TABLE  
L\$TEST:: .WORD L\$PROT ;TEST NUMBER  
L\$DLY:: .WORD 0 ;DELAY COUNT  
L\$HIME:: .WORD 0 ;PTR. TO HIGH MEM

816  
817  
818  
819  
820  
821  
822  
823 002122  
824 002122 000120  
825 002124  
826 002124 013176  
827 002126 013320  
828 002130 013366  
829 002132 013576  
830 002134 013746  
831 002136 014112  
832 002140 014252  
833 002142 014412  
834 002144 014564  
835 002146 014770  
836 002150 015174  
837 002152 015376  
838 002154 015562  
839 002156 015674  
840 002160 016134  
841 002162 016430  
842 002164 016670  
843 002166 017130  
844 002170 017370  
845 002172 017630  
846 002174 020120  
847 002176 020422  
848 002200 020662  
849 002202 021122  
850 002204 021362  
851 002206 021622  
852 002210 022062  
853 002212 022322  
854 002214 022562  
855 002216 023022  
856 002220 023330  
857 002222 023726  
858 002224 024412  
859 002226 024722  
860 002230 025262  
861 002232 025570  
862 002234 025736  
863 002236 026100  
864 002240 026252  
865 002242 026456  
866 002244 026632  
867 002246 027006  
868 002250 027156  
869 002252 027350  
870 002254 027570  
871 002256 030016

.SBTTL DISPATCH TABLE

:///  
:// THE DISPATCH TABLE CONTAINS THE STARTING ADDRESS OF EACH TEST.  
:// IT IS USED BY THE SUPERVISOR TO DISPATCH TO EACH TEST.  
:///

DISPATCH 80.  
WORD 80  
LSDISPATCH::  
.WORD T1  
.WORD T2  
.WORD T3  
.WORD T4  
.WORD T5  
.WORD T6  
.WORD T7  
.WORD T8  
.WORD T9  
.WORD T10  
.WORD T11  
.WORD T12  
.WORD T13  
.WORD T14  
.WORD T15  
.WORD T16  
.WORD T17  
.WORD T18  
.WORD T19  
.WORD T20  
.WORD T21  
.WORD T22  
.WORD T23  
.WORD T24  
.WORD T25  
.WORD T26  
.WORD T27  
.WORD T28  
.WORD T29  
.WORD T30  
.WORD T31  
.WORD T32  
.WORD T33  
.WORD T34  
.WORD T35  
.WORD T36  
.WORD T37  
.WORD T38  
.WORD T39  
.WORD T40  
.WORD T41  
.WORD T42  
.WORD T43  
.WORD T44  
.WORD T45  
.WORD T46

872	002260	030154	.WORD	T47
873	002262	030376	.WORD	T48
874	002264	030542	.WORD	T49
875	002266	030752	.WORD	T50
876	002270	031162	.WORD	T51
877	002272	031372	.WORD	T52
878	002274	031602	.WORD	T53
879	002276	032012	.WORD	T54
880	002300	032222	.WORD	T55
881	002302	032432	.WORD	T56
882	002304	032642	.WORD	T57
883	002306	033054	.WORD	T58
884	002310	033264	.WORD	T59
885	002312	033474	.WORD	T60
886	002314	033704	.WORD	T61
887	002316	034114	.WORD	T62
888	002320	034324	.WORD	T63
889	002322	034534	.WORD	T64
890	002324	034744	.WORD	T65
891	002326	035154	.WORD	T66
892	002330	035364	.WORD	T67
893	002332	035574	.WORD	T68
894	002334	036004	.WORD	T69
895	002336	036214	.WORD	T70
896	002340	036424	.WORD	T71
897	002342	036634	.WORD	T72
898	002344	037044	.WORD	T73
899	002346	037254	.WORD	T74
900	002350	037464	.WORD	T75
901	002352	037674	.WORD	T76
902	002354	040104	.WORD	T77
903	002356	040314	.WORD	T78
904	002360	040524	.WORD	T79
905	002362	040734	.WORD	T80
906				
907				
908				
909				
910				
911				
912				

:LNT.ED DIFINED AT END OF PROGRAM TO BE LAST TEST NUMBER.

913  
914  
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002364  
002364 000011  
002366  
002366  
002366  
002370 174100  
002372 000300  
002374 005000  
002376 000003  
002400 000056  
002402 000000  
002404 000000  
002406 000000  
002410  
002410

.SBTTL DEFAULT HARDWARE P-TABLE

:/ THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF  
:/ THE TEST-DEVICE PARAMETERS. THE STRUCTURE OF THIS TABLE  
:/ IS IDENTICAL TO THE STRUCTURE OF THE RUN-TIME P-TABLE.

BGNHW DFPTBL  
.WORD L10000-L\$HW/2  
L\$HW::  
DFPTBL::

.WORD 6 :MICRO CPU TYPE  
.WORD 174100 :M8200,4,6,7 CSR ADDRESS  
.WORD 300 :M8200,4,6,7 VECTOR ADDRESS  
.WORD 5000 :INTERRUPT PRIORITY LEVEL  
.WORD 3 :LINE UNIT TYPE  
.WORD 56 :SWITCH PACK #1 (DDCMP LINE #)  
.WORD 0 :SWITCH PACK #2 (BM873 BOOT ADDRESS)  
.WORD 0 :SWITCH PACK #3  
.WORD 0 :TEST CONNECTOR INSTALLED FLAG

ENDHW  
L10000:

943  
944  
945  
946  
947  
948  
949  
950 002410  
951 002410 000000  
952 002412  
953 002412  
954  
955  
956 002412  
957 002412  
958  
959  
960  
961  
962  
963

.SBTTL SOFTWARE P-TABLE

://  
:// THE SOFTWARE P-TABLE CONTAINS THE VALUES OF THE PROGRAM  
:// PARAMETERS THAT CAN BE CHANGED BY THE OPERATOR.  
://

BGNSW SFPTBL  
.WORD L10001-L\$SW/2

L\$SW::  
SFPTBL::

ENDSW  
L10001:

964  
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967  
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971  
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1001  
1002  
1003  
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1005  
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1007  
1008  
1009  
1010  
1011  
1012  
1013  
1014  
1015  
1016  
1017  
1018  
1019

002412

100000  
040000  
020000  
010000  
004000  
002000  
001000  
000400  
000200  
000100  
000040  
000020  
000010  
000004  
000002  
000001  
001000  
000400  
000200  
000100  
000040  
000020  
000010  
000004  
000002  
000001  
000040  
000037

.SBTTL GLOBAL EQUATES SECTION

////////////////////////////////////  
// THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT  
// ARE USED IN MORE THAN ONE TEST.  
////////////////////////////////////

EQUALS

: BIT DEFINITIONS

BIT15== 100000  
BIT14== 40000  
BIT13== 20000  
BIT12== 10000  
BIT11== 4000  
BIT10== 2000  
BIT09== 1000  
BIT08== 400  
BIT07== 200  
BIT06== 100  
BIT05== 40  
BIT04== 20  
BIT03== 10  
BIT02== 4  
BIT01== 2  
BIT00== 1

BIT9== BIT09  
BIT8== BIT08  
BIT7== BIT07  
BIT6== BIT06  
BIT5== BIT05  
BIT4== BIT04  
BIT3== BIT03  
BIT2== BIT02  
BIT1== BIT01  
BIT0== BIT00

: EVENT FLAG DEFINITIONS  
: EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION

EF.START== 32. ; START COMMAND WAS ISSUED  
EF.RESTART== 31. ; RESTART COMMAND WAS ISSUED

: CONTINUE COMMAND WAS ISSUED  
: A NEW PASS HAS BEEN STARTED  
: A POWER-FAIL/POWER-UP OCCURRED

1020 000036  
1021 000035  
1022 000034  
1023  
1024  
1025  
1026  
1027 000340  
1028 000300  
1029 000240  
1030 000200  
1031 000140  
1032 000100  
1033 000040  
1034 J00000

EF.CONTINUE== 30.  
EF.NEW== 29.  
EF.PWR== 28.

:  
: PRIORITY LEVEL DEFINITIONS  
:

PRI07== 340  
PRI06== 300  
PRI05== 240  
PRI04== 200  
PRI03== 140  
PRI02== 100  
PRI01== 40  
PRI00== 0

:  
: OPERATOR FLAG BITS  
:

1035  
1036  
1037  
1038 000004  
1039 000010  
1040 000020  
1041 000040  
1042 000100  
1043 000200  
1044 000400  
1045 001000  
1046 002000  
1047 004000  
1048 010000  
1049 020000  
1050 040000  
1051 100000

EVL== 4  
LOT== 10  
ADR== 20  
IDU== 40  
ISR== 100  
UAM== 200  
BOE== 400  
PNT== 1000  
PRI== 2000  
IXE== 4000  
IBE== 10000  
IER== 20000  
LOE== 40000  
HOE== 100000

:\*\*\*\*\*  
:\* INSTRUCTION DEFINITIONS  
:\*\*\*\*\*  
POP2SP=22626 ;INCREMENT STACK TWICE

1052  
1053  
1054  
1055  
1056  
1057  
1058  
1059  
1060 022626  
1061  
1062  
1063  
1064  
1065  
1066  
1067  
1068  
1069

:\*\*\*\*\*  
:\* PROGRAM EVENT FLAG DEFINITIONS  
:\*\*\*\*\*

```
1070 .SBTTL GLOBAL DATA SECTION
1071
1072 :////////////////////
1073 :/ THE GLOBAL DATA SECTION CONTAINS DATA THAT ARE USED
1074 :/ IN MORE THAN ONE TEST.
1075 :////////////////////
1076
1077 :*****
1078 :* PROGRAM CONTROL PARAMETERS
1079 :*****
1080 002412 000000 NEXT: .WORD 0 ;ADDRESS OF NEXT TEST TO BE EXECUTED
1081 002414 000000 LOCK: .WORD 0 ;ADDRESS FOR LOCK CURRENT DATA
1082
1083 :*****
1084 :* BUFFERS FOR INPUT-OUTPUT
1085 :*****
1086 002416 000000 TEMP: 0
1087 002460 .+.40
1088 002460 000000 MDATA: 0
1089 002522 .+.40
1090
1091 :*****
1092 :* MISCELLANEOUS STORAGE
1093 :*****
1094 002522 000000 $TMP0: .WORD 0 ;SCRATCH STORAGE
1095 002524 000000 LOGDEV: .WORD 0 ;LOGICAL DEVICE NUMBER
1096 002526 000000 PSTACK: .WORD 0 ;BASE LEVEL PROGRAM STACK POINTER
1097 002530 000000 SUBRPC: .WORD 0 ;PC OF SUBR CALL FOR ERROR REPORTS
1098 002532 000000 ERRFLG: .WORD 0 ;SUBROUTINE ERROR FLAG
1099 002534 000000 RETADR: .WORD 0 ;SUBR ERROR RETURN ADDRESS
1100 002536 000000 STRTSW: .WORD 0 ;SWITCHES AT START OF PROGRAM
1101 002540 000000 STAT: .WORD 0 ;M8200,4,6,7 STATUS WORD STORAGE
1102 002542 000000 CLKX: .WORD 0
1103 002544 000000 MASKX: .WORD 0
1104 002546 000000 SAVSP: .WORD 0 ;STACK POINTER STORAGE
1105 002550 000000 SAVPC: .WORD 0 ;PROGRAM COUNTER STORAGE
1106 002552 000000 ZERO: .WORD 0
1107 002554 000001 ONE: .WORD 1
1108 002556 041732 MEMLIM: .WORD MEMEND ;HIGHEST LOCATION FOR NPR'S
1109 002560 MEMSZ:
1110 002560 000000 .WORD 0
1111 002562 000001 KMACTV: .BLKW 1 ;M8200,4,6,7 SELECTED ACTIVE
1112 002564 000001 KPNUM: .BLKW 1 ;OCTAL NUMBER OF M8200,4,6,7
1113 002566 000001 SAVACT: .BLKW 1 ;ORIGINAL ACTIVE DEVICES
1114 002570 000001 SAVNUM: .BLKW 1 ;WORKABLE NUMBER
1115 002572 000000 FLAG: .WORD 0 ;SCRATCH STORAGE
1116 002574 000000 RUN: .WORD 0 ;POINTER TO RUNNING DEVICES
1117 002576 000000 MR0: .WORD 0
1118 002600 000006 WTYPE: .WORD 6
1119 002602 000000 TYPE: .WORD 0
1120 002604 000000 PONE: .WORD 0 ; FIRST PASS SWITCH
1121 002606 000000 $GDADR: .WORD 0 ;CONTAINS ADDRESS OF 'GOOD' DATA
1122 002610 000000 $BDADR: .WORD 0 ;CONTAINS ADDRESS OF 'BAD' DATA
1123 002612 000000 $GDDAT: .WORD 0 ;CONTAINS 'GOOD' DATA
1124 002614 000000 $BDDAT: .WORD 0 ;CONTAINS 'BAD' DATA
1125 002616 000000 .WORD 0 ;RESERVED--NOT TO BE USED
```



1126 002620 000000  
1127 002622 000000  
1128 002624 000000  
1129 002626 000000  
1130 002630 000000  
1131 002632 000000  
1132 002634 000000  
1133 002636 000000  
1134 002640 000000

.WORD 0  
FTIME: .WORD 0  
SAVE4: .WORD 0  
SAVE6: .WORD 0  
SCLK: .WORD 0  
SIBS10: .WORD 0  
SIBS11: .WORD 0  
SIBS12: .WORD 0  
SIBS13: .WORD 0

1135  
1136

\*\*\*\*\*  
;\* DATA PATTERNS  
\*\*\*\*\*

1137  
1138

1139 002642 000 377 000  
1140 002645 377 125 252  
1141 002650 125 252  
1142 002652 000 000 377  
1143 002655 377 125 125  
1144 002660 252 252

MEMDAT: .BYTE 0,-1,0,-1,125,252,125,252  
SPDAT: .BYTE 0,0,-1,-1,125,125,252,252

1145

.EVEN

1146  
1147

\*\*\*\*\*  
;\* PROGRAM CONTROL FLAGS  
\*\*\*\*\*

1148  
1149

1150 002662 000  
1151 002664 002664  
1152 002664 000  
1153 002665 000

INIFLG: .BYTE 0 ;PROGRAM INITIALIZING FLAG  
.EVEN  
LOKFLG: .BYTE 0 ;LOCK ON CURRENT TEST FLAG  
QV.FLG: .BYTE 0 ;QUICK VERIFY FLAG  
.EVEN

1154  
1155

\*\*\*\*\*  
;\* DEFINITION OF M8200,4,6,7 STATUS WORDS - STAT1,STAT2,STAT3  
\*\*\*\*\*

1156  
1157

1158  
1159

1160  
1161

STAT1 - BITS 00-08 IS M8200,4,6,7 VECTOR ADDRESS  
BIT15=1 LINE UNIT IS AN M8203  
BIT14=0 NO TEST CONNECTOR(S) USED  
BIT14=1 H-XXX TEST CONNECTOR WILL BE USED  
BIT13=0 LINE UNIT IS AN M8201  
BIT13=1 LINE UNIT IS AN M8202  
BIT12=1 NO LINE UNIT  
BITS 09-11 IS M8200,4,6,7 PRIORITY LEVEL

1162  
1163

1164  
1165

1166  
1167

1168  
1169

STAT2 - LOW BYTE IS SWITCH PACK #1 (DDCMP LINE NUMBER)  
HIGH BYTE IS SWITCH PACK #2 (BM873 BOOT ADDRESS)

1170  
1171

1172  
1173

STAT3 - BIT0=1 DO FREE RUNNING TESTS ON M8200,4,6,7

1174

1175 002666 000000

1176 002670 000000

1177 002672 000000

STAT1: .WORD 0  
STAT2: .WORD 0  
STAT3: .WORD 0

1178  
1179

\*\*\*\*\*  
;\* POINTERS TO M8200,4,6,7 VECTORS AND REGISTERS  
\*\*\*\*\*

1180  
1181

1182 002674 000000  
1183 002676 000000  
1184 002700 000000  
1185 002702 000000  
1186 002704 000000  
1187 002706 000000  
1188 002710 000000  
1189 002712 000000  
1190 002714 000000  
1191  
1192  
1193  
1194 002716  
1195  
1196  
1197 002716 000100  
1198 003116  
1199  
1200  
1201  
1202  
1203  
1204  
1205

KMRVEC: 0 ; POINTER TO M8200.4.6.7 RCV INTRPT VECTOR  
KMRVLVL: 0 ; POINTER TO M8200.4.6.7 RCV INTRPT SERVICE PS  
KMTVEC: 0 ; POINTER TO M8200.4.6.7 TX INTRPT VECTOR  
KMTLVL: 0 ; POINTER TO M8200.4.6.7 TX INTRPT SERVICE PS  
KMCSR: 0 ; POINTER TO M8200.4.6.7 CONTROL STATUS REGISTER  
KMCSRH: 0 ; POINTER TO M8200.4.6.7 CONTROL STATUS REGISTER HIGH BYTE  
KMCTL: 0 ; POINTER TO M8200.4.6.7 CONTROL OUT REGISTER  
KMP04: 0  
KMP06: 0 ; POINTER TO M8200.4.6.7 PORT REGISTER - SEL6

::\*\*\*\* PRIMARY REG ADRS STORAGE FOR THIS UNIT \*\*\*\*\*  
;THESE LOCATIONS WILL BE LOADED FOR THE CURRENT UNIT, IN INIT CODE  
REGADR:

::\*\*\*\* STACK USED FOR SUBROUTINE LINKAGE \*\*\*\*\*  
.BLKW 100  
SSTACK:

1206  
1207  
1208  
1209  
1210  
1211  
1212  
1213  
1214  
1215  
1216  
1217 003116  
1218 003116  
1219 003116 034115 030062 000066  
1220  
1221 003124  
1222 003124  
1223 003124 055103 046513 040502  
1224 003132 020060 046513 030503  
1225 003140 026461 020102 052123  
1226 003146 052101 041511 050040  
1227 003154 051101 030524 000  
1228 003162  
1229  
1230  
1231  
1232  
1233  
1234  
1235  
1236  
1237  
1238  
1239

.SBTTL GLOBAL TEXT SECTION

:XXX  
:X THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,  
:X MESSAGES, AND ASCII INFORMATION THAT ARE USED IN  
:X MORE THAN ONE TEST.  
:XXX

:\*\*\*\*\*  
:\* NAMES OF DEVICES SUPPORTED BY PROGRAM  
:\*\*\*\*\*

DEV TYP <M8206>  
LSDVTYP::  
.ASCIZ /M8206/  
.EVEN  
DESCRIPT <CZKMBAO KMC11-B STATIC PART1>  
L\$DESC::  
.ASCIZ /CZKMBAO KMC11-B STATIC PART1/  
  
.EVEN

:  
: FORMAT STATEMENTS USED IN PRINT CALLS  
:

1240  
1241  
1242  
1243  
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1245  
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1247  
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.SBTTL GLOBAL SUBROUTINES

:/ THE GLOBAL SUBROUTINES ARE CALLED BY MORE THAN ONE TEST

-----  
: MACRO'S NEEDED TO CALL SUBROUTINES  
-----

.MACRO ERROR,XYX  
MOV R4,\$BDDAT  
MOV R2,\$GDADR  
MOV R0,\$BDADR  
ERRDF XYX',EM'XYX',ERR'XYX'

.ENDM  
.MACRO ED\$CALL XY  
.LIST  
:\*\*\*\*\* TEST 'XY' \*\*\*\*\*  
.NLIST  
.ENDM  
.MACRO BADHEAD  
.RADIX 10  
ED\$CALL \T\$TESTNUM+1  
.RADIX 8  
.ENDM  
.MACRO K4ONLY ?N2  
CMP MEMSZ,#2000  
BNE N2  
EXIT TST

N2:

.ENDM  
.MACRO MYINT  
JSR R5,DEBUG ;WILL PUT CSR ADDR. INTO R1  
.ENDM

.MACRO ROMCLK  
JSR R5,.ROMCLK ;CLOCK INSTRUCTION  
.ENDM

.MACRO MSTCLR  
JSR R5,.MSTCLR ;CLEAR M8200,4,6,7  
.ENDM

003162  
003162 105077 177520  
003166 112777 000100 177512  
003174 142777 000300 177504  
003202 000205

.MSTCLR:  
CLRB @KMCSRH ;\*: CLEAR RUN, IF UP  
MOVB #BIT6,@KMCSRH ;SET INST.  
BICB #BIT6!BIT7,@KMCSRH  
R5

:  
:DEBUG THIS ROUTINE IS ENTERED AT THE BEGINING OF EACH TEST  
:  
: IN ORDER TO LOAD THE CSR ADDR. INTO R1..  
:  
: ALSO THIS PROGRAM TRACKS ITSELF HERE.

```

1296          :          CALL=JSR          R5,DEBUG
1297          :
1298 003204     DEBUG:
1299 003204     010537 003224     MOV      R5,TESTAD      ;SAVE ADDR. OF TEST.
1300 003210     013701 002704     MOV      KMCSR,R1      ;LOAD KMCSR INTP R1.
1301 003214     000240     NOP
1302 003216     000240     NOP
1303 003220     000240     NOP
1304 003222     000205     RTS R5
1305 003224     000000     TESTAD: .WORD 0          ;LAST TEST ADDR.
1306 003226     000024     .BLKW 20.            ;PATCH AREA.
1307
1308
1309
1310 003276     ENDBG:
1311          :          UNSAFE TO PATCH ANY OTHER AREA.
1312
1313
1314
1315 003276     .ROMCLK:
1316 003276     152777 000002 177402  BISB    #BIT1,@KMCSRH
1317 003304     012577 177404     MOV     (R5)+,@KMP06
1318 003310     152777 000003 177370  BISB    #BIT1!BIT0,@KMCSRH
1319 003316     142777 000007 177362  BICB    #BIT2!BIT1!BIT0,@KMCSRH
1320 003324     000205     RTS     R5
1321
1322 003326     CLRALL:
1323          :          ;CLEARS C & Z BITS AND BR
1324 003326     ROMCLK          ;0 TO BR
1325 003332     000400          400
1326 003334     ROMCLK          ;SP(0) TO BR
1327 003340     063220          63220
1328 003342     ROMCLK          ;BR,SP(0) + BR
1329 003346     060400          60400
1330 003350     000207          RTS     PC
1331
1332 003352     SETBR0:
1333          :          ;SETS BR0 BIT
1334 003352     ROMCLK          ;1 TO BR
1335 003356     000401          401
1336 003360     000207          RTS     PC
1337
1338 003362     SETBR1:
1339          :          ;THIS SUBROUTINE SETS BR1 BIT
1340
1341 003362     ROMCLK          ;NEXT WORD IS INSTRUCTION
1342 003366     000402          000402     ;BR_002
1343 003370     000207          RTS     PC
1344
1345 003372     SETBR4:
1346          :          ;THIS SUBROUTINE SETS BR4 BIT
1347
1348 003372     ROMCLK          ;NEXT WORD IS INSTRUCTION
1349 003376     000420          420
1350 003400     000207          RTS     PC
1351

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1352 003402          SETBR7:
1353                ;THIS SUBROUTINE SETS BR7 BIT
1354                ROMCLK          ;NEXT WORD IS INSTRUCTION
1355 003402          600
1356 003406 000600   RTS          PC
1357 003410 000207
1358
1359
1360 003412          SETZ:
1361                ;THIS SUBROUTINE SETS THE Z BIT
1362                ROMCLK          ;NEXT WORD IS INSTRUCTION
1363 003412          000777          ;BR_377
1364 003416 000777   RTS          PC
1365 003420 000207
1366
1367 003422          RAMDAT:
1368                ;THIS SUBROUTINE LOADS R4 WITH THE LOWEST
1369                ;8 BITS OF THE CRAM PC.
1370
1371 003422 017605 000000  MOV      @ (SP), R5      ;GOOD DATA
1372 003426 062716 000002  ADD      #2, (SP)      ;ADJUST STACK
1373 003432 005011          CLR      (R1)          ;CLEAR BIT10
1374 003434 052711 000400  BIS      #BIT8, (R1)   ;CLOCK INSTRUCTION IN CRAM THAT
1375                ;JUMPED TO, IT LOADS BR WITH IT
1376 003440 005011          CLR      (R1)          ;CLR BIT8
1377 003442          ROMCLK          ;NEXT WORD IS INSTRUCTION
1378 003446 061225          061225          ;MOV BR TO PORT 5
1379 003450 116104 000005  MOVB    5(R1), R4      ;PUT 'FOUND' IN R4
1380 003454 000207          RTS          PC          ;RETURN
1381
1382
1383 003456          MEMSET:
1384                ;THIS SUBROUTINE LOADS CRAM WITH SPECIAL INSTRUCTIONS
1385                ;FOR THE CRAM JUMP TEST. ALL CRAM LOCATIONS ARE LOADED
1386                ;WITH INSTRUCTIONS THAT MOVE A 37 TO THE BR, EXCEPT THE
1387                ;FOLLOWING CRAM ADDRESSES: 0,1,4,7,525,1777. THESE LOCATIONS
1388                ;CONTAIN INSTRUCTIONS WHICH LOAD THE BR WITH THE LOWEST
1389                ;8 BITS OF THAT CRAM ADDRESS.
1390
1391 003456 005000          CLR      R0          ;R0 = CRAM ADDRESS
1392 003460 012711 002000  1$:  MOV      #BIT10, (R1)  ;SET ROMO
1393 003464 010061 000004  MOV      R0, 4(R1)    ;LOAD CRAM ADDRESS
1394 003470 012761 000437 000006  MOV      #437, 6(R1)  ;LOAD INSTRUCTION
1395 003476 052711 020000  BIS      #BIT13, (R1) ;WRITE INSTRUCTION IN CRAM
1396 003502 005200          INC      R0          ;NEXT ADDRESS
1397 003504 022700 002000  CMP      #2000, R0    ;DONE YET?
1398 003510 001363          BNE     1$          ;BR IF NO
1399 003512 005000          CLR      R0          ;INDEX REGISTER
1400 003514 012711 002000  2$:  MOV      #BIT10, (R1)  ;SET ROMO
1401 003520 016061 003554 000004  MOV      CRAM(R0), 4(R1) ;LOAD CRAM ADDRESS IN SEL4
1402 003526 016061 003570 000006  MOV      INSTU(R0), 6(R1) ;LOAD INSTRUCTION TO BE WRITTEN
1403 003534 052711 020000  BIS      #BIT13, (R1) ;WRITE CRAM!
1404 003540 005720          TST      (R0)+        ;NEXT
1405 003542 022700 000014  CMP      #14, R0     ;DONE YET?
1406 003546 001362          BNE     2$          ;BR IF NO
1407 003550 005011          CLR      (R1)        ;CLEAR ALL BITS

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1408 003552 000207          RTS      PC          ;RETURN
1409
1410 003554 000000 000001 000004 CRAMA: .WORD 0,1,4,7,1777,525
1411 003562 000007 001777 000525
1412
1413 003570 000400          INSTU: 000400          ;BR_0
1414 003572 000401          ;BR_1
1415 003574 000404          ;BR_4
1416 003576 000407          ;BR_7
1417 003600 000777          ;BR_377
1418 003602 000525          ;BR_125
1419
1420 003604          SETVEC:
1421          ;THIS SUBROUTINE LOADS THE VECTORS AND VECTOR LEVELS
1422
1423 003604 012577 177064          MOV      (R5)+,@KMRVEC ;LOAD BASE VECTOR
1424 003610 012577 177064          MOV      (R5)+,@KMTVEC ;LOAD VECTOR + 2
1425 003614 012577 177056          MOV      (R5)+,@KMRLVL ;LOAD VECTOR + 4
1426 003620 012577 177056          MOV      (R5)+,@KMTLVL ;LOAD VECTOR + 6
1427 003624 000205          RTS      R5          ;RETURN
1428
1429
1430 003626          NPRSET:
1431          ;THIS SUBROUTINE LOADS IBUS REGISTERS 0-7
1432          ;WITH NPR INFORMATION (INBA, OUTBA, OUT DATA)
1433
1434 003626 010246          MOV      R2,-(SP)      ;SAVE R2
1435 003630 005002          CLR      R2          ;START AT IBUS REG 0
1436 003632 112561 000004          1$: MOVB    (R5)+,4(R1)   ;LOAD PORT4
1437 003636 042737 000017 003654          BIC      #17,2$      ;CLEAR ADDRESS FIELD OF INSTRUCTION
1438 003644 050237 003654          BIS      R2,2$      ;ADD ADDRESS TO INSTRUCTION
1439 003650
1440 003654 122100          2$: ROMCLK 122100      ;MOVE PORT4 TO IBUS REG
1441 003656 005202          INC      R2          ;NEXT ADDRESS
1442 003660 022702 000010          CMP      #10,R2      ;ALL DONE?
1443 003664 001362          BNE      1$          ;BR IF NO
1444 003666 012602          MOV      (SP)+,R2    ;RESTORE R2
1445 003670 000205          RTS      R5          ;RETURN
1446
1447
1448 003672          MEMLD:
1449          ;THIS SUBROUTINE LOADS THE FIRST 8 LOCATIONS OF MAIN
1450          ;MEMORY WITH THIS DATA: 0,-1,,0,-1,125,252,125,252
1451
1452 003672 013637 002522          MOV      @(SP)+,$TMP0 ;PUT POINTER TO DATA IN R0
1453 003676 062746 000002          ADD      #2,-(SP)    ;ADJUST STACK
1454
1455 003702 013700 002522          MEMLD2: MOV      $TMP0,R0    ;GET ADDR.
1456 003706 012704 000010          MOV      #10,R4      ;DO 8 LOADS
1457 003712
1458 003716 010000          ROMCLK 010000      ;MAR < 0
1459 003720          ROMCLK          ;CLR MAR HI
1460 003724 004000
1461 003726 112077 176760          1$: MOVB    (R0)+,@KMP04 ;LOAD PORT4
1462 003732          ROMCLK
1463 003736 136500          ;MOV DATA TO MEM, AUTO INC MAR
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1464 003740 005304      DEC R4      ;DECREMENT COUNT
1465 003742 001371      BNE 1$      ;BR IF NOT DONE
1466
1467 003744              ROMCLK      ;LOAD MEM ADDR. 0
1468 003750 010000      10000
1469 003752 012703 000010  MOV #10,R3  ;CHECK 8. MEM LOCS.
1470 003756 013700 002522  MOV $TMP0,R0
1471 003762              2$: ROMCLK      ;READ FROM MEM,PUT INTO PORT 4
1472 003766 055224      55224
1473
1474 003770 112037 002612  MOVB (R0)+,$GDDAT ;EXPECTED.
1475 003774 117704 1767'2  MOVB @KMP04,R4  ;RECIEVED.
1476 004000 123704 002612  CMPB $GDDAT,R4  ;OK?
1477 004004 001414      BEQ 3$
1478 004006      ERROR 36
1479 004024 104455      TRAP C$ERDF
1480 004026 000044      .WORD 36
1481 004030 006107      .WORD EM36
1482 004032 012346      .WORD ERR36
1483 004034 000402      BR 4$
1484 004036 005303      3$: DEC R3      ;CHECKED ALL?
1485 004040 001350      BNE 2$      ;NO-DO NEXT ONE.
1486 004042              4$:
1487 004042 000207      RTS PC      ;RETURN
1488
1489
1490 004044              SPLD:
1491              ;THIS SUBROUTINE LOADS THE FIRST 8 SCRATCH PAD
1492              ;LOCATIONS WITH: 0,0,-1,-1,125,125,252,252
1493
1494 004044 013600      MOV @($P)+,R0  ;PUT POINTER TO DATA IN R5
1495 004046 062746 000002  ADD #2,-($P)  ;ADJUST STACK
1496 004052 005004      CLR R4        ;START AT SP ADDRESS 0
1497 004054 112077 176632  MOVB (R0)+,@KMP04 ;LOAD PORT4 WITH DATA
1498 004060 042737 000017 004076  BIC #17,2$   ;CLEAR ADDRESS FIELD OF INSTRUCTION
1499 004066 050437 004076  BIS R4,2$    ;ADD ADDRESS TO INSTRUCTION
1500 004072
1501 004076 123100      2$: ROMCLK      ;MOVE DATA TO SP
1502 004100 005204      INC R4        ;INCREMENT COUNT
1503 004102 022704 000010  CMP #10,R4    ;DONE YET?
1504 004106 001362      BNE 1$      ;BR IF NO
1505 004110 000207      RTS PC      ;RETURN
1506
1507
1508 004112              CLRC:
1509              ;THIS SUBROUTINE CLEARS THE MICRO PROCESSOR C BIT
1510
1511 004112              ROMCLK
1512 004116 010000      010000      ;MAR_0
1513 004120              ROMCLK
1514 004124 040400      040400!<0*20> ;CLEAR C BIT
1515 004126 000207      RTS PC      ;RETURN
1516
1517
1518 004130              SETC:
1519              ;THIS SUBROUTINE SETS THE MICRO PROCESSOR C BIT

```



```
1520
1521 004130
1522 004134 010003 ROMCLK 010003 :MAR_3
1523 004136 ROMCLK
1524 004142 040403 040403!<0*20> :SET C BIT
1525 004144 000207 RTS PC :RETURN
1526
1527
1528
1529 004146 012704 000050 PRDEL: MOV #40.,R4
1530 004152 1$: DELAY 250.
1531 004152 012727 000372 MOV #250.,(PC)+
1532 004156 000000 .WORD 0
1533 004160 013727 002116 MOV L$DLY,(PC)+
1534 004164 000000 .WORD 0
1535 004166 005367 177772 DEC -6(PC)
1536 004172 001375 BNE -4
1537 004174 005367 177756 DEC -22(PC)
1538 004200 001367 BNE -20
1539 004202 005304 DEC R4
1540 004204 001362 BNE 1$ :DELAY 1 SECOND
1541 004206 000207 RTS PC
1542
```

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.SBTTL GLOBAL ERROR REPORT SECTION

:/   
:/ THE GLOBAL ERROR REPORT SECTION CONTAINS ERROR MESSAGES  
:/ THAT ARE USED IN MORE THAN ONE TEST.  
:/

004210	047045	052045	047045	FM1:	.ASCIZ	/%N%T%N/
004216	000					
004217	045	022516	052501	FMX:	.ASCIZ	/%N%AUNIT = %02%A ; FAILING UNIT ADDRESS = %06%N/
004224	044516	020124	020075			
004232	047445	022462	020101			
004240	020073	040506	046111			
004246	047111	020107	047125			
004254	052111	040440	042104			
004262	042522	051523	036440			
004270	022440	033117	047045			
004276	000					
004277	045	031517	051445	TFM1:	.ASCIZ	/%03%S5%03%S5%03%S5%03%N2/
004304	022465	031517	051445			
004312	022465	031517	051445			
004320	022465	031517	047045			
004326	000062					
004330	047445	022466	031123	TFM2:	.ASCIZ	/%06%S2%06%N2/
004336	047445	022466	031116			
004344	000					
004345	045	031517	051445	TFM5:	.ASCIZ	/%03%S5%03%N2/
004352	022465	031517	047045			
004360	000062					
004362	047045	047445	022463	TFM27:	.ASCIZ	/%N%03%S5%03%S7%03%N2/
004370	032523	047445	022463			
004376	033523	047445	022463			
004404	031116	000				
004407	045	030516	040445	FMSG:	.ASCIZ	/%N1%APROGRAM CLOCK = 75 MICROSECONDS%N1/
004414	051120	043517	040522			
004422	020115	046103	041517			
004430	020113	020075	032467			
004436	046440	041511	047522			
004444	042523	047503	042116			
004452	022523	030516	000			
004457	045	030516	040445	SMSG:	.ASCIZ	/%N1%APROGRAM CLOCK = 115 MILLISECONDS%N1/
004464	051120	043517	040522			
004472	020115	046103	041517			
004500	020113	020075	030461			
004506	020065	044515	046114			
004514	051511	041505	047117			
004522	051504	047045	000061			
004530	042522	044507	052123	EM1:	.ASCIZ	%REGISTER ADDRESS TEST%
004536	051105	040440	042104			
004544	042522	051523	052040			

1599	004552	051505	000124		
1600	004556	041111	051525	020052	EM2: .ASCIZ &IBUS* REGISTER DUAL ADDRESSING TEST&
1601	004564	042522	044507	052123	
1602	004572	051105	042040	040525	
1603	004600	020114	042101	051104	
1604	004606	051505	044523	043516	
1605	004614	052040	051505	000124	
1606	004622	041111	051525	051040	EM30: .ASCIZ 'IBUS REGISTER DUAL ADDRESSING TEST''
1607	004630	043505	051511	042524	
1608	004636	020122	052504	046101	
1609	004644	040440	042104	042522	
1610	004652	051523	047111	020107	
1611	004660	042524	052123	000	
1612	004665	102	020122	042522	EM3: .ASCIZ /BR REGISTER DATA TEST/
1613	004672	044507	052123	051105	
1614	004700	042040	052101	020101	
1615	004706	042524	052123	000	
1616	004713	123	051103	052101	EM4: .ASCIZ /SCRATCH PAD DATA TEST/
1617	004720	044103	050040	042101	
1618	004726	042040	052101	020101	
1619	004734	042524	052123	000	
1620	004741	123	051103	052101	EM5: .ASCIZ /SCRATCH PAD DUAL ADDRESSING TEST/
1621	004746	044103	050040	042101	
1622	004754	042040	040525	020114	
1623	004762	042101	051104	051505	
1624	004770	044523	043516	052040	
1625	004776	051505	000124		
1626	005002	040515	047111	046440	EM6: .ASCIZ /MAIN MEMORY DATA TEST/
1627	005010	046505	051117	020131	
1628	005016	040504	040524	052040	
1629	005024	051505	000124		
1630	005030	040515	047111	046440	EM7: .ASCIZ /MAIN MEMORY DUAL ADDRESSING TEST/
1631	005036	046505	051117	020131	
1632	005044	052504	046101	040440	
1633	005052	042104	042522	051523	
1634	005060	047111	020107	042524	
1635	005066	052123	000		
1636	005071	101	052125	020117	EM10: .ASCIZ /AUTO MARINC FUNCTION TEST/
1637	005076	040515	044522	041516	
1638	005104	043040	047125	052103	
1639	005112	047511	020116	042524	
1640	005120	052123	000		
1641	005123	116	051120	052040	EM11: .ASCIZ /NPR TEST/
1642	005130	051505	000124		
1643	005134	052515	052114	050111	EM12: .ASCIZ /MULTIPLE NPR TEST/
1644	005142	042514	047040	051120	
1645	005150	052040	051505	000124	
1646	005156	047516	020116	054105	EM13: .ASCIZ /NON EX MEM FAILED/
1647	005164	046440	046505	043040	
1648	005172	044501	042514	000104	
1649	005200	051120	043517	040522	EM14: .ASCIZ /PROGRAM CLOCK TEST/
1650	005206	020115	046103	041517	
1651	005214	020113	042524	052123	
1652	005222	000			
1653	005223	101	052514	043040	EM15: .ASCIZ /ALU FUNCTION WITH C BIT CLEAR TEST/
1654	005230	047125	052103	047511	

1655	005236	020116	044527	044124	
1656	005244	041440	041040	052111	
1657	005252	041440	042514	051101	
1658	005260	052040	051505	000124	
1659	005266	047520	042527	020122	EM16: .ASCIZ /POWER FAIL: BUS INIT WAS NOT BLOCKED/
1660	005274	040506	046111	020072	
1661	005302	052502	020123	047111	
1662	005310	052111	053440	051501	
1663	005316	047040	052117	041040	
1664	005324	047514	045503	042105	
1665	005332	000			
1666	005333				EM35:
1667	005333	106	051117	042503	EM17: .ASCIZ /FORCE POWER FAIL ERROR/
1668	005340	050040	053517	051105	
1669	005346	043040	044501	020114	
1670	005354	051105	047522	000122	
1671	005362	047516	051511	020105	EM20: .ASCIZ /NOISE TEST ON IBUS*,IBUS,SPAD,MEMORY/
1672	005370	042524	052123	047440	
1673	005376	020116	041111	051525	
1674	005404	026052	041111	051525	
1675	005412	051454	040520	026104	
1676	005420	042515	047515	054522	
1677	005426	000			
1678	005427	101	052514	041440	EM21: .ASCIZ /ALU C BIT TEST FAILURE/
1679	005434	041040	052111	052040	
1680	005442	051505	020124	040506	
1681	005450	046111	051125	000105	
1682	005456	044524	042515	047440	EM22: .ASCIZ /TIME OUT ERROR/
1683	005464	052125	042440	051122	
1684	005472	051117	000		
1685	005475	101	052514	043040	EM23: .ASCIZ /ALU FUNCTION TEST WITH C BIT SET/
1686	005502	047125	052103	047511	
1687	005510	020116	042524	052123	
1688	005516	053440	052111	020110	
1689	005524	020103	044502	020124	
1690	005532	042523	000124		
1691	005536	050125	020103	042523	EM24: .ASCIZ /UPC SEQUENCE ERROR/
1692	005544	052521	047105	042503	
1693	005552	042440	051122	051117	
1694	005560	000			
1695	005561	125	020120	040506	EM31: .ASCIZ 'UP FAILED TO INTERRUPT'
1696	005566	046111	042105	052040	
1697	005574	020117	047111	042524	
1698	005602	051122	050125	000124	
1699	005610	050125	044440	052116	EM32: .ASCIZ 'UP INTERRUPTED TO WRONG VECTOR'
1700	005616	051105	052522	052120	
1701	005624	042105	052040	020117	
1702	005632	051127	047117	020107	
1703	005640	042526	052103	051117	
1704	005646	000			
1705	005647	125	042516	050130	EM33: .ASCIZ 'UNEXPECTED INTERRUPT FROM UP'
1706	005654	041505	042524	020104	
1707	005662	047111	042524	051122	
1708	005670	050125	020124	051106	
1709	005676	046517	052440	000120	
1710	005704	046101	020125	046106	EM34: .ASCIZ 'ALU FLAG TEST'

1711	005712	043501	052040	051505		
1712	005720	000124				
1713	005722	042510	046114	051040	EM25:	.ASCIZ /HELL RAISER TEST/
1714	005730	044501	042523	020122		
1715	005736	042524	052123	000		
1716	005743	115	044501	052116	EM26:	.ASCIZ /MAINTANCE REGISTER ERROR/
1717	005750	047101	042503	051040		
1718	005756	043505	051511	042524		
1719	005764	020122	051105	047522		
1720	005772	000122				
1721	005774	041111	051525	020052	EM27:	.ASCIZ 'IBUS* WRITE/READ ERROR'
1722	006002	051127	052111	027505		
1723	006010	042522	042101	042440		
1724	006016	051122	051117	000		
1725	006023	111	051516	051124	EM28:	.ASCIZ /INSTRUCTION TEST FAILURE/
1726	006030	041525	044524	047117		
1727	006036	052040	051505	020124		
1728	006044	040506	046111	051125		
1729	006052	000105				
1730	006054	041111	051525	047457	EM29:	.ASCIZ 'IBUS/OBUS WRITE/READ ERROR'
1731	006062	052502	020123	051127		
1732	006070	052111	027505	042522		
1733	006076	042101	042440	051122		
1734	006104	051117	000			
1735	006107	111	050117	046440	EM36:	.ASCIZ 'IOP MAIN MEM. LOAD ERROR-RUN MCPU MEM. DIAG.'
1736	006114	044501	020116	042515		
1737	006122	027115	046040	040517		
1738	006130	020104	051105	047522		
1739	006136	026522	052522	020116		
1740	006144	041515	052520	046440		
1741	006152	046505	020056	044504		
1742	006160	043501	000056			
1743	006164	042523	030114	024040	EM99:	.ASCIZ /SELO (CSR) DID NOT CLEAR/
1744	006172	051503	024522	042040		
1745	006200	042111	047040	052117		
1746	006206	041440	042514	051101		
1747	006214	000				

1748	006215	000				DH0:	.ASCIZ	//				
1749	006216	047507	042117	020040		DH1:	.ASCIZ	/GOOD	BAD	UPC	REGISTER/	
1750	006224	020040	040502	020104								
1751	006232	020040	020040	050125								
1752	006240	020103	020040	020040								
1753	006246	042522	044507	052123								
1754	006254	051105	000									
1755	006257	107	047517	020104		DH2:	.ASCIZ	/GOOD	BAD/			
1756	006264	020040	041040	042101								
1757	006272	000										
1758	006273	107	047517	020104		DH3:	.ASCIZ	/GOOD	BAD	UPC	ADDRESS/	
1759	006300	020040	041040	042101								
1760	006306	020040	020040	052440								
1761	006314	041520	020040	020040								
1762	006322	040440	042104	042522								
1763	006330	051523	000									
1764	006333	107	047517	020104		DH4:	.ASCIZ	/GOOD	BAD	FUNCTION/		
1765	006340	020040	041040	042101								
1766	006346	020040	020040	043040								
1767	006354	047125	052103	047511								
1768	006362	000116										
1769	006364	042522	027107	042440		DH27:	.ASCIZ	/REG. EXPECTED FOUND/				
1770	006372	050130	041505	042524								
1771	006400	020104	047506	047125								
1772	006406	000104										
1773												
1774												
1775												
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1777												

.EVEN

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```
-----  
: MACRO'S NEEDED TO REPORT ERRORS  
-----  
.MACRO MDT0  
.ENDM  
  
.MACRO MDT1  
PRINTB #TFM1,$GDDAT,$BDDAT,$GDADR  
.ENDM  
  
.MACRO MDT2  
PRINTB #TFM2,$GDDAT,$BDDAT  
.ENDM  
  
.MACRO MDT27  
PRINTB #TFM27,MRO,$GDDAT,$BDDAT  
.ENDM  
  
.MACRO $MD,ERNB,ERHM,ERFM  
.NLIST  
: ERNB = ERROR NUMBER  
: ERFM = FORMAT NUMBER  
: ERHM = HEADER NUMBER  
.LIST  
BGNMSG ERR'ERNB'  
PRINTB #FMX,LOGDEV,KMCSR  
PRINTB #FM1,#DH'ERHM'  
MDT'ERFM'  
ENDMSG  
  
.ENDM
```

1807 006410  
1808 006410  
1809 006410 013746 002704  
1810 006414 013746 002524  
1811 006420 012746 004217  
1812 006424 012746 000003  
1813 006430 010600  
1814 006432 104414  
1815 006434 062706 000010  
1816 006440 012746 006257  
1817 006444 012746 004210  
1818 006450 012746 000002  
1819 006454 010600  
1820 006456 104414  
1821 006460 062706 000006  
1822 006464 013746 002614  
1823 006470 013746 002612  
1824 006474 012746 004330  
1825 006500 012746 000003  
1826 006504 010600  
1827 006506 104414  
1828 006510 062706 000010  
1829 006514  
1830 006514 104423  
1831 006516  
1832 006516  
1833 006516 013746 002704  
1834 006522 013746 002524  
1835 006526 012746 004217  
1836 006532 012746 000003  
1837 006536 010600  
1838 006540 104414  
1839 006542 062706 000010  
1840 006546 012746 006257  
1841 006552 012746 004210  
1842 006556 012746 000002  
1843 006562 010600  
1844 006564 104414  
1845 006566 062706 000006  
1846 006572 013746 002614  
1847 006576 013746 002612  
1848 006602 012746 004330  
1849 006606 012746 000003  
1850 006612 010600  
1851 006614 104414  
1852 006616 062706 000010  
1853 006622  
1854 006622 104423  
1855 006624  
1856 006624  
1857 006624 013746 002704  
1858 006630 013746 002524  
1859 006634 012746 004217  
1860 006640 012746 000003  
1861 006644 010600  
1862 006646 104414

ERR1:: SMD 1,2,2  
MOV KMCSR,-(SP)  
MOV LOGDEV,-(SP)  
MOV #FMX,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
MOV #DH2,-(SP)  
MOV #FM1,-(SP)  
MOV #2,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #6,SP  
MOV \$BDDAT,-(SP)  
MOV \$GDDAT,-(SP)  
MOV #TFM2,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
L10002: TRAP C\$MSG  
SMD 2,2,2  
ERR2:: MOV KMCSR,-(SP)  
MOV LOGDEV,-(SP)  
MOV #FMX,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
MOV #DH2,-(SP)  
MOV #FM1,-(SP)  
MOV #2,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #6,SP  
MOV \$BDDAT,-(SP)  
MOV \$GDDAT,-(SP)  
MOV #TFM2,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
L10003: TRAP C\$MSG  
SMD 3,2,2  
ERR3:: MOV KMCSR,-(SP)  
MOV LOGDEV,-(SP)  
MOV #FMX,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB



1863 006650 062706 000010  
1864 006654 012746 006257  
1865 006660 012746 004210  
1866 006664 012746 000002  
1867 006670 010600  
1868 006672 104414  
1869 006674 062706 000006  
1870 006700 013746 002614  
1871 006704 013746 002612  
1872 006710 012746 004330  
1873 006714 012746 000003  
1874 006720 010600  
1875 006722 104414  
1876 006724 062706 000010  
1877 006730  
1878 006730 104423  
1879 006732  
1880 006732  
1881 006732 013746 002704  
1882 006736 013746 002524  
1883 006742 012746 004217  
1884 006746 012746 000003  
1885 006752 010600  
1886 006754 104414  
1887 006756 062706 000010  
1888 006762 012746 006216  
1889 006766 012746 004210  
1890 006772 012746 000002  
1891 006776 010600  
1892 007000 104414  
1893 007002 062706 000006  
1894 007006 013746 002606  
1895 007012 013746 002614  
1896 007016 013746 002612  
1897 007022 012746 004277  
1898 007026 012746 000004  
1899 007032 010600  
1900 007034 104414  
1901 007036 062706 000012  
1902 007042  
1903 007042 104423  
1904 007044  
1905 007044  
1906 007044 013746 002704  
1907 007050 013746 002524  
1908 007054 012746 004217  
1909 007060 012746 000003  
1910 007064 010600  
1911 007066 104414  
1912 007070 062706 000010  
1913 007074 012746 006216  
1914 007100 012746 004210  
1915 007104 012746 000002  
1916 007110 010600  
1917 007112 104414  
1918 007114 062706 000006

ADD #10,SP  
MOV #DH2,-(SP)  
MOV #FM1,-(SP)  
MOV #2,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #6,SP  
MOV \$BDDAT,-(SP)  
MOV \$GDDAT,-(SP)  
MOV #TFM2,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
L10004: TRAP C\$MSG  
SMD 4,1,1  
ERR4:: MOV KMCSR,-(SP)  
MOV LOGDEV,-(SP)  
MOV #FMX,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
MOV #DH1,-(SP)  
MOV #FM1,-(SP)  
MOV #2,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #12,SP  
L10005: TRAP C\$MSG  
SMD 5,1,1  
ERR5:: MOV KMCSR,-(SP)  
MOV LOGDEV,-(SP)  
MOV #FMX,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
MOV #DH1,-(SP)  
MOV #FM1,-(SP)  
MOV #2,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #6,SP

1919	007120	013746	002606	MOV	\$GDADR,-(SP)
1920	007124	013746	002614	MOV	\$BDDAT,-(SP)
1921	007130	013746	002612	MOV	\$GDDAT,-(SP)
1922	007134	012746	004277	MOV	#TFM1,-(SP)
1923	007140	012746	000004	MOV	#4,-(SP)
1924	007144	010600		MOV	SP,R0
1925	007146	104414		TRAP	C\$PNTB
1926	007150	062706	000012	ADD	#12,SP
1927	007154				
1928	007154	104423		L10006: TRAP	C\$MSG
1929	007156			SMD	6,3,1
1930	007156			ERR6::	
1931	007156	013746	002704	MOV	KMCSR,-(SP)
1932	007162	013746	002524	MOV	LOGDEV,-(SP)
1933	007166	012746	004217	MOV	#FMX,-(SP)
1934	007172	012746	000003	MOV	#3,-(SP)
1935	007176	010600		MOV	SP,R0
1936	007200	104414		TRAP	C\$PNTB
1937	007202	062706	000010	ADD	#10,SP
1938	007206	012746	006273	MOV	#DH3,-(SP)
1939	007212	012746	004210	MOV	#FM1,-(SP)
1940	007216	012746	000002	MOV	#2,-(SP)
1941	007222	010600		MOV	SP,R0
1942	007224	104414		TRAP	C\$PNTB
1943	007226	062706	000006	ADD	#6,SP
1944	007232	013746	002606	MOV	\$GDADR,-(SP)
1945	007236	013746	002614	MOV	\$BDDAT,-(SP)
1946	007242	013746	002612	MOV	\$GDDAT,-(SP)
1947	007246	012746	004277	MOV	#TFM1,-(SP)
1948	007252	012746	000004	MOV	#4,-(SP)
1949	007256	010600		MOV	SP,R0
1950	007260	104414		TRAP	C\$PNTB
1951	007262	062706	000012	ADD	#12,SP
1952	007266			L10007:	
1953	007266	104423		TRAP	C\$MSG
1954	007270			SMD	7,3,1
1955	007270			ERR7::	
1956	007270	013746	002704	MOV	KMCSR,-(SP)
1957	007274	013746	002524	MOV	LOGDEV,-(SP)
1958	007300	012746	004217	MOV	#FMX,-(SP)
1959	007304	012746	000003	MOV	#3,-(SP)
1960	007310	010600		MOV	SP,R0
1961	007312	104414		TRAP	C\$PNTB
1962	007314	062706	003010	ADD	#10,SP
1963	007320	012746	006273	MOV	#DH3,-(SP)
1964	007324	012746	004210	MOV	#FM1,-(SP)
1965	007330	012746	000002	MOV	#2,-(SP)
1966	007334	010600		MOV	SP,R0
1967	007336	104414		TRAP	C\$PNTB
1968	007340	062706	000006	ADD	#6,SP
1969	007344	013746	002606	MOV	\$GDADR,-(SP)
1970	007350	013746	002614	MOV	\$BDDAT,-(SP)
1971	007354	013746	002612	MOV	\$GDDAT,-(SP)
1972	007360	012746	004277	MOV	#TFM1,-(SP)
1973	007364	012746	000004	MOV	#4,-(SP)
1974	007370	010600		MOV	SP,R0

Year	Code	Address	Value	Operation	Comment
1975	007372	104414		TRAP	C\$PNTB
1976	007374	062706	000012	ADD	#12,SP
1977	007400			L10010:	
1978	007400	104423		TRAP	C\$MSG
1979	007402			\$MD	10,3,1
1980	007402			ERR10::	
1981	007402	013746	002704	MOV	KMCSR,-(SP)
1982	007406	013746	002524	MOV	LOGDEV,-(SP)
1983	007412	012746	004217	MOV	#FMX,-(SP)
1984	007416	012746	000003	MOV	#3,-(SP)
1985	007422	010600		MOV	SP,RO
1986	007424	104414		TRAP	C\$PNTB
1987	007426	062706	000010	ADD	#10,SP
1988	007432	012746	006273	MOV	#DH3,-(SP)
1989	007436	012746	004210	MOV	#FM1,-(SP)
1990	007442	012746	000002	MOV	#2,-(SP)
1991	007446	010600		MOV	SP,RO
1992	007450	104414		TRAP	C\$PNTB
1993	007452	062706	000006	ADD	#6,SP
1994	007456	013746	002606	MOV	\$GDADR,-(SP)
1995	007462	013746	002614	MOV	\$BDDAT,-(SP)
1996	007466	013746	002612	MOV	\$GDDAT,-(SP)
1997	007472	012746	004277	MOV	#TFM1,-(SP)
1998	007476	012746	000004	MOV	#4,-(SP)
1999	007502	010600		MOV	SP,RO
2000	007504	104414		TRAP	C\$PNTB
2001	007506	062706	000012	ADD	#12,SP
2002	007512			L10011:	
2003	007512	104423		TRAP	C\$MSG
2004	007514			\$MD	11,2,2
2005	007514			ERR11::	
2006	007514	013746	002704	MOV	KMCSR,-(SP)
2007	007520	013746	002524	MOV	LOGDEV,-(SP)
2008	007524	012746	004217	MOV	#FMX,-(SP)
2009	007530	012746	000003	MOV	#3,-(SP)
2010	007534	010600		MOV	SP,RO
2011	007536	104414		TRAP	C\$PNTB
2012	007540	062706	000010	ADD	#10,SP
2013	007544	012746	006257	MOV	#DH2,-(SP)
2014	007550	012746	004210	MOV	#FM1,-(SP)
2015	007554	012746	000002	MOV	#2,-(SP)
2016	007560	010600		MOV	SP,RO
2017	007562	104414		TRAP	C\$PNTB
2018	007564	062706	000006	ADD	#6,SP
2019	007570	013746	002614	MOV	\$BDDAT,-(SP)
2020	007574	013746	002612	MOV	\$GDDAT,-(SP)
2021	007600	012746	004330	MOV	#TFM2,-(SP)
2022	007604	012746	000003	MOV	#3,-(SP)
2023	007610	010600		MOV	SP,RO
2024	007612	104414		TRAP	C\$PNTB
2025	007614	062706	000010	ADD	#10,SP
2026	007620			L10012:	
2027	007620	104423		TRAP	C\$MSG
2028	007622			\$MD	12,2,2
2029	007622			ERR12::	
2030	007622	013746	002704	MOV	KMCSR,-(SP)

Address	Offset	Hex	Hex	Instruction	Comment
2031	007626	013746	002524	MOV	LOGDEV,-(SP)
2032	007632	012746	004217	MOV	#FMX,-(SP)
2033	007636	012746	000003	MOV	#3,-(SP)
2034	007642	010600		MOV	SP,R0
2035	007644	104414		TRAP	CSPNTB
2036	007646	062706	000010	ADD	#10,SP
2037	007652	012746	006257	MOV	#DH2,-(SP)
2038	007656	012746	004210	MOV	#FM1,-(SP)
2039	007662	012746	000002	MOV	#2,-(SP)
2040	007666	010600		MOV	SP,R0
2041	007670	104414		TRAP	CSPNTB
2042	007672	062706	000006	ADD	#6,SP
2043	007676	013746	002614	MOV	\$BDDAT,-(SP)
2044	007702	013746	002612	MOV	\$GDDAT,-(SP)
2045	007706	012746	004330	MOV	#TFM2,-(SP)
2046	007712	012746	000003	MOV	#3,-(SP)
2047	007716	010600		MOV	SP,R0
2048	007720	104414		TRAP	CSPNTB
2049	007722	062706	000010	ADD	#10,SP
2050	007726			L10013:	
2051	007726	104423		TRAP	C\$MSG
2052	007730			SMD	13,0,0
2053	007730			ERR13::	
2054	007730	013746	002704	MOV	KMCSR,-(SP)
2055	007734	013746	002524	MOV	LOGDEV,-(SP)
2056	007740	012746	004217	MOV	#FMX,-(SP)
2057	007744	012746	000003	MOV	#3,-(SP)
2058	007750	010600		MOV	SP,R0
2059	007752	104414		TRAP	CSPNTB
2060	007754	062706	000010	ADD	#10,SP
2061	007760	012746	006215	MOV	#DH0,-(SP)
2062	007764	012746	004210	MOV	#FM1,-(SP)
2063	007770	012746	000002	MOV	#2,-(SP)
2064	007774	010600		MOV	SP,R0
2065	007776	104414		TRAP	CSPNTB
2066	010000	062706	000006	ADD	#6,SP
2067	010004			L10014:	
2068	010004	104423		TRAP	C\$MSG
2069	010006			SMD	14,2,2
2070	010006			ERR14::	
2071	010006	013746	002704	MOV	KMCSR,-(SP)
2072	010012	013746	002524	MOV	LOGDEV,-(SP)
2073	010016	012746	004217	MOV	#FMX,-(SP)
2074	010022	012746	000003	MOV	#3,-(SP)
2075	010026	010600		MOV	SP,R0
2076	010030	104414		TRAP	CSPNTB
2077	010032	062706	000010	ADD	#10,SP
2078	010036	012746	006257	MOV	#DH2,-(SP)
2079	010042	012746	004210	MOV	#FM1,-(SP)
2080	010046	012746	000002	MOV	#2,-(SP)
2081	010052	010600		MOV	SP,R0
2082	010054	104414		TRAP	CSPNTB
2083	010056	062706	000006	ADD	#6,SP
2084	010062	013746	002614	MOV	\$BDDAT,-(SP)
2085	010066	013746	002612	MOV	\$GDDAT,-(SP)
2086	010072	012746	004330	MOV	#TFM2,-(SP)

2087	010076	012746	000003	MOV	#3,-(SP)
2088	010102	010600		MOV	SP,R0
2089	010104	104414		TRAP	C\$PNTB
2090	010106	062706	000010	ADD	#10,SP
2091	010112			L10015:	
2092	010112	104423		TRAP	C\$MSG
2093	010114			SMD	15.4.27
2094	010114			ERR15::	
2095	010114	C13746	002704	MOV	KMCSR,-(SP)
2096	010120	013746	002524	MOV	LOGDEV,-(SP)
2097	010124	012746	004217	MOV	#FMX,-(SP)
2098	010130	012746	G00003	MOV	#3,-(SP)
2099	010134	010600		MOV	SP,R0
2100	010136	104414		TRAP	C\$PNTB
2101	010140	062706	000010	ADD	#10,SP
2102	010144	012746	006333	MOV	#DH4,-(SP)
2103	010150	012746	004210	MOV	#FM1,-(SP)
2104	010154	012746	000002	MOV	#2,-(SP)
2105	010160	010600		MOV	SP,R0
2106	010162	104414		TRAP	C\$PNTB
2107	010164	062706	000006	ADD	#6,SP
2108	010170	013746	002614	MOV	\$BDDAT,-(SP)
2109	010174	013746	002612	MOV	\$GDDAT,-(SP)
2110	010200	013746	002576	MOV	MRO,-(SP)
2111	010204	012746	004362	MOV	#TFM27,-(SP)
2112	010210	012746	000004	MOV	#4,-(SP)
2113	010214	010600		MOV	SP,R0
2114	010216	104414		TRAP	C\$PNTB
2115	010220	062706	000012	ADD	#12,SP
2116	010224			L10016:	
2117	010224	104423		TRAP	C\$MSG
2118	010226			SMD	16.0.0
2119	010226			ERR16::	
2120	010226	013746	002704	MOV	KMCSR,-(SP)
2121	010232	013746	002524	MOV	LOGDEV,-(SP)
2122	010236	012746	004217	MOV	#FMX,-(SP)
2123	010242	012746	000003	MOV	#3,-(SP)
2124	010246	010600		MOV	SP,R0
2125	010250	104414		TRAP	C\$PNTB
2126	010252	062706	000010	ADD	#10,SP
2127	010256	012746	006215	MOV	#DH0,-(SP)
2128	010262	012746	004210	MOV	#FM1,-(SP)
2129	010266	012746	000002	MOV	#2,-(SP)
2130	010272	010600		MOV	SP,R0
2131	010274	104414		TRAP	C\$PNTB
2132	010276	062706	000006	ADD	#6,SP
2133	010302			L10017:	
2134	010302	104423		TRAP	C\$MSG
2135	010304			SMD	17.0.0
2136	010304			ERR17::	
2137	010304	013746	002704	MOV	KMCSR,-(SP)
2138	010310	013746	002524	MOV	LOGDEV,-(SP)
2139	010314	012746	004217	MOV	#FMX,-(SP)
2140	010320	012746	000003	MOV	#3,-(SP)
2141	010324	010600		MOV	SP,R0
2142	010326	104414		TRAP	C\$PNTB

2143	010330	062706	000010	ADD	#10,SP
2144	010334	012746	006215	MOV	#DH0,-(SP)
2145	010340	012746	004210	MOV	#FM1,-(SP)
2146	010344	012746	000002	MOV	#2,-(SP)
2147	010350	010600		MOV	SP,R0
2148	010352	104414		TRAP	CSPNTB
2149	010354	062706	000006	ADD	#6,SP
2150	010360			L10020:	
2151	010360	104423		TRAP	C\$MSG
2152	010362			SMD	20,2,2
2153	010362			ERR20::	
2154	010362	013746	002704	MOV	KMCSR,-(SP)
2155	010366	013746	002524	MOV	LOGDEV,-(SP)
2156	010372	012746	004217	MOV	#FMX,-(SP)
2157	010376	012746	000003	MOV	#3,-(SP)
2158	010402	010600		MOV	SP,R0
2159	010404	104414		TRAP	CSPNTB
2160	010406	062706	000010	ADD	#10,SP
2161	010412	012746	006257	MOV	#DH2,-(SP)
2162	010416	012746	004210	MOV	#FM1,-(SP)
2163	010422	012746	000002	MOV	#2,-(SP)
2164	010426	010600		MOV	SP,R0
2165	010430	104414		TRAP	CSPNTB
2166	010432	062706	000006	ADD	#6,SP
2167	010436	013746	002614	MOV	\$BDDAT,-(SP)
2168	010442	013746	002612	MOV	\$GDDAT,-(SP)
2169	010446	012746	004330	MOV	#TFM2,-(SP)
2170	010452	012746	000003	MOV	#3,-(SP)
2171	010456	010600		MOV	SP,R0
2172	010460	104414		TRAP	CSPNTB
2173	010462	062706	000010	ADD	#10,SP
2174	010466			L10021:	
2175	010466	104423		TRAP	C\$MSG
2176	010470			SMD	21,0,0
2177	010470			ERR21::	
2178	010470	013746	002704	MOV	KMCSR,-(SP)
2179	010474	013746	002524	MOV	LOGDEV,-(SP)
2180	010500	012746	004217	MOV	#FMX,-(SP)
2181	010504	012746	000003	MOV	#3,-(SP)
2182	010510	010600		MOV	SP,R0
2183	010512	104414		TRAP	CSPNTB
2184	010514	062706	000010	ADD	#10,SP
2185	010520	012746	006215	MOV	#DH0,-(SP)
2186	010524	012746	004210	MOV	#FM1,-(SP)
2187	010530	012746	000002	MOV	#2,-(SP)
2188	010534	010600		MOV	SP,R0
2189	010536	104414		TRAP	CSPNTB
2190	010540	062706	000006	ADD	#6,SP
2191	010544			L10022:	
2192	010544	104423		TRAP	C\$MSG
2193	010546			SMD	22,0,0
2194	010546			ERR22::	
2195	010546	013746	002704	MOV	KMCSR,-(SP)
2196	010552	013746	002524	MOV	LOGDEV,-(SP)
2197	010556	012746	004217	MOV	#FMX,-(SP)
2198	010562	012746	000003	MOV	#3,-(SP)

2199	010566	010600		MOV	SP,R0
2200	010570	104414		TRAP	C\$PNTB
2201	010572	062706	000010	ADD	#10,SP
2202	010576	012746	006215	MOV	#DH0,-(SP)
2203	010602	012746	004210	MOV	#FM1,-(SP)
2204	010606	012746	000002	MOV	#2,-(SP)
2205	010612	010600		MOV	SP,R0
2206	010614	104414		TRAP	C\$PNTB
2207	010616	062706	000006	ADD	#6,SP
2208	010622				
2209	010622	104423		L10023: TRAP	C\$MSG
2210	010624				\$MD 23,4,1
2211	010624			ERR23::	
2212	010624	013746	002704	MOV	KMCSR,-(SP)
2213	010630	013746	002524	MOV	LOGDEV,-(SP)
2214	010634	012746	004217	MOV	#FMX,-(SP)
2215	010640	012746	000003	MOV	#3,-(SP)
2216	010644	010600		MOV	SP,R0
2217	010646	104414		TRAP	C\$PNTB
2218	010650	062706	000010	ADD	#10,SP
2219	010654	012746	006333	MOV	#DH4,-(SP)
2220	010660	012746	004210	MOV	#FM1,-(SP)
2221	010664	012746	000002	MOV	#2,-(SP)
2222	010670	010600		MOV	SP,R0
2223	010672	104414		TRAP	C\$PNTB
2224	010674	062706	000006	ADD	#6,SP
2225	010700	013746	002606	MOV	\$GDADR,-(SP)
2226	010704	013746	002614	MOV	\$BDDAT,-(SP)
2227	010710	013746	002612	MOV	\$GDDAT,-(SP)
2228	010714	012746	004277	MOV	#TFM1,-(SP)
2229	010720	012746	000004	MOV	#4,-(SP)
2230	010724	010600		MOV	SP,R0
2231	010726	104414		TRAP	C\$PNTB
2232	010730	062706	000012	ADD	#12,SP
2233	010734			L10024:	
2234	010734	104423			TRAP C\$MSG
2235	010736				\$MD 24,0,0
2236	010736			ERR24::	
2237	010736	013746	002704	MOV	KMCSR,-(SP)
2238	010742	013746	002524	MOV	LOGDEV,-(SP)
2239	010746	012746	004217	MOV	#FMX,-(SP)
2240	010752	012746	000003	MOV	#3,-(SP)
2241	010756	010600		MOV	SP,R0
2242	010760	104414		TRAP	C\$PNTB
2243	010762	062706	000010	ADD	#10,SP
2244	010766	012746	006215	MOV	#DH0,-(SP)
2245	010772	012746	004210	MOV	#FM1,-(SP)
2246	010776	012746	000002	MOV	#2,-(SP)
2247	011002	010600		MOV	SP,R0
2248	011004	104414		TRAP	C\$PNTB
2249	011006	062706	000006	ADD	#6,SP
2250	011012			L10025:	
2251	011012	104423			TRAP C\$MSG
2252	011014				\$MD 25,2,2
2253	011014			ERR25::	
2254	011014	013746	002704	MOV	KMCSR,-(SP)

2255	011020	013746	002524	MOV	LOGDEV,-(SP)
2256	011024	012746	004217	MOV	#FMX,-(SP)
2257	011030	012746	000003	MOV	#3,-(SP)
2258	011034	010600		MOV	SP,R0
2259	011036	104414		TRAP	C\$PNTB
2260	011040	062706	000010	ADD	#10,SP
2261	011044	012746	006257	MOV	#DH2,-(SP)
2262	011050	012746	004210	MOV	#FM1,-(SP)
2263	011054	012746	000002	MOV	#2,-(SP)
2264	011060	010600		MOV	SP,R0
2265	011062	104414		TRAP	C\$PNTB
2266	011064	062706	000006	ADD	#6,SP
2267	011070	013746	002614	MOV	\$BDDAT,-(SP)
2268	011074	013746	002612	MOV	\$GDDAT,-(SP)
2269	011100	012746	004330	MOV	#TFM2,-(SP)
2270	011104	012746	000003	MOV	#3,-(SP)
2271	011110	010600		MOV	SP,R0
2272	011112	104414		TRAP	C\$PNTB
2273	011114	062706	000010	ADD	#10,SP
2274	011120				
2275	011120	104423		L10026: TRAP	C\$MSG
2276	011122			\$MD	26,2,2
2277	011122			ERR26::	
2278	011122	013746	002704	MOV	KMCSR,-(SP)
2279	011126	013746	002524	MOV	LOGDEV,-(SP)
2280	011132	012746	004217	MOV	#FMX,-(SP)
2281	011136	012746	000003	MOV	#3,-(SP)
2282	011142	010600		MOV	SP,R0
2283	011144	104414		TRAP	C\$PNTB
2284	011146	062706	000010	ADD	#10,SP
2285	011152	012746	006257	MOV	#DH2,-(SP)
2286	011156	012746	004210	MOV	#FM1,-(SP)
2287	011162	012746	000002	MOV	#2,-(SP)
2288	011166	010600		MOV	SP,R0
2289	011170	104414		TRAP	C\$PNTB
2290	011172	062706	000006	ADD	#6,SP
2291	011176	013746	002614	MOV	\$BDDAT,-(SP)
2292	011202	013746	002612	MOV	\$GDDAT,-(SP)
2293	011206	012746	004330	MOV	#TFM2,-(SP)
2294	011212	012746	000003	MOV	#3,-(SP)
2295	011216	010600		MOV	SP,R0
2296	011220	104414		TRAP	C\$PNTB
2297	011222	062706	000010	ADD	#10,SP
2298	011226			L10027: TRAP	C\$MSG
2299	011226	104423		\$MD	27,27,27
2300	011230			ERR27::	
2301	011230				
2302	011230	013746	002704	MOV	KMCSR,-(SP)
2303	011234	013746	002524	MOV	LOGDEV,-(SP)
2304	011240	012746	004217	MOV	#FMX,-(SP)
2305	011244	012746	000003	MOV	#3,-(SP)
2306	011250	010600		MOV	SP,R0
2307	011252	104414		TRAP	C\$PNTB
2308	011254	062706	000010	ADD	#10,SP
2309	011260	012746	006364	MOV	#DH27,-(SP)
2310	011264	012746	004210	MOV	#FM1,-(SP)



Address	PC	Instruction	Comments
2311	011270	012746	000002
2312	011274	010600	
2313	011276	104414	
2314	011300	062706	000006
2315	011304	013746	002614
2316	011310	013746	002612
2317	011314	013746	002576
2318	011320	012746	004362
2319	011324	012746	000004
2320	011330	010600	
2321	011332	104414	
2322	011334	062706	000012
2323	011340		
2324	011340	104423	
2325	011342		
2326	011342		
2327	011342	013746	002704
2328	011346	013746	002524
2329	011352	012746	004217
2330	011356	012746	000003
2331	011362	010600	
2332	011364	104414	
2333	011366	062706	000010
2334	011372	012746	006257
2335	011376	012746	004210
2336	011402	012746	000002
2337	011406	010600	
2338	011410	104414	
2339	011412	062706	000006
2340	011416	013746	002614
2341	011422	013746	002612
2342	011426	012746	004330
2343	011432	012746	000003
2344	011436	010600	
2345	011440	104414	
2346	011442	062706	000010
2347	011446		
2348	011446	104423	
2349	011450		
2350	011450		
2351	011450	013746	002704
2352	011454	013746	002524
2353	011460	012746	004217
2354	011464	012746	000003
2355	011470	010600	
2356	011472	104414	
2357	011474	062706	000010
2358	011500	012746	006364
2359	011504	012746	004210
2360	011510	012746	000002
2361	011514	010600	
2362	011516	104414	
2363	011520	062706	000006
2364	011524	013746	002614
2365	011530	013746	002612
2366	011534	013746	002576

  

Label	PC	Instruction	Comments
L10030:		TRAP	C\$MSG
		\$MD	28,2,2
ERR28::		MOV	KMCSR,-(SP)
		MOV	LOGDEV,-(SP)
		MOV	#FMX,-(SP)
		MOV	#3,-(SP)
		MOV	SP,R0
		TRAP	C\$PNTB
		ADD	#10,SP
		MOV	#DH2,-(SP)
		MOV	#FM1,-(SP)
		MOV	#2,-(SP)
		MOV	SP,R0
		TRAP	C\$PNTB
		ADD	#6,SP
		MOV	\$BDDAT,-(SP)
		MOV	\$GDDAT,-(SP)
		MOV	#TFM2,-(SP)
		MOV	#3,-(SP)
		MOV	SP,R0
		TRAP	C\$PNTB
		ADD	#10,SP
L10031:		TRAP	C\$MSG
		\$MD	29,27,27
ERR29::		MOV	KMCSR,-(SP)
		MOV	LOGDEV,-(SP)
		MOV	#FMX,-(SP)
		MOV	#3,-(SP)
		MOV	SP,R0
		TRAP	C\$PNTB
		ADD	#10,SP
		MOV	#DH27,-(SP)
		MOV	#FM1,-(SP)
		MOV	#2,-(SP)
		MOV	SP,R0
		TRAP	C\$PNTB
		ADD	#6,SP
		MOV	\$BDDAT,-(SP)
		MOV	\$GDDAT,-(SP)
		MOV	MRO,-(SP)

2367	011540	012746	004362	MOV	#TFM27,-(SP)
2368	011544	012746	000004	MOV	#4,-(SP)
2369	011550	010600		MOV	SP,R0
2370	011552	104414		TRAP	C\$PNTB
2371	011554	062706	000012	ADD	#12,SP
2372	011560			L10032:	
2373	011560	104423		TRAP	C\$MSG
2374	011562			SMD	30,2,2
2375	011562			ERR30::	
2376	011562	013746	002704	MOV	KMCSR,-(SP)
2377	011566	013746	002524	MOV	LOGDEV,-(SP)
2378	011572	012746	004217	MOV	#FMX,-(SP)
2379	011576	012746	000003	MOV	#3,-(SP)
2380	011602	010600		MOV	SP,R0
2381	011604	104414		TRAP	C\$PNTB
2382	011606	062706	000010	ADD	#10,SP
2383	011612	012746	006257	MOV	#DH2,-(SP)
2384	011616	012746	004210	MOV	#FM1,-(SP)
2385	011622	012746	000002	MOV	#2,-(SP)
2386	011626	010600		MOV	SP,R0
2387	011630	104414		TRAP	C\$PNTB
2388	011632	062706	000006	ADD	#6,SP
2389	011636	013746	002614	MOV	\$BDDAT,-(SP)
2390	011642	013746	002612	MOV	\$GDDAT,-(SP)
2391	011646	012746	004330	MOV	#TFM2,-(SP)
2392	011652	012746	000003	MOV	#3,-(SP)
2393	011656	010600		MOV	SP,R0
2394	011660	104414		TRAP	C\$PNTB
2395	011662	062706	000010	ADD	#10,SP
2396	011666			L10033:	
2397	011666	104423		TRAP	C\$MSG
2398	011670			SMD	31,0,0
2399	011670			ERR31::	
2400	011670	013746	002704	MOV	KMCSR,-(SP)
2401	011674	013746	002524	MOV	LOGDEV,-(SP)
2402	011700	012746	004217	MOV	#FMX,-(SP)
2403	011704	012746	000003	MOV	#3,-(SP)
2404	011710	010600		MOV	SP,R0
2405	011712	104414		TRAP	C\$PNTB
2406	011714	062706	000010	ADD	#10,SP
2407	011720	012746	006215	MOV	#DH0,-(SP)
2408	011724	012746	004210	MOV	#FM1,-(SP)
2409	011730	012746	000002	MOV	#2,-(SP)
2410	011734	010600		MOV	SP,R0
2411	011736	104414		TRAP	C\$PNTB
2412	011740	062706	000006	ADD	#6,SP
2413	011744			L10034:	
2414	011744	104423		TRAP	C\$MSG
2415	011746			SMD	32,0,0
2416	011746			ERR32::	
2417	011746	013746	002704	MOV	KMCSR,-(SP)
2418	011752	013746	002524	MOV	LOGDEV,-(SP)
2419	011756	012746	004217	MOV	#FMX,-(SP)
2420	011762	012746	000003	MOV	#3,-(SP)
2421	011766	010600		MOV	SP,R0
2422	011770	104414		TRAP	C\$PNTB

Address	Offset	Instruction	Comment
2423	011772	062706	000010
2424	011776	012746	006215
2425	012002	012746	004210
2426	012006	012746	000002
2427	012012	010600	
2428	012014	104414	
2429	012016	062706	000006
2430	012022		
2431	012022	104423	
2432	012024		
2433	012024		
2434	012024	013746	002704
2435	012030	013746	002524
2436	012034	012746	004217
2437	012040	012746	000003
2438	012044	010600	
2439	012046	104414	
2440	012050	062706	000010
2441	012054	012746	006257
2442	012060	012746	004210
2443	012064	012746	000002
2444	012070	010600	
2445	012072	104414	
2446	012074	062706	000006
2447	012100	013746	002614
2448	012104	013746	002612
2449	012110	012746	004330
2450	012114	012746	000003
2451	012120	010600	
2452	012122	104414	
2453	012124	062706	000010
2454	012130		
2455	012130	104423	
2456	012132		
2457	012132		
2458	012132	013746	002704
2459	012136	013746	002524
2460	012142	012746	004217
2461	012146	012746	000003
2462	012152	010600	
2463	012154	104414	
2464	012156	062706	000010
2465	012162	012746	006257
2466	012166	012746	004210
2467	012172	012746	000002
2468	012176	010600	
2469	012200	104414	
2470	012202	062706	000006
2471	012206	013746	002614
2472	012212	013746	002612
2473	012216	012746	004330
2474	012222	012746	000003
2475	012226	010600	
2476	012230	104414	
2477	012232	062706	000010
2478	012236		

L10035:

ERR33::

L10036:

ERR34::

L10037:

ADD #10,SP  
MOV #DH0,-(SP)  
MOV #FM1,-(SP)  
MOV #2,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #6,SP  
TRAP C\$MSG  
\$MD 33.2.2  
MOV KMCSR,-(SP)  
MOV LOGDEV,-(SP)  
MOV #FMX,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
MOV #DH2,-(SP)  
MOV #FM1,-(SP)  
MOV #2,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #6,SP  
MOV \$BDDAT,-(SP)  
MOV \$GDDAT,-(SP)  
MOV #TFM2,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
TRAP C\$MSG  
\$MD 34.2.2  
MOV KMCSR,-(SP)  
MOV LOGDEV,-(SP)  
MOV #FMX,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
MOV #DH2,-(SP)  
MOV #FM1,-(SP)  
MOV #2,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #6,SP  
MOV \$BDDAT,-(SP)  
MOV \$GDDAT,-(SP)  
MOV #TFM2,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP

2479 012236 104423  
2480 012240  
2481 012240  
2482 012240 013746 002704  
2483 012244 013746 002524  
2484 012250 012746 004217  
2485 012254 012746 000003  
2486 012260 010600  
2487 012262 104414  
2488 012264 062706 000010  
2489 012270 012746 006257  
2490 012274 012746 004210  
2491 012300 012746 000002  
2492 012304 010600  
2493 012306 104414  
2494 012310 062706 000006  
2495 012314 013746 002614  
2496 012320 013746 002612  
2497 012324 012746 004330  
2498 012330 012746 000003  
2499 012334 010600  
2500 012336 104414  
2501 012340 062706 000010  
2502 012344  
2503 012344 104423  
2504 012346  
2505 012346  
2506 012346 013746 002704  
2507 012352 013746 002524  
2508 012356 012746 004217  
2509 012362 012746 000003  
2510 012366 010600  
2511 012370 104414  
2512 012372 062706 000010  
2513 012376 012746 006257  
2514 012402 012746 004210  
2515 012406 012746 000002  
2516 012412 010600  
2517 012414 104414  
2518 012416 062706 000006  
2519 012422 013746 002614  
2520 012426 013746 002612  
2521 012432 012746 004330  
2522 012436 012746 000003  
2523 012442 010600  
2524 012444 104414  
2525 012446 062706 000010  
2526 012452  
2527 012452 104423

TRAP C\$MSG  
\$MD 35.2.2  
ERR35::  
MOV KMCSR,-(SP)  
MOV LOGDEV,-(SP)  
MOV #FMX,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
MOV #DH2,-(SP)  
MOV #FM1,-(SP)  
MOV #2,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #6,SP  
MOV \$BDDAT,-(SP)  
MOV \$GDDAT,-(SP)  
MOV #TFM2,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
L10040:  
TRAP C\$MSG  
\$MD 36.2.2  
ERR36::  
MOV KMCSR,-(SP)  
MOV LOGDEV,-(SP)  
MOV #FMX,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
MOV #DH2,-(SP)  
MOV #FM1,-(SP)  
MOV #2,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #6,SP  
MOV \$BDDAT,-(SP)  
MOV \$GDDAT,-(SP)  
MOV #TFM2,-(SP)  
MOV #3,-(SP)  
MOV SP,R0  
TRAP C\$PNTB  
ADD #10,SP  
L10041:  
TRAP C\$MSG

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.SBTTL REPORT CODING SECTION

;++  
: THE REPORT CODING SECTION CONTAINS THE  
: "PRINTS" CALLS THAT GENERATE STATISTICAL REPORTS.  
:--

L\$RPT:: BGNRPT

EXIT RPT  
.WORD JSJMP  
.WORD L10042-2-

L10042: ENDRPT  
TRAP C\$RPT

L\$PROT:: BGNPROT  
-1  
-1  
-1  
ENDPROT

L\$AUTO:: BGAUTO  
ENDAUTO  
L10044: TRAP C\$AUTO

012454  
012454  
012454 000167  
012456 000000  
012460  
012460 104425  
012462  
012462 177777  
012464 177777  
012466 177777  
012470  
012470  
012470  
012470 104461

2564  
2565  
2566  
2567  
2568  
2569  
2570  
2571 012472  
2572 012472  
2573  
2574  
2575 012472 012705 003116  
2576  
2577 012476 010637 002526  
2578 012502 005737 002622  
2579 012506 001013  
2580 012510 013737 000004 002624  
2581 012516 013737 000006 002626  
2582 012524 005037 002604  
2583  
2584 012530 012737 000001 002622  
2585 012536 013737 002624 000004  
2586 012544 013737 002626 000006  
2587  
2588 012552  
2589 012552 012700 000040  
2590 012556 104447  
2591 012560  
2592 012560 103003  
2593 012562 005037 002604  
2594 012566 000414  
2595 012570  
2596  
2597 012570  
2598 012570 012700 000035  
2599 012574 104447  
2600 012576  
2601 012576 103410  
2602  
2603 012600  
2604 012600 012700 000036  
2605 012604 104447  
2606 012606  
2607 012606 103566  
2608  
2609  
2610 012610  
2611 012610 012700 000037  
2612 012614 104447  
2613 012616  
2614 012616 103003  
2615  
2616 012620  
2617  
2618 012620 012737 177777 002524  
2619

```
.SBTTL INITIALIZE SECTION  
://////!  
:// THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED  
:// AT THE BEGINNING OF EACH PASS.  
://////!  
  
      BGNINIT  
L$INIT::  
  
:INITIALIZE SUBROUTINE STACK  
      MOV      #SSTACK,R5  
:STORE BASE LEVEL PROGRAM STACK POINTER  
      MOV      SP,PSTACK  
      TST      FTIME  
      BNE      1$  
      MOV      @#4,SAVE4  
      MOV      @#6,SAVE6  
      CLR      PONE                ; INIT FLAG  
  
1$:   MOV      #1,FTIME  
      MOV      SAVE4,@#4  
      MOV      SAVE6,@#6  
:SEE IF PROGRAM JUST STARTED, BR IF YES  
      REDEF    #EF.START  
      MOV      #EF.START,R0  
      TRAP    C$REFG  
      BNCOMPLETE 33$  
      BCC     33$  
      CLR     PONE                ; RESET SWITCH  
      BR      NEWST  
  
33$:  
:SEE IF THIS IS A NEW PASS, BR IF YES  
      REDEF    #EF.NEW  
      MOV      #EF.NEW,R0  
      TRAP    C$REFG  
      BCOMPLETE NEWST  
      BCS     NEWST  
:SEE IF PROGRAM WAS JUST CONTINUED  
      REDEF    #EF.CONTINUE  
      MOV      #EF.CONTINUE,R0  
      TRAP    C$REFG  
      BCOMPLETE ENDIT  
      BCS     ENDIT  
  
:SEE IF PROGRAM JUST RESTARTED, BR IF NOT  
      REDEF    #EF.RESTART  
      MOV      #EF.RESTART,R0  
      TRAP    C$REFG  
      BNCOMPLETE GETPRM  
      BCC     GETPRM  
  
NEWST:  
:RESET LOGICAL DEVICE TO -1  
      MOV      #-1,LOGDEV
```

2620  
2621  
2622  
2623 012626  
2624 012626 005237 002524  
2625 012632  
2626 012632 013700 002524  
2627 012636 104442  
2628 012640 010001  
2629 012642  
2630 012642 103371  
2631  
2632 012644 012737 000006 002600  
2633 012652 005721  
2634 012654 011137 002704  
2635  
2636 012660 011137 002706  
2637 012664 005237 002706  
2638  
2639 012670 011137 002710  
2640 012674 062737 000002 002710  
2641  
2642 012702 011137 002712  
2643 012706 062737 000004 002712  
2644  
2645 012714 012137 002714  
2646 012720 062737 000006 002714  
2647  
2648 012726 011137 002674  
2649  
2650 012732 011137 002676  
2651 012736 062737 000002 002676  
2652  
2653 012744 011137 002700  
2654 012750 062737 000004 002700  
2655  
2656 012756 011137 002702  
2657 012762 062737 000006 002702  
2658  
2659 012770 012137 002666  
2660  
2661 012774 052137 002666  
2662  
2663 013000 005711  
2664 013002 001004  
2665 013004 052737 010000 002666  
2666 013012 000416  
2667 013014  
2668  
2669 013014 021127 000001  
2670 013020 001001  
2671 013022 000412  
2672 013024  
2673  
2674 013024 021127 000002  
2675 013030 001004

;GET UNIBUS ADRS, VECTOR, PRIORITY LEVEL, LINE UNIT, SWITCH  
; PACKS, TEST CONNECTOR INFO. FOR THIS M8200.4,6,7 (CURRENT LOGICAL  
; DEVICE).  
GETPRM:  
INC LOGDEV  
GPHARD LOGDEV,R1  
MOV LOGDEV,R0  
TRAP C\$GPHRD  
MOV R0,R1  
BNCOMPLETE GETPRM  
BCC GETPRM  
;GET ADDRESS OF M8200.4,6,7  
MOV #6,WTYPÉ  
TST (R1)+  
MOV (R1),KMCSR  
;GET POINTER TO M8200.4,6,7 CSR HI BYTE  
MOV (R1),KMCSRH  
INC KMCSRH  
;GET POINTER TO M8200.4,6,7 CTL OUT REG  
MOV (R1),KMCTL  
ADD #2,KMCTL  
;GET POINTER TO M8200.4,6,7 PORT REG - SEL 4  
MOV (R1),KMPO4  
ADD #4,KMPO4  
;GET POINTER TO M8200.4,6,7 PORT REG - SEL 6  
MOV (R1)+,KMPO6  
ADD #6,KMPO6  
;GET POINTER TO RCV VECTOR  
MOV (R1),KMRVEC  
;GET POINTER TO RCV PRIORITY LEVEL  
MOV (R1),KMRLVL  
ADD #2,KMRLVL  
;GET POINTER TO TX VECTOR  
MOV (R1),KMTVEC  
ADD #4,KMTVEC  
;GET POINTER TO TX PRIORITY LEVEL  
MOV (R1),KMTLVL  
ADD #6,KMTLVL  
;PUT VECTOR INTO STAT1  
MOV (R1)+,STAT1  
;PUT PRIORITY INTO STAT1  
BIS (R1)+,STAT1  
;SEE IF NO LINE UNIT, SET BIT IF YES  
TST (R1)  
BNE 50000\$  
BIS #BIT12,STAT1  
BR 4\$  
50000\$:  
;SEE IF M8201 LINE UNIT, SET BIT IF YES  
CMP (R1),#1  
BNE 50001\$  
BR 4\$  
50001\$:  
;SEE IF M8202 LINE UNIT, SET BIT IF YES  
CMP (R1),#2  
BNE 50002\$





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013166  
013166  
  
013166  
013166  
013166 104412

```
.SBTTL CLEANUP CODING SECTION
://////
:/ THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS PERFORMED
:/ AT THE END OF EACH PASS.
://////

          BGNCLN
L$CLEAN::

          ENDCLN
L10046: TRAP C$CLEAN
```

2737  
2738  
2739  
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2741  
2742  
2743  
2744 013170  
2745 013170  
2746  
2747 013170  
2748 013170 104433  
2749 013172  
2750 013172  
2751 013172 104453  
2752  
2753  
2754  
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2756

.SBTTL DROP UNIT SECTION

:/ THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE  
:/ TO NO LONGER BE TESTED.

BGNDU

L\$DU::  
:ISSUE UNIBUS RESET TO CLEAN UP  
BRESET  
TRAP C\$RESET  
ENDDU

L10047:  
TRAP C\$DU

2757  
2758  
2759  
2760  
2761  
2762  
2763  
2764  
2765 013174  
2766 013174  
2767 013174  
2768 013174  
2769 013174 104452  
2770  
2771  
2772  
2773  
2774  
2775

.SBTTL ADD UNIT SECTION  
:////////////////////  
:// THE ADD-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE  
:// TO BE (A) TESTED FOR THE FIRST TIME, OR (B) RESUMED IN TESTING. IF  
:// 'EF.AUNIT' IS SET, THE UNIT WILL BE TESTED AS A NEW UNIT.  
:////////////////////  
LSAU:: BGNAU  
L10050: ENDAU  
TRAP CSAU

.SBTTL HARDWARE TESTS

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2800  
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2802  
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013176  
  
013176  
  
013176  
013176  
013176  
013202  
013206  
013214  
013222  
013224  
013226  
013226  
013230  
013232  
013236  
013240  
013242  
013244  
013250  
013266  
013270  
013272  
013274  
  
013276  
013304  
013312  
013312  
013314  
013316  
013316  
013316  
104401

013701 002704  
012705 000004  
012737 013244 000004  
012737 000240 000006  
005711  
000240  
104410  
000066  
062701 000002  
005305  
001370  
000415  
062706 000004  
  
104455  
000001  
004530  
006410  
  
013737 002624 000004  
013737 002626 000006  
  
104410  
C00002  
  
104401

BADHEAD  
:\*\*\*\*\* TEST 1 \*\*\*\*\*  
:\*VERIFY THAT REFERENCING UNIBUS DEVICE REGISTERS  
:\*DOES NOT CAUSE A TIME OUT TRAP  
BADHEAD

BGNTST  
T1::

1\$:

2\$:

3\$:

ENDTST  
L10051:

MOV KCSR,R1 ;R1 CONTAINS BASE M8200,4,6,7 ADDRESS  
MOV #4,R5 ;4 REGISTERS TO BE TESTED  
MOV #2\$,4 ;SET UP TIMEOUT TRAP  
MOV #240,6 ;LEVEL 5  
TST (R1) ;REFERENCE DEVICE REGISTER  
NOP  
ESCAPE TST  
TRAP C\$ESCAPE  
.WORD L10051-.  
ADD #2,R1 ;NEXT REGISTER  
DEC R5 ;DEC REGISTER COUNT  
BNE 1\$ ;BR IF NOT LAST REGISTER  
BR 3\$  
ADD #4,SP  
ERROR 1 ;TIME-OUT ERROR  
TRAP C\$ERDF  
.WORD 1  
.WORD EM1  
.WORD ERR1  
MOV SAVE4,4  
MOV SAVE6,6  
ESCAPE TST  
TRAP C\$ESCAPE  
.WORD L10051-.  
TRAP C\$ETST

```
2818 013320          BADHEAD
2819                :***** TEST 2 *****
2820                :*VERIFY THAT RUN CAN BE CLEARED
2821 013320          BADHEAD
2822                :***** TEST 2 *****
2823
2824 013320          BGNTST
2825 013320          T2::
2826 013320
2827 013324 005011   MYINT
2328 013326 005037 002612 CLR      (R1)          :CLEAR KMCSR
2829 013332 011104 CLR      $GDDAT       :CLEAR 'EXPECTED'
2830 013334 001413 MOV      (R1),R4      :PUT KMCSR IN 'FOUND'
2831 013336          BEQ      1$          :BR IF CLEARED
2832 013354 104455 ERROR  26          :ERROR KMCSR NOT CLEARED
2833 013356 000032 TRAP    C$ERDF
2834 013360 005743 .WORD   26
2835 013362 011122 .WORD   EM26
2836 013364          .WORD   ERR26
2837 013364          1$:
2838 013364          ENDTST
2839 013364 104401 L10052: TRAP    C$ETST
```

2840 013366  
2841  
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2855  
2856  
2857 013366  
2858  
2859  
2860 013366  
2861 013366  
2862 013366  
2863  
2864 013372  
2865 013376 012705 000001  
2866 013402  
2867 013402 104404  
2868 013404 005011  
2869 013406 010537 002612  
2870 013412 010511  
2871 013414 011104  
2872 013416 023704 002612  
2873 013422 001413  
2874 013424  
2875 013442 104455  
2876 013444 000033  
2877 013446 005774  
2878 013450 011230  
2879 013452  
2880 013452 104410  
2881 013454 000014  
2882 013456 005721  
2883 013460 005205  
2884 013462 022705 000005  
2885 013466 001346  
2886 013470  
2887 013470  
2888 013470 104405  
2889 013472 013701 002704  
2890 013476 012705 000001  
2891 013502 012737 013512 002414  
2892 013510  
2893 013510 104404  
2894 013512 010537 002612  
2895 013516 011104

BADHEAD  
:\*\*\*\*\* TEST 3 \*\*\*\*\*  
:\*UNIBUS REGISTER WORD DUAL ADDRESSING TEST  
:\*LOAD ALL REGISTERS WITH INCREMENTING PA.TERN  
:\*READ BACK ALL REGISTERS TO VERIFY CORRECT ADDRESSING  
:\*THE SEQUENCE:  
:\* 1. CLEAR REGISTER  
:\* 2. WRITE PATTERN  
:\* 3. VERIFY PATTERN  
:\* 4. DO ALL 4 REGISTERS  
:\* 5. READ ALL BACK IF ERRORS,  
:\* DUAL ADDRESS PROBLEM.  
:\*  
:\* 1 IN REG 0  
:\* 2 IN REG 2  
:\* 3 IN REG 4  
:\* 4 IN REG 6

BADHEAD  
:\*\*\*\*\* TEST 3 \*\*\*\*\*

BGNTST  
T3::

MYINT  
MSTCLR ;R1 CONTAINS BASE M8200,4,6,7 ADDRESS  
MOV #1,R5 ;MASTER CLEAR M8200,4,6,7  
BGNSEG ;START PATTERN AT 1  
TRAP C\$BSEG  
1\$: CLR (R1) ;CLEAR REGISTER  
MOV R5,\$GDDAT ;PUT DATA IN 'EXPECTED'  
MOV R5,(R1) ;WRITE M8200,4,6,7 REGISTER WITH PATTERN  
MOV (R1),R4 ;READ M8200,4,6,7 REGISTER INTO 'FOUND'  
CMP \$GDDAT,R4 ;IS DATA CORRECT  
BEQ 2\$ ;BR IS YES  
ERROR 27 ;DATA ERROR  
TRAP C\$ERDF  
.WORD 27  
.WORD EM27  
.WORD ERR27  
2\$: ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10000\$-  
TST (R1)+ ;NEXT REGISTER  
INC R5 ;INCREMENT DATA PATTERN  
CMP #5,R5 ;LAST REGISTER?  
BNE 1\$ ;BR IF NO  
ENDSEG  
10000\$: TRAP C\$ESEG  
MOV KMCSR,R1 ;BASE M8200,4,6,7 ADDRESS TO R1  
MOV #1,R5 ;RESTART PATTERN AT 1  
MOV #3\$,LOCK ;NEW SCOPI  
BGNSEG  
3\$: TRAP C\$BSEG ;PUT DATA IN 'EXPECTED'  
MOV R5,\$GDDAT ;READ COMM. MICR-PROCESSOR FAMILY REGISTER INTO 'FOUND'  
MOV (R1),R4

2896	013520	023704	002612		CMP	\$GDDAT,R4		:IS DATA CORRECT
2897	013524	001413			BEQ	4\$		:BR IF YES
2898	013526				ERROR	2		:DUAL ADDRESSING ERROR
2899	013544	104455			TRAP	C\$ERDF		
2900	013546	000002			.WORD	2		
2901	013550	004556			.WORD	EM2		
2902	013552	006516			.WORD	ERR2		
2903	013554			4\$:	ESCAPE	SEG		
2904	013554	104410			TRAP	C\$ESCAPE		
2905	013556	000014			.WORD	10001\$-		
2906	013560	005721			TST	(R1)+		:NEXT REGISTER
2907	013562	005205			INC	R5		:INCREMENT PATTERN
2908	013564	022705	000005		CMP	#5,R5		:LAST REGISTER?
2909	013570	001350			BNE	3\$		:BR IF NO
2910	013572				ENDSEG			
2911	013572			10001\$:				
2912	013572	104405			TRAP	C\$ESEG		
2913	013574			ENDTST				
2914	013574			L10053:				
2915	013574	104401			TRAP	C\$ETST		

```

2916 013576          BADHEAD
2917                :***** TEST 4 *****
2918                :*CONTROL STATUS REGISTER WRITE/READ TEST
2919                :*FLOAT A ONE THROUGH BSEL 0
2920                :*CLEAR BIT0, VERIFY BIT0 WAS CLEARED
2921 013576          BADHEAD
2922                :***** TEST 4 *****
2923
2924 013576          BGNTST
2925 013576          T4::
2926 013576
2927 013602 005037 002576  MSTCLR                :MASTER CLEAR M8200,4,6,7
2928 013606 012702 000001  CLR MRO
2929 013612          MOV #BIT0,R2                :INDICATE BSELO
2930 013612 104404  BGNSEG
2931 013614 013701 002704  TRAP C$BSEG
2932 013620 010237 002612  1$: MOV KMCSR,R1                :PUT REGISTER ADDRESS IN R1
2933 013624 013711 002612  MOV R2,$GDDAT
2934 013630 011104          MOV $GDDAT,(R1)                :WRITE BIT 0
2935 013632 023704 002612  MOV (R1),R4                :READ CONTROL STATUS REGISTER
2936 013636 001413          CMP $GDDAT,R4                :IS DATA CORRECT
2937 013640          BEQ 2$                :BR IF YES
2938 013656 104455          ERROR 27                :DATA ERROR
2939 013660 000033          TRAP C$ERDF
2940 013662 005774          .WORD 27
2941 013664 011230          .WORD EM27
2942 013666          .WORD ERR27
2943 013666 104410  2$: ESCAPE SEG
2944 013670 000052          TRAP C$ESCAPE
2945 013672 040211          .WORD 10000$-
2946 013674 005037 002612  3$: BIC R2,(R1)                :CLEAR BSELO
2947 013700 011104          CLR $GDDAT                :CLEAR 'EXPECTED'
2948 013702 001413          MOV (R1),R4                :READ CONTROL STATUS REGISTER
2949 013704          BEQ 4$                :BR IF ZERO
2950 013722 104455          ERROR 2                :DATA ERROR BSEL NOT CLEARED
2951 013724 000002          TRAP C$ERDF
2952 013726 004556          .WORD 2
2953 013730 006516          .WORD EM2
2954 013732          .WORD ERR2
2955 013732 104410  4$: ESCAPE SEG
2956 013734 000006          TRAP C$ESCAPE
2957 013736 106302          .WORD 10000$-
2958 013740 001325          ASLB R2
2959 013742          BNE 1$
2960 013742          ENDSEG
2961 013742 104405  10000$: TRAP C$ESEG
2962 013744          ENDTST
2963 013744          L10054:
2964 013744 104401          TRAP C$ETST
  
```



```
2965 013746          BADHEAD
2966                  :***** TEST 5 *****
2967                  :*CONTROL STATUS REGISTER WRITE/READ TEST
2968                  :*SET BIT9, VERIFY BIT9 WAS SET
2969                  :*CLEAR BIT9, VERIFY BIT9 WAS CLEARED
2970 013746          BADHEAD
2971                  :***** TEST 5 *****
2972
2973 013746          BGNTST
2974 013746          T5::
2975 013746          MSTCLR          :MASTER CLEAR M8200,4,6,7
2976 013752          BGNSEG
2977 013752          TRAP          C$BSEG
2978 013754          104404          :PUT REGISTER ADDRESS IN R1
2979 013760          013701 002704 001000 002612 1$: MOV #MCSR,R1 :PUT DATA IN 'EXPECTED'
2980 013766          012737 001000 002612 1$: MOV #BIT9,$GDDAT :WRITE BIT 9
2981 013772          013711 002612 1$: MOV $GDDAT,(R1) :READ CONTROL STATUS REGISTER
2982 013774          011104 002612 1$: MOV (R1),R4 :IS DATA CORRECT
2983 014000          023704 002612 1$: CMP $GDDAT,R4 :BR IF YES
2984 014002          001413 002612 1$: BEQ 2$ :DATA ERROR
2985 014020          104455          ERROR
2986 014022          000033          TRAP C$ERDF
2987 014024          005774          .WORD 27
2988 014026          011230          .WORD EM27
2989 014030          014030          .WORD ERR27
2990 014030          104410          2$: ESCAPE SEG
2991 014032          000002          TRAP C$ESCAPE
2992 014034          014034          .WORD 10000$-.
2993 014034          014034          ENDSEG
2994 014034          104405          10000$: TRAP C$ESEG
2995 014036          104404          BGNSEG
2996 014036          042711 001000          TRAP C$BSEG
2997 014040          005037 002612          3$: BIC #BIT9,(R1) :CLEAR BIT 9
2998 014044          011104          CLR $GDDAT :CLEAR 'EXPECTED'
2999 014050          001416          MOV (R1),R4 :READ CONTROL STATUS REGISTER
3000 014052          104455          BEQ 4$ :BR IF ZERO
3001 014054          000032          ERROR 26 :DATA ERROR BIT9 NOT CLEARED
3002 014072          005743          TRAP C$ERDF
3003 014074          011122          .WORD 26
3004 014100          104410          .WORD EM26
3005 014102          000002          .WORD ERR26
3006 014104          104405          ESCAPE SEG
3007 014106          000002          TRAP C$ESCAPE
3008 014106          104405          .WORD 10001$-.
3009 014106          104405          ENDSEG
3010 014106          104405          10001$: TRAP C$ESEG
3011 014110          104401          4$: TRAP C$SETST
3012 014110          104401          ENDTST
3013 014110          104401          L1005:
3014 014110          104401          TRAP C$SETST
3015 014110          104401          TRAP C$SETST
```

```
3016 014112          BADHEAD
3017                :***** TEST 6 *****
3018                :*CONTROL STATUS REGISTER WRITE/READ TEST
3019                :*SET BIT11, VERIFY BIT11 WAS SET
3020                :*CLEAR BIT11, VERIFY BIT11 WAS CLEARED
3021 014112          BADHEAD
3022                :***** TEST 6 *****
3023
3024 014112          BGNTST
3025 014112          T6::
3026 014112          MSTCLR          ;MASTER CLEAR M8200,4,6,7
3027 014116          BGNSEG
3028 014116          TRAP
3029 014120 104404    C$BSEG
3030 014124 013701 002704 002612 1$: MOV KMCSR,R1          ;PUT REGISTER ADDRESS IN R1
3031 014132 012737 004000          MOV #BIT11,$GDDAT      ;PUT DATA IN 'EXPECTED'
3032 014136 013711 002612          MOV $GDDAT,(R1)        ;WRITE BIT 11
3033 014140 011104          MOV (R1),R4            ;READ CONTROL STATUS REGISTER
3034 014144 023704 002612          CMP $GDDAT,R4        ;IS DATA CORRECT
3035 014146          BEQ 2$          ;BR IF YES
3036 014164 104455          ERROR 26          ;DATA ERROR
3037 014166 000032          TRAP C$ERDF
3038 014170 005743          .WORD 26
3039 014172 011122          .WORD EM26
3040 014174          .WORD ERR26
3041 014174 104410          2$: ESCAPE SEG
3042 014176 000002          TRAP C$ESCAPE
3043 014200          .WORD 10000$-.
3044 014200          ENDSEG
3045 014200 104405          10000$: TRAP C$ESEG
3046 014202          BGNSEG
3047 014202 104404          TRAP
3048 014204 042711 004000          BIC #BIT11,(R1)        ;CLEAR BIT 11
3049 014210 005037 002612          CLR $GDDAT          ;CLEAR 'EXPECTED'
3050 014214 011104          MOV (R1),R4            ;READ CONTROL STATUS REGISTER
3051 014216 001414          BEQ 4$          ;BR IF ZERO
3052 014220          ERROR 26          ;DATA ERROR BIT11 NOT CLEARED
3053 014236 104455          TRAP C$ERDF
3054 014240 000032          .WORD 26
3055 014242 005743          .WORD EM26
3056 014244 011122          .WORD ERR26
3057 014246          ENDSEG
3058 014246          10001$:
3059 014246 104405          TRAP C$ESEG
3060 014250          4$:
3061 014250          ENDTST
3062 014250          L10056:
3063 014250 104401          TRAP C$ETST
```

```
3064 014252 BADHEAD
3065 :***** TEST 7 *****
3066 :*CONTROL STATUS REGISTER WRITE/READ TEST
3067 :*SET BIT12, VERIFY BIT12 WAS SET
3068 :*CLEAR BIT12, VERIFY BIT12 WAS CLEARED
3069 014252 BADHEAD
3070 :***** TEST 7 *****
3071
3072 014252 BGNTST
3073 014252 T7::
3074 014252 MSTCLR MASTER CLEAR M8200,4,6,7
3075 014256 BGNSEG
3076 014256 104404 TRAP CSBSEG
3077 014260 013701 002704 002612 1$: MOV KMCSR,R1 ;PUT REGISTER ADDRESS IN R1
3078 014264 012737 010000 002612 MOV #BIT12,$GDDAT ;PUT DATA IN 'EXPECTED'
3079 014272 013711 002612 MOV $GDDAT,(R1) ;WRITE BIT 12
3080 014276 011104 MOV (R1),R4 ;READ CONTROL STATUS REGISTER
3081 014300 023704 002612 CMP $GDDAT,R4 ;IS DATA CORRECT
3082 014304 001413 BEQ 2$ ;BR IF YES
3083 014306 ERROR 26 ;DATA ERROR
3084 014324 104455 TRAP C$ERDF
3085 014326 000032 .WORD 26
3086 014330 005743 .WORD EM26
3087 014332 011122 .WORD ERR26
3088 014334 2$: ESCAPE SEG
3089 014334 104410 TRAP C$ESCAPE
3090 014336 000002 .WORD 10000$-.
3091 014340 ENDSEG
3092 014340 10000$:
3093 014340 104405 TRAP C$ESEG
3094 014342 BGNSEG
3095 014342 104404 TRAP CSBSEG
3096 014344 042711 010000 002612 3$: BIC #BIT12,(R1) ;CLEAR BIT 12
3097 014350 005037 002612 CLR $GDDAT ;CLEAR 'EXPECTED'
3098 014354 011104 MOV (R1),R4 ;READ CONTROL STATUS REGISTER
3099 014356 001414 BEQ 4$ ;BR IF ZERO
3100 014360 ERROR 26 ;DATA ERROR BIT12 NOT CLEARED
3101 014376 104455 TRAP C$ERDF
3102 014400 000032 .WORD 26
3103 014402 005743 .WORD EM26
3104 014404 011122 .WORD ERR26
3105 014406 ENDSEG
3106 014406 10001$:
3107 014406 104405 TRAP C$ESEG
3108 014410 4$:
3109 014410 ENDTST
3110 014410 L10057:
3111 014410 104401 TRAP C$ETST
```

```
3112 014412 BADHEAD
3113 :***** TEST 8 *****
3114 :*CONTROL OUT REGISTER WRITE/READ TEST
3115 :*FLOAT A ONE THROUGH SEL2
3116 014412 BADHEAD
3117 :***** TEST 8 *****
3118
3119 014412 BGNTST
3120 014412 T8::
3121 014412 MSTCLR ;MASTER CLEAR MB200,4,6,7
3122 014416 012737 000002 002576 MOV #2,MRO
3123 014424 012702 000001 MOV #1,R2
3124 014430 BGNSEG
3125 014430 104404 TRAP C$BSEG
3126
3127 014432 013701 002710 1$: MOV KMCTL,R1 ;PUT REGISTER ADDRESS IN R1
3128 014436 010237 002612 MOV R2,$GDDAT ;PUT DATA IN 'EXPECTED'
3129 014442 013711 002612 MOV $GDDAT,(R1) ;WRITE BIT 0
3130 014446 011104 MOV (R1),R4 ;READ CONTROL OUT REGISTER
3131 014450 023704 002612 CMP $GDDAT,R4 ;IS DATA CORRECT
3132 014454 001413 BEQ 2$ ;BR IF YES
3133 014456 ERROR 27 ;DATA ERROR
3134 014474 104455 TRAP C$ERDF
3135 014476 000033 .WORD 27
3136 014500 005774 .WORD EM27
3137 014502 011230 .WORD ERR27
3138 014504 2$: ESCAPE SEG
3139 014504 104410 TRAP C$ESCAPE
3140 014506 000052 .WORD 10000$-
3141 014510 040211 3$: BIC R2,(R1) ;CLEAR BIT
3142 014512 005037 002612 CLR $GDDAT ;CLEAR 'EXPECTED'
3143 014516 011104 MOV (R1),R4 ;READ CONTROL OUT REGISTER
3144 014520 001413 BEQ 4$ ;BR IF ZERO
3145 014522 ERROR 27 ;DATA ERROR BIT0 NOT CLEARED
3146 014540 104455 TRAP C$ERDF
3147 014542 000033 .WORD 27
3148 014544 005774 .WORD EM27
3149 014546 011230 .WORD ERR27
3150 014550 4$: ESCAPEE SEG
3151 014550 104410 TRAP C$ESCAPE
3152 014552 000006 .WORD 10000$-
3153 014554 006302 ASL R2
3154 014556 001325 BNE 1$
3155 014560 ENDSEG
3156 014560 10000$: TRAP C$ESEG
3157 014560 104405
3158 014562 ENDTST
3159 014562 L10060:
3160 014562 104401 TRAP C$ETST
```

```
3161 014564 BADHEAD
3162 :***** TEST 9 *****
3163 :*PORT4 REGISTER WRITE/READ TEST
3164 :*FLOAT A ONE THROUGH PORT4 REGISTER
3165 :*FLOAT A ZERO THROUGH PORT4 REGISTER
3166 014564 BADHEAD
3167 :***** TEST 9 *****
3168
3169
3170 014564 BGNTST
3171 014564 T9::
3172 014564 012737 000004 002576 MOV #4,MRO
3173 014572 MSTCLR ;MASTER CLEAR M8200.4,6,7
3174 014576 013701 002712 MOV KMPO4,R1 ;PUT REGISTER ADDRESS IN R1
3175 014602 012705 000001 MOV #1,R5 ;START WITH BIT0
3176 014606 BGNSEG
3177 014606 104404 TRAP C$BSEG
3178 014610
3179 014610 010537 002612 MOV R5,$GDDAT ;PUT 'EXPECTED' IN $GDDAT
3180 014614 013711 002612 MOV $GDDAT,(R1) ;WRITE PORT4 REGISTER
3181 014620 011104 MOV (R1),R4 ;READ PORT4 REGISTER
3182 014622 023704 002612 CMP $GDDAT,R4 ;COMPARE EXPECTED AND FOUND
3183 014626 001413 BEQ 65$ ;BR IF OK
3184 014630 ERROR 27 ;WRITE/READ ERROR
3185 014646 1044_5 TRAP C$ERDF
3186 014650 000033 .WORD 27
3187 014652 005774 .WORD EM27
3188 014654 011230 .WORD ERR27
3189 014656 65$: ESCAPE SEG
3190 014656 104410 TRAP C$ESCAPE
3191 014660 000010 .WORD 10000$-.
3192 014662 000241 CLC ;CLEAR CARRY
3193 014664 006105 ROL R5 ;SHIFT TO NEXT BIT
3194 014666 001350 BNE 64$ ;BR IF NOT DONE YET?
3195 014670 ENDSEG
3196 014670
3197 014670 104405 10000$: TRAP C$ESEG
3198 014672 012705 000001 MOV #1,R5 ;START WITH BIT0
3199 014676 BGNSEG
3200 014676 104404 TRAP C$BSEG
3201 014700
3202 014700 005105 66$: COM R5 ;CHANGE TO A FLOATING ZERO
3203 014702 010537 002612 MOV R5,$GDDAT ;PUT 'EXPECTED' IN $GDDAT
3204 014706 013711 002612 MOV $GDDAT,(R1) ;WRITE PORT4 REGISTER
3205 014712 011104 MOV (R1),R4
3206 014714 023704 002612 CMP $GDDAT,R4 ;COMPARE EXPECTED AND FOUND
3207 014720 001413 BEQ 67$ ;BR IF OK
3208 014722 ERROR 27 ;WRITE/READ ERROR
3209 014740 104455 TRAP C$ERDF
3210 014742 000033 .WORD 27
3211 014744 005774 .WORD EM27
3212 014746 011230 .WORD ERR27
3213 014750 67$: ESCAPE SEG
3214 014750 104410 TRAP C$ESCAPE
3215 014752 000012 .WORD 10001$-.
3216 014754 005105 COM R5 ;CHANGE BACK TO A FLOATING ONE
```

3217 014756 000241  
3218 014760 006105  
3219 014762 001346  
3220 014764  
3221 014764  
3222 014764 104405  
3223 014766  
3224 014766  
3225 014766 104401

CLC  
ROL R5  
BNE 66\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10061: TRAP C\$ETST

:CLEAR CARRY  
:SHIFT TO NEXT BIT  
:BR IF NOT DONE YET?

```
3226 014770 BADHEAD
3227 :***** TEST 10 *****
3228 :*PORT6 REGISTER WRITE/READ TEST
3229 :*FLOAT A ONE THROUGH PORT6 REGISTER
3230 :*FLOAT A ZERO THROUGH PORT6 REGISTER
3231 014770 BADHEAD
3232 :***** TEST 10 *****
3233
3234 014770 BGNST
3235 014770 T10::
3236 014770 012737 000006 002576 MOV #6,MRO
3237 014776 MSTCLR ;MASTER CLEAR M8200,4,6,7
3238 015002 013701 002714 MOV KMP06,R1 ;PUT REGISTER ADDRESS IN R1
3239 015006 012705 000001 MOV #1,R5 ;START WITH BIT0
3240 015012 BGNSEG
3241 015012 104404 TRAP C$BSEG
3242 015014 64$:
3243 015014 010537 002612 MOV R5,$GDDAT ;PUT 'EXPECTED' IN $GDDAT
3244 015020 013711 002612 MOV $GDDAT,(R1) ;WRITE PORT6 REGISTER
3245 015024 011104 MOV (R1),R4 ;READ PORT6 REGISTER
3246 015026 023704 002612 CMP $GDDAT,R4 ;COMPARE EXPECTED AND FOUND
3247 015032 001413 BEQ 65$ ;BR IF OK
3248 015034 ERROR 27 ;WRITE/READ ERROR
3249 015052 104455 TRAP C$ERDF
3250 015054 000033 .WORD 27
3251 015056 005774 .WORD EM27
3252 015060 011230 .WORD ERR27
3253 015062 65$:
3254 015062 104410 ESCAPE SEG
3255 015064 000010 TRAP C$ESCAPE
3256 015066 000241 .WORD 10000$-.
3257 015070 006105 CLC ;CLEAR CARRY
3258 015072 001350 ROL R5 ;SHIFT TO NEXT BIT
3259 015074 ENDSEG BNE 64$ ;BR IF NOT DONE YET?
3260 015074 10000$:
3261 015074 104405 TRAP C$ESEG
3262 015076 012705 000001 MOV #1,R5 ;START WITH BIT0
3263 015102 BGNSEG
3264 015102 104404 TRAP C$BSEG
3265 015104 66$:
3266 015104 005105 COM R5 ;CHANGE TO A FLOATING ZERO
3267
3268 015106 010537 002612 MOV R5,$GDDAT ;PUT 'EXPECTED' IN $GDDAT
3269 015112 013711 002612 MOV $GDDAT,(R1) ;WRITE PORT6 REGISTER
3270 015116 011104 MOV (R1),R4 ;READ PORT6 REGISTER
3271 015120 023704 002612 CMP $GDDAT,R4 ;COMPARE EXPECTED AND FOUND
3272 015124 001413 BEQ 67$ ;BR IF OK
3273 015126 ERROR 27 ;WRITE/READ ERROR
3274 015144 104455 TRAP C$ERDF
3275 015146 000033 .WORD 27
3276 015150 005774 .WORD EM27
3277 015152 011230 .WORD ERR27
3278 015154 67$:
3279 015154 104410 ESCAPE SEG
3280 015156 000012 TRAP C$ESCAPE
3281 015160 005105 .WORD 10001$-.
COM R5 ;CHANGE BACK TO A FLOATING ONE
```

3282 015162 000241  
3283 015164 006105  
3284 015166 001346  
3285 015170  
3286 015170  
3287 015170 104405  
3288 015172  
3289 015172  
3290 015172 104401

CLC  
ROL R5  
BNE 66\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10062: TRAP C\$ETST

:CLEAR CARRY  
:SHIFT TO NEXT BIT  
:BR IF NOT DONE YET?



```
3291 015174 BADHEAD
3292 :***** TEST 11 *****
3293 :*UNIBUS REGISTER BYTE DUAL ADDRESSING TEST
3294 :*LOAD ALL REGISTERS WITH INCREMENTING PATTERN
3295 :*READ BACK ALL REGISTERS TO VERIFY CORRECT ADDRESSING
3296 015174 BADHEAD
3297 :***** TEST 11 *****
3298
3299 015174 BGNTST
3300 015174 T11::
3301 015174 MYINT
3302 015200 MSTCLR :MASTER CLEAR M8200,4,6,7
3303 015204 012705 000001 MOV #1,R5 ;START PATTERN AT 1
3304 015210 BGNSEG
3305 015210 104404 TRAP C$BSEG
3306 015212 105011 CLR (R1) ;CLEAR REGISTER
3307 015214 010537 002612 MOV R5,$GDDAT ;PUT DATA IN 'EXPECTED'
3308 015220 110511 MOV R5,(R1) ;WRITE M8200,4,6,7 REGISTER WITH PATTERN
3309 015222 111104 MOV (R1),R4 ;READ M8200,4,6,7 REGISTER INTO 'FOUND'
3310 015224 123704 002612 MOV (R1),R4 ;IS DATA CORRECT
3311 015230 001413 CMP $GDDAT,R4 ;BR IF YES
3312 015232 BEQ 2$ ;DATA ERROR
3313 015250 104455 TRAP C$ERDF
3314 015252 000002 .WORD 2
3315 015254 004556 .WORD EM2
3316 015256 006516 .WORD ERR2
3317 015260 2$: ESCAPE SEG
3318 015260 104410 TRAP C$ESCAPE
3319 015262 000024 .WORD 10000$-
3320 015264 105721 TSTB (R1)+ ;NEXT REGISTER
3321 015266 005205 INC R5 ;INCREMENT DATA PATTERN
3322 015270 022705 000011 CMP #11,R5 ;LAST REGISTER?
3323 015274 001346 BNE 1$ ;BR IF NO
3324 015276 013701 002704 MOV KMCSR,R1 ;BASE M8200,4,6,7 ADDRESS TO R1
3325 015302 012705 000001 MOV #1,R5 ;RESTART PATTERN AT 1
3326 015306 ENDSEG
3327 015306 10000$:
3328 015306 104405 TRAP C$ESEG
3329 015310 BGNSEG
3330 015310 104404 TRAP C$BSEG
3331 015312 110537 002612 MOV R5,$GDDAT ;PUT DATA IN 'EXPECTED'
3332 015316 111104 MOV (R1),R4 ;READ COMM.MICRO-PROCESSOR FAMILY REGISTER INTO 'FOUND'
3333 015320 123704 002612 MOV (R1),R4 ;IS DATA CORRECT
3334 015324 001413 CMP $GDDAT,R4 ;BR IF YES
3335 015326 BEQ 4$ ;DUAL ADDRESSING ERROR
3336 015344 104455 ERROR 2
3337 015346 000002 TRAP C$ERDF
3338 015350 004556 .WORD 2
3339 015352 006516 .WORD EM2
3340 015354 4$: .WORD ERR2
3341 015354 104410 ESCAPE SEG
3342 015356 000014 TRAP C$ESCAPE
3343 015360 105721 .WORD 10001$-
3344 015362 005205 TSTB (R1)+ ;NEXT REGISTER
3345 015364 022705 000011 INC R5 ;INCREMENT PATTERN
3346 015370 001350 CMP #11,R5 ;LAST REGISTER?
;BR IF NO
```

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SEQ 0074

3347 015372  
3348 015372  
3349 015372 104405  
3350 015374  
3351 015374  
3352 015374 104401

ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10063: TRAP C\$ETST

```
3353 015376          BADHEAD
3354                :***** TEST 12 *****
3355                :*MAINTENANCE INSTRUCTION REGISTER TEST
3356                :*VERIFY THAT THE MAINT IR CAN BE WRITTEN TO ALL ZEROS'
3357                :*AND ALL ONES'. VERIFY THAT IT IS CLEARED ON A BUS RESET.
3358 015376          BADHEAD
3359                :***** TEST 12 *****
3360
3361                BGNTST
3362 015376          T12::
3363
3364 015376          MSTCLR
3365 015402          MYINT
3366 015406          BGNSEG
3367 015406 104404   TRAP      C$BSEG
3368 015410 012711 003000   MOV      #BIT9!BIT10,(R1) ;SEL6 IS NOW THE IR
3369 015414 005037 002612   CLR      $GDDAT          ;PUT 'EXPECTED' IN $GDDAT
3370 015420 013761 002612   MOV      $GDDAT,6(R1)   ;CLEAR THE IR
3371 015426 016104 000006   MOV      6(R1),R4       ;READ THE IR
3372 015432 023704 002612   CMP      $GDDAT,R4      ;IS IT CLEARED?
3373 015436 001413          BEQ      2$              ;BR IF YES
3374 015440          ERROR  26              ;ERROR IR IS NOT CLEAR
3375 015456 104455   TRAP      C$ERDF
3376 015460 000032   .WORD    26
3377 015462 005743   .WORD    EM26
3378 015464 011122   .WORD    ERR26
3379 015466          2$:   ESCAPE  SEG
3380 015466 104410   TRAP      C$ESCAPE
3381 015470 000002   .WORD    10000$-.
3382 015472          ENDSEG
3383 015472          10000$:
3384 015472 104405   TRAP      C$ESEG
3385 015474 012737 177777 002612   MOV      #-1,$GDDAT      ;PUT 'EXPECTED' IN $GDDAT
3386 015502          BGNSEG
3387 015502 104404   TRAP      C$BSEG
3388 015504 013761 002612 000006 3$:   MOV      $GDDAT,6(R1)   ;WRITE ALL ONES TO THE IR
3389 015512 016104 000006   MOV      6(R1),R4       ;READ THE IR
3390 015516 023704 002612   CMP      $GDDAT,R4      ;IS IT ALL ONES?
3391 015522 001413          BEQ      4$              ;BR IF YES
3392 015524          ERROR  23              ;ERROR IR IS NOT = ALL ONES
3393 015542 104455   TRAP      C$ERDF
3394 015544 000027   .WORD    23
3395 015546 005475   .WORD    EM23
3396 015550 010624   .WORD    ERR23
3397 015552          4$:   ESCAPE  SEG
3398 015552 104410   TRAP      C$ESCAPE
3399 015554 000002   .WORD    10001$-.
3400 015556          ENDSEG
3401 015556          10001$:
3402 015556 104405   TRAP      C$ESEG
3403 015560          ENDTST
3404 015560          L10064:
3405 015560 104401   TRAP      C$ETST
```

3406 015562  
3407  
3408  
3409  
3410  
3411  
3412  
3413 015562  
3414  
3415  
3416 015562  
3417 015562  
3418 015562  
3419 015566  
3420 015572 012761 000377 000004  
3421 015600 012711 001000  
3422 015604 012761 121105 000006  
3423 015612 052711 001400  
3424 015616 000240  
3425 015620 012737 177777 002612  
3426 015626 116104 000005  
3427 015632 123704 002612  
3428 015636 001413  
3429 015640  
3430 015656 104455  
3431 015660 000034  
3432 015662 006023  
3433 015664 011342  
3434  
3435 015666  
3436 015666 104410  
3437 015670 000002  
3438  
3439 015672  
3440 015672  
3441 015672 104401

BADHEAD  
:\*\*\*\*\* TEST 13 \*\*\*\*\*  
:\*MICRO PROCESSOR TEST  
:\*LOAD KMP06 WITH A MICRO-PROCESSOR INSTRUCTION, CLOCK IT  
:\*VERIFY INSTRUCTION EXECUTED PROPERLY  
:\*INSTRUCTION SHOULD MOVE IBUS\*4 TO IBUS\*5, IBUS\*4 IS ALL 1'S  
:\*AND IBUS\*5 IS ALL 0'S. RESULT SHGULD BE ALL 1'S IN SEL4  
BADHEAD  
:\*\*\*\*\* TEST 13 \*\*\*\*\*

BGNTST  
T13::

MYINT  
MSTCLR  
MOV #377,4(R1) ;PORT4 HI BYTE=1'S  
MOV #BIT9,(R1) ;SET ROMI  
MOV #121105,6(R1) ;INSTR TO PORT 6.  
BIS #BIT8!BIT9,(R1) ;CLK INSTR.  
NOP  
MOV #-1,\$GDDAT ;EXPECT ALL ONES.  
MOVB 5(R1),R4 ;READ FOUND.  
CMPB \$GDDAT,R4 ;DATA CORRECT?  
BEQ 1\$  
ERROR 28  
TRAP C\$ERDF  
.WORD 28  
.WORD EM28  
.WORD ERR28  
1\$: ESCAPE TST  
TRAP C\$ESCAPE  
.WORD L10065-

ENDTST  
L10065:

TRAP C\$ETST

```

3442 015674          BADHEAD
3443                :***** TEST 14 *****
3444                :*MICRO PROCESSOR IBUS* REGISTER WRITE/READ TEST
3445                :*FLOAT A 1 THROUGH IBUS* REGISTER 0
3446                :*FLOAT A 0 THROUGH IBUS* REGISTER 0
3447 015674          BADHEAD
3448                :***** TEST 14 *****
3449
3450 015674          BGNST
3451 015674          T14::
3452 015674          MSTCLR                ;MASTER CLEAR M8200,4,6,7
3453 015700 012737 000C00 002576      MOV      #0,MRO                ;SAVE REGISTER ADDRESS FOR TYPEOUT
3454 015706 012705 000001              MOV      #' 25                ;START WITH BIT 0
3455
3456 015712          MYINT
3457 015716          BGNSEG
3458 015716 104404          TRAP      C$BSEG
3459 015720          64$:
3460 015720 010561 000004          MOV      R5,4(R1)            ;PUT PATTERN INTO PORT4
3461 015724          ROMCLK                ;NEXT WORD IS INSTRUCTION, BBN
3462 015730 121100          121100                ;MOV DATA TO IBUS* REGISTER 0
3463 015732          ROMCLK                ;NEXT WORD IS INSTRUCTION, BBN
3464 015736 121005          121005                ;READ FROM IBUS* REGISTER 0
3465 015740 010537 002612          MOV      R5,$GDDAT          ;PUT EXPECTED IN $GDDAT
3466 015744 116104 000005          MOVB    5(R1),R4            ;PUT 'FOUND' INTO R4
3467 015750 123704 002612          CMPB    $GDDAT,R4          ;DATA CORRECT?
3468 015754 001413          BEQ     65$                ;BR IF YES
3469 015756          ERROR                ;ERROR
3470 015774 104455          TRAP    C$ERDF
3471 015776 000033          .WORD   27
3472 016000 005774          .WORD   EM27
3473 016002 011230          .WORD   ERR27
3474 016004          65$:
3475 016004 104410          ESCAPE  SEG
3476 016006 000010          TRAP    C$ESCAPE
3477 016010 000241          .WORD   10000$-
3478 016012 106105          CLC
3479 016014 001341          ROLB    R5                ;CLEAR CARRY
3480 016016          BNE     64$                ;SHIFT BIT 1.. R5
3481 016016          ENDSEG                    ;IF R5=0 THEN DONE
3482 016016 104405          10000$:
3483 016020 012705 000001          TRAP    C$ESEG
3484 016024          MOV      #1,R5                ;START WITH BIT 0
3485 016024 104404          BGNSEG
3486 016026          67$:
3487 016026 005105          TRAP    C$BSEG
3488 016030 010561 000004          COM     R5
3489 016034          MOV      R5,4(R1)            ;PUT PATTERN INTO PORT4
3490 016040 121100          ROMCLK                ;NEXT WORD IS INSTRUCTION, BBN
3491 016042          121100                ;MOV DATA TO IBUS* REGISTER 0
3492 016046 121005          ROMCLK                ;NEXT WORD IS INSTRUCTION, BBN
3493 016050 010537 002612          121005                ;READ FROM IBUS* REGISTER 0
3494 016054 116104 000005          MOV      R5,$GDDAT          ;PUT EXPECTED IN $GDDAT
3495 016060 123704 002612          MOVB    5(R1),R4            ;PUT 'FOUND' INTO R4
3496 016064 001413          CMPB    $GDDAT,R4          ;DATA CORRECT?
3497 016066          BEQ     68$                ;BR IF YES
3497 016066          ERROR                ;ERROR
  
```

3498 016104 104455  
3499 016106 000033  
3500 016110 005774  
3501 016112 011230  
3502 016114  
3503 016114 104410  
3504 016116 000012  
3505 016120 005105  
3506 016122 000241  
3507 016124 106105  
3508 016126 001337  
3509 016130  
3510 016130  
3511 016130 104405  
3512 016132  
3513 016132  
3514 016132 104401

TRAP C\$ERDF  
.WORD 27  
.WORD EM27  
.WORD ERR27  
68\$: ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-.  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
100C1\$: TRAP C\$ESEG  
ENDTST  
L10066: TRAP C\$ETST

:CHANGE TO FLOATING 1  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:IF R5=0 THEN DONE

```
3515 016134 BADHEAD
3516 :***** TEST 15 *****
3517 :*MICRO PROCESSOR IBUS* REGISTER WRITE/READ TEST
3518 :*FLOAT A 1 THROUGH IBUS* REGISTER 1
3519 :*FLOAT A 0 THROUGH IBUS* REGISTER 1
3520 016134 BADHEAD
3521 :***** TEST 15 *****
3522
3523 016134 BGNSTST
3524 016134 T15::
3525 016134
3526 016140 012737 000001 002576 MSTCLR :MASTER CLEAR M8200,4,6,7
3527 016146 012705 000001 MOV #1,MRO :SAVE REGISTER ADDRESS FOR TYPEOUT
3528 MOV #1,R5 :START WITH BIT 0
3529 016152 MYINT
3530 016156 BGNSEG
3531 016156 104404 TRAP C$BSEG
3532 016160 64$:
3533 016160 010561 000004 MOV R5,4(R1) :PUT PATTERN INTO PORT4
3534 016164 042761 000307 000004 BIC #307,4(R1) : MASK
3535 016172 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
3536 016176 121101 121100!1 :MOV DATA TO IBUS* REGISTER 1
3537 016200 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
3538 016204 121025 121005!<1*20> :READ FROM IBUS* REGISTER 1
3539 016206 010537 002612 MOV R5,$GDDAT :PUT EXPECTED IN $GDDAT
3540 016212 116104 000005 MOVB 5(R1),R4 :PUT 'FOUND' INTO R4
3541 016216 042737 000307 002612 BIC #307,$GDDAT
3542 016224 042704 000307 BIC #307,R4
3543 016230 123704 002612 CMPB $GDDAT,R4
3544 016234 001413 BEQ 65$ :DATA CORRECT?
3545 016236 ERROR 27 :BR IF YES
3546 016254 104455 TRAP C$ERDF :ERROR
3547 016256 000033 .WORD 27
3548 016260 005774 .WORD EM27
3549 016262 011230 .WORD ERR27
3550 016264 000241 65$: CLC :CLEAR CARRY
3551 016266 106105 ROLB R5 :SHIFT BIT IN R5
3552 016270 001333 BNE 64$ :IF R5=0 THEN DONE
3553 016272 ENDSEG
3554 016272 10000$:
3555 016272 104405 TRAP C$ESEG
3556 016274 012705 000001 MOV #1,R5 :START WITH BIT 0
3557 016300 BGNSEG
3558 016300 104404 TRAP C$BSEG
3559 016302 67$:
3560 016302 005105 COM R5
3561 016304 010561 000004 MOV R5,4(R1) :PUT PATTERN INTO PORT4
3562 016310 042761 000307 000004 BIC #307,4(R1) : MASK
3563 016316 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
3564 016322 121101 121100!1 :MOV DATA TO IBUS* REGISTER 1
3565 016324 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
3566 016330 121025 121005!<1*20> :READ FROM IBUS* REGISTER 1
3567 016332 010537 002612 MOV R5,$GDDAT :PUT EXPECTED IN $GDDAT
3568 016336 116104 000005 MOVB 5(R1),R4 :PUT 'FOUND' INTO R4
3569 016342 042737 000307 002612 BIC #307,$GDDAT
3570 016350 042704 000307 BIC #307,R4
```

3571 016354 123704 002612  
3572 016360 001413  
3573 016362  
3574 016400 104455  
3575 016402 000033  
3576 016404 005774  
3577 016406 011230  
3578 016410  
3579 016410 104410  
3580 016412 000012  
3581 016414 005105  
3582 016416 000241  
3583 016420 106105  
3584 016422 001327  
3585 016424  
3586 016424 104405  
3587 016424 104405  
3588 016426  
3589 016426  
3590 016426 104401

CMPB \$GDDAT,R4  
BEQ 68\$ ;BR IF YES ;DATA CORRECT?  
ERROR 27 ;ERROR  
TRAP C\$ERDF  
.WORD 27  
.WORD EM27  
.WORD ERR27  
68\$: ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-  
COM R5 ;CHANGE TO FLOATING 1  
CLC ;CLEAR CARRY  
ROLB R5 ;SHIFT BIT IN R5  
BNE 67\$ ;IF R5=0 THEN DONE  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10067: TRAP C\$ETST



```
3591 016430 BADHEAD
3592 :***** TEST 16 *****
3593 :*MICRO PROCESSOR IBUS* REGISTER WRITE/READ TEST
3594 :*FLOAT A 1 THROUGH IBUS* REGISTER 2
3595 :*FLOAT A 0 THROUGH IBUS* REGISTER 2
3596 016430 BADHEAD
3597 :***** TEST 16 *****
3598
3599 016430 BGNSTST
3600 016430 T16::
3601 016430 MSTCLR ;MASTER CLEAR MB200,4,6,7
3602 016434 012737 000002 002576 MOV #2,MRO ;SAVE REGISTER ADDRESS FOR TYPEOUT
3603 016442 012705 000001 MOV #1,R5 ;START WITH BIT 0
3604 016446 MYINT
3605 016452 BGNSEG
3606 016452 104404 TRAP C$BSEG
3607 016454
3608 016454 010561 000004 64$: MOV R5,4(R1) ;PUT PATTERN INTO PORT4
3609 016460 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3610 016464 121102 121100!2 ;MOV DATA TO IBUS* REGISTER 0
3611 016466 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3612 016472 121045 121005!<2*20> ;READ FROM IBUS* REGISTER 2
3613 016474 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
3614 016500 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
3615 016504 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
3616 016510 001413 BEQ 65$ ;BR IF YES
3617 016512 ERROR 27 ;ERROR
3618 016530 104455 TRAP C$ERDF
3619 016532 000033 .WORD 27
3620 016534 005774 .WORD EM27
3621 016536 011230 .WORD ERR27
3622 016540 65$: ESCAPE SEG
3623 016540 104410 TRAP C$ESCAPE
3624 016542 000010 .WORD 10000$-
3625 016544 000241 CLC ;CLEAR CARRY
3626 016546 106105 ROLB R5 ;SHIFT BIT IN R5
3627 016550 001341 BNE 64$ ;IF R5=0 THEN DONE
3628 016552 ENDSEG
3629 016552 10000$:
3630 016552 104405 TRAP C$ESEG
3631 016554 012705 000001 MOV #1,R5 ;START WITH BIT 0
3632 016560 BGNSEG
3633 016560 104404 TRAP C$BSEG
3634 016562 67$:
3635 016562 005105 COM R5
3636 016564 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
3637 016570 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3638 016574 121102 121100!2 ;MOV DATA TO IBUS* REGISTER 2
3639 016576 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3640 016602 121045 121005!<2*20> ;READ FROM IBUS* REGISTER 2
3641 016604 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
3642 016610 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
3643 016614 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
3644 016620 001413 BEQ 68$ ;BR IF YES
3645 016622 ERROR 27 ;ERROR
3646 016640 104455 TRAP C$ERDF
```

3647 016642 000033  
3648 016644 005774  
3649 016646 011230  
3650 016650  
3651 016650 104410  
3652 016652 000012  
3653 016654 005105  
3654 016656 000241  
3655 016660 106105  
3656 016662 001337  
3657 016664  
3658 016664  
3659 016664 104405  
3660 016666  
3661 016666  
3662 016666 104401

68\$: .WORD 27  
.WORD EM27  
.WORD ERR27  
ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-.  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10070: TRAP C\$ETST

:CHANGE TO FLOATING 1  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:IF R5=0 THEN DONE

```
3663 016670 BADHEAD
3664 :***** TEST 17 *****
3665 :*MICRO PROCESSOR IBUS* REGISTER WRITE/READ TEST
3666 :*FLOAT A 1 THROUGH IBUS* REGISTER 3
3667 :*FLOAT A 0 THROUGH IBUS* REGISTER 3
3668 016670 BADHEAD
3669 :***** TEST 17 *****
3670
3671 016670 BGNTST
3672 016670 T17::
3673 016670 MSTCLR ;MASTER CLEAR M8200,4,6,7
3674 016674 012737 000003 002576 MOV #3,MRO ;SAVE REGISTER ADDRESS FOR TYPEOUT
3675 016702 012705 000001 MOV #1,R5 ;START WITH BIT 0
3676 016706 MYINT
3677 016712 BGNSEG
3678 016712 104404 TRAP CSBSEG
3679 016714 64$:
3680 016714 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
3681 016720 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3682 016724 121103 121100!3 ;MOV DATA TO IBUS* REGISTER 3
3683 016726 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3684 016732 121065 121005!<3*20> ;READ FROM IBUS* REGISTER 3
3685 016734 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
3686 016740 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
3687 016744 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
3688 016750 001413 BEQ 65$ ;BR IF YES
3689 016752 ERROR 27 ;ERROR
3690 016770 104455 TRAP C$ERDF
3691 016772 000033 .WORD 27
3692 016774 005774 .WORD EM27
3693 016776 011230 .WORD ERR27
3694 017000 65$: ESCAPE SEG
3695 017000 104410 TRAP C$ESCAPE
3696 017002 000010 .WORD 10000$-
3697 017004 000241 CLC ;CLEAR CARRY
3698 017006 106105 ROLB R5 ;SHIFT BIT IN R5
3699 017010 001341 BNE 64$ ;IF R5=0 THEN DONE
3700 017012 ENDSEG
3701 017012 10000$:
3702 017012 104405 TRAP C$ESEG
3703 017014 012705 000001 MOV #1,R5 ;START WITH BIT 0
3704 017020 BGNSEG
3705 017020 104404 TRAP CSBSEG
3706 017022 67$:
3707 017022 005105 COM R5
3708 017024 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
3709 017030 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3710 017034 121103 121100!3 ;MOV DATA TO IBUS* REGISTER 3
3711 017036 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3712 017042 121065 121005!<3*20> ;READ FROM IBUS* REGISTER 3
3713 017044 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
3714 017050 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
3715 017054 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
3716 017060 001413 BEQ 68$ ;BR IF YES
3717 017062 ERROR 27 ;ERROR
3718 017100 104455 TRAP C$ERDF
```

3719 017102 000033  
3720 017104 005774  
3721 017106 011230  
3722 017110  
3723 017110 104410  
3724 017112 000012  
3725 017114 005105  
3726 017116 000241  
3727 017120 106105  
3728 017122 001337  
3729 017124  
3730 017124  
3731 017124 104405  
3732 017126  
3733 017126  
3734 017126 104401

68\$: .WORD 27  
.WORD EM27  
.WORD ERR27  
ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-.  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10071: TRAP C\$ETST

:CHANGE TO FLOATING 1  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:IF R5=0 THEN DONE

```
3735 017130 BADHEAD
3736 :***** TEST 18 *****
3737 :*MICRO PROCESSOR IBUS* REGISTER WRITE/READ TEST
3738 :*FLOAT A 1 THOUGH IBUS* REGISTER 4
3739 :*FLOAT A 0 THROUGH IBUS* REGISTER 4
3740 017130 BADHEAD
3741 :***** TEST 18 *****
3742
3743 017130 BGNST
3744 017130 T18::
3745 017130
3746 017134 012737 000004 002576 MSTCLR ;MASTER CLEAR M8200,4,6,7
3747 017142 012705 000001 MOV #4,MRO ;SAVE REGISTER ADDRESS FOR TYPEOUT
3748 017146 MYINT MOV #1,R5 ;START WITH BIT 0
3749 017152 BGNSEG
3750 017152 104404 TRAP C$BSEG
3751 017154 64$:
3752 017154 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
3753 017160 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3754 017164 121104 121100!4 ;MOV DATA TO IBUS* REGISTER 4
3755 017166 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3756 017172 121105 121005!<4*20> ;READ FROM IBUS* REGISTER 4
3757 017174 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
3758 017200 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
3759 017204 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
3760 017210 001413 BEQ 65$ ;BR IF YES
3761 017212 ERROR 27 ;ERROR
3762 017230 104455 TRAP C$ERDF
3763 017232 000033 .WORD 27
3764 017234 005774 .WORD EM27
3765 017236 011230 .WORD ERR27
3766 017240 65$: ESCAPE SEG
3767 017240 104410 TRAP C$ESCAPE
3768 017242 000010 .WORD 10000$-.
3769 017244 000241 CLC ;CLEAR CARRY
3770 017246 106105 ROLB R5 ;SHIFT BIT IN R5
3771 017250 001341 BNE 64$ ;IF R5=0 THEN DONE
3772 017252 ENDSEG
3773 017252 10000$:
3774 017252 104405 TRAP C$ESEG
3775 017254 012705 000001 MOV #1,R5 ;START WITH BIT 0
3776 017260 BGNSEG
3777 017260 104404 TRAP C$BSEG
3778 017262 67$:
3779 017262 005105 COM R5
3780 017264 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
3781 017270 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3782 017274 121104 121100!4 ;MOV DATA TO IBUS* REGISTER 4
3783 017276 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3784 017302 121105 121005!<4*20> ;READ FROM IBUS* REGISTER 4
3785 017304 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
3786 017310 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
3787 017314 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
3788 017320 001413 BEQ 68$ ;BR IF YES
3789 017322 ERROR 27 ;ERROR
3790 017340 104455 TRAP C$ERDF
```

3791 017342 000033  
3792 017344 005774  
3793 017346 011230  
3794 017350  
3795 017350 104410  
3796 017352 000012  
3797 017354 005105  
3798 017356 000241  
3799 017360 106105  
3800 017362 001337  
3801 017364  
3802 017364  
3803 017364 104405  
3804 017366  
3805 017366  
3806 017366 104401

68\$: .WORD 27  
.WORD EM27  
.WORD ERR27  
ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-.  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10072: TRAP C\$ETST

:CHANGE TO FLOATING 1  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:IF R5=0 THEN DONE

```
3807 017370 BADHEAD
3808 :***** TEST 19 *****
3809 :*MICRO PROCESSOR IBUS* REGISTER WRITE/READ TEST
3810 :*FLOAT A 1 THROUGH IBUS* REGISTER 5
3811 :*FLOAT A 0 THROUGH IBUS* REGISTER 5
3812 017370 BADHEAD
3813 :***** TEST 19 *****
3814
3815 017370 BGNTST
3816 017370 T19::
3817 017370
3818 017374 012737 000005 002576 MSTCLR ;MASTER CLEAR MB200,4,6,7
3819 017402 012705 000001 MOV #5,MRO ;SAVE REGISTER ADDRESS FOR TYPEOUT
3820 017406 MYINT #1,R5 ;START WITH BIT 0
3821 017412 BGNSEG
3822 017412 104404 TRAP C$BSEG
3823 017414 64$:
3824 017414 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
3825 017420 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3826 017424 121105 121100!5 ;MOV DATA TO IBUS* REGISTER 5
3827 017426 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3828 017432 121125 121005!<5*20> ;READ FROM IBUS* REGISTER 5
3829 017434 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
3830 017440 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
3831 017444 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
3832 017450 001413 BEQ 65$ ;BR IF YES
3833 017452 ERROR 27 ;ERROR
3834 017470 104455 TRAP C$ERDF
3835 017472 000033 .WORD 27
3836 017474 005774 .WORD EM27
3837 017476 011230 .WORD ERR27
3838 017500 65$:
3839 017500 104410 TRAP C$ESCAPE
3840 017502 000010 .WORD 10000$-.
3841 017504 000241 CLC ;CLEAR CARRY
3842 017506 106105 ROLB R5 ;SHIFT BIT IN R5
3843 017510 001341 BNE 64$ ;IF R5=0 THEN DONE
3844 017512 10000$:
3845 017512 TRAP C$ESEG
3846 017512 104405 MOV #1,R5 ;START WITH BIT 0
3847 017514 012705 000001 BGNSEG
3848 017520 TRAP C$BSEG
3849 017520 104404
3850 017522 67$:
3851 017522 005105 COM R5
3852 017524 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
3853 017530 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3854 017534 121105 121100!5 ;MOV DATA TO IBUS* REGISTER 5
3855 017536 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
3856 017542 121125 121005!<5*20> ;READ FROM IBUS* REGISTER 5
3857 017544 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
3858 017550 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
3859 017554 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
3860 017560 001413 BEQ 68$ ;BR IF YES
3861 017562 ERROR 27 ;ERROR
3862 017600 104455 TRAP C$ERDF
```

3863 017602 000033  
3864 017604 005774  
3865 017606 011230  
3866 017610  
3867 017610 104410  
3868 017612 000012  
3869 017614 005105  
3870 017616 000241  
3871 017620 106105  
3872 017622 001337  
3873 017624  
3874 017624  
3875 017624 104405  
3876 017626  
3877 017626  
3878 017626 104401

68\$: .WORD 27  
.WORD EM27  
.WORD ERR27  
ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10073: TRAP C\$ETST

:CHANGE TO FLOATING 1  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:IF R5=0 THEN DONE



```

3879 017630          BADHEAD
3880                :***** TEST 20 *****
3881                :*MICRO PROCESSOR IBUS* REGISTER WRITE/READ TEST
3882                :*FLOAT A 1 THROUGH IBUS* REGISTER 10
3883                :*FLOAT A 0 THROUGH IBUS* REGISTER 10
3884 017630          BADHEAD
3885                :***** TEST 20 *****
3886
3887 017630          BGNTST
3888 017630          T20::
3889 017630          MSTCLR          ;MASTER CLEAR M8200,4,6,7
3890 017534 012737 000010 002576  MOV #10,MRO          ;SAVE REGISTER ADDRESS FOR TYPEOUT
3891 017642 012705 000001          MOV #1,R5           ;START WITH BIT 0
3892 017646          MYINT
3893 017652          BGNSEG
3894 017652 104404          TRAP C$BSEG
3895 017654          64$:
3896 017654 010561 000004          MOV R5,4(R1)         ;PUT PATTERN INTO PORT4
3897 017660 042761 000141 000004  BIC #141,4(R1)      ;CLEAR UNWANTED BITS
3898 017666          ROMCLK          ;NEXT WORD IS INSTRUCTION, BBN
3899 017672 121110          121100!10 ;MOV DATA TO IBUS* REGISTER 10
3900 017674          ROMCLK          ;NEXT WORD IS INSTRUCTION, BBN
3901 017700 121205          121005!<10*20> ;READ FROM IBUS* REGISTER 10
3902 017702 010537 002612          MOV R5,$GDDAT        ;PUT EXPECTED IN $GDDAT
3903 017706 042737 000141 002612  BIC #141,$GDDAT    ;CLEAR UNWANTED BITS
3904 017714 116104 000005          MOV#B 5(R1),R4      ;PUT 'FOUND' INTO R4
3905 017720 123704 002612          CMPB $GDDAT,R4     ;DATA CORRECT?
3906 017724 001415          BEQ 65$              ;BR IF YES
3907 017726          ERROR          ;ERROR
3908 017744 104455          TRAP C$ERDF
3909 017746 000033          .WORD 27
3910 017750 005774          .WORD EM27
3911 017752 011230          .WORD ERR27
3912 017754          ESCAPE          SEG
3913 017754 104410          TRAP C$ESCAPE
3914 017756 000010          .WORD 10000$-.
3915 017760 000241          65$:
3916 017762 106105          CLC              ;CLEAR CARRY
3917 017764 001333          ROLB R5         ;SHIFT BIT IN R5
3918 017766          BNE 64$         ;IF R5=0 THEN DONE
3919 017766          10000$:
3920 017766 104405          TRAP C$ESEG
3921 017770 012705 000001          MOV #1,R5         ;START WITH BIT 0
3922 017774          BGNSEG
3923 017774 104404          TRAP C$BSEG
3924 017776          67$:
3925 017776 005105          COM R5
3926 020000 010561 000004          MOV R5,4(R1)     ;PUT PATTERN INTO PORT4
3927 020004 042761 000141 000004  BIC #141,4(R1)   ;CLEAR UNWANTED BITS
3928 020012          ROMCLK          ;NEXT WORD IS INSTRUCTION, BBN
3929 020016 121110          121100!10 ;MOV DATA TO IBUS* REGISTER 10
3930 020020          ROMCLK          ;NEXT WORD IS INSTRUCTION, BBN
3931 020024 121205          121005!<10*20> ;READ FROM IBUS* REGISTER 10
3932 020026 010537 002612          MOV R5,$GDDAT    ;PUT EXPECTED IN $GDDAT
3933 020032 042737 000141 002612  BIC #141,$GDDAT  ;CLEAR UNWANTED BITS
3934 020040 116104 000005          MOV#B 5(R1),R4   ;PUT 'FOUND' INTO R4

```

3935	020044	123704	002612	CMPB	\$GDDAT,R4		:DATA CORRECT?
3936	020050	00:415		BEQ	68\$		:BR IF YES
3937	020052			ERROR	27		:ERROR
3938	020070	104455		TRAP	C\$ERDF		
3939	020072	000033		.WORD	27		
3940	020074	005774		.WORD	EM27		
3941	020076	011230		.WORD	ERR27		
3942	020100			ESCAPE	SEG		
3943	020100	104410		TRAP	C\$ESCAPE		
3944	020102	000712		.WORD	10001\$-		
3945	020104	005105	68\$:	COM	R5		:CHANGE TO FLOATING 1
3946	020106	000241		CLC			:CLEAR CARRY
3947	020110	106105		ROLB	R5		:SHIFT BIT IN R5
3948	020112	001331		BNE	67\$		:IF R5=0 THEN DONE
3949	020114			ENDSEG			
3950	020114		10001\$:				
3951	020114	104405		TRAP	C\$ESEG		
3952	020116		ENDTST				
3953	020116		L10074:				
3954	020116	104401		TRAP	C\$ETST		

```

3955 020120          BADHEAD
3956                :***** TEST 21 *****
3957                :*MICRO PROCESSOR IBUS* REGISTER WRITE/READ TEST
3958                :*FLOAT A 1 THROUGH IBUS* REGISTER 11
3959                :*FLOAT A 0 THROUGH IBUS* REGISTER 11
3960 020120          BADHEAD
3961                :***** TEST 21 *****
3962
3963 020120          BGNST
3964 020120          T21::
3965 020120          MSTCLR                :MASTER CLEAR M8200,4,6,7
3966 020124 012737 000011 002576          MOV #11,MRO                :SAVE REGISTER ADDRESS FOR TYPEOUT
3967 020132 012705 000001                MOV #1,R5                  :START WITH BIT 0
3968 020136          MYINT
3969 020142          BGNSEG
3970 020142 104404          TRAP C$BSEG
3971 020144          64$:
3972 020144 010561 000004          MOV R5,4(R1)                :PUT PATTERN INTO PORT4
3973 020150 042761 000222 000004          BIC #222,4(R1)            :CLEAR UNWANTED BITS
3974 020156          ROMCLK                :NEXT WORD IS INSTRUCTION, BBN
3975 020162 121111          121100!11                :MOV DATA TO IBUS* REGISTER 11
3976 020164          ROMCLK                :NEXT WORD IS INSTRUCTION, BBN
3977 020170 121225          121005!<11*20>                :READ FROM IBUS* REGISTER 11
3978 020172 010537 002612          MOV R5,$GDDAT                :PUT EXPECTED IN $GDDAT
3979 020176 042737 000222 002612          BIC #222,$GDDAT            :CLEAR UNWANTED BITS
3980 020204 116104 000005          MOVB 5(R1),R4                :PUT 'FOUND' INTO R4
3981 020210 042704 000020          BIC #20,R4
3982 020214 123704 002612          CMPB $GDDAT,R4                :DATA CORRECT?
3983 020220 001415          BEQ 65$                      :BR IF YES
3984 020222          ERROR 27                :ERROR
3985 020240 104455          TRAP C$ERDF
3986 020242 000033          .WORD 27
3987 020244 005774          .WORD EM27
3988 020246 011230          .WORD ERR27
3989 020250          ESCAPE SEG
3990 020250 104410          TRAP C$ESCAPE
3991 020252 000010          .WORD 10000$-
3992 020254 000241          65$: CLC                      :CLEAR CARRY
3993 020256 106105          ROLB R5                      :SHIFT BIT IN R5
3994 020260 001331          BNE 64$                      :IF R5=0 THEN DONE
3995 020262          ENDSEG
3996 020262          10000$:
3997 020262 104405          TRAP C$ESEG
3998 020264 012705 000001          MOV #1,R5                  :START WITH BIT 0
3999 020270          BGNSEG
4000 020270 104404          TRAP C$BSEG
4001 020272          67$:
4002 020272 005105          COM R5
4003 020274 010561 000004          MOV R5,4(R1)                :PUT PATTERN INTO PORT4
4004 020300 042761 000222 000004          BIC #222,4(R1)            :CLEAR UNWANTED BITS
4005 020306          ROMCLK                :NEXT WORD IS INSTRUCTION, BBN
4006 020312 121111          121100!11                :MOV DATA TO IBUS* REGISTER 11
4007 020314          ROMCLK                :NEXT WORD IS INSTRUCTION, BBN
4008 020320 121225          121005!<11*20>                :READ FROM IBUS* REGISTER 11
4009 020322 010537 002612          MOV R5,$GDDAT                :PUT EXPECTED IN $GDDAT
4010 020326 042737 000222 002612          BIC #222,$GDDAT            :CLEAR UNWANTED BITS

```

4011	020334	052737	000020	002612	BIS	#20,\$GDDAT			:ADD THESE BITS
4012	020342	116104	000005		MOVB	5(R1),R4			:PUT 'FOUND' INTO R4
4013	020346	123704	002612		CMPS	\$GDDAT,R4			:DATA CORRECT?
4014	020352	001415			BEG	68\$			:BR IF YES
4015	020354				ERROR	27			:ERROR
4016	020372	104455			TRAP	C\$ERDF			
4017	020374	000033			.WORD	27			
4018	020376	005774			.WORD	EM27			
4019	020400	011230			.WORD	ERR27			
4020	020402				ESCAPE	SEG			
4021	020402	104410			TRAP	C\$ESCAPE			
4022	020404	000012			.WORD	10001\$-			
4023	020406	005105		68\$:	COM	R5			:CHANGE TO FLOATING 1
4024	020410	000241			CLC				:CLEAR CARRY
4025	020412	106105			ROLB	R5			:SHIFT BIT IN R5
4026	020414	001326			BNE	67\$			:IF R5=0 THEN DONE
4027	020416				ENDSEG				
4028	020416			10001\$:					
4029	020416	104405			TRAP	C\$ESEG			
4030	020420			ENDTST					
4031	020420			L10075:					
4032	020420	104401			TRAP	C\$ETST			

```
4033 020422 BADHEAD
4034 :***** TEST 22 *****
4035 :*MICRO PROCESSOR IBUS REGISTER WRITE/READ TEST
4036 :*FLOAT A 1 THROUGH IBUS REGISTER 0
4037 :*FLOAT A 0 THROUGH IBUS REGISTER 0
4038 020422 BADHEAD
4039 :***** TEST 22 *****
4040
4041 020422 BGNTST
4042 020422 T22::
4043 020422 MSTCLR ;MASTER CLEAR COMM. MICRO-PROCESSOR FAMILY
4044 020426 012737 000000 002576 MOV #0,MRO ;SAVE REGISTER ADDRESS FOR TYPEOUT
4045 020434 012705 000001 MOV #1,R5 ;START WITH BIT 0
4046 020440 MYINT
4047 020444 BGNSEG
4048 020444 104404 TRAP C$BSEG
4049 020446 64$:
4050 020446 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
4051 020452 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4052 020456 122100 ;MOV DATA TO IBUS* REGISTER 0
4053 020460 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4054 020464 021005 21005 ;READ FROM IBUS* REGISTER 0
4055 020466 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
4056 020472 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
4057 020476 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
4058 020502 001413 BEQ 65$ ;BR IF YES
4059 020504 ERROR 29 ;ERROR
4060 020522 104455 TRAP C$ERDF
4061 020524 000035 .WORD 29
4062 020526 006054 .WORD EM29
4063 020530 011450 .WORD ERR29
4064 020532 65$: ESCAPE SEG
4065 020532 104410 TRAP C$ESCAPE
4066 020534 000010 .WORD 10000$-.
4067 020536 000241 CLC ;CLEAR CARRY
4068 020540 106105 ROLB R5 ;SHIFT BIT IN R5
4069 020542 001341 BNE 64$ ;IF R5=0 THEN DONE
4070 020544 ENDSEG
4071 020544 10000$:
4072 020544 104405 TRAP C$ESEG
4073 020546 012705 000001 MOV #1,R5 ;START WITH BIT 0
4074 020552 BGNSEG
4075 020552 104404 TRAP C$BSEG
4076 020554 67$:
4077 020554 005105 COM R5
4078 020556 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
4079 020562 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4080 020566 122100 ;MOV DATA TO IBUS* REGISTER 0
4081 020570 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4082 020574 021005 21005 ;READ FROM IBUS* REGISTER 0
4083 020576 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
4084 020602 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
4085 020606 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
4086 020612 001413 BEQ 68$ ;BR IF YES
4087 020614 ERROR 29 ;ERROR
4088 020632 104455 TRAP C$ERDF
```

4089 020634 000035  
4090 020636 006054  
4091 020640 011450  
4092 020642  
4093 020642 104410  
4094 020644 000012  
4095 020646 005105  
4096 020650 000241  
4097 020652 106105  
4098 020654 001337  
4099 020656  
4100 020656  
4101 020656 104405  
4102 020660  
4103 020660  
4104 020660 104401

68\$: .WORD 29  
.WORD EM29  
.WORD ERR29  
ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10076: TRAP C\$ETST

:CHANGE TO FLOATING 1  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:IF R5=0 THEN DONE

```
4105 020662 BADHEAD
4106 :***** TEST 23 *****
4107 :*MICRO PROCESSOR IBUS REGISTER WRITE/READ TEST
4108 :*FLOAT A 1 THROUGH IBUS REGISTER 1
4109 :*FLOAT A 0 THROUGH IBUS REGISTER 1
4110 020662 BADHEAD
4111 :***** TEST 23 *****
4112
4113 020662 BGNTST
4114 020662 T23::
4115 020662
4116 020666 012737 000001 002576 MSTCLR ;MASTER CLEAR COMM. MICRO-PROCESSOR FAMILY
4117 020674 012705 000001 MOV #1,R0 ;SAVE REGISTER ADDRESS FOR TYPEOUT
4118 020700 MYINT MOV #1,R5 ;START WITH BIT 0
4119 020704 BGNSEG
4120 020704 104404 TRAP C$BSEG
4121 020706 64$:
4122 020706 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
4123 020712 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4124 020716 122101 122100!1 ;MOV DATA TO IBUS* REGISTER 1
4125 020720 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4126 020724 021025 21005!<1*20> ;READ FROM IBUS* REGISTER 1
4127 020726 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
4128 020732 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
4129 020736 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
4130 020742 001413 BEQ 65$ ;BR IF YES
4131 020744 ERROR 29 ;ERROR
4132 020762 104455 TRAP C$ERDF
4133 020764 000035 .WORD 29
4134 020766 006054 .WORD EM29
4135 020770 011450 .WORD ERR29
4136 020772 65$: ESCAPE SEG
4137 020772 104410 TRAP C$ESCAPE
4138 020774 000010 .WORD 10000$-.
4139 020776 000241 CLC ;CLEAR CARRY
4140 021000 106105 ROLB R5 ;SHIFT BIT IN R5
4141 021002 001341 BNE 64$ ;IF R5=0 THEN DONE
4142 021004 ENDSEG
4143 021004 10000$:
4144 021004 104405 TRAP C$ESEG
4145 021006 012705 000001 MOV #1,R5 ;START WITH BIT 0
4146 021012 BGNSEG
4147 021012 104404 TRAP C$BSEG
4148 021014 67$:
4149 021014 005105 COM R5
4150 021016 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
4151 021022 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4152 021026 122101 122100!1 ;MOV DATA TO IBUS* REGISTER 1
4153 021030 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4154 021034 021025 21005!<1*20> ;READ FROM IBUS* REGISTER 1
4155 021036 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
4156 021042 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
4157 021046 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
4158 021052 001413 BEQ 68$ ;BR IF YES
4159 021054 ERROR 29 ;ERROR
4160 021072 104455 TRAP C$ERDF
```

4161 021074 000035  
4162 021076 006054  
4163 021100 011450  
4164 021102  
4165 021102 104410  
4166 021104 000012  
4167 021106 005105  
4168 021110 000241  
4169 021112 106105  
4170 021114 001337  
4171 021116  
4172 021116  
4173 021116 104405  
4174 021120  
4175 021120  
4176 021120 104401

68\$: .WORD 29  
.WORD EM29  
.WORD ERR29  
ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10077: TRAP C\$ETST

;CHANGE TO FLOATING 1  
;CLEAR CARRY  
;SHIFT BIT IN R5  
;IF R5=0 THEN DONE



```
4177 021122 BADHEAD
4178 :***** TEST 24 *****
4179 :*MICRO PROCESSOR IBUS REGISTER WRITE/READ TEST
4180 :*FLOAT A 1 THROUGH IBUS REGISTER 2
4181 :*FLOAT A 0 THROUGH IBUS REGISTER 2
4182 021122 BADHEAD
4183 :***** TEST 24 *****
4184
4185 021122 BGNST
4186 021122 T24::
4187 021122
4188 021126 012737 000002 002576 MSTCLR ;MASTER CLEAR COMM. MICRO-PROCESSOR FAMILY
4189 021134 012705 000001 MOV #2,MRO ;SAVE REGISTER ADDRESS FOR TYPEOUT
4190 021140 MYINT MOV #1,R5 ;START WITH BIT 0
4191 021144 BGNSEG
4192 021144 104404 TRAP C$BSEG
4193 021146 64$:
4194 021146 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
4195 021152 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4196 021156 122102 122100!2 ;MOV DATA TO IBUS* REGISTER 2
4197 021160 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4198 021164 0 045 21005!<2*20> ;READ FROM IBUS* REGISTER 2
4199 021166 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
4200 021172 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
4201 021176 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
4202 021202 001413 BEQ 65$ ;BR IF YES
4203 021204 ERROR 29 ;ERROR
4204 021222 104455 TRAP C$ERDF
4205 021224 000035 .WORD 29
4206 021226 006054 .WORD EM29
4207 021230 011450 .WORD ERR29
4208 021232 65$: ESCAPE SEG
4209 021232 104410 TRAP C$ESCAPE
4210 021234 000010 .WORD 10000$-.
4211 021236 000241 CLC ;CLEAR CARRY
4212 021240 106105 ROLB R5 ;SHIFT BIT IN R5
4213 021242 001341 BNE 64$ ;IF R5=0 THEN DONE
4214 021244 ENDSEG
4215 021244 10000$:
4216 021244 104405 TRAP C$ESEG
4217 021246 012705 000001 MOV #1,R5 ;START WITH BIT 0
4218 021252 BGNSEG
4219 021252 104404 TRAP C$BSEG
4220 021254 67$:
4221 021254 005105 COM R5
4222 021256 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
4223 021262 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4224 021266 122102 122100!2 ;MOV DATA TO IBUS* REGISTER 2
4225 021270 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4226 021274 021045 21005!<2*20> ;READ FROM IBUS* REGISTER 2
4227 021276 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
4228 021302 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
4229 021306 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
4230 021312 001413 BEQ 68$ ;BR IF YES
4231 021314 ERROR 29 ;ERROR
4232 021332 104455 TRAP C$ERDF
```

4233 021334 000035  
4234 021336 006054  
4235 021340 011450  
4236 021342  
4237 021342 104410  
4238 021344 000012  
4239 021346 005105  
4240 021350 000241  
4241 021352 106105  
4242 021354 001337  
4243 021356  
4244 021356  
4245 021356 104405  
4246 021360  
4247 021360  
4248 021360 104401

68\$: .WORD 29  
.WORD EM29  
.WORD ERR29  
ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10100: TRAP C\$ETST

:CHANGE TO FLOATING 1  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:IF R5=0 THEN DONE

```
4249 021362 BADHEAD
4250 :***** TEST 25 *****
4251 :*MICRO PROCESSOR IBUS REGISTER WRITE/READ TEST
4252 :*FLOAT A 1 THROUGH IBUS REGISTER 3
4253 :*FLOAT A 0 THROUGH IBUS REGISTER 3
4254 021362 BADHEAD
4255 :***** TEST 25 *****
4256
4257 021362 BGNTST
4258 021362 T25::
4259 021362
4260 021366 012737 002903 002576 MSTCLR ;MASTER CLEAR COMM. MICRO-PROCESSOR FAMILY
4261 021374 012705 0000J1 MOV #3,R0 ;SAVE REGISTER ADDRESS FOR TYPEOUT
4262 021400 MYINT #1,R5 ;START WITH BIT 0
4263 021404 BGNSEG
4264 021404 104404 TRAP C$BSEG
4265 021406 64$:
4266 021406 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
4267 021412 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4268 021416 122103 122100!3 ;MOV DATA TO IBUS* REGISTER 3
4269 021420 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4270 021424 021065 21005!<3*20> ;READ FROM IBUS* REGISTER 3
4271 021426 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
4272 021432 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
4273 021436 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
4274 021442 001413 BEQ 65$ ;BR IF YES
4275 021444 ERROR 29 ;ERROR
4276 021462 104455 TRAP C$ERDF
4277 021464 000035 .WORD 29
4278 021466 006054 .WORD EM29
4279 021470 011450 .WORD ERR29
4280 021472 65$:
4281 021472 104410 ESCAPE SEG
4282 021474 000010 TRAP C$ESCAPE
4283 021476 000241 .WORD 10000$-
4284 021500 106105 CLC ;CLEAR CARRY
4285 021502 001341 ROLB 15 ;SHIFT BIT IN R5
4286 021504 ENDSEG 64$ ;IF R5=0 THEN DONE
4287 021504 10000$:
4288 021504 104405 TRAP C$ESEG
4289 021506 012705 000001 MOV #1,R5 ;START WITH BIT 0
4290 021512 BGNSEG
4291 021512 104404 TRAP C$BSEG
4292 021514 67$:
4293 021514 005105 COM R5
4294 021516 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
4295 021522 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4296 021526 122103 122100!3 ;MOV DATA TO IBUS* REGISTER 3
4297 021530 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4298 021534 021065 21005!<3*20> ;READ FROM IBUS* REGISTER 3
4299 021536 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
4300 021542 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
4301 021546 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
4302 021552 001413 BEQ 68$ ;BR IF YES
4303 021554 ERROR 29 ;ERROR
4304 021572 104455 TRAP C$ERDF
```

4305 021574 000035  
4306 021576 006054  
4307 021600 011450  
4308 021602  
4309 021602 104410  
4310 021604 000012  
4311 021606 005105  
4312 021610 000241  
4313 021612 106105  
4314 021614 001337  
4315 021616  
4316 021616  
4317 021616 104405  
4318 021620  
4319 021620  
4320 021620 104401

68\$: .WORD 29  
.WORD EM29  
.WORD ERR29  
ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10101: TRAP C\$ETST

:CHANGE TO FLOATING 1  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:IF R5=0 THEN DONE

```
4321 021622          BADHEAD
4322                :***** TEST 26 *****
4323                :*MICRO PROCESSOR IBUS REGISTER WRITE/READ TEST
4324                :*FLOAT A 1 THROUGH IBUS REGISTER 4
4325                :*FLOAT A 0 THROUGH IBUS REGISTER 4
4326 021622          BADHEAD
4327                :***** TEST 26 *****
4328
4329 021622          BGNST
4330 021622          T26::
4331 021622          MSTCLR                ;MASTER CLEAR COMM. MICRO-PROCESSOR FAMILY
4332 021626 012737 000004 002576      MOV    #4,MRO                ;SAVE REGISTER ADDRESS FOR TYPEOUT
4333 021634 012705 000001                MOV    #1,R5                ;START WITH BIT 0
4334 021640          MYINT
4335 021644          BGNSEG
4336 021644 104404          TRAP    C$BSEG
4337 021646
4338 021646 010561 000004          64$:  MOV    R5,4(R1)          ;PUT PATTERN INTO PORT4
4339 021652          ROMCLK                ;NEXT WORD IS INSTRUCTION, BBN
4340 021656 122104          122100!4          ;MOV DATA TO IBUS* REGISTER 4
4341 021660          ROMCLK                ;NEXT WORD IS INSTRUCTION, BBN
4342 021664 021105          21005!<4*20>          ;READ FROM IBUS* REGISTER 4
4343 021666 010537 002612          MOV    R5,$GDDAT          ;PUT EXPECTED IN $GDDAT
4344 021672 116104 000005          MOVB  5(R1),R4          ;PUT 'FOUND' INTO R4
4345 021676 123704 002612          CMPB  $GDDAT,R4          ;DATA CORRECT?
4346 021702 001413          BEQ   65$                ;BR IF YES
4347 021704          ERROR 29                ;ERROR
4348 021722 104455          TRAP  C$ERDF
4349 021724 000035          .WORD 29
4350 021726 006054          .WORD EM29
4351 021730 011450          .WORD ERR29
4352 021732          65$:  ESCAPE SEG
4353 021732 104410          TRAP  C$ESCAPE
4354 021734 000010          .WORD 10000$-
4355 021736 000241          CLC
4356 021740 106105          ROLB  R5                ;CLEAR CARRY
4357 021742 001341          BNE  64$                ;SHIFT BIT IN R5
4358 021744          ENDSEG                ;IF R5=0 THEN DONE
4359 021744          10000$:
4360 021744 104405          TRAP  C$ESEG
4361 021746 012705 000001          MOV    #1,R5                ;START WITH BIT 0
4362 021752          BGNSEG
4363 021752 104404          TRAP  C$BSEG
4364 021754          67$:
4365 021754 005105          COM   R5
4366 021756 010561 000004          MOV    R5,4(R1)          ;PUT PATTERN INTO PORT4
4367 021762          ROMCLK                ;NEXT WORD IS INSTRUCTION, BBN
4368 021766 122104          122100!4          ;MOV DATA TO IBUS* REGISTER 4
4369 021770          ROMCLK                ;NEXT WORD IS INSTRUCTION, BBN
4370 021774 021105          21005!<4*20>          ;READ FROM IBUS* REGISTER 4
4371 021776 010537 002612          MOV    R5,$GDDAT          ;PUT EXPECTED IN $GDDAT
4372 022002 116104 000005          MOVB  5(R1),R4          ;PUT 'FOUND' INTO R4
4373 022006 123704 002612          CMPB  $GDDAT,R4          ;DATA CORRECT?
4374 022012 001413          BEQ   68$                ;BR IF YES
4375 022014          ERROR 29                ;ERROR
4376 022032 104455          TRAP  C$ERDF
```

4377 022034 000035  
4378 022036 006054  
4379 022040 011450  
4380 022042  
4381 022042 104410  
4382 022044 000012  
4383 022046 005105  
4384 022050 000241  
4385 022052 106105  
4386 022054 001337  
4387 022056  
4388 022056  
4389 022056 104405  
4390 022060  
4391 022060  
4392 022060 104401

68\$: .WORD 29  
.WORD EM29  
.WORD ERR29  
ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-.  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10102: TRAP C\$ETST

:CHANGE TO FLOATING 1  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:IF R5=0 THEN DONE

```
4393 022062 BADHEAD
4394 :***** TEST 27 *****
4395 :*MICRO PROCESSOR IBUS REGISTER WRITE/READ TEST
4396 :*FLOAT A 1 THROUGH IBUS REGISTER 5
4397 :*FLOAT A 0 THROUGH IBUS REGISTER 5
4398 022062 BADHEAD
4399 :***** TEST 27 *****
4400
4401 022062 BGNST
4402 022062 T27::
4403 022062
4404 022066 012737 000005 002576 MSTCLR ;MASTER CLEAR COMM. MICRO-PROCESSOR FAMILY
4405 022074 012705 000001 MOV #5,MRO ;SAVE REGISTER ADDRESS FOR TYPEOUT
4406 022100 MYINT ;START WITH BIT 0
4407 022104 BGNSEG
4408 022104 104404 TRAP C$BSEG
4409 022106 64$:
4410 022106 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
4411 022112 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4412 022116 122105 122100!5 ;MOV DATA TO IBUS* REGISTER 5
4413 022120 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4414 022124 021125 21005!<5*20> ;READ FROM IBUS* REGISTER 5
4415 022126 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
4416 022132 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
4417 022136 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
4418 022142 001413 BEQ 65$ ;BR IF YES
4419 022144 ERROR 29 ;ERROR
4420 022162 104455 TRAP C$ERDF
4421 022164 000035 .WORD 29
4422 022166 006054 .WORD EM29
4423 022170 011450 .WORD ERR29
4424 022172 65$:
4425 022172 104410 ESCAPE SEG
4426 022174 000010 TRAP C$ESCAPE
4427 022176 000241 .WORD 10000$-.
4428 022200 106105 CLC ;CLEAR CARRY
4429 022202 001341 ROLB R5 ;SHIFT BIT IN R5
4430 022204 ENDSEG ;IF R5=0 THEN DONE
4431 022204 10000$:
4432 022204 104405 TRAP C$ESEG
4433 022206 012705 000001 MOV #1,R5 ;START WITH BIT 0
4434 022212 BGNSEG
4435 022212 104404 TRAP C$BSEG
4436 022214 67$:
4437 022214 005105 COM R5
4438 022216 010561 000004 MOV R5,4(R1) ;PUT PATTERN INTO PORT4
4439 022222 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4440 022226 122105 122100!5 ;MOV DATA TO IBUS* REGISTER 5
4441 022230 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4442 022234 021125 21005!<5*20> ;READ FROM IBUS* REGISTER 5
4443 022236 010537 002612 MOV R5,$GDDAT ;PUT EXPECTED IN $GDDAT
4444 022242 116104 000005 MOVB 5(R1),R4 ;PUT 'FOUND' INTO R4
4445 022246 123704 002612 CMPB $GDDAT,R4 ;DATA CORRECT?
4446 022252 001413 BEQ 68$ ;BR IF YES
4447 022254 ERROR 29 ;ERROR
4448 022277 104455 TRAP C$ERDF
```

4449 022274 000035  
4450 022276 006054  
4451 022300 011450  
4452 022302  
4453 022302 104410  
4454 022304 000012  
4455 022306 005105  
4456 022310 000241  
4457 022312 106105  
4458 022314 001337  
4459 022316  
4460 022316  
4461 022316 104405  
4462 022320  
4463 022320  
4464 022320 104401

68\$: .WORD 29  
.WORD EM29  
.WORD ERR29  
ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10103: TRAP C\$ETST

:CHANGE TO FLOATING 1  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:IF R5=0 THEN DONE



```
4465 022322          BADHEAD
4466                :***** TEST 28 *****
4467                :*MICRO PROCESSOR IBUS REGISTER WRITE/READ TEST
4468                :*FLOAT A 1 THROUGH IBUS REGISTER 6
4469                :*FLOAT A 0 THROUGH IBUS REGISTER 6
4470 022322          BADHEAD
4471                :***** TEST 28 *****
4472
4473 022322          BGN1ST
4474 022322          T28::
4475 022322          MSTCLR          ;MASTER CLEAR COMM. MICRO-PROCESSOR FAMILY
4476 022326 012737 000006 002576  MOV #6,MRO          ;SAVE REGISTER ADDRESS FOR TYPEOUT
4477 022334 012705 000001          MOV #1,R5          ;START WITH BIT 0
4478 022340          MYINT
4479 022344          BGNSEG
4480 022344 104404          TRAP C$BSEG
4481 022346
4482 022346 010561 000004          64$: MOV R5,4(R1)          ;PUT PATTERN INTO PORT4
4483 022352          ROMCLK          ;NEXT WORD IS INSTRUCTION, BBN
4484 022356 122106          122100!6 ;MOV DATA TO IBUS* REGISTER 6
4485 022360          ROMCLK          ;NEXT WORD IS INSTRUCTION, BBN
4486 022364 021145          21005!<6*20> ;READ FROM IBUS* REGISTER 6
4487 022366 010537 002612          MOV R5,$GDDAT          ;PUT EXPECTED IN $GDDAT
4488 022372 116104 000005          MOVB 5(R1),R4          ;PUT 'FOUND' INTO R4
4489 022376 123704 002612          CMPB $GDDAT,R4          ;DATA CORRECT?
4490 022402 001413          BEQ 65$          ;BR IF YES
4491 022404          ERROR 29          ;ERROR
4492 022422 104455          TRAP C$ERDF
4493 022424 000035          .WORD 29
4494 022426 006054          .WORD EM29
4495 022430 011450          .WORD ERR29
4496 022432          65$: ESCAPE SEG
4497 022432 104410          TRAP C$ESCAPE
4498 022434 000010          .WORD 10000$-.
4499 022436 000241          CLC          ;CLEAR CARRY
4500 022440 106105          ROLB R5          ;SHIFT BIT IN R5
4501 022442 001341          BNE 64$          ;IF R5=0 THEN DONE
4502 022444          ENDSEG
4503 022444          10000$:
4504 022444 104405          TRAP C$ESEG
4505 022446 012705 000001          MOV #1,R5          ;START WITH BIT 0
4506 022452          BGNSEG
4507 022452 104404          TRAP C$BSEG
4508 022454          67$:
4509 022454 005105          COM R5
4510 022456 010561 000004          MOV R5,4(R1)          ;PUT PATTERN INTO PORT4
4511 022462          ROMCLK          ;NEXT WORD IS INSTRUCTION, BBN
4512 022466 122106          122100!6 ;MOV DATA TO IBUS* REGISTER 6
4513 022470          ROMCLK          ;NEXT WORD IS INSTRUCTION, BBN
4514 022474 021145          21005!<6*20> ;READ FROM IBUS* REGISTER 6
4515 022476 010537 002612          MOV R5,$GDDAT          ;PUT EXPECTED IN $GDDAT
4516 022502 116104 000005          MOVB 5(R1),R4          ;PUT 'FOUND' INTO R4
4517 022506 123704 002612          CMPB $GDDAT,R4          ;DATA CORRECT?
4518 022512 001413          BEQ 68$          ;BR IF YES
4519 022514          ERROR 29          ;ERROR
4520 022532 104455          TRAP C$ERDF
```

4521 022534 000035  
4522 022536 006054  
4523 022540 011450  
4524 022542  
4525 022542 104410  
4526 022544 000012  
4527 022546 005105  
4528 022550 000241  
4529 022552 106105  
4530 022554 001337  
4531 022556  
4532 022556  
4533 022556 104405  
4534 022560  
4535 022560  
4536 022560 104401

68\$: .WORD 29  
.WORD EM29  
.WORD ERR29  
ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-.  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10104: TRAP C\$ETST

:CHANGE TO FLOATING 1  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:IF R5=0 THEN DONE

```
4537 022562 BADHEAD
4538 :***** TEST 29 *****
4539 :*MICRO PROCEOR IBUS* REGISTER WRITE/READ TEST
4540 :*FLOAT A 1 THOUGH IBUS* REGISTER 7
4541 :*FLOAT A 0 THROUGH IBUS* REGISTER 7
4542 022562 BADHEAD
4543 :***** TEST 29 *****
4544
4545 022562 BGNTST
4546 022562 T29::
4547 022562 MSTCLR :MASTER CLEAR COMM. MICRO-PROCESSOR FAMILY
4548 022566 012737 000007 002576 MOV #7,MRO :SAVE REGISTER ADDRESS FOR TYPEOUT
4549 022574 012705 000001 MOV #1,R5 :START WITH BIT 0
4550 022600 MYINT
4551 022604 BGNSEG
4552 022604 104404 TRAP C$BSEG
4553 022606 64$:
4554 022606 010561 000004 MOV R5,4(R1) :PUT PATTERN INTO PORT4
4555 022612 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
4556 022616 122107 122100!7 :MOV DATA TO IBUS* REGISTER 7
4557 022620 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
4558 022624 021165 21005!<7*20> :READ FROM IBUS* REGISTER 7
4559 022626 010537 002612 MOV R5,$GDDAT :PUT EXPECTED IN $GDDAT
4560 022632 116104 000005 MOVB 5(R1),R4 :PUT 'FOUND' INTO R4
4561 022636 123704 002612 CMPB $GDDAT,R4 :DATA CORRECT?
4562 022642 001413 BEQ 65$ :BR IF YES
4563 022644 ERROR 29 :ERROR
4564 022662 104455 TRAP C$ERDF
4565 022664 000035 .WORD 29
4566 022666 006054 .WORD EM29
4567 022670 011450 .WORD ERR29
4568 022672 65$:
4569 022672 104410 ESCAPE SEG
4570 022674 000010 TRAP C$ESCAPE
4571 022676 000241 .WORD 10000$-.
4572 022700 106105 CLC :CLEAR CARRY
4573 022702 001341 ROLB R5 :SHIFT BIT IN R5
4574 022704 BNE 64$ :IF R5=0 THEN DONE
4575 022704 10000$:
4576 022704 104405 TRAP C$ESEG
4577 022706 012705 000001 MOV #1,R5 :START WITH BIT 0
4578 022712 BGNSEG
4579 022712 104404 TRAP C$BSEG
4580 022714 67$:
4581 022714 005105 COM R5
4582 022716 010561 000004 MOV R5,4(R1) :PUT PATTERN INTO PORT4
4583 022722 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
4584 022726 122107 122100!7 :MOV DATA TO IBUS* REGISTER 7
4585 022730 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
4586 022734 021165 21005!<7*20> :READ FROM IBUS* REGISTER 7
4587 022736 010537 002612 MOV R5,$GDDAT :PUT EXPECTED IN $GDDAT
4588 022742 116104 000005 MOVB 5(R1),R4 :PUT 'FOUND' INTO R4
4589 022746 123704 002612 CMPB $GDDAT,R4 :DATA CORRECT?
4590 022752 001413 BEQ 68$ :BR IF YES
4591 022754 ERROR 29 :ERROR
4592 022772 104455 TRAP C$ERDF
```

4593 022774 000035  
4594 022776 006054  
4595 023000 011450  
4596 023002  
4597 023002 104410  
4598 023004 000012  
4599 023006 005105  
4600 023010 000241  
4601 023012 106105  
4602 023014 001337  
4603 023016  
4604 023016  
4605 023016 104405  
4606 023020  
4607 023020  
4608 023020 104401

68\$: .WORD 29  
.WORD EM29  
.WORD ERR29  
ESCAPE SEG  
TRAP C\$ESCAPE  
.WORD 10001\$-.  
COM R5  
CLC  
ROLB R5  
BNE 67\$  
ENDSEG  
10001\$: TRAP C\$ESEG  
ENDTST  
L10105: TRAP C\$ETST

:CHANGE TO FLOATING 1  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:IF R5=0 THEN DONE

```
4609 023022 BADHEAD
4610 :***** TEST 30 *****
4611 :*MICRO PROCESSOR IBUS DUAL ADDRESS TEST
4612 :*WRITE ALL IBUS REGISTERS WITH INCREMENTING PATTERN
4613 :*READ ALL IBUS REGISTERS TO VERIFY CORRECT ADDRESSING
4614 023022 BADHEAD
4615 :***** TEST 30 *****
4616
4617 023022 BGNTST
4618 023022 T30::
4619 023022 MSTCLR ;MASTER CLEAR COMM. MICRO-PROCESSOR FAMILY
4620 023026 012705 000001 MOV #1,R5 ;START WITH A ONE
4621 023032 005002 CLR R2 ;R2 CONTAINS ADDRESS OF REGISTER
4622 023034 MYINT
4623 023040 BGNSEG
4624 023040 104404 TRAP C$BSEG
4625 023042 010203 1$: MOV R2,R3 ;R3=REGISTER ADDRESS
4626 023044 010561 000004 MOV R5,4(R1) ;WRITE DATA TO PORT4
4627 023050 042737 000017 023066 BIC #17,5$ ;CLEAR ADDRESS FIELD OF INSTRUCTION
4628 023056 050337 023066 BIS R3,5$ ;ADD ADDRESS TO INSTRUCTION
4629 023062 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4630 023066 122100 5$: MOV R2,R3 ;MOVE DATA TO IBUS REGISTER
4631 023070 006303 ASL R3 ;SHIFT ADDRESS
4632 023072 006303 ASL R3 ;4 TIMES TO GET
4633 023074 006303 ASL R3 ;IT TO BITS 4-7
4634 023076 006303 ASL R3 ;OF NEXT INSTRUCTION
4635 023100 042737 000360 023116 BIC #360,6$ ;CLEAR ADDRESS FIELD
4636 023106 050337 023116 BIS R3,6$ ;ADD ADDRESS TO INSTRUCTION
4637 023112 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
4638 023116 021005 6$: MOV R5,$GDDAT ;READ FROM IBUS REGISTER
4639 023120 010537 002612 MOV 5(R1),R4 ;PUT 'EXPECTED' IN $GDDAT
4640 023124 116104 000005 MOV R5,R4 ;PUT 'FOUND' IN R4
4641 023130 123704 002612 CMPB $GDDAT,R4 ;IS DATA CORRECT?
4642 023134 001413 BEQ 2$ ;BR IF YES
4643 023136 ERROR 29 ;DATA ERROR
4644 023154 104455 TRAP C$ERDF
4645 023156 000035 .WORD 29
4646 023160 006054 .WORD EM29
4647 023162 011450 .WORD ERR29
4648 023164 2$: ESCAPE SEG
4649 023164 104 10 TRAP C$ESCAPE
4650 023166 00LJ14 .WORD 10000$-.
4651 023170 005205 INC R5 ;INCREMENT PATTERN
4652 023172 005202 INC R2 ;INCREMENT REGISTER ADDRESS
4653 023174 022702 000010 CMP #7+1,R2 ;LAST ADDRESS DONE?
4654 023200 001320 BNE 1$ ;BR IF NO
4655 023202 ENDSEG
4656 023202 10000$:
4657 023202 104405 TRAP C$ESEG
4658 023204 012705 000001 MOV #1,R5 ;RESTART PATTERN TO 1
4659 023210 005002 CLR R2 ;RESTART AT ADDRESS 0
4660 023212 BGNSEG
4661 023212 104404 TRAP C$BSEG
4662 023214 005003 CLR R3 ;RESTART AT ADDRESS 0
4663 023216 042737 000360 023234 3$: BIC #360,7$ ;CLEAR ADDRESS FIELD OF INSTRUCTION
4664 023224 050337 023234 BIS R3,7$ ;ADD ADDRESS TO INSTRUCTION
```

4665	023230		
4666	023234	021005	
4667	023236	010537	002612
4668	023242	116104	000005
4669	023246	123704	002612
4670	023252	001413	
4671	023254		
4672	023272	104455	
4673	023274	000036	
4674	023276	004622	
4675	023300	011562	
4676	023302		
4677	023302	104410	
4678	023304	000020	
4679	023306	005205	
4680	023310	005202	
4681	023312	062703	000020
4682	023316	022702	000010
4683	023322	001335	
4684	023324		
4685	023324		
4686	023324	104405	
4687	023326		
4688	023326		
4689	023326	104401	

7\$:	ROMCLK		:NEXT WORD IS INSTRUCTION, BBN
	21005		:READ FROM IBUS REGISTER
	MOV R5,\$GDDAT		:PUT 'EXPECTED' IN \$GDDAT
	MOVB 5(R1),R4		:PUT 'FOUND' IN \$GDDAT
	CMPB \$GDDAT,R4		:DATA CORRECT?
	BEO 4\$		:BR IF YES
	ERROR 30		:DUAL ADDRESSING ERROR
	TRAP C\$ERDF		
	.WORD 30		
	.WORD EM3C		
	.WORD ERR30		
4\$:	ESCAPE SEG		
	TRAP C\$ESCAPE		
	.WORD 10001\$-		
	INC R5		:INCREMENT PATTERN
	INC R2		:NEXT ADDRESS
	ADD #20,R3		:ADD 1 TO ADDRESS IN R3(SHIFTED 4 TIMES)
	CMP #7+1,R2		:LAST ADDRESS DONE?
	BNE 3\$		:BR IF NO
	ENDSEG		
10001\$:			
	TRAP C\$ESEG		
ENDTST			
L10106:	TRAP C\$ETST		

```
4690 023330 BADHEAD
4691 :***** TEST 31 *****
4692 :*IBUS*0-10,1-11,3-13 DUAL ADDRESS TEST
4693 :*READ IBUS*10,11,13 AND SAVE VALUES
4694 :*WRITE IBUS*0,1,3 WITH THE RESPECTIVE COMPLEMENTS OF THESE VALUES
4695 :*CHECK IBUS*10,11,13 FOR ANY CHANGES
4696 023330 BADHEAD
4697 :***** TEST 31 *****
4698
4699 023330 BGNSTST
4700 023330 T31::
4701 023330 MSTCLR ; CLEAR M8206
4702 023334 MYINT ;
4703 023340 005037 002632 CLR SIBS10 ; CLEAR SAVE REGISTERS
4704 023344 005037 002634 CLR SIBS11 ;
4705 023350 005037 002640 CLR SIBS13 ;
4706 023354 ROMCLK
4707 023360 121205 121005!<10*20> ; READ IBUS* 10
4708 023362 116137 000005 002632 MOV 5(R1),SIBS10 ; SAVE IT
4709 023370 ROMCLK
4710 023374 121225 121005!<11*20> ; READ IBUS* 11
4711 023376 116137 000005 002634 MOV 5(R1),SIBS11 ; SAVE IT
4712 023404 ROMCLK
4713 023410 121265 121005!<13*20> ; READ IBUS* 13
4714 023412 116137 000005 002640 MOV 5(R1),SIBS13 ; SAVVE IT
4715 023420 013705 002632 1$: MOV SIBS10,R5 ; GET DATA
4716 023424 005105 COM R5 ; MAKE OPPOSITE
4717 023426 110561 000004 MOV R5,4(R1) ; LOAD 0
4718 023432 ROMCLK
4719 023436 121100 121100
4720 023440 012705 000070 MOV #70,R5 ; LEGAL BITS
4721 023444 110561 000004 MOV R5,4(R1) ; LOAD 1
4722 023450 ROMCLK
4723 023454 121101 121101
4724 023456 013705 002640 MOV SIBS13,R5 ; GET DATA
4725 023462 005105 COM R5 ; MAKE OPPOSITE
4726 023464 110561 000004 MOV R5,4(R1) ; LOAD 3
4727 023470 ROMCLK
4728 023474 121103 121103
4729 023476 012737 000010 002576 2$: MOV #10,MRO ; CHECK 10
4730 023504 013737 002632 002612 MOV SIBS10,$GDDAT ; EXPECTED
4731 023512 ROMCLK
4732 023516 121205 121005!<10*20> ; READ 10
4733 023520 116104 000005 MOV 5(R1),R4 ; GET DATA
4734 023524 123704 002612 CMPB $GDDAT,R4 ; CHECK IT
4735 023530 001413 BEQ 3$ ; SKIP IF OK
4736 023532 ERROR 27
4737 023550 104455 TRAP C$ERDF
4738 023552 000033 .WORD 27
4739 023554 005774 .WORD EM27
4740 023556 011230 .WORD ERR27
4741 023560 012737 000011 002576 3$: MOV #11,MRO ; CHECK 11
4742 023566 013737 002634 002612 MOV SIBS11,$GDDAT ; EXPECTED
4743 023574 ROMCLK
4744 023600 121225 121005!<11*20>
4745 023602 115104 000005 MOV 5(R1),R4 ; GET DATA
```

4746	023606	123704	002612			CMPB	\$GDDAT,R4		: CHECK IT
4747	023612	001413				BEQ	4\$		: SKIP IF OK
4748	023614					ERROR	27		
4749	023632	104455				TRAP	C\$ERDF		
4750	023634	000033				.WORD	27		
4751	023636	005774				.WORD	EM27		
4752	023640	011230				.WORD	ERR27		
4753	023642	012737	000013	002576	4\$:	MOV	#13,MRO		: CHECK 13
4754	023650	013737	002640	002612		MOV	SIBS13,\$GDDAT		: EXPECTED
4755	023656					ROMCLK			
4756	023662	121265				121005!	<13*20>		: READ 13
4757	023664	116104	000005			MOVB	5(R1),R4		: GET DATA
4758	023670	123704	002612			CMPB	\$GDDAT,R4		: CHECK IT
4759	023674	001413				BEQ	5\$		: SKIP IF OK
4760	023676					ERROR	27		
4761	023714	104455				TRAP	C\$ERDF		
4762	023716	000033				.WORD	27		
4763	023720	005774				.WORD	EM27		
4764	023722	011230				.WORD	ERR27		
4765	023724				5\$:	ENDTST			
4766	023724				L10107:				
4767	023724	104401				TRAP	C\$ETST		



4768 023726  
4769  
4770  
4771 023726  
4772  
4773  
4774 023726  
4775 023726  
4776 023726  
4777 023732  
4778 023736 005037 002630  
4779 023742 012761 0C0020 000004  
4780 023750  
4781 023754 121111  
4782 023756  
4783 023756 012727 000001  
4784 023762 000000  
4785 023764 013727 002116  
4786 023770 000000  
4787 023772 005367 177772  
4788 023776 001375  
4789 024000 005367 177756  
4790 024004 001367  
4791 024006  
4792 024012 121225  
4793 024014 132761 000020 000005  
4794 024022 001140  
4795 024024  
4796 024024 012727 000372  
4797 024030 000000  
4798 024032 013727 002116  
4799 024036 000000  
4800 024040 005367 177772  
4801 024044 001375  
4802 024046 005367 177756  
4803 024052 001367  
4804 024054  
4805 024054 012727 000372  
4806 024060 000000  
4807 024062 013727 002116  
4808 024066 000000  
4809 024070 005367 177772  
4810 024074 001375  
4811 024076 005367 177756  
4812 024102 001367  
4813 024104  
4814 024104 012727 000372  
4815 024110 000000  
4816 024112 013727 002116  
4817 024116 000000  
4818 024120 005367 177772  
4819 024124 001375  
4820 024126 005367 177756  
4821 024132 001367  
4822 024134  
4823 024134 012727 000372

BADHEAD  
:\*\*\*\*\* TEST 32 \*\*\*\*\*  
:\*TEST THE DELAY ON PASS 1 AND REPORT ITS VALUE  
BADHEAD  
:\*\*\*\*\* TEST 32 \*\*\*\*\*

BGNTST  
T32::

MSTCLR ; CLEAR M8206  
MYINT  
CLR SCLK ; CLEAR FLAG  
MOV #20,4(R1) ; HIT TIMER  
ROMCLK  
121111  
DELAY 1 ; WAIT 100 USEC  
MOV #1,(PC)+  
.WORD 0  
MOV L\$DLY,(PC)+  
.WORD 0  
DEC -6(PC)  
BNE -4  
DEC -22(PC)  
BNE -20  
ROMCLK  
121005!<11\*20> ; READ IBUS\* 11  
BITB #20,5(R1) ; CHECK CLOCK  
BNE 25 ; SKIP IF SET  
DELAY 250 ; WAIT 25 MSEC  
MOV #250.,(PC)+  
.WORD 0  
MOV L\$DLY,(PC)+  
.WORD 0  
DEC -6(PC)  
BNE -4  
DEC -22(PC)  
BNE -20  
DELAY 250 ; WAIT 25 MSEC  
MOV #250.,(PC)+  
.WORD 0  
MOV L\$DLY,(PC)+  
.WORD 0  
DEC -6(PC)  
BNE -4  
DEC -22(PC)  
BNE -20  
DELAY 250 ; WAIT 25 MSEC  
MOV #250.,(PC)+

4824	024140	000000			.WORD	0	
4825	024142	013727	002116		MOV	LSDLY,(PC)+	
4826	024146	000000			.WORD	0	
4827	024150	005367	177772		DEC	-6(PC)	
4828	024154	001375			BNE	-4	
4829	024156	005367	177756		DEC	-22(PC)	
4830	024162	001367			BNE	-20	
4831	024164				DELAY	250.	; WAIT 25 MSEC
4832	024164	012727	000372		MOV	#250.,(PC)+	
4833	024170	000000			.WORD	0	
4834	024172	013727	002116		MOV	LSDLY,(PC)+	
4835	024176	000000			.WORD	0	
4836	024200	005367	17.772		DEC	-6(PC)	
4837	024204	001375			BNE	-4	
4838	024206	005367	177756		DEC	-22(PC)	
4839	024212	001367			BNE	-20	
4840	024214				DELAY	250.	; WAIT 25 MSEC
4841	024214	012727	000372		MOV	#250.,(PC)+	
4842	024220	000000			.WORD	0	
4843	024222	013727	002116		MOV	LSDLY,(PC)+	
4844	024226	000000			.WORD	0	
4845	024230	005367	177772		DEC	-6(PC)	
4846	024234	001375			BNE	-4	
4847	024236	005367	177756		DEC	-22(PC)	
4848	024242	001367			BNE	-20	
4849	024244				ROMCLK		
4850	024250	121225			121005!<11*20>		; READ IBUS* 11
4851	024252	132761	000020	000005	BITB	#20,5(R1)	; CHECK CLOCK
4852	024260	001404			BEQ	1\$	; NOT SET
4853	024262	012737	000001	002630	MOV	#1,SCLK	; SET SLOW FLAG
4854	024270	000415			BR	2\$	; LONG DELAY
4855	024272				ERROR	27	; CLOCK DID NOT SET
4856	024310	104455			TRAP	C\$ERDF	
4857	024312	000033			.WORD	27	
4858	024314	005774			.WORD	EM27	
4859	024316	011230			.WORD	ERR27	
4860	024320				ESCAPE	TST	
4861	024320	104410			TRAP	C\$ESCAPE	
4862	024322	000066			.WORD	L10110-	
4863	024324	005737	002604		TST	PONE	; SKIP IF NOT FIRST PASS
4864	024330	001027			BNE	4\$	
4865	024332	012737	177777	002604	MOV	#-1,PONE	; SET SWITCH
4866	024340	005737	002630		TST	SCLK	; FAST OR SLOW?
4867	024344	001011			BNE	3\$	; SKIP IF SLOW
4868	024346				PRINTB	#FMSG	
4869	024346	012746	004407		MOV	#FMSG,-(SP)	
4870	024352	012746	000001		MOV	#1,-(SP)	
4871	024356	010600			MOV	SP,RO	
4872	024360	104414			TRAP	C\$PNTB	
4873	024362	062706	000004		ADD	#4,SP	
4874	024366	000410			BR	4\$	
4875	024370				PRINTB	#SMSG	
4876	024370	012746	004457		MOV	#SMSG,-(SP)	
4877	024374	012746	000001		MOV	#1,-(SP)	
4878	024400	010600			MOV	SP,RO	
4879	024402	104414			TRAP	C\$PNTB	

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SEQ 0115

4880 024404 062706 000004  
4881 024410  
4882 024410  
4883 024410 104401

48: ADD #4.SP  
L10110: ENDTST  
TRAP CSETST

```

4884 024412          BADHEAD
4885                :***** TEST 33 *****
4886                :*MICRO PROCESSOR BR REGISTER TEST
4887                :*FLOAT A 1 THOUGH THE BR
4888                :*FLOAT A 0 THOUGH THE BR
4889 024412          BADHEAD
4890                :***** TEST 33 *****
4891
4892 024412          BGNTST
4893 024412          T33::
4894
4895 024412          MSTCLR          ;R1 CONTAINS BASE COMM. MICRO-PROCESSOR FAMILY ADDRESS
4896 024416 012705 000001      MOV          #1,R5          ;MASTER CLEAR COMM. MICRO-PROCESSOR FAMILY
4897 024422          MYINT          ;START PATTERN WITH BIT0
4898 024426          BGNSEG
4899 024426 104404          TRAP          C$BSEG
4900 024430          64$:
4901 024430 010561 000004      MOV          R5,4(R1)      ;WRITE PATTERN IN PORT4
4902 024434          ROMCLK          ;NEXT WORD IS INSTRUCTION, BBN
4903 024440 120500          120500      ;MOVE DATA TO THE BR REGISTER
4904 024442          ROMCLK          ;NEXT WORD IS INSTRUCTION, BBN
4905 024446 061225          061225      ;MOVE BR TO PORT 5
4906 024450 010537 002612      MOV          R5,$GDDAT      ;PUT 'EXPECTED' IN $GDDAT
4907 024454 116104 000005      MOVB         5(R1),R4      ;PUT 'FOUND' IN R4
4908 024460 123704 002612      CMPB         $GDDAT,R4    ;IS DATA CORRECT?
4909 024464 001413          BEQ          65$          ;BR IF YES
4910 024466          ERROR          3          ;DATA ERROR
4911 024504 104455          TRAP          C$ERDF
4912 024506 000003          .WORD          3
4913 024510 004665          .WORD          EM3
4914 024512 006624          .WORD          ERR3
4915 024514          65$:
4916 024514 104410          ESCAPE          SEG
4917 024516 000010          TRAP          C$ESCAPE
4918 024520 000241          .WORD          10000$-.
4919 024522 106105          CLC          ;CLEAR CARRY
4920 024524 001341          ROLB          R5          ;SHIFT BIT IN R5
4921 024526          BNE          64$          ;DONE IF R5=0
4922 024526          ENDSEG
4923 024526 104405          10000$:
4924 024530 012705 000001      TRAP          C$ESEG
4925 024534          69$:
4926 024534          MOV          #1,R5          ;START PATTERN WITH BIT0
4927 024534 104404          BGNSEG
4928 024536          TRAP          C$BSEG
4929 024536 005105          67$:
4930 024540 010561 000004      COM          R5
4931 024544          MOV          R5,4(R1)      ;WRITE PATTERN IN PORT4
4932 024550 120500          ROMCLK          ;NEXT WORD IS INSTRUCTION, BBN
4933 024552          120500      ;MOVE DATA TO THE BR REGISTER
4934 024556 061225          ROMCLK          ;NEXT WORD IS INSTRUCTION, BBN
4935 024560 010537 002612      MOV          R5,$GDDAT      ;MOVE BR TO PORT 5
4936 024564 116104 000005      MOVB         5(R1),R4      ;PUT 'EXPECTED' IN $GDDAT
4937 024570 123704 002612      CMPB         $GDDAT,R4    ;PUT 'FOUND' IN $GDDAT
4938 024574 001413          BEQ          68$          ;DATA CORRECT?
4939 024576          ERROR          3          ;BR IF YES
                                ;DATA ERROR

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4940	024614	104455		TRAP	C\$ERDF	
4941	024616	000003		.WORD	3	
4942	024620	004665		.WORD	EM3	
4943	024622	006624		.WORD	ERR3	
4944	024624		68\$:	ESCAPE	SEG	
4945	024624	104410		TRAP	C\$ESCAPE	
4946	024626	000070		.WORD	10001\$-	
4947	024630	052711	040000	BIS	#40000,(R1)	:SET MASTER CLEAR
4948	024634	042711	040000	BIC	#40000,(R1)	:CLEAR IT
4949	024640			ROMCLK	061225	:PUT BR IN PORT5
4950	024644	061225		MOVW	5(R1),R4	:READ IT
4951	024646	116104	000005	BEQ	70\$	:IF ZERO, OK
4952	024652	001415		CLR	R5	
4953	024654	005005		ERROR	3	:MASTER CLEAR
4954	024656			TRAP	C\$ERDF	
4955	024674	104455		.WORD	3	
4956	024676	000003		.WORD	EM3	
4957	024700	004665		.WORD	ERR3	
4958	024702	006624		CKLOOP		
4959	024704			TRAP	C\$CLP1	
4960	024704	104406				:FAILED TO CLEAR
4961						:BRG
4962	024706		70\$:	COM	R5	:CHANGE BACK TO A ONE
4963	024706	005105		CLC		:CLEAR CARRY
4964	024710	000241		ROLB	R5	:SHIFT BIT IN R5
4965	024712	106105		BNE	67\$	:DONE IF R5=0
4966	024714	001310		ENDSEG		
4967	024716					
4968	024716		10001\$:	TRAP	C\$ESEG	
4969	024716	104405				
4970	024720		ENDTST			
4971	024720		L10111:	TRAP	C\$ETST	
4972	024720	104401				
4973						
4974	024722			BADHEAD		
4975				:***** TEST 34 *****		
4976				:*SCRATCH PAD TEST		
4977				:*FLOAT A 1 THOUGH EACH SCRATCH PAD LOCATION		
4978				:*FLOAT A 0 THOUGH EACH SCRATCH PAD LOCATION		
4979	024722			BADHEAD		
4980				:***** TEST 34 *****		
4981						
4982	024722		BGNTST			
4983	024722		T34::			
4984	024722			MYINT		
4985	024726			MSTCLR		:MASTER CLEAR COMM. MICRO-PROCESSOR FAMILY
4986	024732	005002		CLR	R2	:START AT ADDRESS ZERO
4987	024734	012705	000001	MOV	#1,R5	:START WITH BIT0
4988	024740			BGNSUB		
4989	024740		T34.1:			
4990	024740	104402		TRAP	C\$SUB	
4991	024742		1\$:	BGNSEG		
4992	024742	104404		TRAP	C\$BSEG	
4993	024744	042737	000017	BIC	#17,65\$	:CLEAR ADDRESS FIELD OF INSTRUCTION
4994	024752	050237	024766	BIS	R2,65\$	:ADD ADDRESS TO INSTRUCTION
4995	024756	010561	000004	MOV	R5,4(R1)	:WRITE PATTERN IN PORT4

Address	Instruction	Comments
4996	024762	ROMCLK
4997	024766 123100	123100
4998	024770 042737 000017 025006	65\$: BIC #17,66\$
4999	024776 050237 025006	BIS R2,66\$
5000	025002	ROMCLK
5001	025006 040600	66\$: 040600
5002	025010	ROMCLK
5003	025014 061225	061225
5004	025016 010537 002612	MOV R5,\$GDDAT
5005	025022 116104 000005	MOV 5(R1),R4
5006	025026 123704 002612	CMPB \$GDDAT,R4
5007	025032 001413	BEQ 67\$
5008	025034	ERROR 4
5009	025052 104455	TRAP C\$ERDF
5010	025054 000004	.WORD 4
5011	025056 004713	.WORD EM4
5012	025060 006732	.WORD ERR4
5013	025062	67\$: ESCAPE SEG
5014	025062 104410	TRAP C\$ESCAPE
5015	025064 000010	.WORD 10000\$-
5016	025066 000241	CLC
5017	025070 106105	ROLB R5
5018	025072 001324	BNE 64\$
5019	025074	ENDSEG
5020	025074	10000\$:
5021	025074 104405	TRAP C\$ESEG
5022	025076 012705 000001	MOV #1,R5
5023	025102	BGNSEG
5024	025102 104404	TRAP C\$BSEG
5025		
5026	025104 005105	73\$: COM R5
5027	025106 042737 000017 025130	69\$: BIC #17,70\$
5028	025114 050237 025130	BIS R2,70\$
5029	025120 010561 000004	MOV R5,4(R1)
5030	025124	ROMCLK
5031	025130 123100	70\$: 123100
5032	025132 042737 000017 025150	BIC #17,71\$
5033	025140 050237 025150	BIS R2,71\$
5034	025144	ROMCLK
5035	025150 040600	71\$: 040600
5036	025152	ROMCLK
5037	025156 061225	061225
5038	025160 010537 002612	MOV R5,\$GDDAT
5039	025164 116104 000005	MOV 5(R1),R4
5040	025170 123704 002612	CMPB \$GDDAT,R4
5041	025174 001413	BEQ 72\$
5042	025176	ERROR 4
5043	025214 104455	TRAP C\$ERDF
5044	025216 000004	.WORD 4
5045	025220 004713	.WORD EM4
5046	025222 006732	.WORD ERR4
5047	025224	72\$: ESCAPE TST
5048	025224 104410	TRAP C\$ESCAPE
5049	025226 000032	.WORD L10112-
5050	025230 005105	CCM R5
5051	025232 000241	CLC

:NEXT WORD IS INSTRUCTION, BBN  
:WRITE SCRATCH PAD(ADDRESS IN R2)  
:CLEAR ADDRESS FIELD OF INSTRUCTION  
:ADD ADDRESS TO INSTRUCTION  
:NEXT WORD IS INSTRUCTION, BBN  
:MOVE SP TO BR  
:NEXT WORD IS INSTRUCTION, BBN  
:MOVE BR TO PORT5  
:PUT 'EXPECTED' IN \$GDDAT  
:PUT 'FOUND' IN R4  
:DATA CORRECT  
:BR IF YES  
:DATA ERROR  
:CLEAR CARRY  
:SHIFT BIT IN R5  
:DONE IF R5=0  
:START WITH BIT0  
:CHANGE TO FLOATING ZERO  
:CLEAR ADDRESS FIELD OF INSTRUCTION  
:ADD ADDRESS TO INSTRUCTION  
:WRITE PATTERN IN PORT4  
:NEXT WORD IS INSTRUCTION, BBN  
:WRITE SCRATCH PAD(ADDRESS IN R2)  
:CLEAR ADDRESS FIELD OF INSTRUCTION  
:ADD ADDRESS TO INSTRUCTION  
:NEXT WORD IS INSTRUCTION, BBN  
:MOVE SP TO BR  
:NEXT WORD IS INSTRUCTION, BBN  
:MOVE BR TO PORT5  
:PUT 'EXPECTED' IN \$GDDAT  
:PUT 'FOUND' IN \$GDDAT  
:DATA CORRECT?  
:BR IF YES  
:DATA ERROR  
:CHANGE BACK TO A ONE  
:CLEAR CARRY

5052	025234	106105				ROLB	R5		:SHIFT BIT IN R5
5053	025236	001322				BNE	73\$		:DONE IF R5=0
5054	025240					ENDSEG			
5055	025240					10001\$:			
5056	025240	104405				TRAP	C\$ESEG		
5057	025242	012705	000001			MOV	#1,R5		:RESTART AT BIT 0
5058	025246	005202				INC	R2		:NEXT SP ADDRESS
5059	025250	022702	000020			CMP	#20,R2		:LAST ADDRESS?
5060	025254	001232				BNE	1\$		:BR IF NO
5061	025256					ENDSUB			
5062	025256					L10113:			
5063	025256	104403				TRAP	C\$ESUB		
5064	025260					ENDTST			
5065	025260					L10112:			
5066	025260	104401				TRAP	C\$ETST		
5067									
5068	025262					BADHEAD			
5069						:***** TEST 35 *****			
5070						:*SCRATCH PAD DUAL ADDRESSING TEST			
5071						:*WRITE AN INCREMENTING PATTERN IN ALL SP LOCATIONS			
5072						:*READ ALL SP LOCATIONS TO VERIFY CORRECT ADDRESSING			
5073	025262					BADHEAD			
5074						:***** TEST 35 *****			
5075									
5076	025262					BGNTST			
5077	025262					T35::			
5078	025262					MSTCLR			:MASTER CLEAR COMM. MICRO-PROCESSOR FAMILY
5079	025266	012705	000001			MOV	#1,R5		:START WITH A 1
5080	025272	005003				CLR	R3		:ADDRESS 0
5081	025274					MYINT			
5082	025300					BGNSEG			
5083	025300	104404				TRAP	C\$BSEG		
5084	025302	010302				1\$:			
5085	025304	042737	000017	025326		MOV	R3,R2		:MOVE ADDRESS TO R2
5086	025312	050237	025326			BIC	#17,2\$		:CLEAR ADDRESS FIELD
5087	025316	010561	000004			BIS	R2,2\$		:ADD ADDRESS TO INSTRUCTION
5088	025322					MOV	R5,4(R1)		:WRITE PATTERN IN PORT4
5089	025326	123100				ROMCLK			:NEXT WORD IS INSTRUCTION, BBN
5090	025330	042737	000017	025346		2\$:	123100		:WRITE SP(ADDRESS IN R2)
						BIC	#17,3\$		:CLEAR ADDRESS FIELD OF INSTRUCTION

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SEQ 0120

5091 025336 050237 025346  
5092 025342

BIS R2,3\$  
ROMCLK

:ADD ADDRESS TO INSTRUCTION  
:NEXT WORD IS INSTRUCTION, BBN



5093	025346	060600		3\$:	60600		:MOVE SP TO BR	
5094	025350				ROMCLK		:NEXT WORD IS INSTRUCTION, BBN	
5095	025354	061225			61225		:MOVE BR TO PORT5	
5096	025356	010537	002612		MOV	R5,\$GDDAT	:PUT 'EXPECTED' IN \$GDDAT	
5097	025362	116104	000005		MOV	5(R1),R4	:PUT 'FOUND' IN R4	
5098	025366	123704	002612		CMP	\$GDDAT,R4	:DATA CORRECT	
5099	025372	001413			BEQ	4\$	:BR IF YES	
5100	025374				ERROR	4	:DATA ERROR	
5101	025412	104455			TRAP	C\$ERDF		
5102	025414	000004			.WORD	4		
5103	025416	004713			.WORD	EM4		
5104	025420	006732			.WORD	ERR4		
5105	025422			4\$:	ESCAPE	SEG		
5106	025422	104410			TRAP	C\$ESCAPE		
5107	025424	000014			.WORD	10000\$-		
5108	025426	005205			INC	R5	:INCREMENT PATTERN	
5109	025430	005203			INC	R3	:NEXT ADDRESS	
5110	025432	022703	000020		CMP	#20,R3	:LAST ADDRESS DONE?	
5111	025436	001321			BNE	1\$	:BR IF NO	
5112	025440				ENDSEG			
5113	025440			10000\$:				
5114	025440	104405			TRAP	C\$ESEG		
5115	025442	012705	000001		MOV	#1,R5	:RESTART PATTERN AT 1	
5116	025446	005003			CLR	R3	:RESTART AT ADDRESS ZERO	
5117	025450				BGNSEG			
5118	025450	104404			TRAP	C\$BSEG		
5119	025452	010302		5\$:	MOV	R3,R2	:PUT ADDRESS IN R2	
5120	025454	042737	000017	025472	69\$:	BIC	#17,6\$	:CLEAR ADDRESS FIELD OF INSTRUCTION
5121	025462	050237	025472		BIS	R2,6\$	:ADD ADDRESS TO INSTRUCTION	
5122	025466				ROMCLK		:NEXT WORD IS INSTRUCTION, BBN	
5123	025472	060600		6\$:	60600		:MOVE SP TO BR	
5124	025474				ROMCLK		:NEXT WORD IS INSTRUCTION, BBN	
5125	025500	061225			61225		:MOVE BR TO PORT5	
5126	025502	010537	002612		MOV	R5,\$GDDAT	:PUT 'EXPECTED' IN \$GDDAT	
5127	025506	116104	000005		MOV	5(R1),R4	:PUT 'FOUND' IN \$GDDAT	
5128	025512	123704	002612		CMP	\$GDDAT,R4	:DATA CORRECT?	
5129	025516	001413			BEQ	7\$	:BR IF YES	
5130	025520				ERROR	5	:SP ADDRESSING ERROR	
5131	025536	104455			TRAP	C\$ERDF		
5132	025540	000005			.WORD	5		
5133	025542	004741			.WORD	EM5		
5134	025544	007044			.WORD	ERR5		
5135	025546			7\$:	ESCAPE	SEG		
5136	025546	104410			TRAP	C\$ESCAPE		
5137	025550	000014			.WORD	10001\$-		
5138	025552	005205			INC	R5	:INCREMENT PATTERN	
5139	025554	005203			INC	R3	:NEXT ADDRESS	
5140	025556	022703	000020		CMP	#20,R3	:LAST ADDRESS DONE?	
5141	025562	001333			BNE	5\$	:BR IF NO	
5142	025564				ENDSEG			
5143	025564			10001\$:				
5144	025564	104405			TRAP	C\$ESEG		
5145	025566			ENDTST				
5146	025566			L10114:				
5147	025566	104401			TRAP	C\$ETST		
5148								

```
5149 025570          BADHEAD
5150                :***** TEST 36 *****
5151                :*INTERRUPT TEST
5152                :*TEST THAT DEVICE CAN INTERRUPT TO VECTOR A
5153 025570          BADHEAD
5154                :***** TEST 36 *****
5155
5156 025570          BGNTST
5157 025570          T36::
5158 025570          MYINT
5159 025574 004737 004146 JSR PC,PRDEL ; WAIT FOR ANY PRINTOUTS TO CLEAR TERMINAL
5160 025600 000005 RESET ;BUS RESET
5161 025602 005011 CLR (R1) ;CLEAR RUN
5162 025604 004537 003604 JSR R5,SETVEC ;SET UP VECTORS
5163 025610 025730 3$ ;XX0
5164 025612 025702 2$ ;XX4
5165 025614 000340 000340 .WORD 340,340 ;LEVEL 7
5166 025620 1$: SETPRI #PRI07 ;PS = LEVEL 7
5167 025620 012700 000340 MOV #PRI07,R0
5168 025624 104441 TRAP C$SPRI
5169 025626 012761 000200 000004 MOV #200,4(R1) ;WRITE PORT4
5170 025634 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
5171 025640 121111 121111 ;SET BR R0 IN IBUS* REG 11
5172 025642 SETPRI #PRI00 ;ALLOW INTERRUPT
5173 025642 012700 000000 MOV #PRI00,R0
5174 025646 104441 TRAP C$SPRI
5175 025650 000240 NOP
5176 025652 ERROR 31 ;NO INTERRUPT
5177 025670 104455 TRAP C$ERDF
5178 025672 000037 .WORD 31
5179 025674 005561 .WORD EM31
5180 025676 011670 .WORD ERR31
5181 025700 000415 BR 4$
5182 025702 2$: ERROR 32 ;WRONG VECTOR
5183 025720 104455 TRAP C$ERDF
5184 025722 000040 .WORD 32
5185 025724 003610 .WORD EM32
5186 025726 011746 .WORD ERR32
5187 025730 062706 000004 3$: ADD #4,SP ;RESET STACK
5188 025734 4$:
5189 025734 ENDTST
5190 025734 L10115:
5191 025734 104401 TRAP C$ETST
5192
5193 025736          BADHEAD
5194                :***** TEST 37 *****
5195                :*INTERRUPT TEST
5196                :*TEST THAT DEVICE CAN INTERRUPT TO VECTOR B
5197 025736          BADHEAD
5198                :***** TEST 37 *****
5199
5200 025736          BGNTST
5201 025736          T37::
5202 025736          MYINT
5203 025742 004537 003604 MSTCLR ;MASTER CLEAR M8200,4,6,7
5204 025746 JSR R5,SETVEC ;SET UP VECTORS
```

```
5205 025752 026044 2$ :XX0
5206 025754 026072 3$ :XX4
5207 025756 000340 000340 .WORD 340,340 :LEVEL 7
5208 025762 1$: SETPRI #PRI07 :PS = LEVEL 7
5209 025762 012700 000340 MOV #PRI07,R0
5210 025766 104441 TRAP C$SPRI
5211 025770 012761 000300 000004 MOV #300,4(R1) :WRITE PORT4
5212 025776 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
5213 026002 121111 121111 :SET BR RQ IN IBUS* REG 11
5214 026004 SETPRI #PRI00 :ALLOW INTERRUPT
5215 026004 012700 000000 MOV #PRI00,R0
5216 026010 104441 TRAP C$SPRI
5217 026012 000240 NOP
5218 026014 ERROR 31 :NO INTERRUPT
5219 026032 104455 TRAP C$ERDF
5220 026034 000037 .WORD 31
5221 026036 005561 .WORD EM31
5222 026040 011670 .WORD ERR31
5223 026042 000415 BR 4$
5224 026044 2$: ERROR 32 :WRONG VECTOR
5225 026062 104455 TRAP C$ERDF
5226 026064 000040 .WORD 32
5227 026066 005610 .WORD EM32
5228 026070 011746 .WORD ERR32
5229 026072 062706 000004 3$: ADD #4,SP ;RESET STACK
5230 026076 4$:
5231 026076 ENDTST
5232 026076 L10116:
5233 026076 104401 TRAP C$ETST
5234
5235 026100 BADHEAD
5236 :***** TEST 38 *****
5237 :*PRIORITY INTERRUPT TEST
5238 :*SET PS TO ALL BR LEVELS EQUAL OR GREATER THAN
5239 :*THE M8200,4,6,7 LEVEL, VERIFY THAT COMM. MICRO-PROCESSOR FAMILY DOES NOT INTER
5240 026100 BADHEAD
5241 :***** TEST 38 *****
5242
5243 026100 BGNTST
5244 026100 T38::
5245 026100 MYINT
5246 026104 MSTCLR :MASTER CLEAR M8200,4,6,7
5247 026110 012704 000340 MOV #340,R4 :PUT LEVEL 7 IN R2
5248 026114 SETPRI R4 :SET PRIORITY TO 7
5249 026114 010400 MOV R4,R0
5250 026116 104441 TRAP C$SPRI
5251 026120 013705 002666 MOV STAT1,R5 :GET BR LEVEL OF M8200,4,6,7
5252 026124 006205 ASR R5 :SHIFT R5 4 TIMES
5253 026126 006205 ASR R5 :TO GET PROPER LEVEL
5254 026130 006205 ASR R5
5255 026132 006205 ASR R5
5256 026134 042705 177437 BIC #177437,R5 :CLEAR UNWANTED BITS
5257 026140 010537 002612 MOV R5,$GDDAT
5258 026144 004537 003604 JSR R5,SETVEC :SET UP VECTORS
5259 026150 026214 2$ :A VECTOR
5260 026152 026214 2$ :B VECTOR
```

```

5261 026154 000340 000340      .WORD 340,340      ;PRIORITY 7
5262 026160 012761 000200 000004 4$: MOV #200,4(R1)    ;LOAD PORT4
5263 026166      ROMCLK      ;NEXT WORD IS INSTRUCTION, BBN
5264 026172 121111      121111      ;SET BR REQUEST
5265 026174      5$: SETPRI R4      ;PUT LEVEL IN R2 IN PS
5266 026174 010400      MOV R4,R0
5267 026176 104441      TRAP C$SPRI
5268 026200 000240      NOP
5269 026202 020504      CMP R5,R4      ;IS PRESENT PS LEVEL = TO M8200,4,6,7 LEVEL
5270 026204 001417      BEQ 1$        ;BR IF YES
5271 026206 162704 000040      SUB #40,R4    ;NO GET NEXT LOWER LEVEL IN R2
5272 026212 000770      BR 5$        ;AND CONTINUE WITH TEST
5273 026214      2$: ERROR 33      ;ERROR UNEXPECTED INTERRUPT
5274 026232 104455      TRAP C$ERDF
5275 026234 000041      .WORD 33
5276 026236 005647      .WORD EM33
5277 026240 012024      .WORD ERR33
5278 026242 000002      RTI
5279 026244      1$: MSTCLR
5280 026250      L10117: ENDTST
5281 026250      TRAP C$ETST
5282 026250 104401
5283
5284 026252      BADHEAD
5285      :***** TEST 39 *****
5286      :*PRIORITY INTERRUPT TESTS
5287      :*SET PS TO ALL BR LEVELS LESS THAN THE M8200,4,6,7 LEVEL
5288      :*VERIFY THAT M8200,4,6,7 WILL INTERRUPT
5289 026252      BADHEAD
5290      :***** TEST 39 *****
5291
5292      BGNTST
5293      T39::
5294
5295      MYINT
5296      MSTCLR      ;MASTER CLEAR M8200,4,6,7
5297      MOV #340,R4 ;PUT LEVEL 7 IN R2
5298      SETPRI R4   ;SET PRIORITY TO 7
5299      MOV R4,R0
5300      TRAP C$SPRI
5301      MOV STAT1,R5 ;GET BR LEVEL OF M8200,4,6,7
5302      ASR R5      ;SHIFT R5 4 TIMES
5303      ASR R5      ;TO GET PROPER LEVEL
5304      ASR R5
5305      BIC #177437,R5 ;CLEAR UNWANTED BITS
5306      MOV R5,R2   ;PUT M8200,4,6,7 LEVEL IN R2
5307      SUB #40,R2 ;GET NEXT LOWER LEVEL IN R2
5308      JSR R5,SETVEC ;SET UP VECTORS
5309      2$        ;A VECTOR
5310      3$        ;B VECTOR
5311      .WORD 340,340 ;PRIORITY 7
5312 026334 012761 000200 000004 4$: MOV #200,4(R1)    ;LOAD PORT4
5313 026342      ROMCLK      ;NEXT WORD IS INSTRUCTION, BBN
5314 026346 121111      121111      ;SET BR REQUEST
5315 026350      5$: SETPRI R2      ;PUT LEVEL IN R2 IN PS
5316 026350 010200      MOV R2,R0

```

5317	026352	104441		TRAP	C\$SPRI	
5318	026354	000240		NOP		
5319	026356			ERROR	31	:ERROR, NO INTERRUPT
5320	026374	104455		TRAP	C\$ERDF	
5321	026376	000037		.WORD	31	
5322	026400	005561		.WORD	EM31	
5323	026402	011670		.WORD	ERR31	
5324	026404	000421		6\$: BR	1\$	
5325	026406	012716	026404	2\$: MOV	#6\$, (SP)	:SET UP FOR RTI
5326	026412	000002		RTI		
5327	026414			3\$: ERROR	32	:ERROR, WRONG VECTOR
5328	026432	104455		TRAP	C\$ERDF	
5329	026434	000040		.WORD	32	
5330	026436	005610		.WORD	EM32	
5331	026440	011746		.WORD	ERR32	
5332	026442	012716	026450	MOV	#1\$, (SP)	:SET UP FOR RTI
5333	026446	000002		RTI		
5334	026450			1\$: MSTCLR		
5335	026454			ENDTST		
5336	026454			L10120:		
5337	026454	104401		TRAP	C\$ETST	
5338						
5339	026456			BADHEAD		
5340				:*****	TEST 40 *****	
5341				:*NPR TEST		
5342				:*TEST OF DATO, 1 WORD FROM UPROC TO 11 MEMORY		
5343	026456			BADHEAD		
5344				:*****	TEST 40 *****	
5345						
5346	026456			BGNTST		
5347	026456			T40::		
5348	026456	004737	004146	JSR	PC,PRDEL	: WAIT FOR PRINTOUTS
5349	026462	000005		RESET		:BUS RESET
5350						
5351	026464			MYINT		
5352	026470	005011		CLR	(R1)	:CLEAR RUN
5353	026472	005061	000004	CLR	4(R1)	:CLR PORT4
5354	026476	004537	003626	JSR	R5,NPRSET	:SET UP IBUS REG 0-7
5355	026502	000000		0		:IN DATA
5356	026504	177777		-1		:OUT DATA
5357	026506	026626		3\$		:IN BA
5358	026510	026624		2\$		:OUT BA
5359	026512	005037	026624	CLR	2\$	:CLEAR 2\$
5360	026516	005061	000004	CLR	4(R1)	:CLEAR PORT 4
5361	026522			ROMCLK		:NOW MOVE TO IBUS*<11>
5362	026526	121111		121111		
5363	026530	012761	000021 000004	MOV	#21,4(R1)	:WRITE PORT4
5364	026536			ROMCLK		:NEXT WORD IS INSTRUCTION, BBN
5365	026542	121110		121110		:SET NPR BITS IN IBUS* REG 10
5366	026544	000240		NOP		
5367	026546	012737	177777 002612	MOV	#-1,\$GDDAT	:PUT 'EXPECTED' IN \$GDDAT
5368	026554	013704	026624	MOV	2\$,R4	:PUT 'FOUND' IN R4
5369	026560	023704	002612	CMP	\$GDDAT,R4	:DATA CORRECT?
5370	026564	001415		BEQ	4\$	:BR IF YES
5371	026566			ERROR	11	:ERROR NPR FAILED
5372	026604	104455		TRAP	C\$ERDF	

5373 026606 000013  
5374 026610 005123  
5375 026612 007514  
5376 026614  
5377 026614 :04410  
5378 026616 000012  
5379 026620  
5380 026620 104432  
5381 026622 000006  
5382 026624 000000  
5383 026626 000000  
5384 026630  
5385 026630  
5386 026630 1044C1  
5387  
5388 026632  
5389  
5390  
5391  
5392 026632  
5393  
5394  
5395 026632  
5396 026632  
5397 026632  
5398 026636  
5399 026642 005061 000004  
5400 026646 004537 003626  
5401 026652 000000  
5402 026654 177777  
5403 026656 027002  
5404 026660 027000  
5405 026662 012737 177777 027002  
5406 026670 012761 000001 000004  
5407 026676  
5408 026702 121110  
5409 026704 000240  
5410 026706 012737 177777 002612  
5411 026714  
5412 026720 021004  
5413 026722  
5414 026726 021025  
5415 026730 016104 000004  
5416 026734 023704 002612  
5417 026740 001415  
5418 026742  
5419 026760 104455  
5420 026762 000013  
5421 026764 005123  
5422 026766 007514  
5423 026770  
5424 026770 104410  
5425 026772 000012  
5426 026774  
5427 026774 104432  
5428 026776 000006

```

.WORD 11
.WORD EM11
.WORD ERR11
ESCAPE TST
TRAP C$ESCAPE
.WORD L10121-.
4$: EXIT TST
TRAP C$EXIT
.WORD L10121-.
2$: 0 ;OUT BA
3$: 0 ;IN BA
ENDTST
L10121: TRAP C$SETST

BADHEAD
:***** TEST 41 *****
:*NPR TEST
:*TEST OF DAT1, 1 WORD FROM 11 MEMORY TO UPROC
BADHEAD
:***** TEST 41 *****

BGNTST
T41::
MYINT
MSTCLR ;MASTER CLEAR M8200,4,6,7
CLR 4(R1) ;CLR PORT4
JSR R5,NPRSET ;SET UP IBUS REG 0-7
0 ;IN DATA
-1 ;OUT DATA
3$ ;IN BA
2$ ;OUT BA
MOV #-1,3$ ;PUT DATA IN 3$
MOV #1,4(R1) ;WRITE PORT4
ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
121110 ;SET NPR BITS IN IBUS* REG 11
NOP
MOV #-1,$GDDAT ;PUT 'EXPECTED' IN $GDDAT
ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
021004 ;MOVE IN DATA LOW BYTE TO PORT4
ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
021025 ;MOVE IN DATA HIGH BYTE TO PORT5
MOV 4(R1),R4 ;PUT 'FOUND' IN R4
CMP $GDDAT,R4 ;DATA CORRECT?
BEQ 4$ ;BR IF YES
ERROR 11 ;ERROR NPR FAILED
TRAP C$ERDF
.WORD 11
.WORD EM11
.WORD ERR11
ESCAPE TST
TRAP C$ESCAPE
.WORD L10122-.
4$: EXIT TST
TRAP C$EXIT
.WORD L10122-.

```

```
5429 02700C 000000      2$: 0          :OUT BA
5430 027002 000000      3$: 0          :IN BA
5431 027004          ENDTST
5432 027004          L10122:
5433 027004 104401      TRAP  CSETST
5434
5435 027006          BADHEAD
5436          :***** TEST 42 *****
5437          :*NPR TEST
5438          :*TEST OF DATOB, 1 BYTE FROM UPROC TO 11 MEMORY
5439 027006          BADHEAD
5440          :***** TEST 42 *****
5441
5442 027006          BGNTST
5443 027006          T42::
5444 027006          MYINT
5445 027012          MSTCLR          :MASTER CLEAR M8200,4,6,7
5446 027016 005061 000004 CLR 4(R1)          :CLR PORT4
5447 027022 004537 003626 JSR R5,NPRSET      :SET UP IBUS REG 0-7
5448 027026 000000          0          :IN DATA
5449 027030 177777          -1          :OUT DATA
5450 027032 027152          3$          :IN BA
5451 027034 027151          2$+1        :OUT BA
5452 027036 005037 027150 CLR 2$          :CLEAR 2$
5453 027042 005061 000004 CLR 4(R1)        :CLEAR PORT 4
5454 027046          ROMCLK          :NOW MOVE IT TO IBUS*<11>
5455 027052 121111          121111
5456 027054 012761 000221 000004 MOV #221,4(R1)    :WRITE PORT4
5457 027062          ROMCLK          :NEXT WORD IS INSTRUCTION, BBN
5458 027066 121110          121110          :SET NPR BITS IN IBUS* REG 11
5459 027070 000240          NOP
5460 027072 012737 177400 002612 MOV #177400,$GDDAT :PUT 'EXPECTED' IN $GDDAT
5461 027100 013704 027150 MOV 2$,R4        :PUT 'FOUND' IN R4
5462 027104 023704 002612 CMP $GDDAT,R4    :DATA CORRECT?
5463 027110 001415          BEQ 4$          :BR IF YES
5464 027112          ERROR 11          :ERROR NPR FAILED
5465 027130 104455          TRAP C$ERDF
5466 027132 000013          .WORD 11
5467 027134 005123          .WORD EM11
5468 027136 007514          .WORD ERR11
5469 027140          ESCAPE TST
5470 027140 104410          TRAP C$ESCAPE
5471 027142 000012          .WORD L10123-.
5472 027144          4$: EXIT TST
5473 027144 104432          TRAP C$EXIT
5474 027146 000006          .WORD L10123-.
5475 027150 000000          2$: 0          :OUT BA
5476 027152 000000          3$: 0          :IN BA
5477 027154          ENDTST
5478 027154          L10123:
5479 027154 104401      TRAP  CSETST
5480
5481 027156          BADHEAD
5482          :***** TEST 43 *****
5483          :*TEST OF EA BITS 16 AND 17
5484          :*DO A DATO TO AN ADDRESS USING OUT BA BITS 16 AND 17
```

```

5485                                     :*VERIFY CORRECT RESULTS
5486 027156                               BADHEAD
5487                                     :***** TEST 43 *****
5488
5489 027156                               BGN1ST
5490 027156                               T43::
5491 027156                               MSTCLR                               :MASTER CLEAR M8200,4,6,7
5492 027162                               MYINT
5493 027166 013737 002714 027214          MOV      KMP06,1$                       :USE SEL4 FOR ADDRESS
5494 027174 013737 002714 027212          MOV      KMP06,2$                       :USE SEL4 FOR ADDRESS
5495 027202 004537 003626          JSR      R5,NPRSET                       :LOAD BA AND DATA
5496 027206 000000          0                                         :IN DATA
5497 027210 125252          125252                                   :OUT DATA
5498 027212 000000          2$: 0                                     :IN BA
5499 027214 000000          1$: 0                                     :OUT BA
5500 027216 012761 000014 000004          MOV      #14,4(R1)                       :LOAD SEL 4 WITH CUT BA16 AND 17
5501 027224          ROMCLK                               :NEXT WORD IS INSTRUCTION, BBN
5502 027230 121111          121111                                   :SET OUTBA 16 AND 17
5503 027232 012761 000021 000004          MOV      #21,4(R1)                       :LOAD SEL4
5504 027240 012711 003000          MOV      #BIT9!BIT10,(R1)
5505 027244 012761 121110 000006          MOV      #121110,6(R1)                   :PUT INSTRUCTION IN SEL6
5506 027252 052711 00040C          BIS      #BIT8,(R1)                       :CLOCK IT!
5507 027256 000240          NOP                                       :WAIT FOR NPR
5508 027260 012737 121110 002612          MOV      #121110,$GDDAT                   :PUT 'EXPECTED' IN $GDDAT
5509 027266 000240          NOP
5510 027270 000240          NOP
5511                                     :OK.LISTEN UP!EXPLANATION TIME.
5512                                     :
5513                                     :ON THE NPR OUT,THE DATA ENDED UP
5514                                     :IN THE IBUS(NOT IBUS*) SENCE SEL A
5515                                     :WAS ONLY SELECTED IN THE NPR CYCLE.
5516                                     :THAT IS,WE DIDN'T REALLY DO AN NPR TO
5517                                     :PORT 6,THE NPR OUT REALLY ENDED UP IN
5518                                     :OUT DATA LOW,AND OUT DATA HIGH
5519                                     : (IBUS <2> AND IBUS <3>).
5520
5521                                     :WHAT WE'RE DOING NEXT IS READING IBUS 2&3
5522                                     :TO SEE IF THE DATA GOT XFERRERD CORRECTLY.
5523 027272          ROMCLK
5524 027276 021044          021044                                   :READ IBUS <2> PUT IN PORT 4
5525 027300          ROMCLK
5526 027304 021065          021065                                   :READ IBUS <3> PUT IN PORT 5
5527 027306 016104 000004          MOV      4(R1),R4                           :PUT 'FOUND' IN R4
5528 027312 023704 002612          CMP      $GDDAT,R4                         :CORRECT RESULTS?
5529 027316 001413          BEQ     3$
5530 027320          ERROR 11                               :BR IF YES
5531 027336 104455          TRAP   C$ERDF                             :ERROR BA 16 AND 17 FAILED
5532 027340 000013          .WORD  11
5533 027342 005123          .WORD  EM11
5534 027344 007514          .WORD  ERR11
5535                                     3$:
5536 027346          ENDTST
5537 027346          L10124:
5538 027346 104401          TRAP   C$ETST
5539
5540 027350          BADHEAD

```



```
5541 :***** TEST 44 *****
5542 :*TEST OF EA BITS 16 AND 17
5543 :*DO A DATA USING IN BA BITS 16 AND 17
5544 :*VERIFY CORRECT RESULTS
5545 :*IN ORDER TO DO THIS TEST, WE WILL READ THE DATA FROM THE
5546 :*CONSOL TTY CSR IF ONE EXISTS
5547 :*IF NO CONSOL TTY CSR AT ADDRESS 177560, THIS TEST
5548 :*WILL BE SKIPPED
5549 027350 BADHEAD
5550 :***** TEST 44 *****
5551
5552 027350 BGNST
5553 027350 T44::
5554 027350 MYINT
5555 027354 MSTCLR ;MASTER CLEAR M8200,4,6,7
5556 027360 012737 027546 000004 STOP: MOV #TOUTT,4 ;SET UP FOR TRAP IN CASE IF NO
5557 027366 012737 000340 000006 MOV #340,6 ;TTY AT ADDRESS 177560
5558 027374 005737 177560 TST 177560 ;ADDRESS THE TTY-TRAPS HERE IF NONE.
5559 027400 012737 177560 027426 MOV #177560,1$ ;USE SEL4 FOR ADDRESS
5560 027406 012737 177560 027424 MOV #177560,2$ ;USE SEL4 FOR ADDRESS
5561 027414 004537 003626 JSR R5,NPRSET ;LOAD BA AND DATA
5562 027420 000000 0 ;IN DATA
5563 027422 125252 125252 ;OUT DATA
5564 027424 000000 2$: 0 ;IN BA
5565 027426 000000 1$: 0 ;OUT BA
5566 027430 012761 000015 000004 MOV #15,4(R1)
5567 027436 012711 003000 MOV #BIT9!BIT10,(R1);SET CROMI AND CROMO!!
5568 027442 012761 121110 000006 MOV #121110,6(R1) ;PUT INSTR INTO SEL6 MW*
5569 027450 052711 000400 BIS #BIT8,(R1) ;CLOCK IT!
5570 027454 000240 NOP ;WAIT FOR NPR
5571 027456 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
5572 027462 021004 021004 ;MOVE OUT DATA LB TO SEL4
5573 027464 ROMCLK ;NEXT WORD IS INSTRUCTION, BBN
5574 027470 021025 021025 ;MOVE OUT DATA HB TO SEL5
5575 027472 016104 000004 MOV 4(R1),R4 ;PUT 'FOUND' IN R4
5576 027476 013737 177560 002612 MOV 177560,$GDDAT
5577 027504 042737 000200 002612 BIC #200,$GDDAT
5578 027512 023704 002612 CMP $GDDAT,R4 ;CORRECT RESULTS?
5579 027516 C01415 BEQ TOUTP ;BR IF YES
5580 027520 ERROR 11 ;ERROR BA 16 AND 17 FAILED
5581 027536 104455 TRAP C$ERDF
5582 027540 000013 .WORD 11
5583 027542 005123 .WORD EM11
5584 027544 007514 .WORD ERR11
5585 027546 3$:
5586 027546 062706 000004 TOUTT: ADD #4,SP ;UPDATE STACK POITNTER
5587 027552 013737 002626 000006 TOUTP: MOV SAVE6,6 ;RESTORE TRAP VECTOR
5588 027560 013737 002624 000004 MOV SAVE4,4
5589 027566 ENDTST
5590 027566 L10125:
5591 027566 104401 TRAP C$ETST
5592
5593 027570 BADHEAD
5594 :***** TEST 45 *****
```

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N 10

SEQ 0130

```
5595                                     : *NPR NON-EXISTENT MEMORY TEST
5596                                     : *DO A DATO TO A NON-EXISTENT ADDRESS
5597                                     : *VERIFY THAT THE NON-EXISTENT BIT SET IN IBUS REG 11
5598 027570                             BADHEAD
5599                                     : ***** TEST 45 *****
5600
5601 027570                             BGNTST
5602 027570                             T45::
5603 027570
5604 027574                             MYINT
5605 027600 004537 003626             MSTCLR
5606 027604 000000                     JSR      R5,NPRSET      ;MASTER CLEAR M8200,4,6,7
5607 027606 000000                     0                ;LOAD IBUS REGISTERS 0-7
5608 027610 177320                     0                ;IN DATA
5609 027612 177320                     177320           ;OUT DATA
5610 027614 012761 000014 000004     177320           ;IN BA
5611                                     177320           ;IN BA
5612                                     MOV      #14,4(R1)    ;SET OUT BA BITS 16+17 IN PORT4
```

5611	027622				ROMCLK		:NEXT WORD IS INSTRUCTION, BBN
5612	027626	121111			121111		:SET OUTBA 16 AND 17
5613	027630	012761	000021	000004	MOV	#21,4(R1)	:SET NPR REQUEST BITS IN PORT4
5614	027636				ROMCLK		:NEXT WORD IS INSTRUCTION, BBN
5615	027642	121110			121110		:MOV IBUS* 4 TO IBUS* 10
5616	027644	000240			NOP		
5617	027646				ROMCLK		:NEXT WORD IS INSTRUCTION, BBN
5618	027652	121225			121225		:MOV IBUS*11 TO IBUS*5
5619	027654	012737	000001	002612	MOV	#1,\$GDDAT	:PUT 'EXPECTED' IN \$GDDAT
5620	027662	116104	000005		MOV	5(R1),R4	:PUT 'FOUND' IN R4
5621	027666	042704	177776		BIC	#177776,R4	:CLEAR UNWANTED BITS
5622	027672	023704	002612		CMR	\$GDDAT,R4	:DATA CORRECT?
5623	027676	001413			BEQ	1\$	:BR IF YES
5624	027700				ERROR	13	:ERROR NON-EXISTENT MEM BIT FAILED TO SET
5625	027716	104455			TRAP	C\$ERDF	
5626	027720	000015			.WORD	13	
5627	027722	005156			.WORD	EM13	
5628	027724	007730			.WORD	ERR13	
5629	027726						
5630	027726	152761	000100	000001	BISB	#100,1(R1)	:SET MASTER CLEAR
5631	027734	142761	000100	000001	BICB	#100,1(R1)	:CLEAR MASTER
5632	027742				ROMCLK		:MOV IBUS*11 TO
5633	027746	121225			121225		:PORT5
5634	027750	005037	002612		CLR	\$GDDAT	:EXPECT CLEAR
5635	027754	116104	000005		MOV	5(R1),R4	:GET NPR REG
5636	027760	042704	177776		BIC	#177776,R4	:CLEAR JUNK
5637	027764	001413			BEQ	2\$	:EXIT IF CLEAR
5638	027766				ERROR	13	:NON-EXISTANT MEM
5639	030004	104455			TRAP	C\$ERDF	
5640	030006	000015			.WORD	13	
5641	030010	005156			.WORD	EM13	
5642	030012	007730			.WORD	ERR13	
5643							:BIT FAILED TO CLEAR
5644	030014						
5645	030014						
5646	030014						
5647	030014	104401			TRAP	C\$ETST	
5648							
5649	030016				BADHEAD		
5650					:***** TEST 46 *****		
5651					:*NPR NON-EXISTENT MEMORY TEST		
5652					:*DO A DATI FROM A NON-EXISTENT ADDRESS		
5653					:*VERIFY THAT THE NON-EXISTENT BIT SET IN IBUS REG 11		
5654	030016				BADHEAD		
5655					:***** TEST 46 *****		
5656							
5657	030016				BGNTST		
5658	030016				T46::		
5659	030016				MYINT		
5660	030022				MSTCLR		:MASTER CLEAR M8200,4,6,7
5661	030026	004537	003626		JSR	R5,NPRSET	:LOAD IBUS REGISTERS 0-7
5662	030032	000000			0		:IN DATA
5663	030034	000000			0		:OUT DATA
5664	030036	177320			177320		:IN BA
5665	030040	177320			177320		:OUT BA
5666	030042	005061	000004		CLR	4(R1)	

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5667 030046 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
5668 030052 121111 :CLEAR NON-EXISTENT BIT
5669 030054 012761 000015 000004 MOV #15,4(R1) :SET NPR REQUEST BITS IN PORT4
5670 030062 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
5671 030066 121110 121110 :MOV IBUS* 4 TO IBUS* 10
5672 030070 000240 NOP
5673 030072 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
5674 030076 121225 121225 :MOV IBUS*11 TO IBUS*5
5675 030100 012737 000001 002612 MOV #1,$GDDAT :PUT 'EXPECTED' IN $GDDAT
5676 030106 116104 000005 MOVB 5(R1),R4 :PUT 'FOUND' IN R4
5677 030112 042704 177776 BIC #177776,R4 :CLEAR UNWANTED BITS
5678 030116 023704 002612 CMP $GDDAT,R4 :DATA CORRECT?
5679 030122 001413 BEQ 1$ :BR IF YES
5680 030124 ERROR 13 :ERROR NON-EXISTENT MEM BIT FAILED TO SET
5681 030142 104455 TRAP C$ERDF
5682 030144 000015 .WORD 13
5683 030146 005156 .WORD EM13
5684 030150 007730 .WORD ERR13
5685 030152
5686 030152 1$:
5687 030152 ENDTST
5688 030152 104401 L10127:
5689 TRAP C$ETST
5690 030154
5691 BADHEAD
5692 :***** TEST 47 *****
5693 :*NPR TEST
5694 :*USING DATO, NPR A BINARY COUNT (0-377)
5695 :*FROM MICRO-PROCESSOR TO ALL AVAILABLE MEMORY
5696 BADHEAD
5697 :***** TEST 47 *****
5698 030154
5699 030154 BGNTST
5700 030154 T47::
5701 030160 MYINT
5702 030164 005037 030372 MSTCLR :MASTER CLEAR M8200,4,6,7
5703 030170 005005 CLR 5$ :START FLAG AT 0
5704 030172 012702 041630 CLR R5 :DATA
5705 030176 MOV #CORMAX,R2 :ADDRESS
5706 030176 010537 030226 1$: MOV R5,2$ :LOAD DATA
5707 030202 010237 030232 MOV R2,4$ :LOAD BA
5708 030206 032702 000001 BIT #BIT0,R2 :IS BA ODD?
5709 030212 001402 BEQ .+6 :BR IF NO
5710 030214 000337 030226 SWAB 2$ :IF ODD PUT DATA IN HI-BYTE
5711 030220 004537 003626 JSR R5,NPRSET :LOAD NPR REGISTERS
5712 030224 000000 0 :IN DATA
5713 030226 000000 2$: 0 :OUT DATA
5714 030230 000000 0 :IN BA
5715 030232 000000 4$: 0 :OUT BA
5716 030234 105012 CLRB (R2) :CLEAR MEMORY LOCATION
5717 030236 012761 000221 000004 MOV #221,4(R1) :LOAD PORT4
5718 030244 ROMCLK :NEXT WORD IS INSTRUCTION, BBN
5719 030250 121110 121110 :DO THE NPR
5720 030252 000240 NOP
5721 030254 010537 002612 MOV R5,$GDDAT :PUT 'EXPECTED' IN $GDDAT
5722 030260 111204 MOVB (R2),R4 :PUT 'FOUND' IN R4

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5723 030262 123704 002612      CMPB    $GDDAT,R4      ;IS DATA CORRECT?
5724 030266 001413      BEQ     3$            ;BR IF YES
5725 030270      ERROR   11            ;ERROR, DATA INCORRECT
5726 030306 104155      TRAP   C$ERDF
5727 030310 000013      .WORD  11
5728 030312 005123      .WORD  EM11
5729 030314 007514      .WORD  ERR11
5730 030316      3$:  ESCAPE  TST
5731 030316 104410      TRAP   C$ESCAPE
5732 030320 000054      .WORD  L10130-.
5733 030322 005205      INC     R5            ;NEXT CHARACTER
5734 030324 042705 177400      BIC     #177400,R5    ;USE ONLY LOW BYTE
5735 030330 005737 030372      TST     5$            ;HAS MAX MEMORY BEEN REACHED YET?
5736 030334 001402      BEQ     6$            ;BR IF NO
5737 030336 005705      TST     R5            ;DONE PATTERN?
5738 030340 001412      BEQ     7$            ;BR IF YES
5739 030342 005202      6$:  INC     R2            ;INC BA
5740 030344 023702 002556      CMP     MEMLIM,R2    ;REACHED MEMORY LIMIT YET?
5741 030350 001312      BNE     1$            ;BR IF NOT
5742 030352 012702 041630      MOV     #CORMAX,R2   ;RESTART BA AT FIRST ADDRESS
5743 030356 012737 177777 030372      MOV     #-1,5$      ;SET FLAG TO END TEST AT END OF DATA PATTERN
5744 030364 000704      BR     1$            ;CONTINUE
5745 030366      7$:
5746 030366      EXIT   TST
5747 030366      TRAP  C$EXIT
5748 030370 000004      .WORD  L10130-.
5749 030372 000000      5$:  0            ;THIS LOCATION IS A FLAG, IT STARTS AT 0,
5750      ;AND IS SET TO -1 WHEN LAST MEMORY ADDRESS
5751      ;IS USED, TEST IS THEN ENDED WHEN PATTERN IS FINISHED
5752 030374      ENDTST
5753 030374      L10130:
5754 030374 104401      TRAP   C$ETST
5755      ;$MEM1
5756      ;$MEM0
5757      ;$MEM2 1K
5758      ;$MEM3 1K
5759
5760 030376      BADHEAD
5761      ;***** TEST 48 *****
5762      ;*ALU C BIT TEST
5763      ;*TEST THAT AN ADD OF 377 AND 377 WILL SET THE C BIT
5764 030376      BADHEAD
5765      ;***** TEST 48 *****
5766
5767 030376      BGNTST
5768 030376      T48::
5769 030376
5770 030402      MYINT
5771 030406 004737 003672      MSTCLR
5772 030412 030532      JSR    PC,MEMLD     ;MASTER CLEAR M8200,4,6,7
5773 030414 004737 004044      TDATA
5774 030420 030532      JSR    PC,SPLD     ;LOAD MAINMEM DATA
5775 030422      TDATA
5776 030422 104404      BGNSEG
5777 030424      TRAP  C$BSEG
5778 030424      1$:  ROMCLK
      ;NEXT WORD IS INSTRUCTION, BBN
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5779 030430 010000          010000          :MAR 0
5780 030432          ROMCLK          :NEXT WORD IS INSTRUCTION, BBN
5781 030436 054400          054400!<0*20>      :ADD 377 AND 377, TO SET C BIT
5782 030440          ROMCLK          :NEXT WORD IS INSTRUCTION, BBN
5783 030444 040421          040401!<1*20>      :ADD 0 AND 0 AND THE C BIT
5784 030446          ROMCLK          :NEXT WORD IS INSTRUCTION, BBN
5785 030452 061224          61224             :PUT RESULTS IN PORT4
5786 030454 012737 000001 002612  MOV #1,$GDDAT      :PUT 'EXPECTED' IN $GDDAT
5787 030462 016104 000004      MOV 4(R1),R4      :PUT 'FOUND' IN R4
5788 030466 123704 002612  CMPB $GDDAT,R4    :DATA CORRECT?
5789 030472 001413          BEQ 2$           :BR IF YES
5790 030474          ERROR 34             :ERROR C BIT NOT SET
5791 030512 104455          TRAP C$ERDF
5792 030514 000042          .WORD 34
5793 030516 005704          .WORD EM34
5794 030520 012132          .WORD ERR34
5795 030522          2$: ESCAPE SEG
5796 030522 104410          TRAP C$ESCAPE
5797 030524 000002          .WORD 10000$-.
5798 030526          ENDSEG
5799 030526          10000$:
5800 030526 104405          TRAP C$ESEG
5801 030530          ENDTST
5802 030530          L10131:
5803 030530 104401          TRAP C$ETST
5804 030532 377 000 000 000  TDATA: .BYTE -1,0,0,0,0,0,0,0
5805 030535 000 000 000
5806 030540 000 000 000
5807
5808          .EVEN
5809
5810 030542          BADHEAD
5811          :***** TEST 49 *****
5812          :*ALU TEST
5813          :*TEST OF ALU FUNCTION SEL B WITH C BIT CLEARED
5814          :*ALU FUNCTION (B) CODE=11
5815          :*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
5816          :*PERFORM THE FUNCTION, VERIFY THE RESULTS
5817 030542          BADHEAD
5818          :***** TEST 49 *****
5819
5820 030542          BGNST
5821 030542          T49::
5822 030542          MYINT
5823 030546          MSTCLR          :MASTER CLEAR M8200,4,6,7
5824 030552 005005          CLR R5           :MEM + SP ADDRESS
5825 030554 012702 030740  MOV #5$,R2        :POINTER TO CORRECT DATA
5826 030560 004737 003672  JSR FC,MEMLD      :LOAD 8 WORDS OF MAIN MEMORY
5827 030564 002642          MEMDAT          :POINTER TO DATA
5828 030566 004737 004044  JSR PC,SPLD       :LOAD 8 WORDS OF SP
5829 030572 002652          SPDAT          :POINTER TO DATA
5830 030574          BGNSEG
5831 030574 104404          TRAP C$BSEG
5832 030576 004737 004112  1$: JSR PC,CLRC      :CLEAR C BIT!
5833 030602 042737 000017 030620  BIC #17,2$        :CLEAR ADDRESS FIELD OF INSTRUCTION
5834 030610 050537 030620  BJS R5,2$         :ADD ADDRESS TO INSTRUCTION
```

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5835 030614 ROMCLK :NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
5836 030620 010000 :LOAD MAR
5837 030622 042737 000017 030640 2$: BIC #17,3$ :CLEAR ADDRESS OF INSTRUCTION
5838 030630 050537 030640 BIS R5,3$ :ADD ADDRESS TO INSTRUCTION
5839 030634 ROMCLK :NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
5840 030640 040620 3$: 040400!<11*20> :BR SEL B
5841 030642 ROMCLK :NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
5842 030646 061224 61224 :MOVE BR TO PORT4
5843 030650 111237 002612 MOVB (R2), $GDDAT :PUT 'EXPECTED' IN $GDDAT
5844 030654 116104 000004 MOVB 4(R1), R4 :PUT 'FOUND' IN R4
5845 030660 123704 002612 CMPB $GDDAT, R4 :DATA CORRECT?
5846 030664 001413 BEQ 4$ :BR IF YES
5847 030666 ERROR 15 :ALU ERROR
5848 030704 104455 TRAP C$ERDF
5849 030706 000017 .WORD 15
5850 030710 005223 .WORD EM15
5851 030712 010114 .WORD ERR15
5852 030714 4$: ESCAPE SEG
5853 030714 104410 TRAP C$ESCAPE
5854 030716 000014 .WORD 10000$-.
5855 030720 005202 INC R2 :NEXT DATA
5856 030722 005205 INC R5 :NEXT ADDRESS
5857 030724 022705 000010 CMP #10, R5 :DONE YET?
5858 030730 001322 BNE 1$ :BR IF NO
5859 030732 ENDSEG
5860 030732 10000$:
5861 030732 104405 TRAP C$ESEG
5862 030734 EXIT TST
5863 030734 104432 TRAP C$EXIT
5864 030736 000012 .WORD L10132-.
5865 030740 000 377 000 5$: .BYTE 0,-1,0,-1,125,252,125,252
5866 030743 377 125 252
5867 030746 125 252
5868
5869 .EVEN
5870 030750 ENDTST
5871 030750 L10132:
5872 030750 104401 TRAP C$ETST
5873
5874 030752 BADHEAD
5875 :***** TEST 50 *****
5876 :*ALU TEST
5877 :*TEST OF ALU FUNCTION SEL A WITH C BIT CLEARED
5878 :*ALU FUNCTION (A) CODE=10
5879 :*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
5880 :*PERFORM THE FUNCTION, VERIFY THE RESULTS
5881 030752 BADHEAD
5882 :***** TEST 50 *****
5883
5884 030752 BGNTST
5885 030752 T50::
5886 030752 MYINT
5887 030756 MSTCLR :MASTER CLEAR M8200,4,6,7
5888 030762 005005 CLR R5 :MEM + SP ADDRESS
5889 030764 012702 031150 MOV #5$, R2 :POINTER TO CORRECT DATA
5890 030770 004737 003672 JSR PC, MEMLD :LOAD 8 WORDS OF MAIN MEMORY

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5891 030774 002642 MEMDAT
5892 030776 004737 004044 JSR PC,SPLD ;POINTER TO DATA
5893 031002 002652 SPDAT ;LOAD 8 WORDS OF SP
5894 031004 BGNSEG ;POINTER TO DATA
5895 031004 104404 TRAP C$BSEG
5896 031006 004737 004112 1$: JSR PC,CLRC ;CLEAR C BIT!
5897 031012 042737 000017 031030 BIC #17,2$ ;CLEAR ADDRESS FIELD OF INSTRUCTION
5898 031020 050537 031030 BIS R5,2$ ;ADD ADDRESS TO INSTRUCTION
5899 031024 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
5900 031030 010000 010000 2$: ;LOAD MAR
5901 031032 042737 000017 031050 BIC #17,3$ ;CLEAR ADDRESS OF INSTRUCTION
5902 031040 050537 031050 BIS R5,3$ ;ADD ADDRESS TO INSTRUCTION
5903 031044 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
5904 031050 040600 3$: 040400! <10*20> ;BR SEL A
5905 031052 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
5906 031056 061224 61224 ;MOVE BR TO PORT4
5907 031060 111237 002612 MOVB (R2), $GDDAT ;PUT 'EXPECTED' IN $GDDAT
5908 031064 116104 000004 MOVB 4(R1), R4 ;PUT 'FOUND' IN R4
5909 031070 123704 002612 CMPB $GDDAT, R4 ;DATA CORRECT?
5910 031074 001413 BEQ 4$ ;BR IF YES
5911 031076 ERROR 15 ;ALU ERROR
5912 031114 104455 TRAP C$ERDF
5913 031116 000017 .WORD 15
5914 031120 005223 .WORD EM15
5915 031122 010114 .WORD ERR15
5916 031124 4$: ESCAPE SEG
5917 031124 104410 TRAP C$ESCAPE
5918 031126 000014 .WORD 10000$-
5919 031130 005202 INC R2 ;NEXT DATA
5920 031132 005205 INC R5 ;NEXT DATA
5921 031134 022705 000010 CMP #10, R5 ;DONE YET?
5922 031140 001322 BNE 1$ ;BR IF NO
5923 031142 ENDSEG
5924 031142 10000$:
5925 031142 104405 TRAP C$ESEG
5926 031144 EXIT TST
5927 031144 104432 TRAP C$EXIT
5928 031146 000012 .WORD L10133-
5929 031150 000 000 377 5$: .BYTE 0,0,-1,-1,125,125,252,252
5930 031153 377 125 125
5931 031156 252 252
5932
5933 .EVEN
5934 031160 ENDTST
5935 031160 L10133:
5936 031160 104401 TRAP C$ETST
5937
5938 031162 BADHEAD
5939 ;***** TEST 51 *****
5940 ;*ALU TEST
5941 ;*TEST OF ALU FUNCTION A OR NOTB WITH C BIT CLEARED
5942 ;*ALU FUNCTION (A OR NOTB) CODE=12
5943 ;*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
5944 ;*PERFORM THE FUNCTION, VERIFY THE RESULTS
5945 031162 BADHEAD
5946 ;***** TEST 51 *****

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5947
5948 031162
5949 031162
5950 031162
5951 031166
5952 031172 0050C5
5953 031174 012702 031360
5954 031200 004737 003672
5955 031204 002642
5956 031206 004737 004044
5957 031212 002652
5958 031214
5959 031214 104404
5960 031216 004737 004112
5961 031222 042737 000017 031240 1$:
5962 031230 050537 031240
5963 031234
5964 031240 010000
5965 031242 042737 000017 031260 2$:
5966 031250 050537 031260
5967 031254
5968 031260 040640 3$:
5969 031262
5970 031266 061224
5971 031270 111237 002612
5972 031274 116104 000004
5973 031300 123704 002612
5974 031304 001413
5975 031306
5976 031324 104455
5977 031326 000017
5978 031330 005223
5979 031332 010114
5980 031334 4$:
5981 031334 104410
5982 031336 000014
5983 031340 005202
5984 031342 005205
5985 031344 022705 000010
5986 031350 001322
5987 031352
5988 031352 10000$:
5989 031352 104405
5990 031354
5991 031354 104432
5992 031356 000012
5993 031360 377 000 377 5$:
5994 031363 377 377 125
5995 031366 252 377
5996
5997
5998 031370
5999 031370
6000 031370 104401
6001
6002 031372
```

BGNTST  
T51::

MYINT  
MSTCLR  
CLR R5 ;MASTER CLEAR M8200,4,6,7  
MCMV #5\$,R2 ;MEM + SP ADDRESS  
JSR PC,MEMLD ;POINTER TO CORRECT DATA  
MEMDAT ;LOAD 8 WORDS OF MAIN MEMORY  
JSR PC,SPLD ;POINTER TO DATA  
SPDAT ;LOAD 8 WORDS OF SP  
BGNSEG ;POINTER TO DATA  
TRAP C\$BSEG  
JSR PC,CLRC ;CLEAR C BIT!  
BIC #17,2\$ ;CLEAR ADDRESS FIELD OF INSTRUCTION  
BIS R5,2\$ ;ADD ADDRESS TO INSTRUCTION  
ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304  
010000 ;LOAD MAR  
BIC #17,3\$ ;CLEAR ADDRESS OF INSTRUCTION  
BIS R5,3\$ ;ADD ADDRESS TO INSTRUCTION  
ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304  
040400!<12\*20> ;BR A OR NOTB  
ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304  
61224 ;MOVE BR TO PORT4  
MOVB (R2), \$GDDAT ;PUT 'EXPECTED' IN \$GDDAT  
MOVB 4(R1), R4 ;PUT 'FOUND' IN R4  
CMPB \$GDDAT, R4 ;DATA CORRECT?  
BEQ 4\$ ;BR IF YES  
ERROR 15 ;ALU ERROR  
TRAP C\$ERDF  
.WORD 15  
.WORD EM15  
.WORD ERR15  
ESCAPE SEG 4\$:  
TRAP C\$ESCAPE  
.WORD 10000\$-  
INC R2 ;NEXT DATA  
INC R5 ;NEXT DATA  
CMP #10, R5 ;DONE YET?  
BNE 1\$ ;BR IF NO  
ENDSEG  
TRAP C\$ESEG  
EXIT TST  
TRAP C\$EXIT  
.WORD L10134-  
.BYTE -1,0,-1,-1,-1,125,252,-1

.EVEN  
ENDTST  
L10134:  
TRAP C\$ETST  
BADHEAD

```

6003 :***** TEST 52 *****
6004 :*ALU TEST
6005 :*TEST OF ALU FUNCTION A AND B WITH C BIT CLEARED
6006 :*ALU FUNCTION (A AND B) CODE=13
6007 :*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
6008 :*PERFORM THE FUNCTION, VERIFY THE RESULTS
6009 031372 BADHEAD
6010 :***** TEST 52 *****
6011
6012 031372 BGNTST
6013 031372 T52::
6014 031372 MYINT
6015 031376 MSTCLR ;MASTER CLEAR MB200,4,6,7
6016 031402 005005 CLR R5 ;MEM + SP ADDRESS
6017 031404 012702 031570 MOV #5$,R2 ;POINTER TO CORRECT DATA
6018 031410 004737 003672 JSR PC,MEMLD ;LOAD 8 WORDS OF MAIN MEMORY
6019 031414 002642 MEMDAT ;POINTER TO DATA
6020 031416 004737 004044 JSR PC,SPLD ;LOAD 8 WORDS OF SP
6021 031422 002652 SPDAT ;POINTER TO DATA
6022 031424 BGNSEG
6023 031424 104404 TRAP C$BSEG
6024 031426 004737 004112 1$: JSR PC,CLRC ;CLEAR C BIT!
6025 031432 042737 000017 031450 BIC #17,2$ ;CLEAR ADDRESS FIELD OF INSTRUCTION
6026 031440 050537 031450 BIS R5,2$ ;ADD ADDRESS TO INSTRUCTION
6027 031444 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6028 031450 010000 010000 2$: ;LOAD MAR
6029 031452 042737 000017 031470 BIC #17,3$ ;CLEAR ADDRESS OF INSTRUCTION
6030 031460 050537 031470 BIS R5,3$ ;ADD ADDRESS TO INSTRUCTION
6031 031464 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6032 031470 040660 3$: 040400!<13*20> ;BR A AND B
6033 031472 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6034 031476 061224 61224 ;MOVE BR TO PORT4
6035 031500 111237 002612 MOVB (R2), $GDDAT ;PUT 'EXPECTED' IN $GDDAT
6036 031504 116104 000004 MOVB 4(R1), R4 ;PUT 'FOUND' IN R4
6037 031510 123704 002612 CMPB $GDDAT, R4 ;DATA CORRECT?
6038 031514 001413 BEQ 4$ ;BR IF YES
6039 031516 ERROR 15 ;ALU ERROR
6040 031534 104455 TRAP C$ERDF
6041 031536 000017 .WORD 15
6042 031540 005223 .WORD EM15
6043 031542 010114 .WORD ERR15
6044 031544 4$: ESCAPE SEG
6045 031544 104410 TRAP C$ESCAPE
6046 031546 000014 .WORD 10000$-
6047 031550 005202 INC R2 ;NEXT DATA
6048 031552 005205 INC R5 ;NEXT DATA
6049 031554 022705 000010 CMP #10,R5 ;DONE YET?
6050 031560 001322 BNE 1$ ;BR IF NO
6051 031562 ENDSEG
6052 031562 10000$:
6053 031562 104405 TRAP C$ESEG
6054 031564 EXIT TST
6055 031564 104432 TRAP C$EXIT
6056 031566 000012 .WORD L10135-
6057 031570 000 000 5$: .BYTE 0,0,0,-1,125,0,0,252
6058 031573 377 125 000

```

6059 031576 00C 252

6060

6061

6062 031600

6063 031600

6064 031600 104401

6065

6066 031602

6067

6068

6069

6070

6071

6072

6073 031602

6074

6075

6076 031602

6077 031602

6078 031602

6079 031606

6080 031612 005005

6081 031614 012702 032000

6082 031620 004737 003672

6083 031624 002642

6084 031626 004737 004044

6085 031632 002652

6086 031634

6087 031634 104404

6088 031636 004737 004112

6089 031642 042737 000017 031660

6090 031650 050537 031660

6091 031654

6092 031660 010000

6093 031662 042737 000017 031700

6094 031670 050537 031700

6095 031674

6096 031700 040700

6097 031702

6098 031706 061224

6099 031710 111237 002612

6100 031714 116104 000004

6101 031720 123704 002612

6102 031724 001413

6103 031726

6104 031744 104455

6105 031746 000017

6106 031750 005223

6107 031752 010114

6108 031754

6109 031754 104410

6110 031756 000014

6111 031760 005202

6112 031762 005205

6113 031764 022705 000010

6114 031770 001322

.EVEN  
 ENDTST  
 L10135:

TRAP C\$ETST

BADHEAD

:\*\*\*\*\* TEST 53 \*\*\*\*\*

:\*ALU TEST

:\*TEST OF ALU FUNCTION A OR B WITH C BIT CLEARED

:\*ALU FUNCTION (A OR B) CODE=14

:\*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA

:\*PERFORM THE FUNCTION, VERIFY THE RESULTS

BADHEAD

:\*\*\*\*\* TEST 53 \*\*\*\*\*

BGNTST  
 T53::

MYINT

MSTCLR

CLR R5

MOV #5\$,R2

JSR PC,MEMLD

MEMDAT

JSR PC,SPLD

SPDAT

BGNSEG

TRAP C\$BSEG

JSR PC,CLRC

BIC #17,2\$

BIS R5,2\$

ROMCLK

010000

BIC #17,3\$

BIS R5,3\$

ROMCLK

040400! <14\*20>

ROMCLK

61224

MOV (R2), \$GDDAT

MOV 4(R1), R4

CMPB \$GDDAT, R4

BEQ 4\$

ERROR 15

TRAP C\$ERDF

.WORD 15

.WORD EM15

.WORD ERR15

ESCAPE SEG

TRAP C\$ESCAPE

.WORD 10000\$-

INC R2

INC R5

CMP #10, R5

BNE 1\$

:MASTER CLEAR M8200,4,6,7

:MEM + SP ADDRESS

:POINTER TO CORRECT DATA

:LOAD 8 WORDS OF MAIN MEMORY

:POINTER TO DATA

:LOAD 8 WORDS OF SP

:POINTER TO DATA

:CLEAR C BIT!

:CLEAR ADDRESS FIELD OF INSTRUCTION

:ADD ADDRESS TO INSTRUCTION

:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304

:LOAD MAR

:CLEAR ADDRESS OF INSTRUCTION

:ADD ADDRESS TO INSTRUCTION

:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304

:BR A OR B

:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304

:MOVE BR TO PORT4

:PUT 'EXPECTED' IN \$GDDAT

:PUT 'FOUND' IN R4

:DATA CORRECT?

:BR IF YES

:ALU ERROR

:NEXT DATA

:NEXT DATA

:DONE YET?

:BR IF NO

```
6115 031772          ENDSEG
6116 031772          10000$:
6117 031772 104405   TRAP   C$ESEG
6118 031774          EXIT   TST
6119 031774 104432   TRAP   C$EXIT
6120 031776 000012   .WORD L10136-
6121 032000 000      377      377 5$: .BYTE 0,-1,-1,-1,125,-1,-1,252
6122 032003 377      125      377
6123 032006 377      252
6124
6125          .EVEN
6126 032010          ENDTST
6127 032010          L10136:
6128 032010 104401   TRAP   C$ETST
6129
6130 032012          BADHEAD
6131          :***** TEST 54 ****
6132          :*ALU TEST
6133          :*TEST OF ALU FUNCTION A XOR B WITH C BIT CLEARED
6134          :*ALU FUNCTION (A XOR B) CODE=15
6135          :*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
6136          :*PERFORM THE FUNCTION. VERIFY THE RESULTS
6137 032012          BADHEAD
6138          :***** TEST 54 *****
6139
6140 032012          BGNST
6141 032012          T54::
6142 032012          MYINT
6143 032016          MSTCLR          ;MASTER CLEAR M8200,4,6,7
6144 032022 005005   CLR     R5          ;MEM + SP ADDRESS
6145 032024 012702 032210  MOV    #5$,R2      ;POINTER TO CORRECT DATA
6146 032030 004737 003672  JSR    PC,MEMLD   ;LOAD 8 WORDS OF MAIN MEMORY
6147 032034 002642          MEMDAT          ;POINTER TO DATA
6148 032036 004737 004044  JSR    PC,SPLD   ;LOAD 8 WORDS OF SP
6149 032042 002652          SPDAT          ;POINTER TO DATA
6150 032044          BGNSEG
6151 032044 104404          TRAP   C$BSEG
6152 032046 004737 004112  JSR    PC,CLRC   ;CLEAR C BIT!
6153 032052 042737 000017 032070  BIC    #17,2$    ;CLEAR ADDRESS FIELD OF INSTRUCTION
6154 03206C 050537 032070  BIS    R5,2$    ;ADD ADDRESS TO INSTRUCTION
6155 032064          ROMCLK          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6156 032070 010000          010000          ;LOAD MAR
6157 032072 042737 000017 032110  BIC    #17,3$    ;CLEAR ADDRESS OF INSTRUCTION
6158 032100 050537 032110  BIS    R5,3$    ;ADD ADDRESS TO INSTRUCTION
6159 032104          ROMCLK          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6160 032110 040720          040400!<15*20> ;BR A XOR B
6161 032112          ROMCLK          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6162 032116 061224          61224          ;MOVE BR TO PORT4
6163 03212C 111237 002612  MOVB   (R2), $GDDAT ;PUT 'EXPECTED' IN $GDDAT
6164 032124 116104 000004  MOVB   4(R1), R4   ;PUT 'FOUND' IN R4
6165 032130 123704 002612  CMPB   $GDDAT, R4 ;DATA CORRECT?
6166 032134 001413          BEQ    4$         ;BR IF YES
6167 032136          ERROR 15          ;ALU ERROR
6168 032154 104455          TRAP   C$ERDF
6169 032156 000017          .WORD 15
6170 032160 005223          .WORD EM15
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6227 032330 111237 002612      MOVB (R2),SGDDAT      :PUT 'EXPECTED' IN SGDDAT
6228 032334 116104 000004      MOVB 4(R1),R4        :PUT 'FOUND' IN R4
6229 032340 123704 0C2612      CMPB SGDDAT,R4       :DATA CORRECT?
6230 032344 001413      BEQ 4$               :BR IF YES
6231 032346      ERROR 15            :ALU ERROR
6232 032364 104455      TRAP C$ERDF
6233 032366 000017      .WORD 15
6234 032370 005223      .WORD EM15
6235 032372 010114      .WORD ERR15
6236 032374      4$: ESCAPE SEG
6237 032374 104410      TRAP C$ESCAPE
6238 032376 000014      .WORD 10000$-
6239 032400 005202      INC R2               :NEXT DATA
6240 032402 005205      INC R5               :NEXT DATA
6241 032404 022705 000010      CMP #10,R5           :DONE YET?
6242 032410 001322      BNE 1$               :BR IF NO
6243 032412      ENDSEG
6244 032412      10000$:
6245 032412 104405      TRAP C$ESEG
6246 032414      EXIT TST
6247 032414 104432      TRAP C$EXIT
6248 032416 000012      .WORD L10140-
6249 032420 000 377 377 5$: .BYTE 0,-1,-1,376,252,-1,-1,124
6250 032423 376 252 377
6251 032426 377 124
6252
6253      .EVEN
6254 032430      ENDTST
6255 032430      L10140:
6256 032430 104401      TRAP C$ETST
6257
6258 032432      BADHEAD
6259      :***** TEST 56 *****
6260      :*ALU TEST
6261      :*TEST OF ALU FUNCTION 2A W.C WITH C BIT CLEARED
6262      :*ALU FUNCTION (A PLUS A PLUS C) CODE=6
6263      :*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
6264      :*PERFORM THE FUNCTION, VERIFY THE RESULTS
6265 032432      BADHEAD
6266      :***** TEST 56 *****
6267
6268 032432      BGNST
6269 032432      T56::
6270 032432      MYINT
6271 032436      MSTCLR
6272 032442 005005      CLR R5              :MASTER CLEAR M8200.4.6.7
6273 032444 012702 032630      MOV #5$,R2         :MEM + SP ADDRESS
6274 032450 004737 003672      JSR PC,MEMLD      :POINTER TO CORRECT DATA
6275 032454 002642      MEMDAT            :LOAD 8 WORDS OF MAIN MEMORY
6276 032456 004737 004044      JSR PC,SPLD       :POINTER TO DATA
6277 032462 002652      SPDAT            :LOAD 8 WORDS OF SP
6278 032464      BGNSEG
6279 032464      TRAP C$BSEG
6280 032466 004737 004112      JSR PC,CLRC       :CLEAR C BIT!
6281 032472 042737 000017 032510 1$: BIC #17,2$         :CLEAR ADDRESS FIELD OF INSTRUCTION
6282 032500 050537 032510      BIS R5,2$         :ADD ADDRESS TO INSTRUCTION

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6283	032504					ROMCLK		:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6284	032510	010000				010000		:LOAD MAR
6285	032512	042737	000017	032530	2\$:	BIC	#17,3\$	:CLEAR ADDRESS OF INSTRUCTION
6286	032520	050537	032530			BIS	R5,3\$	:ADD ADDRESS TO INSTRUCTION
6287	032524					ROMCLK		:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6288	032530	040540			3\$:	040400!	<6*20>	:BR 2A W/C
6289	032532					ROMCLK		:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6290	032536	061224				61224		:MOVE BSR TO PORT4
6291	032540	111237	002612			MOVB	(R2), \$GDDAT	:PUT 'EXPECTED' IN \$GDDAT
6292	032544	116104	000004			MOVB	4(R1), R4	:PUT 'FOUND' IN R4
6293	032550	123704	002612			CMPB	\$GDDAT, R4	:DATA CORRECT?
6294	032554	001413				BEQ	4\$	:BR IF YES
6295	032556					ERROR	15	:ALU ERROR
6296	032574	104455				TRAP	C\$ERDF	
6297	032576	000017				.WORD	15	
6298	032600	005223				.WORD	EM15	
6299	032602	010114				.WORD	ERR15	
6300	032604				4\$:	ESCAPE	SEG	
6301	032604	104410				TRAP	C\$ESCAPE	
6302	032606	000014				.WORD	10000\$-	
6303	032610	005202				INC	R2	:NEXT DATA
6304	032612	005205				INC	R5	:NEXT ADDRESS
6305	032614	022705	000010			CMP	#10, R5	:DONE YET?
6306	032620	001322				BNE	1\$	:BR IF NO
6307	032622					ENDSEG		
6308	032622				10000\$:			
6309	032622	104405				TRAP	C\$ESEG	
6310	032624					EXIT	TST	
6311	032624	104432				TRAP	C\$EXIT	
6312	032626	000012				.WORD	L10141-	
6313	032630	000	000	376	5\$:	.BYTE	0,0,376,376,252,252,124,124	
6314	032633	376	252	252				
6315	032636	124	124					
6316								
6317						.EVEN		
6318	032640					ENDTST		
6319	032640					L10141:		
6320	032640	104401				TRAP	C\$ETST	
6321								
6322	032642					BADHEAD		
6323						:***** TEST 57 *****		
6324						:*ALU TEST		
6325						:*TEST OF ALU FUNCTION SUB WITH C BIT CLEARED		
6326						:*ALU FUNCTION (A-B) CODE=16		
6327						:*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA		
6328						:*PERFORM THE FUNCTION, VERIFY THE RESULTS		
6329	032642					BADHEAD		
6330						:***** TEST 57 *****		
6331								
6332	032642				BGNTST			
6333	032642				T57::			
6334	032642					MYINT		
6335	032646					MSTCLR		:MASTER CLEAR M8200,4,6,7
6336	032652	005005				CLR	R5	:MEM + SP ADDRESS
6337	032654	012702	033042			MOV	#5\$, R2	:POINTER TO CORRECT DATA
6338	032660	004737	003672			JSR	PC, MEMLD	:LOAD 8 WORDS OF MAIN MEMORY

6339	032664	002642							MEMDAT		: POINTER TO DATA
6340	032666	004737	004044						JSR	PC,SPLD	: LOAD 8 WORDS OF SP
6341	032672	002652							SPDAT		: POINTER TO DATA
6342	032674								BGNSEG		
6343	032674	104404							TRAP	C\$BSEG	
6344	032676	004737	004112		1\$:				JSR	PC,CLRC	: CLEAR C BIT!
6345	032702	042737	000017	032720					BIC	#17,2\$	: CLEAR ADDRESS FIELD OF INSTRUCTION
6346	032710	050537	032720						BIS	R5,2\$	: ADD ADDRESS TO INSTRUCTION
6347	032714								ROMCLK		: NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6348	032720	010900			2\$:				010000		: LOAD MAR
6349	032722	042737	000017	032740					BIC	#17,3\$	: CLEAR ADDRESS OF INSTRUCTION
6350	032730	050537	032740						BIS	R5,3\$	: ADD ADDRESS TO INSTRUCTION
6351	032734								ROMCLK		: NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6352	032740	040740			3\$:				040400!	<16*20>	: BR - SUB
6353	032742								ROMCLK		
6354	032746	061224							61224		: MOVE BR TO PORT4
6355	032750	111237	002612						MOVB	(R2), \$GDDAT	: PUT 'EXPECTED' IN \$GDDAT
6356	032754	116104	000004						MOVB	4(R1), R4	: PUT 'FOUND' IN R4
6357	032760	123737	002612	002612					CMPB	\$GDDAT, \$GDDAT	: DATA CORRECT?
6358	032766	001413							BEQ	4\$	: BR IF YES
6359	032770								ERROR	15	: ALU ERROR
6360	033006	104455							TRAP	C\$ERDF	
6361	033010	000017							.WORD	15	
6362	033012	005223							.WORD	EM15	
6363	033014	010114							.WORD	ERR15	
6364	033016				4\$:				ESCAPE	SEG	
6365	033016	104410							TRAP	C\$ESCAPE	
6366	033020	000014							.WORD	10000\$-	
6367	033022	005202							INC	R2	: NEXT DATA
6368	033024	005205							INC	R5	: NEXT ADDRESS
6369	033026	022705	000010						CMP	#10, R5	: DONE YET?
6370	033032	001321							BNE	1\$	: BR IF NO
6371	033034								ENDSEG		
6372	033034				10000\$:						
6373	033034	104405							TRAP	C\$ESEG	
6374	033036								EXIT	TST	
6375	033036	104432							TRAP	C\$EXIT	
6376	033040	000012							.WORD	L10142-	
6377	033042	000	001	377	5\$:				.BYTE	0,1,-1,0,0,253,125,0	
6378	033045	000	000	253							
6379	033050	125	000								
6380											
6381											
6382									.EVEN		
6383	033052								ENDTST		
6384	033052								L10142:		
6385	033052	104401							TRAP	C\$ETST	
6386											
6387											
6388	033054								BADHEAD		
6389									:***** TEST 58 *****		
6390									:*ALU TEST		
6391									:*TEST OF ALU FUNCTION ADD W/C WITH C BIT CLEARED		
6392									:*ALU FUNCTION (A PLUS B PLUS C) CODE=01		
6393									:*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA		
6394									:*PERFORM THE FUNCTION, VERIFY THE RESULTS		



```
6395 033054          BADHEAD
6396                                     ;***** TEST 58 *****
6397
6398 033054          BGNTST
6399 033054          T58::
6400 033054          MYINT
6401 033060          MSTCLR                                     :MASTER CLEAR M8200,4,6,7
6402 033064 005005   CLR      R5                                     :MEM + SP ADDRESS
6403 033066 012702 033252 MOV     #5$,R2                                     :POINTER TO CORRECT DATA
6404 033072 004737 003672 JSR    PC,MEMLD                                     :LOAD 8 WORDS OF MAIN MEMORY
6405 033076 002642          MEMDAT                                     :POINTER TO DATA
6406 033100 004737 004044 JSR    PC,SPLD                                     :LOAD 8 WORDS OF SP
6407 033104 002652          SPDAT                                     :POINTER TO DATA
6408 033106          BGNSEG
6409 033106 104404          TRAP    C$BSEG
6410 033110 004737 004112 JSR    PC,CLRC                                     :CLEAR C BIT!
6411 033114 042737 000017 033132 1$: BIC    #17,2$                                     :CLEAR ADDRESS FIELD OF INSTRUCTION
6412 033122 050537 033132 BIS    R5,2$                                     :ADD ADDRESS TO INSTRUCTION
6413 033126          ROMCLK                                     :NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6414 033132 010000          TRAP    010000                                     :LOAD MAR
6415 033134 042737 000017 033152 2$: BIC    #17,3$                                     :CLEAR ADDRESS OF INSTRUCTION
6416 033142 050537 033152 BIS    R5,3$                                     :ADD ADDRESS TO INSTRUCTION
6417 033146          ROMCLK                                     :NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6418 033152 040420          3$: 040400!<01*20>                                     :BR  ADD W/C
6419 033154          ROMCLK                                     :NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6420 033160 061224          61224
6421 033162 111237 002612 MOVB   (R2), $GDDAT                                     :PUT 'EXPECTED' IN $GDDAT
6422 033166 116104 000004 MOVB   4(R1), R4                                     :PUT 'FOUND' IN R4
6423 033172 123704 002612 CMPB   $GDDAT, R4                                     :DATA CORRECT?
6424 033176 001413          BEQ    4$
6425 033200          ERROR 15
6426 033216 104455          TRAP    C$ERDF
6427 033220 000017          .WORD  15
6428 033222 005223          .WORD  EM15
6429 033224 010114          .WORD  ERR15
6430 033226          4$: ESCAPE  SEG
6431 033226 104410          TRAP    C$ESCAPE
6432 033230 000014          .WORD  10000$-.
6433 033232 005202          INC    R2
6434 033234 005205          INC    R5
6435 033236 022705 000010 CMP    #10, R5
6436 033242 001322          BNE   1$
6437 033244          ENDSEG
6438 033244          10000$:
6439 033244 104405          TRAP    C$ESEG
6440 033246          EXIT  TST
6441 033246 104432          TRAP    C$EXIT
6442 033250 000012          .WORD  L10143-.
6443 033252 000 377 377 5$: .BYTE  0,-1,-1,376,252,-1,-1,124
6444 033255 376 252 377
6445 033260 377 124
6446
6447
6448 033262          .EVEN
6449 033262          ENDTST
6450 033262 104401          L10143: TRAP    C$ETST
```

6451  
6452  
6453 033264  
6454  
6455  
6456  
6457  
6458  
6459  
6460 033264  
6461  
6462  
6463 033264  
6464 033264  
6465 033264

BADHEAD  
:\*\*\*\*\* TEST 59 \*\*\*\*\*  
:\*ALU TEST  
:\*TEST OF ALU FUNCTION SUB W/C WITH C BIT CLEARED  
:\*ALU FUNCTION (A-B-C) CODE=2  
:\*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA  
:\*PERFORM THE FUNCTION, VERIFY THE RESULTS  
BADHEAD  
:\*\*\*\*\* TEST 59 \*\*\*\*\*

BGNTST  
T59::  
MYINT

6466	033270						MSTCLR		:MASTER CLEAR M8200,4,6,7
6467	033274	005005					CLR R5		:MEM + SP ADDRESS
6468	033276	012702	033462				MOV #5\$,R2		:POINTER TO CORRECT DATA
6469	033302	004737	003672				JSR PC, MEMLD		:LOAD 8 WORDS OF MAIN MEMORY
6470	033306	002642					MEMDAT		:POINTER TO DATA
6471	033310	004737	004044				JSR PC, SPLD		:LOAD 8 WORDS OF SP
6472	033314	002652					SPDAT		:POINTER TO DATA
6473	033316						BGNSEG		
6474	033316	104404					TRAP C\$BSEG		
6475	033320	004737	004112				JSR PC, CLRC	1\$:	:CLEAR C BIT!
6476	033324	042737	000017	033342			BIC #17,2\$		:CLEAR ADDRESS FIELD OF INSTRUCTION
6477	033332	050537	033342				BIS R5,2\$		:ADD ADDRESS TO INSTRUCTION
6478	033336						ROMCLK		:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6479	033342	010000					010000	2\$:	:LOAD MAR
6480	033344	042737	000017	033362			BIC #17,3\$		:CLEAR ADDRESS OF INSTRUCTION
6481	033352	050537	033362				BIS R5,3\$		:ADD ADDRESS TO INSTRUCTION
6482	033356						ROMCLK		:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6483	033362	040440					040400!<2*20>	3\$:	:BR _ SUB W/C
6484	033364						ROMCLK		
6485	033370	061224					61224		:MOVE BR TO PORT4
6486	033372	111237	002612				MOVB (R2), \$GDDAT		:PUT 'EXPECTED' IN \$GDDAT
6487	033376	116104	000004				MOVB 4(R1), R4		:PUT 'FOUND' IN R4
6488	033402	123704	002612				CMPB \$GDDAT, R4		:DATA CORRECT?
6489	033406	001413					BEQ 4\$		:BR IF YES
6490	033410						ERROR 15		:ALU ERROR
6491	033426	104455					TRAP C\$ERDF		
6492	033430	000017					.WORD 15		
6493	033432	005223					.WORD EM15		
6494	033434	010114					.WORD ERR15		
6495	033436						ESCAPE SEG	4\$:	
6496	033436	104410					TRAP C\$ESCAPE		
6497	033440	000014					.WORD 10000\$-		
6498	033442	005202					INC R2		:NEXT DATA
6499	033444	005205					INC R5		:NEXT ADDRESS
6500	033446	022705	000010				CMP #10, R5		:DONE YET?
6501	033452	001322					BNE 1\$		:BR IF NO
6502	033454						ENDSEG		
6503	033454							10000\$:	
6504	033454	104405					TRAP C\$ESEG		
6505	033456						EXIT TST		
6506	033456	104432					TRAP C\$EXIT		
6507	033460	000012					.WORD L10144-		
6508	033462	377	000	376	5\$:		.BYTE -1,0,376,-1,-1,252,124,-1		
6509	033465	377	377	252					
6510	033470	124	377						
6511									
6512									
6513							.EVEN		
6514	033472						ENDTST		
6515	033472						L10144:		
6516	033472	104401					TRAP C\$ETST		
6517									
6518									
6519	033474						BADHEAD		
6520							:***** TEST 60 *****		
6521							:*ALU TEST		

```
6522 ;*TEST OF ALU FUNCTION INC A WITH C BIT CLEARED
6523 ;*ALU FUNCTION (A PLUS 1) CODE=3
6524 ;*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
6525 ;*PERFORM THE FUNCTION, VERIFY THE RESULTS
6526 033474 BADHEAD
6527 ;***** TEST 60 *****
6528
6529 033474 BGNSTST
6530 033474 T60::
6531 033474
6532 033500 MYINT
6533 033504 012702 033672 MSTCLR ;MASTER CLEAR M8200,4,6,7
6534 033510 005005 MOV #5$,R2 ;POINTER TO CORRECT DATA
6535 033512 004737 003672 CLR R5
6536 033516 002642 JSR PC,MEMLD ;LOAD 8 WORDS OF MAIN MEMRY
6537 033520 004737 004044 MEMDAT ;POINTER TO DATA
6538 033524 002652 JSR PC,SPLD ;LOAD 8 WORDS OF SP
6539 033526 SPDAT ;POINTER TO DATA
6540 033526 104404 BGNSEG
6541 033530 004737 004112 TRAP C$BSEG
6542 033534 042737 000017 033552 1$: JSR PC,CLRC ;CLEAR C BIT!
6543 033542 050537 033552 BIC #17,2$ ;CLEAR ADDRESS FIELD OF INSTRUCTION
6544 033546 BIS R5,2$ ;ADD ADDRESS TO INSTRUCTION
6545 033552 010000 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6546 033554 042737 000017 033572 2$: 010000 ;LOAD MAR
6547 033562 050537 033572 BIC #17,3$ ;CLEAR ADDRESS OF INSTRUCTION
6548 033566 BIS R5,3$ ;ADD ADDRESS TO INSTRUCTION
6549 033572 040460 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6550 033574 040460 3$: 040400!<3*20> ;BR INC A
6551 033600 061224 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6552 033602 111237 002612 61224 ;MOVE BR TO PORT4
6553 033606 116104 000004 MOVB (R2), $GDDAT ;PUT 'EXPECTED' IN $GDDAT
6554 033612 123704 002612 MOVB 4(R1), R4 ;PUT 'FOUND' IN R4
6555 033616 001413 CMPB $GDDAT, R4 ;DATA CORRECT?
6556 033620 BEQ 4$ ;BR IF YES
6557 033636 104455 ERROR 15 ;ALU ERROR
6558 033640 000017 TRAP C$ERDF
6559 033642 005223 .WORD 15
6560 033644 010114 .WORD EM15
6561 033646 4$: .WORD ERR15
6562 033646 104410 ESCAPE SEG
6563 033650 000014 TRAP C$ESCAPE
6564 033652 005202 .WORD 10000$-.
6565 033654 005205 INC R2 ;NEXT DATA
6566 033656 022705 000010 INC R5
6567 033662 001322 CMP #10, R5 ;DONE YET?
6568 033664 BNE 1$ ;BR IF NO
6569 033664 10000$: ENDSEG
6570 033664 104405 TRAP C$ESEG
6571 033666 EXIT TST
6572 033666 104432 TRAP C$EXIT
6573 033670 000012 .WORD L10145-.
6574 033672 001 001 000 5$: .BYTE 1,1,0,0,126,126,253,253
6575 033675 000 126 126
6576 033700 253 253
6577
```

6578  
6579 033702  
6580 033702  
6581 033702 104401  
6582  
6583  
6584 033704  
6585  
6586  
6587  
6588  
6589  
6590  
6591 033704  
6592  
6593  
6594 033704  
6595 033704  
6596 033704  
6597 033710  
6598 033714 005005  
6599 033716 012702 034102  
6600 033722 004737 003672  
6601 033726 002642  
6602 033730 004737 004044  
6603 033734 002652  
6604 033736  
6605 033736 104404  
6606 033740 004737 004112  
6607 033744 042737 000017 033762  
6608 033752 050537 033762  
6609 033756  
6610 033762 010000  
6611 033764 042737 000017 034002  
6612 033772 050537 034002  
6613 033776  
6614 034002 040520  
6615 034004  
6616 034010 061224  
6617 034012 111237 002612  
6618 034016 116104 000004  
6619 034022 123704 002612  
6620 034026 001413  
6621 034030  
6622 034046 104455  
6623 034050 000017  
6624 034052 005223  
6625 034054 010114  
6626 034056  
6627 034056 104410  
6628 034060 000014  
6629 034062 005202  
6630 034064 005205  
6631 034066 022705 000010  
6632 034072 001322  
6633 034074

.EVEN  
ENDTST  
L10145:

TRAP CSETST

BADHEAD

:\*\*\*\*\* TEST 61 \*\*\*\*\*  
:\*ALU TEST  
:\*TEST OF ALU FUNCTION 2A WITH C BIT CLEARED  
:\*ALU FUNCTION (A PLUS A) CODE=5  
:\*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA  
:\*PERFORM THE FUNCTION, VERIFY THE RESULTS  
BADHEAD  
:\*\*\*\*\* TEST 61 \*\*\*\*\*

BGNTST  
T61::

MYINT  
MSTCLR  
CLR R5  
MOV #5\$,R2  
JSR PC,MEMLD  
MEMDAT  
JSR PC,SPLD  
SPDAT  
BGNSEG

:MASTER CLEAR DMC11  
:MEM \* SP ADDRESS  
:POINTER TO CORRECT DATA  
:LOAD 8 WORDS OF MAIN MEMORY  
:POINTER TO DATA  
:LOAD 8 WORDS OF SP  
:POINTER TO DATA

TRAP CSBSEG  
1\$: JSR PC,CLRC  
BIC #17,2\$  
BIS R5,2\$

:CLEAR C BIT!  
:CLEAR ADDRESS FIELD OF INSTRUCTION  
:ADD ADDRESS TO INSTRUCTION  
:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304

2\$: ROMCLK 010000  
BIC #17,3\$  
BIS R5,3\$

:LOAD MAR  
:CLEAR ADDRESS OF INSTRUCTION  
:ADD ADDRESS TO INSTRUCTION  
:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304

3\$: ROMCLK 040400! <5\*20>  
ROMCLK 61224

:BR 2A  
:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304  
:MOVE BR TO PORT4

MOVB (R2),SGDDAT  
MOVB 4(R1),R4  
CMPB SGDDAT,R4  
BEQ 4\$

:PUT 'EXPECTED' IN SGDDAT  
:PUT 'FOUND' IN R4  
:DATA CORRECT?  
:BR IF YES

ERROR 15  
TRAP CSERDF  
.WORD 15

:ALU ERROR

4\$: .WORD EM15  
.WORD ERR15  
ESCAPE SEG  
TRAP C\$ESCAPE

.WORD 10000\$-  
INC R2  
INC R5  
CMP #10,R5  
BNE 1\$  
ENDSEG

:NEXT DATA  
:NEXT ADDRESS  
:DONE YET?  
:BR IF NO

6634	034074					10000\$:			
6635	034074	104405					TRAP	C\$ESEG	
6636	034076						EXIT	TST	
6637	034076	104432					TRAP	C\$EXIT	
6638	034100	000012					.WORD	L10146-	
6639	034102	000	000	376	5\$:		.BYTE	0,0,376,376,252,252,124,124	
6640	034105	376	252	252					
6641	034110	124	124						
6642									
6643									
6644	034112								
6645	034112								
6646	034112	104401					TRAP	C\$ETST	
6647									
6648									
6649	034114								
6650									
6651									
6652									
6653									
6654									
6655									
6656	034114								
6657									
6658									
6659	034114								
6660	034114								
6661	034114								
6662	034120								
6663	034124	005005							
6664	034126	012702	034312						
6665	034132	004737	003672						
6666	034136	002642							
6667	034140	004737	004044						
6668	034144	002652							
6669	034146								
6670	034146	104404							
6671	034150	004737	004112						
6672	034154	042737	000017	034172	1\$:				
6673	034162	050537	034172						
6674	034166								
6675	034172	010000			2\$:				
6676	034174	042737	000017	034212					
6677	034202	050537	034212						
6678	034206								
6679	034212	040500			3\$:				
6680	034214								
6681	034220	061224							
6682	034222	111237	002612						
6683	034226	116104	000004						
6684	034232	123704	002612						
6685	034236	001413							
6686	034240								
6687	034256	104455							
6688	034260	000017							
6689	034262	005223							

.EVEN  
ENDTST  
L10146:

TRAP C\$ETST

BADHEAD

```

:***** TEST 62 *****
:*ALU TEST
:*TEST OF ALU FUNCTION A PLUS C WITH C BIT CLEARED
:*ALU FUNCTION (A PLUS C) CODE=4
:*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
:*PERFORM THE FUNCTION, VERIFY THE RESULTS
BADHEAD
:***** TEST 62 *****
    
```

BGNTST  
T62::

```

MYINT
MSTCLR
CLR R5 :MASTER CLEAR M8200,4,6,7
MOV #5$,R2 :MEM + SP ADDRESS
JSR PC,MEMLD :POINTER TO CORRECT DATA
MEMDAT :LOAD 8 WORDS OF MAIN MEMORY
JSR PC,SPLD :POINTER TO DATA
SPDAT :LOAD 8 WORDS OF SP
BGNSEG :POINTER TO DATA
    
```

1\$:

```

TRAP C$BSEG
JSR PC,CLRC :CLEAR C BIT!
BIC #17,2$ :CLEAR ADDRESS FIELD OF INSTRUCTION
BIS R5,2$ :ADD ADDRESS TO INSTRUCTION
ROMCLK :NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
    
```

2\$:

```

010000 :LOAD MAR
BIC #17,3$ :CLEAR ADDRESS OF INSTRUCTION
BIS R5,3$ :ADD ADDRESS TO INSTRUCTION
ROMCLK :NEXT WORD IS INSTRUCTION, ROMCLK PC=304
    
```

3\$:

```

040400!<4*20> :BR A PLUS C
ROMCLK :NEXT WORD IS INSTRUCTION, ROMCLK PC=5305
61224 :MOVE BR TO PORT4
    
```

```

MOV (R2), $GDDAT :PUT 'EXPECTED' IN $GDDAT
MOV 4(R1), R4 :PUT 'FOUND' IN R4
CMPB $GDDAT, R4 :DATA CORRECT?
BEQ 4$ :BR IS YES
ERROR 15 :ALU ERROR
TRAP C$ERDF
    
```

.WORD 15  
.WORD EM15

```

6690 034264 010114
6691 034266
6692 034266 104410
6693 034270 000014
6694 034272 005202
6695 034274 005205
6696 034276 022705 000010
6697 034302 001322
6698 034304
6699 034304
6700 034304 104405
6701 034306
6702 034306 104432
6703 034310 000012
6704 034312 000 000 377 5$:
6705 034315 377 125 125
6706 034320 252 252
6707
6708
6709 034322
6710 034322
6711 034322 104401
6712
6713
6714 034324
6715
6716
6717
6718
6719
6720
6721 034324
6722
6723
6724 034324
6725 034324
6726 034324
6727 034330
6728 034334 005005
6729 034336 012702 034522
6730 034342 004737 003672
6731 034346 002642
6732 034350 004737 004044
6733 034354 002652
6734 034356
6735 034356 104404
6736 034360 004737 004112
6737 034364 042737 000017 034402 1$:
6738 034372 050537 034402
6739 034376
6740 034402 010000
6741 034404 042737 000017 034422 2$:
6742 034412 050537 034422
6743 034416
6744 034422 040760 3$:
6745 034424

```

```

        .WORD  ERR15
4$:     ESCAPE  SEG
        TRAP   C$ESCAPE
        .WORD  10000$-.
        INC    R2           ;NEXT DATA
        INC    R5           ;NEXT ADDRESS
        CMP    #10,R5      ;DONE YET?
        BNE    1$          ;BR IF NO
        ENDSEG

10000$:
        TRAP   C$ESEG
        EXIT   TST
        TRAP   C$EXIT
        .WORD  L10147-.
5$:     .BYTE  0,0,-1,-1,125,125,252,252

        .EVEN
        ENDTST
L10147:
        TRAP   C$ETST

BADHEAD
:***** TEST 63 *****
:*ALU TEST
:*TEST OF ALU FUNCTION 2'S COMP SUB WITH C BIT CLEARED
:*ALU FUNCTION (A-B-1) CODE=17
:*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
:*PERFORM THE FUNCTION, VERIFY THE RESULTS
BADHEAD
:***** TEST 63 *****

BGNTST
T63::
        MYINT
        MSTCLR           ;MASTER CLEAR M8200.4,6,7
        CLR    R5        ;MEM + SP ADDRESS
        MOV    #5$,R2    ;POINTER TO CORRECT DATA
        JSR   PC,MEMLD   ;LOAD 8 WORDS OF MAIN MEMORY
        MEMDAT           ;POINTER TO DATA
        JSR   PC,SPLD    ;LOAD 8 WORDS OF SP
        SPDAT           ;POINTER TO DATA
        BGNSEG
        TRAP   C$BSEG
1$:     JSR    PC,CLRC    ;CLEAR C BIT!
        BIC   #17,2$    ;CLEAR ADDRESS FIELD OF INSTRUCTION
        BIS   R5,2$     ;ADD ADDRESS TO INSTRUCTION
        ROMCLK           ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
        BIC   #17,3$    ;LOAD MAR
        BIS   R5,3$     ;CLEAR ADDRESS OF INSTRUCTION
        ROMCLK           ;ADD ADDRESS TO INSTRUCTION
        BIC   #17,3$    ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
        BIS   R5,3$     ;BR 2'S COMP SUB
        ROMCLK 040400!<17*20>
        ROMCLK           ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304

```

```
6746 034430 061224 61224 ;MOVE BR TO PORT4
6747 034432 111237 002612 MOVB (R2),SGDDAT ;PUT 'EXPECTED' IN SGDDAT
6748 034436 116104 000004 MOVB 4(R1),R4 ;PUT 'FOUND' IN R4
6749 034442 123704 002612 CMFB SGDDAT,R4 ;DATA CORRECT?
6750 034446 001413 BEQ 4$ ;BR IS YES
6751 034450 ERROR 15 ;ALU ERROR
6752 034466 104455 TRAP C$ERDF
6753 034470 000017 .WORD 15
6754 034472 005223 .WORD EM15
6755 034474 010114 .WORD ERR15
6756 034476 4$: ESCAPE SEG
6757 034476 104410 TRAP C$ESCAPE
6758 034500 000014 .WORD 10000$-
6759 034502 005202 INC R2 ;NEXT DATA
6760 034504 005205 INC R5 ;NEXT ADDRESS
6761 034506 022705 000010 CMP #10,R5 ;DONE YET?
6762 034512 001322 BNE 1$ ;BR IF NO
6763 034514 ENDSEG
6764 034514 10000$:
6765 034514 104405 TRAP C$ESEG
6766 034516 EXIT TST
6767 034516 104432 TRAP C$EXIT
6768 034520 000012 .WORD L10150-
6769 034522 377 000 376 5$: .BYTE -1,0,376,-1,-1,252,124,-1
6770 034525 377 377 252
6771 034530 124 377
6772
6773 .EVEN
6774 034532 ENDTST
6775 034532 L10150:
6776 034532 104401 TRAP C$ETST
6777
6778
6779 034534 BADHEAD
6780 :***** TEST 64 *****
6781 :*ALU TEST
6782 :*TEST OF ALU FUNCTION DEC A WITH C BIT CLEARED
6783 :*ALU FUNCTION (A-1) CODE=7
6784 :*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
6785 :*PERFORM THE FUNCTION, VERIFY THE RESULTS
6786
6787 034534 BADHEAD
6788 :***** TEST 64 *****
6789
6790 034534 BGNTST
6791 034534 T64::
6792 034534 MYINT
6793 034540 MSTCLR
6794 034544 005005 CLR R5 ;MASTER CLEAR DMC11
6795 034546 012702 034732 MOV #5$,R2 ;MEM + SP ADDRESS
5796 034552 004737 003672 JSR PC,MEMLD ;POINTER TO CORRECT DATA
6797 034556 002642 MEMDAT ;LOAD 8 WORDS OF MAIN MEMMOR
6798 034560 004737 004044 JSR PC,SPLD ;POINTER TO DATA
6799 034564 002652 SPDAT ;LOAD 8 WORDS OF SP
6800 034566 BGNSEG ;POINTER TO DATA
6801 034566 104404 TRAP C$BSEG
```



6802	034570	004737	004112		1\$:	JSR	PC,CLRC	:CLEAR C BIT!
6803	034574	042737	000017	034612		BIC	#17,2\$	:CLEAR ADDRESS FIELD OF INSTRUCTION
6804	034602	050537	034612			BIS	R5,2\$	:ADD ADDRESS TO INSTRUCTION
6805	034606					ROMCLK		:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6806	034612	010000			2\$:	010000		:LOAD MAR
6807	034614	042737	000017	034632		BIC	#17,3\$	:CLEAR ADDRESS OF INSTRUCTION
6808	034622	050537	034632			BIS	R5,3\$	:ADD ADDRESS TO INSTRUCTION
6809	034626					ROMCLK		:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6810	034632	040560			3\$:	040400!	<7*20>	:BR DEC A
6811	034634					ROMCLK		:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6812	034640	061224				61224		:MOVE BR TO PORT4
6813	034642	111237	002612			MOVB	(R2), \$GDDAT	:PUT 'EXPECTED' IN \$GDDAT
6814	034646	116104	000004			MOVB	4(R1), R4	:PUT 'FOUND' IN R4
6815	034652	123704	002612			CMPB	\$GDDAT, R4	:DATA CORRECT?
6816	034656	001413				BEQ	4\$	:BR IF YES
6817	034660					ERROR	15	:ALU ERROR
6818	034676	104455				TRAP	C\$ERDF	
6819	034700	000017				.WORD	15	
6820	034702	005223				.WORD	EM15	
6821	034704	010114				.WORD	ERR15	
6822	034706				4\$:	ESCAPE	SEG	
6823	034706	104410				TRAP	C\$ESCAPE	
6824	034710	000014				.WORD	10000\$-	
6825	034712	005202				INC	R2	:NEXT DATA
6826	034714	005205				INC	R5	:NEXT ADDRESS
6827	034716	022705	000010			CMP	#10, R5	:DONE YET?
6828	034722	001322				BNE	1\$	:BR IF NO
6829	034724					ENDSEG		
6830	034724				10000\$:			
6831	034724	104405				TRAP	C\$ESEG	
6832	034726					EXIT	TST	
6833	034726	104432				TRAP	C\$EXIT	
6834	034730	000012				.WORD	L10151-	
6835	034732	377	377	376	5\$:	.BYTE	-1,-1,376,376,124,124,251,251	
6836	034735	376	124	124				
6837	034740	251	251					
6838								
6839						.EVEN		
6840	034742					ENDTST		
6841	034742					L10151:		
6842	034742	104401				TRAP	C\$ETST	
6843								
6844								
6845	034744					BADHEAD		
6846						:*****	TEST 65 *****	
6847						:*ALU TEST		
6848						:*TEST OF ALU FUNCTION SEL B WITH C BIT SET		
6849						:*ALU FUNCTION (B) CODE=11		
6850						:*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA		
6851						:*PERFORM THE FUNCTION, VERIFY THE RESULTS		
6852	034744					BADHEAD		
6853						:*****	TEST 65 *****	
6854								
6855	034744					BGNTST		
6856	034744					T65::		
6857	034744					MYINT		

6858	034750					MSTCLR		:MASTER CLEAR M8200,4,6,7
6859	034754	005005				CLR R5		:MEM + SP ADDRESS
6860	034756	012702	035142			MOV #5\$,R2		:POINTER TO CORRECT DATA
6861	034762	004737	003672			JSR PC,MEMLD		:LOAD 8 WORDS OF MAIN MEMORY
6862	034766	002642				MEMDAT		:POINTER TO DATA
6863	034770	004737	004044			JSR PC,SPLD		:LOAD 8 WORDS OF SP
6864	034774	002652				SPDAT		:POINTET TO DATA
6865	034776					BGNSEG		
6866	034776	104404				TRAP C\$BSEG		
6867	035000	004737	004130		1\$:	JSR PC,SETC		:SET C BIT!
6868	035004	042737	000017	035022		BIC #17,2\$		:CLEAR ADDRESS FIELD OF INSTRUCTION
6869	035012	050537	035022			BIS R5,2\$		:ADD ADDRESS TO INSTRUCTION
6870	035016					ROMCLK		:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6871	035022	010000			2\$:	010000		:LOAD MAR
6872	035024	042737	000017	035042		BIC #17,3\$		:CLEAR ADDRESS OF INSTRUCTION
6873	035032	050537	035042			BIS R5,3\$		:ADD ADDRESS TO INSTRUCTION
6874	035036					ROMCLK		:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6875	035042	040620			3\$:	040400!<11*20>		:BR SEL B
6876	035044					ROMCLK		:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6877	035050	061224				61224		:MOVE BR TO PORT4
6878	035052	111237	002612			MOVB (R2), \$GDDAT		:PUT 'EXPECTED' IN \$GDDAT
6879	035056	116104	000004			MOVB 4(R1), R4		:PUT 'FOUND' IN R4
6880	035062	123704	002612			CMPB \$GDDAT, R4		:DATA CORRECT?
6881	035066	001413				BEG 4\$		:BR IF YES
6882	035070					ERROR 23		:ALU ERROR
6883	035106	104455				TRAP C\$ERDF		
6884	035110	000027				.WORD 23		
6885	035112	005475				.WORD EM23		
6886	035114	010624				.WORD ERR23		
6887	035116				4\$:	ESCAPE SEG		
6888	035116	104410				TRAP C\$ESCAPE		
6889	035120	000014				.WORD 10000\$-		
6890	035122	005202				INC R2		:NEXT DATA
6891	035124	005205				INC R5		:NEXT ADDRESS
6892	035126	022705	000010			CMP #10, R5		:DONE YET?
6893	035132	001322				BNE 1\$		:BR IF NO
6894	035134					ENDSEG		
6895	035134				10000\$:			
6896	035134	104405				TRAP C\$ESEG		
6897	035136					EXIT TST		
6898	035136	104432				TRAP C\$EXIT		
6899	035140	000012				.WORD L10152-		
6900	035142	000	377	000	5\$:	.BYTE 0,-1,0,-1,125,252,125,252		
6901	035145	377	125	252				
6902	035150	125	252					
6903								
6904								
6905	035152					.EVEN		
6906	035152					ENDTST		
6907	035152	104401				L10152:		
6908						TRAP C\$ETST		
6909								
6910	035154					BADHEAD		
6911						:***** TEST 66 *****		
6912						:*ALU TEST		
6913						:*TEST OF ALU FUNCTION SEL A WITH C BIT SET		

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HARDWARE TESTS

SEQ 0155

6914  
6915  
6916  
6917 035154  
6918

:\*ALU FUNCTION (A) CODE=10  
:\*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA  
:\*PERFORM THE FUNCTION, VERIFY THE RESULTS  
BADHEAD  
:\*\*\*\*\* TEST 66 \*\*\*\*\*

```

6919
6920
6921 035154          BGNTST
6922 035154          T66::
6923 035154          MYINT
6924 035160          MSTCLR
6925 035164 005005  CLR          R5          ;MASTER CLEAR M8200,4,6,7
6926 035166 012702 035352  MOV          #5$,R2      ;MEM + SP ADDRESS
6927 035172 004737 003672  JSR          PC,MEMLD   ;POINTER TO CORRECT DATA
6928 035176 002642          MEMDAT
6929 035200 004737 004044  JSR          PC,SPLD   ;LOAD 8 WORDS OF MAIN MEMORY
6930 035204 002652          SPDAT
6931 035206          BGNSEG
6932 035206 104404          TRAP          C$BSEG
6933 035210 004737 004130          JSR          PC,SETC   ;SET C BIT!
6934 035214 042737 000017 035232  BIC          #17,2$     ;CLEAR ADDRESS FIELD OF INSTRUCTION
6935 035222 050537 035232          BIS          R5,2$     ;ADD ADDRESS TO INSTRUCTION
6936 035226          ROMCLK
6937 035232 010000          2$: 010000          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6938 035234 042737 000017 035252  BIC          #17,3$     ;LOAD MAR
6939 035242 050537 035252          BIS          R5,3$     ;CLEAR ADDRESS OF INSTRUCTION
6940 035246          ROMCLK
6941 035252 040600          3$: 040400! <10*20>    ;ADD ADDRESS TO INSTRUCTION
6942 035254          ROMCLK
6943 035260 061224          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6944 035262 111237 002612  MOVB        (R2), $GDDAT ;BR SEL A
6945 035266 116104 000004  MOVB        4(R1), R4    ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
6946 035272 123704 002612  CMPB        $GDDAT, R4   ;MOVE BR TO PORT4
6947 035276 001413          BEQ          4$          ;PUT 'EXPECTED' IN $GDDAT
6948 035300          ERROR 23          ;PUT 'FOUND' IN R4
6949 035316 104455          TRAP        C$ERDF     ;DATA CORRECT?
6950 035320 000027          .WORD      23          ;BR IF YES
6951 035322 005475          .WORD      EM23        ;ALU ERROR
6952 035324 010624          .WORD      ERR23
6953 035326          4$: ESCAPE SEG
6954 035326 104410          TRAP        C$ESCAPE
6955 035330 000014          .WORD      10000$-.
6956 035332 005202          INC         R2          ;NEXT DATA
6957 035334 005205          INC         R5          ;NEXT ADDRESS
6958 035336 022705 000010  CMP         #10, R5     ;DONE YET?
6959 035342 001322          BNE         1$          ;BR IF NO
6960 035344          ENDSEG
6961 035344          10000$:
6962 035344 104405          TRAP        C$ESEG
6963 035346          EXIT          TST
6964 035346 104432          TRAP        C$EXIT
6965 035350 000012          .WORD      L10153-.
6966 035352 000 000 377 5$: .BYTE      0,0,-1,-1,125,125,252,252
6967 035355 377 125 125
6968 035360 252 252
6969
6970          .EVEN
6971 035362          ENDTST
6972 035362          L10153:
6973 035362 104401          TRAP        C$ETST
6974

```

6975  
6976 035364  
6977  
6978  
6979  
6980  
6981  
6982  
6983 035364  
6984  
6985  
6986 035364  
6987 035364  
6988 035364  
6989 035370  
6990 035374 005005  
6991 035376 012702 035562  
6992 035402 004737 003672  
6993 035406 002642  
6994 035410 004737 004044  
6995 035414 002652  
6996 035416  
6997 035416 104404  
6998 035420 004737 004130  
6999 035424 042737 000017 035442  
7000 035432 050537 035442  
7001 035436  
7002 035442 010000  
7003 035444 042737 000017 035462  
7004 035452 050537 035462  
7005 035456  
7006 035462 040640  
7007 035464  
7008 035470 061224  
7009 035472 111237 002612  
7010 035476 116104 000004  
7011 035502 123704 002612  
7012 035506 001413  
7013 035510  
7014 035526 104455  
7015 035530 000017  
7016 035532 005223  
7017 035534 010114  
7018 035536  
7019 035536 104410  
7020 035540 000014  
7021 035542 005202  
7022 035544 005205  
7023 035546 022705 000010  
7024 035552 001322  
7025 035554  
7026 035554  
7027 035554 104405  
7028 035556  
7029 035556 104432  
7030 035560 000012

BADHEAD

:\*\*\*\*\* TEST 67 \*\*\*\*\*  
:\*ALU TEST  
:\*TEST OF ALU FUNCTION A OR NOTB WITH C BIT SET  
:\*ALU FUNCTION (A OR NOTB) CODE=12  
:\*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA  
:\*PERFORM THE FUNCTION, VERIFY THE RESULTS  
BADHEAD  
:\*\*\*\*\* TEST 67 \*\*\*\*\*

BGNTST  
T67::

MYINT

MSTCLR

CLR R5

MOV #5\$,R2

JSR PC,MEMLD

MEMDAT

JSR PC,SPLD

SPDAT

BGNSEG

TRAP C\$BSEG

JSR PC,SEIC

BIC #17,2\$

BIS R5,2\$

ROMCLK

010000

BIC #17,3\$

BIS R5,3\$

ROMCLK

040400!<12\*20>

ROMCLK

61224

MOVB (R2), \$GDDAT

MOVB 4(R1),R4

CMPB \$GDDAT,R4

BEQ 4\$

ERROR 15

TRAP C\$ERDF

.WORD 15

.WORD EM15

.WORD ERR15

ESCAPE SEG

TRAP C\$ESCAPE

.WORD 10000\$-

INC R2

INC R5

CMP #10,R5

BNE 1\$

ENDSEG

TRAP C\$ESEG

EXIT TST

TRAP C\$EXIT

.WORD L10154-

:MASTER CLEAR M8200.4,6,7

:MEM + SP ADDRESS

:POINTER TO CORRECT DATA

:LOAD 8 WORDS OF MAIN MEMORY

:POINTER TO DATA

:LOAD 8 WORDS OF SP

:POINTER TO DATA

:SET C BIT!

:CLEAR ADDRESS FIELD OF INSTRUCTION

:ADD ADDRESS TO INSTRUCTION

:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304

:LOAD MAR

:CLEAR ADDRESS OF INSTRUCTION

:ADD ADDRESS TO INSTRUCTION

:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304

:BR A OR NOTB

:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304

:MOVE BR TO PORT4

:PUT 'EXPECTED' IN \$GDDAT

:PUT 'FOUND' IN R4

:DATA CORRECT?

:BR IF YES

:ALU ERROR

:NEXT DATA

:NEXT ADDRESS

:DONE YET?

:BR IF NO

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7031 035562 377 000 377 5$: .BYTE -1,0,-1,-1,-1,125,252,-1
7032 035565 377 377 125
7033 035570 252 377
7034
7035
7036 035572 .EVEN
7037 035572 ENDTST
7038 035572 104401 L10154:
7039 TRAP C$ETST
7040
7041 035574 BADHEAD
7042 :***** TEST 68 *****
7043 :*ALU TEST
7044 :*TEST OF ALU FUNCTION A AND B WITH C BIT SET
7045 :ALU FUNCTION (A AND B) CODE='3
7046 :*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
7047 :*PERFORM THE FUNCTION, VERIFY THE RESULTS
7048 035574 BADHEAD
7049 :***** TEST 68 *****
7050
7051 035574 BGNTST
7052 035574 T68::
7053 035574 MYINT
7054 035600 MSTCLR ;MASTER CLEAR M8200,4,6,7
7055 035604 005005 CLR R5 ;MEM + SP ADDRESS
7056 035606 012702 035772 MOV #5$,R2 ;POINTER TO CORRECT ADDRESS
7057 035612 004737 003672 JSR PC,MEMLD ;LOAD 8 WORDS OF MAIN MEMORY
7058 035616 002642 MEMDAT ;POINTER TO DATA
7059 035620 004737 004044 JSR PC,SPLD ;LOAD 8 WORDS OF SP
7060 035624 002652 SPDAT ;POINTER TO DATA
7061 035626
7062 035626 104404 BGNSEG
7063 035630 004737 004130 TRAP C$BSEG
7064 035634 042737 000017 035652 1$: JSR PC,SETC ;SET C BIT!
7065 035642 050537 035652 BIC #17,2$ ;CLEAR ADDRESS FIELD OF INSTRUCTION
7066 035646 BIS R5,2$ ;ADD ADDRESS TO INSTRUCTION
7067 035652 010000 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7068 035654 042737 000017 035672 2$: 010000 ;LOAD MAR
7069 035662 050537 035672 BIC #17,3$ ;CLEAR ADDRESS OF INSTRUCTION
7070 035666 BIS R5,3$ ;ADD ADDRESS TO INSTRUCTION
7071 035672 040660 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7072 035674 ROMCLK 040400!<13*20> ;BR A AND B
7073 035700 061224 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7074 035702 111237 002612 61224 ;MOVE BR TO PORT4
7075 035706 116104 000004 MOVB (R2), $GDDAT ;PUT 'EXPECTED' IN $GDDAT
7076 035712 123704 002612 MOVB 4(R1), R4 ;PUT 'FOUND' IN R4
7077 035716 001413 CMPB $GDDAT, R4 ;DATA CORRECT?
7078 035720 BEQ 4$ ;BR IF YES
7079 035736 104455 ERROR 23 ;ALU ERROR
7080 035740 000027 TRAP C$ERDF
7081 035742 005475 .WORD 23
7082 035744 010624 .WORD EM23
7083 035746 .WORD ERR23
7084 035746 104410 4$: ESCAPE SEG
7085 035750 000014 TRAP C$ESCAPE
7086 035752 005202 .WORD 10000$-
INC R2 ;NEXT DATA

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7087	035754	005205				INC	R5		:NEXT ADDRESS
7088	035756	022705	000010			CMP	#10,R5		:DONE YET?
7089	035762	001322				BNE	1\$		:BR IF NO
7090	035764					ENDSEG			
7091	035764				10000\$:				
7092	035764	104405				TRAP	C\$ESEG		
7093	035766					EXIT	TST		
7094	035766	104432				TRAP	C\$EXIT		
7095	035770	000012				.WORD	L10155-		
7096	035772	000	000	000	5\$:	.BYTE	0,0,0,-1,125,0,0,252		
7097	035775	377	125	000					
7098	036000	000	252						
7099									
7100						.EVEN			
7101	036002					ENDTST			
7102	036002					L10155:			
7103	036002	104401				TRAP	C\$ETST		
7104									
7105									
7106	036004					BADHEAD			
7107						:*****	TEST 69	*****	
7108						:*ALU TEST			
7109						:*TEST OF ALU FUNCTION A OR B WITH C BIT SET			
7110						:*ALU FUNCTION (A OR B) CODE=14			
7111						:*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA			
7112						:*PERFORM THE FUNCTION, VERIFY THE RESULTS			
7113	036004					BADHEAD			
7114						:*****	TEST 69	*****	
7115									
7116	036004					BGNTST			
7117	036004					T69::			
7118	036004					MYINT			
7119	036010					MSTCLR			:MASTER CLEAR M8200,4,6,7
7120	036014	005005				CLR	R5		:MEM + SP ADDRESS
7121	036016	012702	036202			MOV	#5\$,R2		:POINTER TO CORRECT DATA
7122	036022	004737	003672			JSR	PC,MEMLD		:LOAD 8 WORDS OF MAIN MEMORY
7123	036026	002642				MEMDAT			:POINTER TO DATA
7124	036030	004737	004044			JSR	PC,SPLD		:LOAD 8 WORDS OF SP
7125	036034	002652				SPDAT			:POINTER TO DATA
7126	036036					BGNSEG			
7127	036036	104404				TRAP	C\$BSEG		
7128	036040	004737	004130		1\$:	JSR	PC,SETC		:SET C BIT!
7129	036044	042737	000017	036062		BIC	#17,2\$		:CLEAR ADDRESS FIELD OF INSTRUCTION
7130	036052	050537	036062			BIS	R5,2\$		:ADD ADDRESS TO INSTRUCTION
7131	036056					ROMCLK			:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7132	036062	010000			2\$:	010000			:LOAD MAR
7133	036064	042737	000017	036102		BIC	#17,3\$		:CLEAR ADDRESS OF INSTRUCTION
7134	036072	050537	036102			BIS	R5,3\$		:ADD ADDRESS TO INSTRUCTION
7135	036076					ROMCLK			:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7136	036102	040700			3\$:	040400!<14*20>			:BR A OR B
7137	036104					ROMCLK			:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7138	036110	061224				61224			:MOVE BR TO PORT4
7139	036112	111237	002612			MOVB	(R2), \$GDDAT		:PUT 'EXPECTED' IN R4
7140	036116	116104	000004			MOVB	4(R1), R4		:PUT 'FOUND' IN R4
7141	036122	123704	002612			CMPB	\$GDDAT, R4		:DATA CORRECT?
7142	036126	001413				BEQ	4\$		:BR IF YES

7143	036130					ERROR	23		:ALU ERROR
7144	036146	104455				TRAP	C\$ERDF		
7145	036150	000027				.WORD	23		
7146	036152	005475				.WORD	EM23		
7147	036154	010624				.WORD	ERR23		
7148	036156				4\$:	ESCAPE	SEG		
7149	036156	104410				TRAP	C\$ESCAPE		
7150	036160	000014				.WORD	10000\$-		
7151	036162	005202				INC	R2		:NEXT DATA
7152	036164	005205				INC	R5		:NEXT ADDRESS
7153	036166	022705	000010			CMF	#10,R5		:DONE YET?
7154	036172	001322				BNE	1\$		:BR IF NO
7155	036174					ENDSEG			
7156	036174				10000\$:				
7157	036174	104405				TRAP	C\$ESEG		
7158	036176					EXIT	TST		
7159	036176	104432				TRAP	C\$EXIT		
7160	036200	000012				.WORD	L10156-		
7161	036202	000	377	377	5\$:	.BYTE	0,-1,-1,-1,125,-1,-1,252		
7162	036205	377	125	377					
7163	036210	377	252						
7164									
7165						.EVEN			
7166	036212					ENDTST			
7167	036212					L10156:			
7168	036212	104401				TRAP	C\$ETST		
7169									
7170									
7171	036214					BADHEAD			
7172						:*****	TEST 70	*****	
7173						:*ALU TEST			
7174						:*TEST OF ALU FUNCTION A XOR B WITH C BIT SET			
7175						:*ALU FUNCTION (A XOR B) CODE=15			
7176						:*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA			
7177						:*PERFORM THE FUNCTION, VERIFY THE RESULTS			
7178									
7179	036214					BADHEAD			
7180						:*****	TEST 70	*****	
7181	036214					BGNTST			
7182	036214				170::				
7183	036214					MYINT			
7184	036220					MSTCLR			:MASTER CLEAR M8200,4,6,7
7185	036224	005005				CLR	R5		:MEM + SP ADDRESS
7186	036226	012702	036412			MOV	#5\$,R2		:POINTER TO CORRECT DATA
7187	036232	004737	003672			JSR	PC,MEMLD		:LOAD 8 WORDS OF MAIN MEMORY
7188	036236	002642				MEMDAT			:POINTER TO DATA
7189	036240	004737	004044			JSR	PC,SPLD		:LOAD 8 WORDS OF SP
7190	036244	002652				SPDAT			:POINTER TO DATA
7191	036246					BGNSEG			
7192	036246	104404				TRAP	C\$BSEG		
7193	036250	004737	004130		1\$:	JSR	PC,SETC		:SET C BIT!
7194	036254	042737	000017	036272		BIC	#17,2\$		:CLEAR ADDRESS FIELD OF INSTRUCTION
7195	036262	050537	036272			BIS	R5,2\$		:ADD ADDRESS TO INSTRUCTION
7196	036266					ROMCLK			:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7197	036272	010000			2\$:	010000			:LOAD MAR
7198	036274	042737	000017	036312		BIC	#17,3\$		:CLEAR ADDRESS OF INSTRUCTION



7199	036302	050537	036312	BIS	R5,3\$		:ADD ADDRESS TO INSTRUCTION
7200	036306			ROMCLK			:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7201	036312	040720		3\$:	040400!<15*20>		:BR A XOR B
7202	036314			ROMCLK			:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7203	036320	061224			61224		:MOVE BR TO PORT4
7204	036322	111237	002612	MOVB	(R2),\$GDDAT		:PUT 'EXPECTED' IN \$GDDAT
7205	036326	116104	000004	MOVB	4(R1),R4		:PUT 'FOUND' IN R4
7206	036332	123704	002612	CMPB	\$GDDAT,R4		:DATA CORRECT?
7207	036336	001413		BEQ	4\$		:BR IF YES
7208	036340			ERROR	23		:ALU ERROR
7209	036356	104455		TRAP	C\$ERDF		
7210	036360	000027		.WORD	23		
7211	036362	005475		.WORD	EM23		
7212	036364	010624		.WORD	ERR23		
7213	036366			4\$:	ESCAPE	SEG	
7214	036366	104410		TRAP	C\$ESCAPE		
7215	036370	000014		.WORD	10000\$-		
7216	036372	005202		INC	R2		:NEXT DATA
7217	036374	005205		INC	R5		:NEXT ADDRESS
7218	036376	022705	000010	CMF	#10,R5		:DONE YET?
7219	036402	001322		BNE	1\$		:BR IF NO
7220	036404				ENDSEG		
7221	036404			10000\$:			
7222	036404	104405		TRAP	C\$ESEG		
7223	036406			EXIT	TST		
7224	036406	104432		TRAP	C\$EXIT		
7225	036410	000012		.WORD	L10157-		
7226	036412	000	377	5\$:	.BYTE	0,-1,-1,0,0,-1,-1,0	
7227	036415	000	000				
7228	036420	377	000				
7229							
7230							
7231	036422			.EVEN			
7232	036422			ENDTST			
7233	036422	104401		L10157:			
7234				TRAP	C\$ETST		
7235							
7236	036424			BADHEAD			
7237				:*****	TEST 71	*****	
7238				:*	ALU TEST		
7239				:*	TEST OF ALU FUNCTION ADD WITH C BIT SET		
7240				:*	ALU FUNCTION (A PLUS B) CODE=00		
7241				:*	LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA		
7242				:*	PERFORM THE FUNCTION, VERIFY THE RESULTS		
7243	036424			BADHEAD			
7244				:*****	TEST 71	*****	
7245							
7246	036424			BGNTST			
7247	036424			T71::			
7248	036424			MYINT			
7249	036430			MSTCLR			:MASTER CLEAR M8200,4,6,7
7250	036434	005005		CLR	R5		:MEM + SP ADDRESS
7251	036436	012702	036622	MOV	#5\$,R2		:POINTER TO CORRECT DATA
7252	036442	004737	003672	JSR	PC,MEMLD		:LOAD 8 WORDS OF MAIN MEMORY
7253	036446	002642		MEMDAT			:POINTER TO DATA
7254	036450	004737	004044	JSR	PC,SPLD		:LOAD 8 WORDS OF SP

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7255 036454 002652          SPDAT          :OOINTER TO DATA
7256 036456                BGNSEG
7257 036456 104404          TRAP          CSBSEG
7258 036460 004737 004130  036502 1$: JSR          PC,SETC  :SET C BIT!
7259 036464 042737 000017  BIC          #17,2$  :CLEAR ADDRESS FIELD OF INSTRUCTION
7260 036472 050537 036502  BIS          R5,2$   :ADD ADDRESS TO INSTRUCTION
7261 036476                ROMCLK
7262 036502 010000          010000         :NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7263 036504 042737 000017  036522 2$: BIC          #17,3$  :LOAD MAR
7264 036512 050537 036522  BIS          R5,3$   :CLEAR ADDRESS OF INSTRUCTION
7265 036516                ROMCLK
7266 036522 040400          040400!<00*20> :ADD ADDRESS TO INSTRUCTION
7267 036524                ROMCLK
7268 036530 061224          61224          :NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7269 036532 111237 002612  MOVB        (R2),%GDDAT :BR ADD
7270 036536 116104 000004  MOVB        4(R1),R4    :NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7271 036542 123704 002612  CMPB        %GDDAT,R4  :MOVE BR TO PORT4
7272 036546 001413          BEQ          4$        :PUT 'EXPECTED' IN %GDDAT
7273 036550                ERROR          23          :PUT 'FOUND' IN R4
7274 036566 104455          TRAP        C$ERDF    :DATA CORRECT?
7275 036570 000027          .WORD      23
7276 036572 005475          .WORD      EM23
7277 036574 010624          .WORD      ERR23
7278 036576                4$: ESCAPE      SEG
7279 036576 104410          TRAP        C$ESCAPE  :BR IF YES
7280 036600 000014          .WORD      10000$-   :ALU ERROR
7281 036602 005202          INC         R2
7282 036604 005205          INC         R5
7283 036606 022705 000010  CMP         #10,R5     :NEXT DATA
7284 036612 001322          BNE        1$
7285 036614                ENDSEG
7286 036614                10000$:
7287 036614 104405          TRAP        C$ESEG
7288 036616                EXIT          TST
7289 036616 104432          TRAP        C$EXIT
7290 036620 000012          .WORD      L10160-
7291 036622 000 377 377 5$: .BYTE      0,-1,-1,376,252,-1,-1,124
7292 036625 376 252
7293 036630 377 124
7294
7295          .EVEN
7296 036632          ENDTST
7297 036632          L10160:
7298 036632 104401          TRAP        C$ETST
7299
7300
7301 036634          BADHEAD
7302          :***** TEST 72 *****
7303          :*ALU TEST
7304          :*TEST OF ALU FUNCTION 2A W/C WITH C BIT SET
7305          :*ALU FUNCTION (A PLUS A PLUS C) CODE=6
7306          :*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
7307          :*PERFORM THE FUNCTION, VERIFY THE RESULTS
7308 036634          BADHEAD
7309          :***** TEST 72 *****
7310

```

```
7311 036634          BGNST
7312 036634          T72::
7313 036634
7314 036640
7315 036644 005005
7316 036646 012702 037032
7317 036652 004737 003672
7318 036656 002642
7319 036660 004737 004044
7320 036664 002652
7321 036666
7322 036666 104404
7323 036670 004737 004130
7324 036674 042737 000017 036712 1$:
7325 036702 050537 036712
7326 036706
7327 036712 010000
7328 036714 042737 000017 036732 2$:
7329 036722 050537 036732
7330 036726
7331 036732 040540 3$:
7332 036734
7333 036740 061224
7334 036742 111237 002612
7335 036746 116104 000004
7336 036752 123704 002612
7337 036756 001413
7338 036760
7339 036776 104455
7340 037000 000027
7341 037002 005475
7342 037004 010624
7343 037006
7344 037006 104410
7345 037010 000014
7346 037012 005202
7347 037014 005205
7348 037016 022705 000010
7349 037022 001322
7350 037024
7351 037024
7352 037024 104405 10000$:
7353 037026
7354 037026 104432
7355 037030 000012
7356 037032 001 001 377 5$:
7357 037035 377 253 253
7358 037040 125 125
7359
7360
7361 037042
7362 037042
7363 037042 104401
7364
7365
7366 037044          BADHEAD

MYINT
MSTCLR
CLR R5
MOV #5$,R2
JSR PC,MEMLD
MEMDAT
JSR PC,SPLD
SPDAT
BGNSEG
TRAP C$BSEG
JSR PC,SETC
BIC #17,2$
BIS R5,2$
ROMCLK
010000
BIC #17,3$
BIS R5,3$
ROMCLK
040400! <6*20>
ROMCLK
61224
MOVB (R2), $GDDAT
MOVB 4(R1), R4
CMPB $GDDAT, R4
BEQ 4$
ERROR 23
TRAP C$ERDF
.WORD 23
.WORD EM23
.WORD ERR23
4$: ESCAPE SEG
TRAP C$ESCAPE
.WORD 10000$-.
INC R2
INC R5
CMP #10, R5
BNE 1$
ENDSEG
10000$:
TRAP C$ESEG
EXIT TST
TRAP C$EXIT
.WORD L10161-.
.BYTE 1,1,-1,-1,253,253,125,125

:MASTER CLEAR M8200,4,6,7
:MEM + SP ADDRESS
:POINTER TO CORRECT DATA
:LOAD 8 WORDS OF MAIN MEMORY
:POINTER TO DATA
:LOAD 8 WORDS OF SP
:POINTER TO DATA
:SET C BIT!
:CLEAR ADDRESS FIELD OF INSTRUCTION
:ADD ADDRESS TO INSTRUCTION
:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
:LOAD MAR
:CLEAR ADDRESS OF INSTRUCTION
:ADD ADDRESS TO INSTRUCTION
:NEXT WORD IS INSTRUCTION, ROMCLK PC=55304
:BR 2A W/C
:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
:MOVE BR TO PORT4
:PUT 'WXPECTED' IN $GDDAT
:PUT 'FOUND' IN R4
:DATA CORRECT?
:BR IF YES
:ALU ERROR
:NEXT DATA
:NEXT ADDRESS
:DONE YET?
:BR IF NO

.EVEN
ENDTST
L10161:
TRAP C$ETST
```

```

7367 :***** TEST 73 *****
7368 :*ALU TEST
7369 :*TEST OF ALU FUNCTION SUB WITH C BIT SET
7370 :*ALU FUNCTION (A-B) CODE=16
7371 :*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
7372 :*PERFORM THE FUNCTION, VERIFY THE RESULTS
7373 037044 BADHEAD
7374 :***** TEST 73 *****
7375
7376 037044 BGNTST
7377 037044 T73::
7378 037044 MYINT
7379 037050 MSTCLR ;MASTER CLEAR M8200,4,6,7
7380 037054 005005 CLR R5 ;MEM + SP ADDRESS
7381 037056 012702 037242 MOV #5$,R2 ;POINTER TO CORRECT DATA
7382 037062 004737 003672 JSR PC,MEMLD ;LOAD 8 WORDS OF MAIN MEMORY
7383 037066 002642 MEMDAT ;POINTER TO DATA
7384 037070 004737 004044 JSR PC,SPLD ;LOAD 8 WORDS OF SP
7385 037074 002652 SPDAT ;POINTER TO DATA
7386 037076 BGNSEG
7387 037076 104404 TRAP C$BSEG
7388 037100 004737 004130 1$: JSR PC,SETC ;SET C BIT!
7389 037104 042737 000017 037122 BIC #17,2$ ;CLEAR ADDRESS FIELD OF INSTRUCTION
7390 037112 050537 037122 BIS R5,2$ ;ADD ADDRESS TO INSTRUCTION
7391 037116 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7392 037122 010000 2$: 010000 ;LOAD MAR
7393 037124 042737 000017 037142 BIC #17,3$ ;CLEAR ADDRESS OF INSTRUCTION
7394 037132 050537 037142 BIS R5,3$ ;ADD ADDRESS TO INSTRUCTION
7395 037136 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7396 037142 040740 3$: 040400!<16*20> ;BR SUB
7397 037144 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7398 037150 061224 61224 ;MOVE BR TO PORT4
7399 037152 111237 002612 MOVB (R2), $GDDAT ;PUT "EXPECTED" IN $GDDAT
7400 037156 116104 000004 MOVB 4(R1), R4 ;PUT "FOUND" IN R4
7401 037162 123704 002612 CMPB $GDDAT, R4 ;DATA CORRECT?
7402 037166 001413 BEQ 4$ ;BR IF YES
7403 037170 ERROR 23 ;ALU ERROR
7404 037206 104455 TRAP C$ERDF
7405 037210 000027 .WORD 23
7406 037212 005475 .WORD EM23
7407 037214 010624 .WORD ERR23
7408 037216 4$: ESCAPE SEG
7409 037216 104410 TRAP C$ESCAPE
7410 037220 000014 .WORD 10000$-.
7411 037222 005202 INC R2 ;NEXT DATA
7412 037224 005205 INC R5 ;NEXT ADDRESS
7413 037226 022705 000010 CMP #10, R5 ;DONE YET?
7414 037232 001322 BNE 1$ ;BR IF NO
7415 037234 ENDSEG
7416 037234 10000$:
7417 037234 104405 TRAP C$ESEG
7418 037236 EXIT TST
7419 037236 104432 TRAP C$EXIT
7420 037240 000012 .WORD L10162-.
7421 037242 000 001 377 5$: .BYTE 0,1,-1,0,0,253,125,0
7422 037245 000 000 253

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7423	037250	125	000		
7424					
7425					
7426	037252				
7427	037252				
7428	037252	104401			
7429					
7430					
7431	037254				
7432					
7433					
7434					
7435					
7436					
7437					
7438	037254				
7439					
7440					
7441	037254				
7442	037254				
7443	037254				
7444	037260				
7445	037264	005005			
7446	037266	012702	037452		
7447	037272	004737	003672		
7448	037276	002642			
7449	037300	004737	004044		
7450	037304	002652			
7451	037306				
7452	037306	104404			
7453	037310	004737	004130		
7454	037314	042737	000017	037332	
7455	037322	050537	037332		
7456	037326				
7457	037332	010000			
7458	037334	042737	000017	037352	
7459	037342	050537	037352		
7460	037346				
7461	037352	040420			
7462	037354				
7463	037360	061224			
7464	037362	111237	002612		
7465	037366	116104	000004		
7466	037372	123704	002612		
7467	037376	001413			
7468	037400				
7469	037416	104455			
7470	037420	000027			
7471	037422	005475			
7472	037424	010624			
7473	037426				
7474	037426	104410			
7475	037430	000014			
7476	037432	005202			
7477	037434	005205			
7478	037436	022705	000010		

.EVEN  
 ENDTST  
 L10162:  
 TRAP CSETST

BADHEAD  
 :\*\*\*\*\* TEST 74 \*\*\*\*\*  
 :\*ALU TEST  
 :\*TEST OF ALU FUNCTION ADD W/C WITH C BIT SET  
 :\*ALU FUNCTION (A PLUS B PLUS C) CODE=01  
 :\*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA  
 :\*PERFORM THE FUNCTION, VERIFY THE RESULTS  
 BADHEAD  
 :\*\*\*\*\* TEST 74 \*\*\*\*\*

BGNTST  
 T74::  
 MYINT  
 MSTCLR ;MASTER CLEAR M8200.4,6,7  
 CLR R5 ;MEM +SP ADDRESS  
 MOV #5\$,R2 ;POINTER TO CORRECT DATA  
 JSR PC,MEMLD ;LOAD 8 WORDS OF MAIN MEMORY  
 MEMDAT ;POINTER TO DATA  
 JSR PC,SPLD ;LOAD 8 WORDS OF SP  
 SPDAT ;POINTER TO DATA  
 BGNSEG  
 TRAP C\$BSEG  
 JSR PC,SETC ;SET C BIT!  
 BIC #17,2\$ ;CLEAR ADDRESS FIELD OF INSTRUCTION  
 BIS R5,2\$ ;ADD ADDRESS TO INSTRUCTION  
 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304  
 010000 ;LOAD MAR  
 2\$: BIC #17,3\$ ;CLEAR ADDRESS OF INSTRUCTION  
 BIS R5,3\$ ;ADD ADDRESS TO INSTRUCTION  
 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304  
 040400!<01\*20> ;BR - ADD W/C  
 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304  
 61224 ;MOVE BR TO PORT4  
 MOVB (R2),SGDDAT ;PUT 'EXPECTED' IN SGDDAT  
 MOVB 4(R1),R4 ;PUT 'FOUND' IN R4  
 CMPB SGDDAT,R4 ;DATA CORRECT?  
 BEQ 4\$ ;BR IF YES  
 ERROR 23 ;ALU ERROR  
 TRAP C\$ERDF  
 .WORD 23  
 .WORD EM23  
 .WORD ERR23  
 4\$: ESCAPE SEG  
 TRAP C\$ESCAPE  
 .WORD 10000\$-  
 INC R2 ;NEXT DATA  
 INC R5 ;NEXT ADDRESS  
 CMP #10,R5 ;DONE YET?

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7479 037442 001322          BNE      1$          ;BR IF NO
7480 037444                ENDSEG
7481 037444                10000$:
7482 037444 104405          TRAP     C$ESEG
7483 037446                EXIT     TST
7484 037446 104432          TRAP     C$EXIT
7485 037450 000012          .WORD   L10163-
7486 037452 001 000 000 5$: .BYTE   1,0,0,-1,253,0,0,125
7487 037455 377 253 000
7488 037460 000 125
7489
7490                .EVEN
7491 037462                ENDTST
7492 037462                L10163:
7493 037462 104401          TRAP     C$ETST
7494
7495
7496 037464                BADHEAD
7497                :***** TEST 75 *****
7498                :*ALU TEST
7499                :*TEST OF ALU FUNCTION SUB W/C WITH C BIT SET
7500                :*ALU FUNCTION (A-B-C) CODE=2
7501                :*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
7502                :*PERFORM THE FUNCTION, VERIFY THE RESULTS
7503 037464                BADHEAD
7504                :***** TEST 75 *****
7505
7506
7507 037464                BGNST
7508 037464                T75::
7509 037464                MYINT
7510 037470                MSTCLR          ;MASTER CLEAR M8200,4,6,7
7511 037474 005005          CLR      R5          ;MEM + SP ADDRESS
7512 037476 012702 037662  MOV     #5$,R2      ;POINTER TO CORRECT DATA
7513 037502 004737 003672  JSR     PC,MEMLD    ;LOAD 8 WORDS OF MAIN MEMORY
7514 037506 002642          MEMDAT          ;POINTER TO DATA
7515 037510 004737 004044  JSR     PC,SPLD    ;LOAD 8 WORDS OF SP
7516 037514 002652          SPDAT          ;POINTER TO DATA
7517 037516
7518 037516 104404          BGNSEG
7519 037520 004737 004130  TRAP     C$BSEG
7520 037524 042737 000017 037542 1$: JSR     PC,SETC    ;SET C BIT!
7521 037532 050537 037542  BIC     #17,2$     ;CLEAR ADDRESS FIELD OF INSTRUCTION
7522 037536                BIS     R5,2$      ;ADD ADDRESS TO INSTRUCTION
7523 037542 010000          ROMCLK          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7524 037544 042737 000017 037562 2$: BIC     #17,3$     ;CLEAR ADDRESS OF INSTRUCTION
7525 037552 050537 037562  BIS     R5,3$      ;ADD ADDRESS TO INSTRUCTION
7526 037556                ROMCLK          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=55304
7527 037562 040440          ROMCLK!<2*20>    ;BR SUB W/C
7528 037564                ROMCLK          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7529 037570 061224          61224          ;MOVE BR TO PORT4
7530 037572 111237 002612  MOV     (R2),%GDDAT ;PUT 'EXPECTED' IN %GDDAT
7531 037576 116104 000004  MOV     4(R1),R4    ;PUT 'FOUND' IN R4
7532 037602 123704 002612  CMP     %GDDAT,R4  ;DATA CORRECT?
7533 037606 001413          BEQ     4$          ;BR IF YES
7534 037610                ERROR    23          ;ALU ERROR

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7535 037626 104455          TRAP  C$ERDF
7536 037630 000027          .WORD 23
7537 037632 005475          .WORD EM23
7538 037634 010624          .WORD ERR23
7539 037636          4$:  ESCAPE  SEG
7540 037636 104410          TRAP  C$ESCAPE
7541 037640 000014          .WORD 10000$-.
7542 037642 005202          INC   R2          ;NEXT DATA
7543 037644 005205          INC   R5          ;NEXT ADDRESS
7544 037646 022705 000010      CMP   #10,R5     ;DONE YET?
7545 037652 001322          BNE   1$          ;BR IF NO
7546 037654          ENDSEG
7547 037654          10000$:
7548 037654 104405          TRAP  C$ESEG
7549 037656          EXIT  TST
7550 037656 104432          TRAP  C$EXIT
7551 037660 000012          .WORD L10164-.
7552 037662 000 001 377 5$: .BYTE 0,1,-1,0,0,253,125,0
7553 037665 000 000 253
7554 037670 125 000
7555
7556          .EVEN
7557 037672          ENDTST
7558 037672          L10164:
7559 037672 104401          TRAP  C$ETST
7560
7561
7562 037674          BADHEAD
7563          :***** TEST 76 *****
7564          :*ALU TEST
7565          :*TEST OF ALU FUNCTION INC A WITH C BIT SET
7566          :*ALU FUNCTION (A PLUS 1) CODE=3
7567          :*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
7568          :*PERFORM THE FUNCTION, VERIFY THE RESULTS
7569 037674          BADHEAD
7570          :***** TEST 76 *****
7571
7572 037674          BGNTST
7573 037674          T76::
7574 037674
7575 037700          MYINT
7576 037704 005005          MSTCLR          ;MASTER CLEAR M8200,4,6,7
7577 037706 012702 040072      CLR   R5          ;MEM + SP ADDRESS
7578 037712 004737 003672      MOV  #5$,R2      ;POINTER TO CORRECT DATA
7579 037716 002642          JSR  PC,MEMLD    ;LOAD 8 WORDS OF MAIN MEMORY
7580 037720 004737 004044      MEMDAT          ;POINTER TO DATA
7581 037724 002652          JSR  PC,SPLD    ;LOAD 8 WORDS OF SP
7582 037726          SPDAT          ;POINTER TO DATA
7583 037726 104404          BGNSEG
7584 037730 004737 004130          TRAP  C$BSEG
7585 037734 042737 000017 037752 1$: JSR  PC,SETC    ;SET C BIT!
7586 037742 050537 037752      BIC  #17,2$     ;CLEAR ADDRESS FIELD OF INSTRUCTION
7587 037746          ROMCLK          ;ADD ADDRESS TO INSTRUCTION
7588 037752 010000          2$: 010000     ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7589 037754 042737 000017 037772 1$: BIC  #17,3$     ;LOAD MAR
7590 037762 050537 037772      BIS  R5,3$     ;CLEAR ADDRESS OF INSTRUCTION
              ;ADD ADDRESS TO INSTRUCTION

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7591	037766					ROMCLK			:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7592	037772	040460				040400!<3*20>			:BR INC A
7593	037774					ROMCLK			:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7594	040000	061224				61224			:MOVE BR TO PORT4
7595	040002	111237	002612			MOVE (R2),SGDDAT			:PUT 'EXPECTED' IN SGDDAT
7596	040006	116104	000004			MOVB 4(R1),R4			:PUT 'FOUND IN R4
7597	040012	123704	002612			CMPB \$GDDAT,R4			:DATA CORRECT?
7598	040016	001413				BEG 4\$			:BR IF YES
7599	040020					ERROR 23			:ALU ERROR
7600	040036	104455				TRAP C\$ERDF			
7601	040040	000027				.WORD 23			
7602	040042	005475				.WORD EM23			
7603	040044	010624				.WORD ERR23			
7604	040046					4\$: ESCAPE SEG			
7605	040046	104410				TRAP C\$ESCAPE			
7606	040050	000014				.WORD 10000\$-			
7607	040052	005202				INC R2			:NEXT DATA
7608	040054	005205				INC R5			:NEXT ADDRESS
7609	040056	022705	000010			CMP #10,R5			:DONE YET?
7610	040062	001322				BNE 1\$			:BR IF NO
7611	040064					ENDSEG			
7612	040064					10000\$: TRAP C\$ESEG			
7613	040064	104405				EXIT TST			
7614	040066					TRAP C\$EXIT			
7615	040066	104432				.WORD L10165-			
7616	040070	000012				5\$: .BYTE 1,1,0,0,126,126,253,253			
7617	040072	001	001	000					
7618	040075	000	126	126					
7619	040100	253	253						
7620									
7621									
7622	040102					.EVEN			
7623	040102					ENDTST			
7624	040102	104401				L10165: TRAP C\$ETST			
7625									
7626									
7627	040104					BADHEAD			
7628						:***** TEST 77 *****			
7629						:*ALU TEST			
7630						:*TEST OF ALU FUNCTION 2A WITH C BIT SET			
7631						:*ALU FUNCTION (A PLUS A) CODE=5			
7632						:*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA			
7633						:*PERFORM THE FUNCTION, VERIFY THE RESULTS			
7634	040104					BADHEAD			
7635						:***** TEST 77 *****			
7636									
7637	040104					BGNTST			
7638	040104					T77::			
7639	040104					MYINT			
7640	040110					MSTCLR			:MASTER CLEAR M8200,4,6,7
7641	040114	005005				CLR R5			:MEM + SP ADDRESS
7642	040116	012702	040302			MOV #5\$,R2			:POINTER TO CORRECT DATA
7643	040122	004737	003672			JSR PC,MEMLD			:LOAD 8 WORDS OF MAIN MEMORY
7644	040126	002642				MEMDAT			:POINTER TO DATA
7645	040130	004737	004044			JSR PC,SPLD			:LOAD 8 WORDS OF SP
7646	040134	002652				SPDAT			:POINTER TO DATA



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7647 040136          BGNSEG
7648 040136 104404  TRAP      C$BSEG
7649 040140 004737 004130 1$:      JSR      PC,SETC      ;SET C BIT!
7650 040144 042737 000017 040162 BIC      #17,2$      ;CLEAR ADDRESS FIELD OF INSTRUCTION
7651 040152 050537 040162 BIS      R5,2$      ;ADD ADDRESS TO INSTRUCTION
7652 040156          ROMCLK          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7653 040162 010000 2$:      LOAD MAR
7654 040164 042737 000017 040202 BIC      #17,3$      ;CLEAR ADDRESS OF INSTRUCTION
7655 040172 050537 040202 BIS      R5,3$      ;ADD ADDRESS TO INSTRUCTION
7656 040176          ROMCLK          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7657 040202 040520 3$:      040400!<5*20> ;BR      2A
7658 040204          ROMCLK          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7659 040210 061224 61224      ;MOVE BR TO PORT4
7660 040212 111237 002612 MCVB     (R2), $GDDAT ;PUT 'EXPECTED' IN $GDDAT
7661 040216 116104 000004 MOVB     4(R1), R4    ;PUT 'FOUND IN R4
7662 040222 123704 002612 CMPB     $GDDAT, R4   ;DATA CORRECT?
7663 040226 001413 BEQ      4$          ;BR IF YES
7664 040230          ERROR      23      ;ALU ERROR
7665 040246 104455  TRAP      C$ERDF
7666 040250 000027 .WORD   23
7667 040252 005475 .WORD   EM23
7668 040254 010624 .WORD   ERR23
7669 040256          4$:      ESCAPE   SEG
7670 040256 104410  TRAP      C$ESCAPE
7671 040260 000014 .WORD   10000$-.
7672 040262 005202 INC      R2          ;NEXT DATA
7673 040264 005205 INC      R5          ;NEXT ADDRESS
7674 040266 022705 000010 CMP      #10, R5    ;DONE YET?
7675 040272 001322 BNE     1$          ;BR IF NO
7676 040274          ENDSEG
7677 040274          10000$:
7678 040274 104405  TRAP      C$ESEG
7679 040276          EXIT      TST
7680 040276 104432  TRAP      C$EXIT
7681 040300 000012 .WORD   L10166-.
7682 040302 000      000      376 5$:      .BYTE   0,0,376,376,252,252,124,124
7683 040305 376      252      252
7684 040310 124      124
7685
7686          .EVEN
7687 040312          ENDTST
7688 040312          L10166:
7689 040312 104401  TRAP      C$ETST
7690
7691
7692 040314          BADHEAD
7693          ;***** TEST 78 *****
7694          ;*ALU TEST
7695          ;*TEST OF ALU FUNCTION A PLUS C WITH C BIT SET
7696          ;*ALU FUNCTION (A PLUS C)      CODE=4
7697          ;*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
7698          ;*PERFORM THE FUNCTION, VERIFY THE RESULTS
7699 040314          BADHEAD
7700          ;***** TEST 78 *****
7701
7702 040314          BGNST

```

```

7703 040314          T78::
7704 040314          MYINT
7705 040320          MSTCLR          ;MASTER CLEAR MB200.4,6,7
7706 040324 005005   CLR          R5          ;MEM + SP ADDRESS
7707 040326 012702 040512  MOV          #5$,R2      ;POINTER TO CORRECT DATA
7708 040332 004737 003672  JSR          PC,MEMLD    ;LOAD 8 WORDS OF MAIN MEMORY
7709 040336 002642          MEMDAT          ;POINTER TO DATA
7710 040340 004737 004044  JSR          PC,SPLD    ;LOAD 8 WORDS OF SP
7711 040344 002652          SPDAT          ;POINTER TO DATA
7712 040346          BGNSEG
7713 040346 104404          TRAP          C$BSEG
7714 040350 004737 004130 1$: JSR          PC,SETC    ;SET C BIT!
7715 040354 042737 000017 040372 BIC          #17,2$     ;CLEAR ADDRESS FIELD OF INSTRUCTION
7716 040362 050537 040372  BIS          R5,2$     ;ADD ADDRESS TO INSTRUCTION
7717 040366          ROMCLK          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7718 040372 010000          TRAP          010000    ;LOAD MAR
7719 040374 042737 000017 040412 BIC          #17,3$     ;CLEAR ADDRESS OF INSTRUCTION
7720 040402 050537 040412  BIS          R5,3$     ;ADD ADDRESS TO INSTRUCTION
7721 040406          ROMCLK          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7722 040412 040500 3$: 040400!<4*20> ;BR A PLUS C
7723 040414          ROMCLK          ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7724 040420 061224          61224          ;MOVE BR TO PORT4
7725 040422 111237 002612  MOVB          (R2),%GDDAT ;PUT 'EXPECTED' IN %GDDAT
7726 040426 116104 000004  MOVB          4(R1),R4    ;PUT 'FOUND IN R4
7727 040432 123704 002612  CMPB          %GDDAT,R4  ;DATA CORRECT?
7728 040436 001413          BEQ          4$         ;BR IF YES
7729 040440          ERROR          23          ;ALU ERROR
7730 040456 104455          TRAP          C$ERDF
7731 040460 000027          .WORD          23
7732 040462 005475          .WORD          EM23
7733 040464 010624          .WORD          ERR23
7734 040466          4$: ESCAPE          SEG
7735 040466 104410          TRAP          C$ESCAPE
7736 040470 000014          .WORD          10000$-.
7737 040472 005202          INC          R2          ;NEXT DATA
7738 040474 005205          INC          R5          ;NEXT ADDRESS
7739 040476 022705 000010  CMP          #10,R5     ;DONE YET?
7740 040502 001322          BNE          1$         ;BR IF NO
7741 040504          ENDSEG
7742 040504 10000$:
7743 040504 104405          TRAP          C$ESEG
7744 040506          EXIT          TST
7745 040506 104432          TRAP          C$EXIT
7746 040510 000012          .WORD          L10167-.
7747 040512 001 001 000 5$: .BYTE          1,1,0,0,126,126,253,253
7748 040515 000 126 126
7749 040520 253 253
7750
7751          .EVEN
7752 040522          ENDTST
7753 040522          L10167:
7754 040522 104401          TRAP          C$ETST
7755
7756 040524          BADHEAD
7757          ;***** TEST 79 *****
7758          ;*ALU TEST

```

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C 14

SEQ 0171

7759  
7760  
7761  
7762  
7763 040524  
7764  
7765  
7766 040524  
7767 040524

;\*TEST OF ALU FUNCTION 2'S COMP SUB WITH C BIT SET  
;\*ALU FUNCTION (A-B-1) CODE=17  
;\*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA  
;\*PERFORM THE FUNCTION, VERIFY THE RESULTS  
BADHEAD  
;\*\*\*\*\* TEST 79 \*\*\*\*\*

BGNTST  
T79::

7768	040524				MYINT				
7769	040530				MSTCLR				:MASTER CLEAR M8200,4,6,7
7770	040534	005005			CLR	R5			:MEM + SP ADDRESS
7771	040536	012702	040722		MOV	#5,R2			:POINTER TO CORRECT DATA
7772	040542	004737	003672		JSR	PC,MEMLD			:LOAD 8 WORDS OF MAIN MEMORY
7773	040546	002642			MEMDAT				:POINTER TO DATA
7774	040550	004737	004044		JSR	PC,SPLD			:LOAD 8 WORDS OF SP
7775	040554	002652			SPDAT				:POINTER TO DATA
7776	040556				BGNSEG				
7777	040556	104404			TRAP	C\$BSEG			
7778	040560	004737	004130		JSR	PC,SETC			:SET C BIT!
7779	040564	042737	000017	040602	BIC	#17,2\$			:CLEAR ADDRESS FIELD OF INSTRUCTION
7780	040572	050537	040602		BIS	R5,2\$			:ADD ADDRESS TO INSTRUCTION
7781	040576				ROMCLK	.			:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7782	040602	010000			010000				:LOAD MAR
7783	040604	042737	000017	040622	BIC	#17,3\$			:CLEAR ADDRESS OF INSTRUCTION
7784	040612	050537	040622		BIS	R5,3\$			:ADD ADDRESS TO INSTRUCTION
7785	040616				ROMCLK	040400!<17*20>			:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7786	040622	040760			3\$:				:BR 2'S COMP SUB
7787	040624				ROMCLK				:NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7788	040630	061224			61224				:MOVE BR TO PORT4
7789	040632	111237	002612		MOVB	(R2), \$GDDAT			:PUT 'EXPECTED' IN \$GDDAT
7790	040636	116104	000004		MOVB	4(R1), R4			:PUT 'FOUND IN R4
7791	040642	123704	002612		CMPB	\$GDDAT, R4			:DATA CORRECT?
7792	040646	001413			BEQ	4\$			:BR IF YES
7793	040650				ERROR	23			:ALU ERROR
7794	040666	104455			TRAP	C\$ERDF			
7795	040670	000027			.WORD	23			
7796	040672	005475			.WORD	EM23			
7797	040674	010624			.WORD	ERR23			
7798	040676				4\$:	ESCAPE	SEG		
7799	040676	104410			TRAP	C\$ESCAPE			
7800	040700	000014			.WORD	10000\$-.			
7801	040702	005202			INC	R2			:NEXT DATA
7802	040704	005205			INC	R5			:NEXT ADDRESS
7803	040706	022705	000010		CMP	#10, R5			:DONE YET?
7804	040712	001322			BNE	1\$			:BR IF NO
7805	040714				ENDSEG				
7806	040714				10000\$:				
7807	040714	104405			TRAP	C\$ESEG			
7808	040716				EXIT	TST			
7809	040716	104432			TRAP	C\$EXIT			
7810	040720	000012			.WORD	L10170-			
7811	040722	377	000	376	5\$:	.BYTE	-1,0,376,-1,-1,252,124,-1		
7812	040725	377	377	252					
7813	040730	124	377						
7814									
7815					.EVEN				
7816	040732				ENDTST				
7817	040732				L10170:				
7818	040732	104401			TRAP	C\$ETST			
7819									
7820									
7821	040734				BADHEAD				
7822					:***** TEST 80 *****				
7823					:*ALU TEST				

```

7824 ;*TEST OF ALU FUNCTION DEC A WITH C BIT SET
7825 ;*ALU FUNCTION (A-1) CODE=7
7826 ;*LOAD MAIN MEM AND SP WITH 8 WORDS OF DATA
7827 ;*PERFORM THE FUNCTION, VERIFY THE RESULTS
7828 040734 BADHEAD
7829 ;***** TEST 80 *****
7830
7831 040734 BGNTST
7832 040734 T80::
7833 040734 M:INT
7834 040740 MSTCLR ;MASTER CLEAR M8200,4,6,7
7835 040744 005005 CLR R5 ;MEM + SP ADDRESS
7836 040746 012702 041132 MOV #5$,R2 ;POINTER TO CORRECT DATA
7837 040752 004737 003672 JSR PC,MEMLD ;LOAD 8 WORDS OF MAIN MEMORY
7838 040756 002642 MEMDAT ;POINTER TO DATA
7839 040760 004737 004044 JSR PC,SPLD ;LOAD 8 WORDS OF SP
7840 040764 002652 SPDAT ;POINTER TO DATA
7841 040766 BGNSEG
7842 040766 104404 TRAP C$BSEG
7843 040770 004737 004130 1$: JSR PC,SETC ;SET C BIT!
7844 040774 042737 000017 041012 BIC #17,2$ ;CLEAR ADDRESS FIELD OF INSTRUCTION
7845 041002 050537 041012 BIS R5,2$ ;ADD ADDRESS TO INSTRUCTION
7846 041006 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7847 041012 010000 010000 2$: ;LOAD MAR
7848 041014 042737 000017 041032 BIC #17,3$ ;CLEAR ADDRESS OF INSTRUCTION
7849 041022 050537 041032 BIS R5,3$ ;ADD ADDRESS TO INSTRUCTION
7850 041026 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7851 041032 040560 3$: 040400!<7*20> ;BR DEC A
7852 041034 ROMCLK ;NEXT WORD IS INSTRUCTION, ROMCLK PC=5304
7853 041040 061224 61224 ;MOVE BR TO PORT4
7854 041042 111237 002612 MOVB (R2), $GDDAT ;PUT 'EXPECTED' IN $GDDAT
7855 041046 116104 000004 MOVB 4(R1), R4 ;PUT 'FOUND IN R4
7856 041052 123704 002612 CMPB $GDDAT, R4 ;DATA CORRECT?
7857 041056 001413 BEQ 4$ ;BR IF YES
7858 041060 ERROR 23 ;ALU ERROR
7859 041076 104455 TRAP C$ERDF
7860 041100 000027 .WORD 23
7861 041102 005475 .WORD EM23
7862 041104 010624 .WORD ERR23
7863 041106 4$: ESCAPE SEG
7864 041106 104410 TRAP C$ESCAPE
7865 041110 000014 .WORD 10000$-
7866 041112 005202 INC R2 ;NEXT DATA
7867 041114 005205 INC R5 ;NEXT ADDRESS
7868 041116 022705 000010 CMP #10, R5 ;DONE YET?
7869 041122 001322 BNE 1$ ;BR IF NO
7870 041124 ENDSEG
7871 041124 10000$:
7872 041124 104405 TRAP C$SEEG
7873 041126 EXIT TST
7874 041126 104432 TRAP C$EXIT
7875 041130 000012 .WORD L10171-
7876 041132 377 377 376 5$: .BYTE -1,-1,376,376,124,124,251,251
7877 041135 376 124 124
7878 041140 251 251
7879

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SEQ 0174

7880  
7881 041142  
7882 041142  
7883 041142 104401  
7884  
7885

.EVEN  
ENDTST  
L10171:  
TRAP CSETST

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SEQ 0175

7886  
7887  
7888  
7889  
7890  
7891

.SBTTL HARDWARE PARAMETER CODING SECTION

7892  
7893  
7894  
7895  
7896  
7897  
7898  
7899  
7900  
7901  
7902  
7903  
7904  
7905 041144  
7906 041144 000015  
7907 041146  
7908  
7909  
7910 041146  
7911 041146 001031  
7912 041150 041267  
7913 041152 160000  
7914 041154 177776  
7915 041156  
7916 041156 002031  
7917 041160 041306  
7918 041162 000000  
7919 041164 000674  
7920 041166  
7921 041166 003032  
7922 041170 041330  
7923 041172 007000  
7924 041174 000004  
7925 041176 000007  
7926  
7927  
7928  
7929  
7930  
7931 041200  
7932  
7933 041200  
7934  
7935 041200 044127 041511 020110  
7936 041206 044515 051103 026517  
7937 041214 041512 041517 051505  
7938 041222 047523 035122 000  
7939 041227 060 046475 031070  
7940 041234 030060 032054 046475  
7941 041242 031070 032060 033054  
7942 041250 046475 031070 033060  
7943 041256 033454 046475 031070  
7944 041264 033460 000  
7945 041267 103 051123 040440  
7946 041274 042104 042522 051523  
7947 041302 035040 000040

:/ THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS  
:/ THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE  
:/ MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE  
:/ INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE  
:/ MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS  
:/ WITH THE OPERATOR.  
:////////////////////

BGNHRD  
.WORD L10172-L\$HARD/2  
L\$HARD::  
:  
: GPRMD WMP,0,0,7,0,7,YES  
: GPRMA ADDRES,2,0,160000,177776,YES  
: .WORD T\$CODE  
: .WORD ADDRES  
: .WORD T\$LLOLIM  
: .WORD T\$HILIM  
: GPRMA VECTOR,4,0,0,674,YES  
: .WORD T\$CODE  
: .WORD VECTOR  
: .WORD T\$LLOLIM  
: .WORD T\$HILIM  
: GPRMD PRIORITY,6,0,7000,4,7,YES  
: .WORD T\$CODE  
: .WORD PRIORITY  
: .WORD 7000  
: .WORD T\$LLOLIM  
: .WORD T\$HILIM  
: GPRMD LNUNIT,10,0,3,0,3,YES  
: GPRMD SWPAC1,12,0,377,0,377,YES  
: GPRMD SWPAC2,14,0,377,0,377,YES  
: GPRMD LOOPBK,16,0,40000,0,1,YES

ENDHRD  
.EVEN  
L10172:  
WMP: .ASCIZ 'WHICH MICRO-PROCESSOR:'  
.ASCIZ '0=M8200,4=M8204,6=M8206,7=M8207'  
ADDRES: .ASCIZ /CSR ADDRESS : /



7948	041306	042526	052103	051117	VECTOR: .ASCIZ /VECTOR ADDRESS : /
7949	041314	040440	042104	042522	
7950	041322	051523	035040	000040	
7951	041330	051120	047511	044522	PRIPTY: .ASCIZ /PRIORITY LEVEL : /
7952	041336	054524	046040	053105	
7953	041344	046105	03504C	000040	
7954	041352	044127	041511	020110	LNUNIT: .ASCIZ /WHICH LINE UNIT (0-3)? 0=NONE,1=M8201,2=M8202,3=M8203 : /
7955	041360	044514	042516	052440	
7956	041366	044516	020124	030050	
7957	041374	031455	037451	030040	
7958	041402	047075	047117	026105	
7959	041410	036461	034115	030062	
7960	041416	026061	036462	034115	
7961	041424	030062	026062	036463	
7962	041432	034115	030062	020063	
7963	041440	020072	000		
7964	041443	123	044527	041524	SWPAC1: .ASCIZ /SWITCH PACK #1 (DDCMP LINE #) : /
7965	041450	020110	040520	045503	
7966	041456	021440	020061	042050	
7967	041464	041504	050115	046040	
7968	041472	047111	020105	024443	
7969	041500	035040	000040		
7970	041504	053523	052111	044103	SWPAC2: .ASCIZ /SWITCH PACK #2 (BM873 BOOT ADR) : /
7971	041512	050040	041501	020113	
7972	041520	031043	024040	046502	
7973	041526	033470	020063	047502	
7974	041534	052117	040440	051104	
7975	041542	020051	020072	000	
7976	041547	127	046111	020114	LOOPBK: .ASCIZ /WILL TEST CONNECTOR(S) BE USED ? 0=NO,1=YES : /
7977	041554	042524	052123	041440	
7978	041562	047117	042516	052103	
7979	041570	051117	051450	020051	
7980	041576	042502	052440	042523	
7981	041604	020104	020077	036460	
7982	041612	047516	030454	054475	
7983	041620	051505	035040	000040	
7984					
7985					.EVEN
7986					
7987					
7988					
7989					
7990					
7991					

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041626  
041626 000000  
041630  
  
041630  
  
  
041630  
  
041630  
041630 000000  
041732 041732  
041732 000000  
041734  
041734 000000  
041736 000000  
041740  
  
000120  
  
  
  
  
  
  
  
000001

.SBTTL SOFTWARE PARAMETER CODING SECTION  
  
:////////////////////  
:/ THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS  
:/ THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES. THE  
:/ MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE  
:/ INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES. THE  
:/ MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS  
:/ WITH THE OPERATOR.  
:////////////////////  
  
                  BGNSFT  
                  .WORD L10173-L\$SOFT/2  
L\$SOFT::  
  
                  ENDSFT  
                  .EVEN  
L10173:  
  
                  .EVEN  
  
  
                  ENDMOD  
  
CORMAX:  
  
                  .WORD 0                   ;START OF NPR AREA (TEST 55)  
                  .EVEN  
MEMEND:          .WORD 0                   ;END OF NPR AREA  
                  LASTAD  
                  .EVEN  
                  .WORD 0  
                  .WORD 0  
L\$LAST::  
                  LTN.ED=T\$TESTNUM  
  
                  ; W A R N I N G < < < <  
  
                  ;AREA BETWEEN CORMAX AND MEMEND USED BY TESTS IN DIAGNOSTIC.  
                  ; NO PATCHS OR DATA MY BE STORED IN THIS AREA.  
                  ;A SMALL PATCH AREA IS PROVIDED NEAR AREA 'DEBUG' FOR YOUR USE.  
                  ;ALSO THE AREA ABOVE ADDRESS 077776 MAY BE USED.  
                  :  
                  ;ANYONE FOOLISH ENOUGH TO IGNOR THIS WARNING WILL BE DESTROYED!  
                  :  
  
                  .END



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CROSS REFERENCE TABLE -- USER SYMBOLS

L 14

SEQ 0180

C\$DOOU= 000051	691#														
C\$DRPT= 000024	691#														
C\$DU = 000053	691#	2751													
C\$EDI1= 000003	691#	769													
C\$ERDF= 000055	691#	1479	2805	2832	2875	2899	2938	2950	2985	3002	3036	3053	3084		
	3101	3134	3146	3185	3209	3249	3274	3313	3336	3375	3393	3430	3470		
	3498	3546	3574	3618	3646	3690	3718	3762	3790	3834	3862	3908	3938		
	3985	4016	4060	4088	4132	4160	4204	4232	4276	4304	4348	4376	4420		
	4448	4492	4520	4564	4592	4644	4672	4737	4749	4761	4856	4911	4940		
	4955	5009	5043	5101	5131	5177	5183	5219	5225	5274	5320	5328	5372		
	5419	5465	5531	5581	5625	5639	5681	5726	5791	5848	5912	5976	6040		
	6104	6168	6232	6296	6360	6426	6491	6557	6622	6687	6752	6818	6883		
	6949	7014	7079	7144	7209	7274	7339	7404	7469	7535	7600	7665	7730		
	7794	7859													
C\$ERHR= 000056	691#														
C\$ERRO= 000060	691#														
C\$ERSF= 000054	691#														
C\$ERSO= 000057	691#														
C\$ESCA= 000010	691#	2797	2813	2880	2904	2943	2955	2990	3007	3041	3089	3139	3151		
	3190	3214	3254	3279	3318	3341	3380	3398	3436	3475	3503	3579	3623		
	3651	3695	3723	3767	3795	3839	3867	3913	3943	3990	4021	4065	4093		
	4137	4165	4209	4237	4281	4309	4353	4381	4425	4453	4497	4525	4569		
	4597	4649	4677	4861	4916	4945	5014	5048	5106	5136	5377	5424	5470		
	5731	5796	5853	5917	5981	6045	6109	6173	6237	6301	6365	6431	6496		
	6562	6627	6692	6757	6823	6888	6954	7019	7084	7149	7214	7279	7344		
	7409	7474	7540	7605	7670	7735	7799	7864							
C\$ESEG= 000005	691#	2888	2912	2961	2994	3011	3045	3059	3093	3107	3157	3197	3222		
	3261	3287	3328	3349	3384	3402	3482	3511	3555	3587	3630	3659	3702		
	3731	3774	3803	3846	3875	3920	3951	3997	4029	4072	4101	4144	4173		
	4216	4245	4288	4317	4360	4389	4432	4461	4504	4533	4576	4605	4657		
	4686	4923	4969	5021	5056	5114	5144	5800	5861	5925	5989	6053	6117		
	6181	6245	6309	6373	6439	6504	6570	6635	6700	6765	6831	6896	6962		
	7027	7092	7157	7222	7287	7352	7417	7482	7548	7613	7678	7743	7807		
	7872														
C\$ESUB= 000003	691#	5063													
C\$ETST= 000001	691#	2817	2839	2915	2964	3015	3063	3111	3160	3225	3290	3352	3405		
	3441	3514	3590	3662	3734	3806	3878	3954	4032	4104	4176	4248	4320		
	4392	4464	4536	4608	4689	4767	4883	4972	5066	5147	5191	5233	5282		
	5337	5386	5433	5479	5538	5591	5647	5688	5754	5803	5872	5936	6000		
	6064	6128	6192	6256	6320	6385	6450	6516	6581	6646	6711	6776	6842		
	6907	6973	7038	7103	7168	7233	7298	7363	7428	7493	7559	7624	7689		
	7754	7818	7883												
C\$EXIT= 000032	691#	5380	5427	5473	5747	5863	5927	5991	6055	6119	6183	6247	6311		
	6375	6441	6506	6572	6637	6702	6767	6833	6898	6964	7029	7094	7159		
	7224	7289	7354	7419	7484	7550	7615	7680	7745	7809	7874				
C\$GETB= 000026	691#														
C\$GETW= 000027	691#														
C\$GMAN= 000043	691#														
C\$GPHR= 000042	691#	2627													
C\$GPLO= 000030	691#														
C\$GPRI= 000040	691#														
C\$INIT= 000011	691#	2710													
C\$INLP= 000020	691#														
C\$MANI= 000050	691#														
C\$MEM = 000031	691#														
C\$MSG = 000023	691#	1830	1854	1878	1903	1928	1953	1978	2003	2027	2051	2068	2092		





ERR31	011670 G	2399#	5180	5222	5323									
ERR32	011746 G	2416#	5186	5228	5331									
ERR33	012024 G	2433#	5277											
ERR34	012132 G	2457#	5794											
ERR35	012240 G	2481#												
ERR36	012346 G	1482	2505#											
ERR4	006732 G	1880#	5012	5046	5104									
ERR5	007044 G	1905#	5134											
ERR6	007156 G	1930#												
ERR7	007270 G	1955#												
EVL	= 000004 G	1038#												
E\$END	= 002100	691#												
E\$LOAD	= 000035	691#	792											
FLAG	002572	1115#												
FMSG	004407	1582#	4869											
FMX	004217	1554#	1811	1835	1859	1883	1908	1933	1958	1983	2008	2032	2056	2073
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		2378	2402	2419	2436	2460	2484	2508						
FM1	004210	1552#	1817	1841	1865	1889	1914	1939	1964	1989	2014	2038	2062	2079
		2103	2128	2145	2162	2186	2203	2220	2245	2262	2286	2310	2335	2359
		2384	2408	2425	2442	2466	2490	2514						
FTIME	002622	1127#	2578	2584*										
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F\$AUTO	= 000020	691#	2557	2559										
F\$BGN	= 000040	691#	697	1808	1832	1856	1880	1905	1930	1955	1980	2005	2029	2053
		2070	2094	2119	2136	2153	2177	2194	2211	2236	2253	2277	2301	2326
		2350	2375	2399	2416	2433	2457	2481	2505	2537	2543	2550	2557	2572
		2726	2745	2766	2789	2797	2813	2816	2825	2838	2861	2867	2880	2893
		2904	2914	2925	2930	2943	2955	2963	2974	2977	2990	2996	3007	3014
		3025	3028	3041	3047	3062	3073	3076	3089	3095	3110	3120	3125	3139
		3151	3159	3171	3177	3190	3200	3214	3224	3235	3241	3254	3264	3279
		3289	3300	3305	3318	3330	3341	3351	3362	3367	3380	3387	3398	3404
		3417	3436	3440	3451	3458	3475	3485	3503	3513	3524	3531	3558	3579
		3589	3600	3606	3623	3633	3651	3661	3672	3678	3695	3705	3723	3733
		3744	3750	3767	3777	3795	3805	3816	3822	3839	3849	3867	3877	3888
		3894	3913	3923	3943	3953	3964	3970	3990	4000	4021	4031	4042	4048
		4065	4075	4093	4103	4114	4120	4137	4147	4165	4175	4186	4192	4209
		4219	4237	4247	4258	4264	4281	4291	4309	4319	4330	4336	4353	4363
		4381	4391	4402	4408	4425	4435	4453	4463	4474	4480	4497	4507	4525
		4535	4546	4552	4569	4579	4597	4607	4618	4624	4649	4661	4677	4688
		4700	4766	4775	4861	4882	4893	4899	4916	4927	4945	4971	4983	4989
		4992	5014	5024	5048	5062	5065	5077	5083	5106	5118	5136	5146	5157
		5190	5201	5232	5244	5281	5293	5336	5347	5377	5380	5385	5396	5424
		5427	5432	5443	5470	5473	5478	5490	5537	5553	5590	5602	5646	5658
		5687	5699	5731	5747	5753	5768	5776	5796	5802	5821	5831	5853	5863
		5871	5885	5895	5917	5927	5935	5949	5959	5981	5991	5999	6013	6023
		6045	6055	6063	6077	6087	6109	6119	6127	6141	6151	6173	6183	6191
		6205	6215	6237	6247	6255	6269	6279	6301	6311	6319	6333	6343	6365
		6375	6384	6399	6409	6431	6441	6449	6464	6474	6496	6506	6515	6530
		6540	6562	6572	6580	6595	6605	6627	6637	6645	6660	6670	6692	6702
		6710	6725	6735	6757	6767	6775	6791	6801	6823	6833	6841	6856	6866
		6888	6898	6906	6922	6932	6954	6964	6972	6987	6997	7019	7029	7037
		7052	7062	7084	7094	7102	7117	7127	7149	7159	7167	7182	7192	7214
		7224	7232	7247	7257	7279	7289	7297	7312	7322	7344	7354	7362	7377
		7387	7409	7419	7427	7442	7452	7474	7484	7492	7508	7518	7540	7550
		7558	7573	7583	7605	7615	7623	7638	7648	7670	7680	7688	7703	7713

F\$CLEA= 0000C7  
F\$DU = 000016  
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7906	8005	8020										
691#	2726	2730										
691#	2745	2750										
691#	697	1831	1855	1879	1904	1929	1954	1979	2004	2028	2052	2069
2093	2118	2135	2152	2176	2193	2210	2235	2252	2276	2300	2325	2349
2374	2398	2415	2432	2456	2480	2504	2528	2541	2548	2561	2711	2732
2752	2770	2789	2797	2813	2816	2818	2825	2838	2840	2861	2880	2889
2904	2913	2914	2916	2925	2943	2955	2962	2963	2965	2974	2990	2995
3007	3012	3014	3016	3025	3041	3046	3060	3062	3064	3073	3089	3094
3108	3110	3112	3120	3139	3151	3158	3159	3161	3171	3190	3198	3214
3223	3224	3226	3235	3254	3262	3279	3288	3289	3291	3300	3318	3329
3341	3350	3351	3353	3362	3380	3385	3398	3403	3404	3406	3417	3436
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3589	3591	3600	3623	3631	3651	3660	3661	3663	3672	3695	3703	3723
3732	3733	3735	3744	3767	3775	3795	3804	3805	3807	3816	3839	3847
3867	3876	3877	3879	3888	3913	3921	3943	3952	3953	3955	3964	3990
3998	4021	4030	4031	4033	4042	4065	4073	4093	4102	4103	4105	4114
4137	4145	4165	4174	4175	4177	4186	4209	4217	4237	4246	4247	4249
4258	4281	4289	4309	4318	4319	4321	4330	4353	4361	4381	4390	4391
4393	4402	4425	4433	4453	4462	4463	4465	4474	4497	4505	4525	4534
4535	4537	4546	4569	4577	4597	4606	4607	4609	4618	4649	4658	4677
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4945	4970	4971	4973	4983	4989	5014	5022	5048	5057	5062	5064	5065
5067	5077	5106	5115	5136	5145	5146	5148	5157	5190	5192	5201	5232
5234	5244	5281	5283	5293	5336	5338	5347	5377	5380	5385	5387	5396
5424	5427	5432	5434	5443	5470	5473	5478	5480	5490	5537	5539	5553
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5768	5796	5801	5802	5804	5821	5853	5862	5863	5871	5873	5865	5917
5926	5927	5935	5937	5949	5981	5990	5991	5999	6001	6013	6045	6054
6055	6063	6065	6077	6109	6118	6119	6127	6129	6141	6173	6182	6183
6191	6193	6205	6237	6246	6247	6255	6257	6269	6301	6310	6311	6319
6321	6333	6365	6374	6375	6384	6386	6399	6431	6440	6441	6449	6451
6464	6496	6505	6506	6515	6517	6530	6562	6571	6572	6580	6582	6595
6627	6636	6637	6645	6647	6660	6692	6701	6702	6710	6712	6725	6757
6766	6767	6775	6777	6791	6823	6832	6833	6841	6843	6856	6888	6897
6898	6906	6908	6922	6954	6963	6964	6972	6974	6987	7019	7028	7029
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7169	7182	7214	7223	7224	7232	7234	7247	7279	7288	7289	7297	7299
7312	7344	7353	7354	7362	7364	7377	7409	7418	7419	7427	7429	7442
7474	7483	7484	7492	7494	7508	7540	7549	7550	7558	7560	7573	7605
7614	7615	7623	7625	7638	7670	7679	7680	7688	7690	7703	7735	7744
7745	7753	7755	7767	7799	7808	7809	7817	7819	7832	7864	7873	7874
7882	7884	7934	8012	8020								
691#	7906	7932										
691#	922	937										
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691#	2541	5380	5427	5473	5747	5863	5927	5991	6055	6119	6183	6247
6311	6375	6441	6506	6572	6637	6702	6767	6833	6898	6964	7029	7094
7159	7224	7289	7354	7419	7484	7550	7615	7680	7745	7809	7874	
691#	697	8020										
691#	1808	1829	1832	1853	1856	1877	1880	1902	1905	1927	1930	1952
1955	1977	1980	2002	2005	2026	2029	2050	2053	2067	2070	2091	2094
2116	2119	2133	2136	2150	2153	2174	2177	2191	2194	2208	2211	2233
2236	2250	2253	2274	2277	2298	2301	2323	2326	2347	2350	2372	2375
2396	2399	2413	2416	2430	2433	2454	2457	2478	2481	2502	2505	2526

F\$HARD= 000004  
F\$HW = 000013  
F\$INIT= 000016  
F\$JMP = 000050

F\$MOD = 000000  
F\$MSG = 000011





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IBE = 010000	G	1048#													
IDU = 000040	G	1041#													
IER = 020000	G	1049#													
INIFLG = 002662		1150#													
INSTU = 003570		1402	1413#												
ISR = 000100	G	1042#													
IXE = 004000	G	1047#													
I\$AU = 000041		691#	2766#	2770#											
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I\$HRD = 000041		7906#	7934#												
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		1955#	1979#	1980#	2004#	2005#	2028#	2029#	2052#	2053#	2069#	2070#	2093#	2094#	
		2118#	2119#	2135#	2136#	2152#	2153#	2176#	2177#	2193#	2194#	2210#	2211#	2235#	
		2236#	2252#	2253#	2276#	2277#	2300#	2301#	2325#	2326#	2349#	2350#	2374#	2375#	
		2398#	2399#	2415#	2416#	2432#	2433#	2456#	2457#	2480#	2481#	2504#	2505#	2528#	
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I\$PTAB = 000041		691#													
I\$PWR = 000041		691#													
I\$RPT = 000041		691#	2537#	2548#											
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		2955	2962#	2974	2977#	2990	2995#	2996#	3007	3012#	3025	3028#	3041	3046#	
		3047#	3060#	3073	3076#	3089	3094#	3095#	3108#	3120	3125#	3139	3151	3158#	
		3171	3177#	3190	3198#	3200#	3214	3223#	3235	3241#	3254	3262#	3264#	3279	
		3288#	3300	3305#	3318	3329#	3330#	3341	3350#	3362	3367#	3380	3385#	3387#	
		3398	3403#	3417	3451	3458#	3475	3483#	3485#	3503	3512#	3524	3531#	3556#	
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		3839	3847#	3849#	3867	3876#	3888	3894#	3913	3921#	3923#	3943	3952#	3964	
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		4246#	4258	4264#	4281	4289#	4291#	4309	4318#	4330	4336#	4353	4361#	4363#	
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		4507#	4525	4534#	4546	4552#	4569	4577#	4579#	4597	4606#	4618	4624#	4649	
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		5145#	5157	5201	5244	5293	5347	5396	5443	5490	5553	5602	5658	5699	
		5768	5776#	5796	5801#	5821	5831#	5853	5862#	5885	5895#	5917	5926#	5949	
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5821	5885	5949	6013	6077	6141	6205	6269	6333	6399	6464	6530	6595
6660	6725	6791	6856	6922	6987	7052	7117	7182	7247	7312	7377	7442
7508	7573	7638	7703	7767	7832							
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2963#	2965#	2974#	3014#	3016#	3025#	3062#	3064#	3073#	3110#	3112#	3120#	3159#
3161#	3171#	3224#	3226#	3235#	3289#	3291#	3300#	3351#	3353#	3362#	3404#	3406#
3417#	3436	3440#	3442#	3451#	3513#	3515#	3524#	3589#	3591#	3600#	3661#	3663#
3672#	3733#	3735#	3744#	3805#	3807#	3816#	3877#	3879#	3888#	3953#	3955#	3964#
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4983#	4989	5048	5065#	5067#	5077#	5146#	5148#	5157#	5190#	5192#	5201#	5232#
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5768#	5802#	5804#	5821#	5863	5871#	5873#	5885#	5927	5935#	5937#	5949#	5991
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KMCSRH 002706  
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LOKFLG 002664  
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PRI07 = 000340 G	1027#	5167	5209													
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RUN 002574	1116#															
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SFPTBL 002412 G	953#															
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SIBS13 002640	1134#	4705*	4714*	4724	4754											
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	7450	7516	7581	7646	7711	7775	7840									
SPLD 004044	1490#	5773	5828	5892	5956	6020	6084	6148	6212	6276	6340	6406	6471			
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	7384	7449	7515	7580	7645	7710	7774	7839								
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3730#	3733#	3767#	3768#	3773#	3795#	3796#	3802#	3805#	3839#	3840#	3845#	3867#
3868#	3874#	3877#	3913#	3914#	3919#	3943#	3944#	3950#	3953#	3990#	3991#	3996#
4021#	4022#	4028#	4031#	4065#	4066#	4071#	4093#	4094#	4100#	4103#	4137#	4138#
4143#	4165#	4166#	4172#	4175#	4209#	4210#	4215#	4237#	4238#	4244#	4247#	4281#
4282#	4287#	4309#	4310#	4316#	4319#	4353#	4354#	4359#	4381#	4382#	4388#	4391#
4425#	4426#	4431#	4453#	4454#	4460#	4463#	4497#	4498#	4503#	4525#	4526#	4532#
4535#	4569#	4570#	4575#	4597#	4598#	4604#	4607#	4649#	4650#	4656#	4677#	4678#
4685#	4688#	4766#	4861#	4862	4882#	4916#	4917#	4922#	4945#	4946#	4968#	4971#
5014#	5015#	5020#	5048#	5049	5055#	5062#	5065#	5106#	5107#	5113#	5136#	5137#
5143#	5146#	5190#	5232#	5281#	5336#	5377#	5378	5380#	5381	5385#	5424#	5425
5427#	5428	5432#	5470#	5471	5473#	5474	5478#	5537#	5590#	5646#	5687#	5731#
5732	5747#	5748	5753#	5796#	5797#	5799#	5802#	5853#	5854#	5860#	5863#	5864
5871#	5917#	5918#	5924#	5927#	5928	5935#	5981#	5982#	5988#	5991#	5992	5999#
6045#	6046#	6052#	6055#	6056	6063#	6109#	6110#	6116#	6119#	6120	6127#	6173#
6174#	6180#	6183#	6184	6191#	6237#	6238#	6244#	6247#	6248	6255#	6301#	6302#
6308#	6311#	6312	6319#	6365#	6366#	6372#	6375#	6376	6384#	6431#	6432#	6438#
6441#	6442	6449#	6496#	6497#	6503#	6506#	6507	6515#	6562#	6563#	6569#	6572#
6573	6580#	6627#	6628#	6634#	6637#	6638	6645#	6692#	6693#	6699#	6702#	6703
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T\$TEST= 000120

T\$TSTM= 177777

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7156#	7159#	7160	7167#	7214#	7215#	7221#	7224#	7225	7232#	7279#	7280#	7286#
7289#	7290	7297#	7344#	7345#	7351#	7354#	7355	7362#	7409#	7410#	7416#	7419#
7420	7427#	7474#	7475#	7481#	7484#	7485	7492#	7540#	7541#	7547#	7550#	7551
7558#	7605#	7606#	7612#	7615#	7616	7623#	7670#	7671#	7677#	7680#	7681	7688#
7735#	7736#	7742#	7745#	7746	7753#	7799#	7800#	7806#	7809#	7810	7817#	7864#
7865#	7871#	7874#	7875	7882#	7911#	7916#	7921#	7932#	8010#	8020#		
691#	2782	2786	2789#	2819	2822	2825#	2841	2858	2861#	2917	2922	2925#
2966	2971	2974#	3017	3022	3025#	3065	3070	3073#	3113	3117	3120#	3162
3167	3171#	3227	3232	3235#	3292	3297	3300#	3354	3359	3362#	3407	3414
3417#	3443	3448	3451#	3516	3521	3524#	3592	3597	3600#	3664	3669	3672#
3736	3741	3744#	3808	3813	3816#	3880	3885	3888#	3956	3961	3964#	4034
4039	4042#	4106	4111	4114#	4178	4183	4186#	4250	4255	4258#	4322	4327
4330#	4394	4399	4402#	4466	4471	4474#	4538	4543	4546#	4610	4615	4618#
4691	4697	4700#	4769	4772	4775#	4885	4890	4893#	4975	4980	4983#	4989
5069	5074	5077#	5150	5154	5157#	5194	5198	5201#	5236	5241	5244#	5285
5290	5293#	5340	5344	5347#	5389	5393	5396#	5436	5440	5443#	5482	5487
5490#	5541	5550	5553#	5594	5599	5602#	5650	5655	5658#	5691	5696	5699#
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6269#	6323	6330	6333#	6389	6396	6399#	6454	6461	6464#	6520	6527	6530#
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7700	7703#	7757	7764	7767#	7822	7829	7832#	8032				
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1878	1886	1892	1900	1903	1911	1917	1925	1928	1936	1942	1950	1953
1961	1967	1975	1978	1986	1992	2000	2003	2011	2017	2024	2027	2035
2041	2048	2051	2059	2065	2068	2076	2082	2089	2092	2100	2106	2114
2117	2125	2131	2134	2142	2148	2151	2159	2165	2172	2175	2183	2189
2192	2200	2206	2209	2217	2223	2231	2234	2242	2248	2251	2259	2265
2272	2275	2283	2289	2296	2299	2307	2313	2321	2324	2332	2338	2345
2348	2356	2362	2370	2373	2381	2387	2394	2397	2405	2411	2414	2422
2428	2431	2439	2445	2452	2455	2463	2469	2476	2479	2487	2493	2500
2503	2511	2517	2524	2527	2547	2560	2590	2599	2605	2612	2627	2710
2731	2748	2751	2769	2797	2805	2813	2817	2832	2839	2867	2875	2880
2888	2893	2899	2904	2912	2915	2930	2938	2943	2950	2955	2961	2964
2977	2985	2990	2994	2996	3002	3007	3011	3015	3028	3036	3041	3045
3047	3053	3059	3063	3076	3084	3089	3093	3095	3101	3107	3111	3125
3134	3139	3146	3151	3157	3160	3177	3185	3190	3197	3200	3209	3214
3222	3225	3241	3249	3254	3261	3264	3274	3279	3287	3290	3305	3313
3318	3328	3330	3336	3341	3349	3352	3367	3375	3380	3384	3387	3393
3398	3402	3405	3430	3436	3441	3458	3470	3475	3482	3485	3498	3503
3511	3514	3531	3546	3555	3558	3574	3579	3587	3590	3606	3618	3623
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3731	3734	3750	3762	3767	3774	3777	3790	3795	3803	3806	3822	3834
3839	3846	3849	3862	3867	3875	3878	3894	3908	3913	3920	3923	3938
3943	3951	3954	3970	3985	3990	3997	4000	4016	4021	4029	4032	4048
4060	4065	4072	4075	4088	4093	4101	4104	4120	4132	4137	4144	4147
4160	4165	4173	4176	4192	4204	4209	4216	4219	4232	4237	4245	4248
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6375	6385	6409	6426	6431	6439	6441	6450	6474	6491	6496	6504	6506	
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6670	6687	6692	6700	6702	6711	6735	6752	6757	6765	6767	6776	6801	
6818	6823	6831	6833	6842	6866	6883	6888	6896	6898	6907	6932	6949	
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7224	7233	7257	7274	7279	7287	7289	7298	7322	7339	7344	7352	7354	
7363	7387	7404	7409	7417	7419	7428	7452	7469	7474	7482	7484	7493	
7518	7535	7540	7548	7550	7559	7583	7600	7605	7613	7615	7624	7648	
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7799	7807	7809	7818	7842	7859	7864	7872	7874	7883				
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	3418#	3452#	3525#	3601#	3673#	3745#	3817#	3889#	3965#	4043#	415#	4187#	4259#
	4331#	4403#	4475#	4547#	4619#	4701#	4776#	4894#	4984#	5078#	5158#	5202#	5245#
	5294#	5348#	5397#	5444#	5491#	5554#	5603#	5659#	5700#	5769#	5822#	5886#	5950#
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	6857#	6923#	6988#	7053#	7118#	7183#	7248#	7313#	7378#	7443#	7509#	7574#	7639#
	7704#	7768#	7833#										
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	7906#	7933											
	922#	937											
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	1808#	1829	1832#	1853	1856#	1877	1880#	1902	1905#	1927	1930#	1952	1955#
	1977	1980#	2002	2005#	2026	2029#	2050	2053#	2067	2070#	2091	2094#	2116
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	2250	2253#	2274	2277#	2298	2301#	2323	2326#	2347	2350#	2372	2375#	2396
	2399#	2413	2416#	2430	2433#	2454	2457#	2478	2481#	2502	2505#	2526	
	2550#												
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	2996#	3007	3010#	3028#	3041	3044#	3047#	3058#	3076#	3089	3092#	3095#	3106#
	3125#	3139	3151	3156#	3177#	3190	3196#	3200#	3214	3221#	3241#	3254	3260#
	3264#	3279	3286#	3305#	3318	3327#	3330#	3341	3348#	3367#	3380	3383#	3387#
	3398	3401#	3458#	3475	3481#	3485#	3503	3510#	3531#	3554#	3558#	3579	3586#
	3606#	3623	3629#	3633#	3651	3658#	3678#	3695	3701#	3705#	3723	3730#	3750#
	3767	3773#	3777#	3795	3802#	3822#	3839	3845#	3849#	3867	3874#	3894#	3913
	3919#	3923#	3943	3950#	3970#	3990	3996#	4000#	4021	4028#	4048#	4065	4071#
	4075#	4093	4100#	4120#	4137	4143#	4147#	4165	4172#	4192#	4209	4215#	4219#
	4237	4244#	4264#	4281	4287#	4291#	4309	4316#	4336#	4353	4359#	4363#	4381
	4388#	4408#	4425	4431#	4435#	4453	4460#	4480#	4497	4503#	4507#	4525	4532#
	4552#	4569	4575#	4579#	4597	4604#	4624#	4649	4656#	4661#	4677	4685#	4899#
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TSTSTS= 000001

TSSAU = 010050  
TSSAUT= 010044  
TSSCLE= 010046  
TSSDU = 010047  
TSSHAR= 010172  
TSSHW = 010000  
TSSINI= 010045  
TSSMSG= 010041

TSSPRO= 010043  
TSSRPT= 010042  
TSSSEG= 010000

TSSOF= 010173  
TSSSUB= 010113  
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5981	5988#	6023#	6045	6052#	6087#	6109	6116#	6151#	6173	6180#	6215#	6237
6244#	6279#	6301	6308#	6343#	6365	6372#	6409#	6431	6438#	6474#	6496	6503#
6540#	6562	6569#	6605#	6627	6634#	6670#	6692	6699#	6735#	6757	6764#	6801#
6823	6830#	6866#	6888	6895#	6932#	6954	6961#	6997#	7019	7026#	7062#	7084
7091#	7127#	7149	7156#	7192#	7214	7221#	7257#	7279	7286#	7322#	7344	7351#
7387#	7409	7416#	7452#	7474	7481#	7518#	7540	7547#	7583#	7605	7612#	7648#
7670	7677#	7713#	7735	7742#	7777#	7799	7806#	7842#	7864	7871#		
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4990#	5062											
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2790#	2797	2813	2816	2826#	2838	2862#	2914	2926#	2963	2975#	3014	3026#
3062	3074#	3110	3121#	3159	3172#	3224	3236#	3289	3301#	3351	3363#	3404
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3817#	3877	3889#	3953	3965#	4031	4043#	4103	4115#	4175	4187#	4247	4259#
4319	4331#	4391	4403#	4463	4475#	4535	4547#	4607	4619#	4688	4701#	4766
4776#	4861	4882	4894#	4971	4984#	5048	5065	5078#	5146	5158#	5190	5202#
5232	5245#	5281	5294#	5336	5348#	5377	5380	5385	5397#	5424	5427	5432
5444#	5470	5473	5478	5491#	5537	5554#	5590	5603#	5646	5659#	5687	5700#
5731	5747	5753	5769#	5802	5822#	5863	5871	5886#	5927	5935	5950#	5991
5999	6014#	6055	6063	6078#	6119	6127	6142#	6183	6191	6206#	6247	6255
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7688	7704#	7745	7753	7768#	7809	7817	7833#	7874	7882			
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4989#												

T1	013176	G
T10	014770	G
T11	015174	G
T12	015376	G
T13	015562	G
T14	015674	G
T15	016134	G
T16	016430	G
T17	016670	G
T18	017130	G
T19	017370	G
T2	013320	G
T20	017630	G
T21	020120	G
T22	020422	G
T23	020662	G
T24	021122	G
T25	021362	G
T26	021622	G
T27	022062	G
T28	022322	G
T29	022562	G
T3	013366	G
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T31	023330	G
T32	023726	G
T33	024412	G
T34	024722	G
T34.1	024740	

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T36	025570 G	861	5157#			
T37	025736 G	862	5201#			
T38	026100 G	863	5244#			
T39	026252 G	864	5293#			
T4	013576 G	829	2925#			
T40	026456 G	865	5347#			
T41	026632 G	866	5396#			
T42	027006 G	867	5443#			
T43	027156 G	868	5490#			
T44	027350 G	869	5553#			
T45	027570 G	870	5602#			
T46	030016 G	871	5658#			
T47	030154 G	872	5699#			
T48	030376 G	873	5768#			
T49	030542 G	874	5821#			
T5	013746 G	830	2974#			
T50	030752 G	875	5885#			
T51	031162 G	876	5949#			
T52	031372 G	877	6013#			
T53	031602 G	878	6077#			
T54	032012 G	879	6141#			
T55	032222 G	880	6205#			
T56	032432 G	881	6269#			
T57	032642 G	882	6333#			
T58	033054 G	883	6399#			
T59	033264 G	884	6464#			
T6	014112 G	831	3025#			
T60	033474 G	885	6530#			
T61	033704 G	886	6595#			
T62	034114 G	887	6660#			
T63	034324 G	888	6725#			
T64	034534 G	889	6791#			
T65	034744 G	890	6856#			
T66	035154 G	891	6922#			
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T68	035574 G	893	7052#			
T69	036004 G	894	7117#			
T7	014252 G	832	3073#			
T70	036214 G	895	7182#			
T71	036424 G	896	7247#			
T72	036634 G	897	7312#			
T73	037044 G	898	7377#			
T74	037254 G	899	7442#			
T75	037464 G	900	7508#			
T76	037674 G	901	7573#			
T77	040104 G	902	7638#			
T78	040314 G	903	7703#			
T79	040524 G	904	7767#			
T8	014412 G	833	3120#			
T80	040734 G	905	7832#			
T9	014564 G	834	3171#			
UAM	= 000200 G	1043#				
VECTOR	041306	7917	7948#			
WMP	041200	7935#				
WTYPE	002600	1118#	2632*	2697	2699	2704

