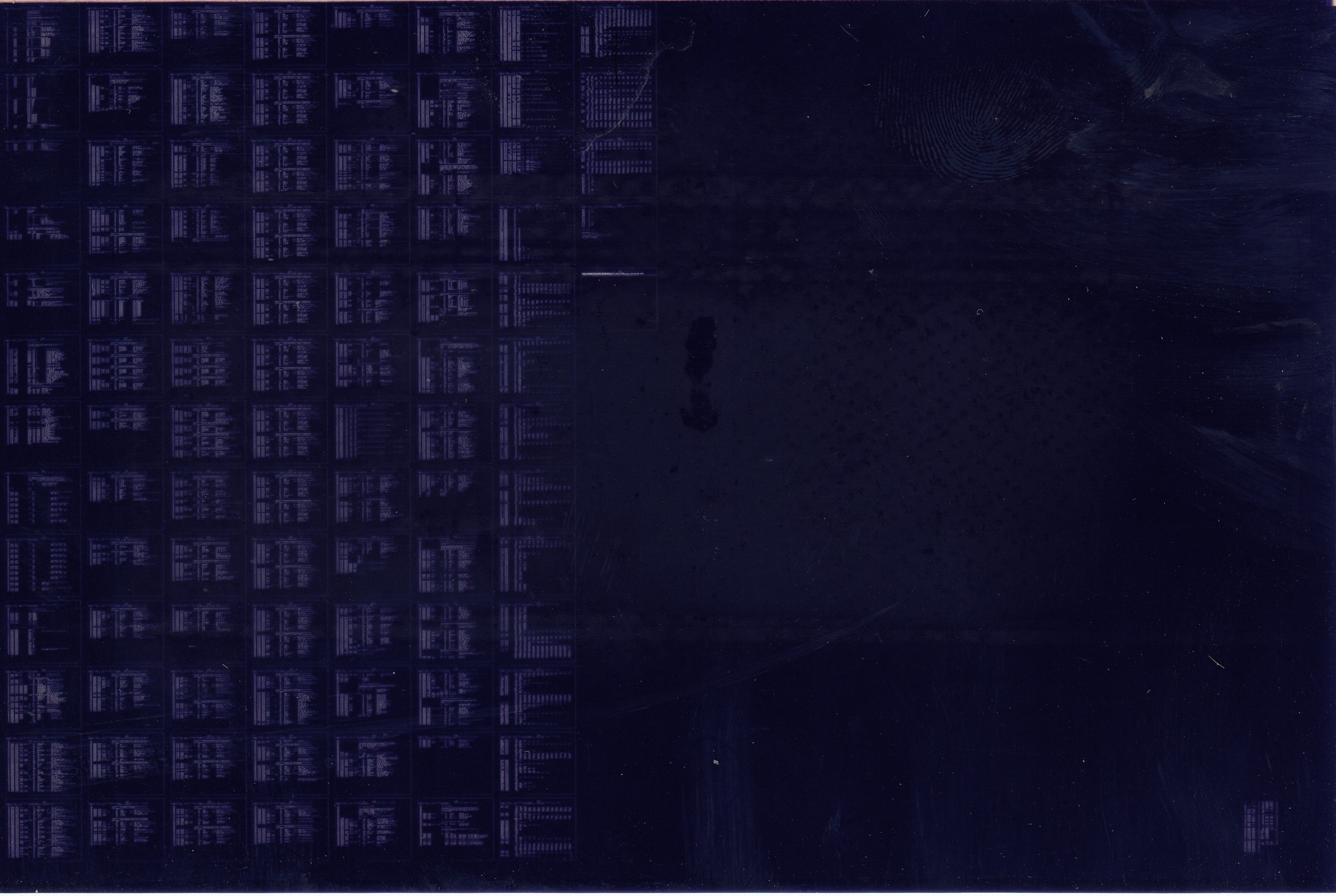


VSV01

VIDEO GRAPHIC TEST
MD-11-DZVSE-A

EP-DZVSE-A-DL-A
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MADE IN USA



ABASE=172600
AVECT1=100360
APRIOR=200
ADEVM=3

.SBTTL OPERATIONAL SWITCH SETTINGS

SWITCH	USE
15	HALT ON ERROR
14	LOOP ON TEST
13	INHIBIT ERROR TYPEOUTS
12	INHIBIT SUB-TEST DELAY
10	INHIBIT VISUAL SYNC/CHARACTER TEST PATTERNS
8	LOOP ON TEST IN SWR<7:0>
9	INHIBIT VISUAL BIT MAP TEST PATTERNS

.SBTTL TRAP CATCHER

.=0
:*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
:*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
:*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

.=174
DISPREG: .WORD 0 ;;SOFTWARE DISPLAY REGISTER
SWREG: .WORD 0 ;;SOFTWARE SWITCH REGISTER

.SBTTL STARTING ADDRESS(ES)

JMP	Q#BEGIN	::JUMP TO STARTING ADDRESS OF PROGRAM
JMP	Q#HATCH	:DISPLAY CROSS HATCH PATTERN
JMP	Q#CROSS	:DISPLAY CROSS HAIRS FOR OPERATOR
JMP	FRANKD	:DISPLAY CROSS HAIRS WITH TWO MAPS
JMP	BURNIN	:JUMP TO SET "BURN-IN" MODE FLAG
JMP	BUSOUT	:JUMP TO BUS TIME-OUT LOOP

```

172600
100360
000200
000003
000000
000174
000174
000176
000000
000000
000137 001572
000204 000137 015442
000210 000137 015642
000214 000137 016322
000220 000137 001564
000224 000137 016416

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.SBTTL ACT11 HOOKS

:*****
:HOOKS REQUIRED BY ACT11
    $SVPC=.                ;SAVE PC
    =46
    $ENDAD                 ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .SEOP
    =52
    .WORD 0                ;;2)SET LOC.52 TO ZERO
    =$SVPC                 ;; RESTORE PC
    =1000

.SBTTL APT PARAMETER BLOCK

:*****
:SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
:*****
    .SX=.                 ;;SAVE CURRENT LOCATION
    =24                   ;;SET POWER FAIL TO POINT TO START OF PROGRAM
    200                   ;;FOR APT START UP
    =44                   ;;POINT TO APT INDIRECT ADDRESS PNTR.
    $APTHDR               ;;POINT TO APT HEADER BLOCK
    =.SX                  ;;RESET LOCATION COUNTER

:*****
:SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
:INTERFACE SPEC.

$APTHD:
$HIBTS: .WORD 0           ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
$MBADR: .WORD $MAIL      ;;ADDRESS OF APT MAILBOX (BITS 0-15)
$STMT:  .WORD            ;;RUN TIM OF LONGEST TEST
$PASTM: .WORD            ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
$UNITM: .WORD            ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
        .WORD $ETEND-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)

```

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000230
000046
000046
003304
000052
000052
000000
000230
001000
001000
000024
000200
000044
001000
001000
001000
001000
000000
001172
000000
000000
000000
000000
000032

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.SBTTL COMMON TAGS

::*****
:*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
:*USED IN THE PROGRAM.

001100

SCMTAG: .=1100

:: START OF COMMON TAGS

001100
001100
001102
001103
001104
001106
001110
001112
001114
001115
001116
001120
001122
001124
001126
001130
001132
001134
001135
001136
001140
001142
001144
001146
001150
001152
001154
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001156
001157
001160
001162
001164
001166
001167
001170

.WORD 0
\$STNM: .BYTE 000
\$ERFLG: .BYTE 000
\$ICNT: .WORD 000000
\$LPADR: .WORD 000000
\$LPERR: .WORD 000000
\$ERTTL: .WORD 000000
\$ITEMB: .BYTE 000
\$ERMAX: .BYTE 001
\$ERRPC: .WORD 000000
\$GDADR: .WORD 000000
\$BDADR: .WORD 000000
\$GDDAT: .WORD 000000
\$BDDAT: .WORD 000000
\$AUTOB: .BYTE 000
\$INTAG: .BYTE 000
\$SWR: .WORD DSWR
DISPLAY: .WORD DDISP
\$TKS: 177560
\$TKB: 177562
\$TPS: 177564
\$TPB: 177566
\$NULL: .BYTE 0
\$FILLS: .BYTE 2
\$FILLC: .BYTE 12
\$TPFLG: .BYTE 0
\$REGAD: .WORD 0
\$REGO: .WORD 0
\$REGI: .WORD 0
\$QUES: .ASCII ??
\$CRLF: .ASCII <15>
\$LF: .ASCII <12>

:: CONTAINS THE TEST NUMBER
:: CONTAINS ERROR FLAG
:: CONTAINS SUBTEST ITERATION COUNT
:: CONTAINS SCOPE LOOP ADDRESS
:: CONTAINS SCOPE RETURN FOR ERRORS
:: CONTAINS TOTAL ERRORS DETECTED
:: CONTAINS ITEM CONTROL BYTE
:: CONTAINS MAX. ERRORS PER TEST
:: CONTAINS PC OF LAST ERROR INSTRUCTION
:: CONTAINS ADDRESS OF 'GOOD' DATA
:: CONTAINS ADDRESS OF 'BAD' DATA
:: CONTAINS 'GOOD' DATA
:: CONTAINS 'BAD' DATA
:: RESERVED--NOT TO BE USED

:: AUTOMATIC MODE INDICATOR
:: INTERRUPT MODE INDICATOR

:: ADDRESS OF SWITCH REGISTER
:: ADDRESS OF DISPLAY REGISTER
:: TTY KBD STATUS
:: TTY KBD BUFFER
:: TTY PRINTER STATUS REG. ADDRESS
:: TTY PRINTER BUFFER REG. ADDRESS
:: CONTAINS NULL CHARACTER FOR FILLS
:: CONTAINS # OF FILLER CHARACTERS REQUIRED
:: INSERT FILL CHARS. AFTER A "LINE FEED"
:: "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
:: CONTAINS THE ADDRESS FROM WHICH (\$REGO) WAS OBTAINED
:: CONTAINS ((\$REGAD)+0)
:: CONTAINS ((\$REGAD)+2)
:: QUESTION MARK
:: CARRIAGE RETURN
:: LINE FEED

::*****
.SBTTL APT MAILBOX-ETABLE

::*****
\$EVEN
\$MAIL: :: APT MAILBOX
\$MSGTY: .WORD AMSGTY :: MESSAGE TYPE CODE
\$FATAL: .WORD AFATAL :: FATAL ERROR NUMBER
\$TESTN: .WORD ATESTN :: TEST NUMBER
\$PASS: .WORD APASS :: PASS COUNT
\$DEVCT: .WORD ADEVCT :: DEVICE COUNT
\$UNIT: .WORD AUNIT :: I/O UNIT NUMBER

263	001206	000000	\$MSGAD: .WORD	AMSGAD	:: MESSAGE ADDRESS
264	001210	000000	\$MSGLG: .WORD	AMSGLG	:: MESSAGE LENGTH
265	001212		\$ETABLE:		:: APT ENVIRONMENT TABLE
266	001212	000	\$ENV: .BYTE	AENV	:: ENVIRONMENT BYTE
267	001213	000	\$ENVM: .BYTE	AENVM	:: ENVIRONMENT MODE BITS
268	001214	000000	\$SWREG: .WORD	ASWREG	:: APT SWITCH REGISTER
269	001216	000000	\$USWR: .WORD	AUSWR	:: USER SWITCHES
270	001220	000000	\$CPUOP: .WORD	ACPUOP	:: CPU TYPE, OPTIONS
271			*		BITS 15-11=CPU TYPE
272			*		11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05
273			*		11/70=06, PDQ=07, Q=10
274			*		BIT 10=REAL TIME CLOCK
275			*		BIT 9=FLOATING POINT PROCESSOR
276			*		BIT 8=MEMORY MANAGEMENT
277	001222	000	\$MAMS1: .BYTE	AMAMS1	:: HIGH ADDRESS, M.S. BYTE
278	001223	000	\$MTYP1: .BYTE	AMTYP1	:: MEM. TYPE, BLK#1
279			*		MEM. TYPE BYTE -- (HIGH BYTE)
280			*		900 NSEC CORE=001
281			*		300 NSEC BIPOLAR=002
282			*		500 NSEC MOS=003
283	001224	000000	\$MADR1: .WORD	AMADR1	:: HIGH ADDRESS, BLK#1
284			*		MEM. LAST ADDR.=3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
285	001226	000	\$MAMS2: .BYTE	AMAMS2	:: HIGH ADDRESS, M.S. BYTE
286	001227	000	\$MTYP2: .BYTE	AMTYP2	:: MEM. TYPE, BLK#2
287	001230	000000	\$MADR2: .WORD	AMADR2	:: MEM. LAST ADDRESS, BLK#2
288	001232	000	\$MAMS3: .BYTE	AMAMS3	:: HIGH ADDRESS, M.S. BYTE
289	001233	000	\$MTYP3: .BYTE	AMTYP3	:: MEM. TYPE, BLK#3
290	001234	000000	\$MADR3: .WORD	AMADR3	:: MEM. LAST ADDRESS, BLK#3
291	001236	000	\$MAMS4: .BYTE	AMAMS4	:: HIGH ADDRESS, M.S. BYTE
292	001237	000	\$MTYP4: .BYTE	AMTYP4	:: MEM. TYPE, BLK#4
293	001240	000000	\$MADR4: .WORD	AMADR4	:: MEM. LAST ADDRESS, BLK#4
294	001242	100360	\$VECT1: .WORD	AVECT1	:: INTERRUPT VECTOR#1, BUS PRIORITY#1
295	001244	000000	\$VECT2: .WORD	AVECT2	:: INTERRUPT VECTOR#2, BUS PRIORITY#2
296	001246	172600	\$BASE: .WORD	ABASE	:: BASE ADDRESS OF EQUIPMENT UNDER TEST
297	001250	000003	\$DEVN: .WORD	ADEVN	:: DEVICE MAP
298	001252	000000	\$CDW1: .WORD	ACDW1	:: CONTROLLER DESCRIPTION WORD#1
299	001254	000000	\$CDW2: .WORD	ACDW2	:: CONTROLLER DESCRIPTION WORD#2
300	001256		\$ETEND:		
301			.MEXIT		

ERROR POINTER TABLE

.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
;*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
;*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

::* EM ::POINTS TO THE ERROR MESSAGE
::* DH ::POINTS TO THE DATA HEADER
::* DT ::POINTS TO THE DATA
::* DF ::POINTS TO THE DATA FORMAT

0001
0002
0003
0004
0005
0006
0007
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0016
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0018
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0020
0021
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0031
0032
0033

001256

\$ERRTB:

;ITEM 1

001256 023032
001260 024004
001262 024226
001264 024322

EM1
DH1
DT1
DF0

:BUS ERROR TRAP ON CHARACTER ADDRESSES
:ERRPC ADR
:\$ERRPC \$BDDAT

;ITEM 2

001266 023133
001270 024004
001272 024226
001274 024322

EM2
DH1
DT1
DF0

:BUS ERROR TRAP ON BIT MAP ADDRESSES
:ERRPC ADR
:\$ERRPC \$BDDAT

;ITEM 3

001276 023214
001300 024060
001302 024234
001304 024322

EM3
DH3
DT3
DF0

:CHARACTER STATUS REGISTER ERROR
:ERRPC ADR GOOD BAD
:\$ERRPC VCCSR \$GDDAT \$BDDAT

;ITEM 4

001306 023254
001310 024105
001312 024246
001314 024322

EM4
DH4
DT4
DF0

:VERTICAL SYNC ERROR
:ERRPC ADR TIMED TIME1
:\$ERRPC VCCSR SAVED SAVE2

;ITEM 5

001316 023300
001320 024135
001322 024260
001324 024322

EM5
DH5
DT5
DF0

:VERTICAL SYNC ERROR/
:ERRPC VCCSR
:\$ERRPC VCCSR

;ITEM 6

001326 023336
001330 024151
001332 024266
001334 024322

EM6
DH6
DT6
DF0

:BIT MAP STATUS REGISTER ERROR
:ERRPC ADR GOOD BAD
:\$ERRPC VGCSR \$GDDAT \$BDDAT

334			:ITEM	7		
335				EM7	:BIT MAP P.C. REGISTER ERROR	
336	001336	023374		DH6	:ERRPC VGCSR GOOD BAD	
337	001340	024151		DT6	:SERRPC VGCSR \$GDDAT \$BDDAT	
338	001342	024266		DF0		
339	001344	024322				
340			:ITEM	10		
341				EM10	:BIT MAP P.C INCREMENT ERROR	
342	001346	023430		DH6	:ERRPC VGCSR GOOD BAD	
343	001350	024151		DT6	:SERRPC VGCSR \$GDDAT \$BDDAT	
344	001352	024266		DF0		
345	001354	024322				
346			:ITEM	11		
347				EM11	:WORD ERROR ON PIXEL BIT 0	
348	001356	023465		DH6	:ERRPC VGCSR GOOD BAD	
349	001360	024151		DT6	:SERRPC VGCSR \$GDDAT \$BDDAT	
350	001362	024266		DF0		
351	001364	024322				
352			:ITEM	12		
353				EM12	:BYTE ERROR ON PIXEL BIT 0	
354	001366	023524		DH6	:ERRPC VGCSR GOOD BAD	
355	001370	024151		DT6	:SERRPC VGCSR \$GDDAT \$BDDAT	
356	001372	024266		DF0		
357	001374	024322				
358			:ITEM	13		
359				EM13	:CHARACTER READY ERROR	
360	001376	023563		DH6	:ERRPC VCCSR GOOD BAD	
361	001400	024151		DT6	:SERRPC VCCSR \$GDDAT \$BDDAT	
362	001402	024266		DF0		
363	001404	024322				
364			:ITEM	14		
365				EM14	:BIT MAP READY ERROR	
366	001406	023611		DH6	:ERRPC VGCSR GOOD BAD	
367	001410	024151		DT6	:SERRPC VGCSR \$GDDAT \$BDDAT	
368	001412	024266		DF0		
369	001414	024322				
370			:ITEM	15		
371				EM14	:BIT MAP READY FAILED TO SET	
372	001416	023611		DH15	:ERRPC VGCSR	
373	001420	024176		DT15	:SERRPC \$VGCSR	
374	001422	024300		DF0		
375	001424	024322				
376			:ITEM	16		
377				EM16	:PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW	
378	001426	023635		DH16	:ERRPC VSV01	
379	001430	024212		DT16	:SERRPC \$BDDAT	
380	001432	024306		DF0		
381	001434	024322				
382			:ITEM	17		
383				EM17	:PREVIOUSLY EXISTING VTVO1 CONTROLLER DOES NOT EXIST NOW	
384	001436	023714		DH16	:ERRPC VSV01	
385	001440	024212		DT16	:SERRPC \$GDADR	
386	001442	024306		DF0		
387	001444	024322				

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001446 172600
001450 172602
001452 172603
001454 172604
001456 172605
001460 172620
001462 172622
001464 172624
001466 172630

001470 100360
001472 100362

001474 000000
001476 000000
001500 000105
001502 000000
001504 000000
001506 000000
001510 000000
001512 000000
001514 000000
001516 000000
001520 003100
001522 000100
001524 000030
001526 000014
001530 000000
001532 000137
001534 000000
001536 000400
001540 000400
001542 010000
001544 000000
001546 000001
001550 167772
001552 000000
001554 000000
001556 000000
001560 000000
001562 000000

;VTVO1 BUS ADDRESSES AND VECTOR

.SBTTL VSVO1 BUS ADDRESSES AND VECTORS

VCCSR: ABASE
VCHRL0: ABASE+2
VCHRHI: ABASE+3
VCPOS: ABASE+4
VCPOSH: ABASE+5
VGCSR: ABASE+20
VGPC: ABASE+22
VGBUF: ABASE+24
VGCOLR: ABASE+30

VCINT: AVECT1
VCINT1: AVECT1+2

;MISC. LOCATIONS

.SBTTL MISC. LOCATIONS

\$TEMP: 0
\$TEMPO: 0
KE: 105
TEMP1: 0
TEMP2: 0
TEMP3: 0
SAVED: 0
SAVE1: 0
SAVE2: 0
SAVE3: 0
TOTALC: 1600.
WIDTH: 64.
VHD: 24.
VH1: 12.
STCHAR: 0
LASTCH: 137
TEMPO: 0
XHAIR: 256.
YHAIR: 256.
NUMPIX: 4096.
BURN: 0
COAXSW: 1
COAX: 167772
MSAVED: 0
MSAVE1: 0
MSAVE2: 0
MSAVE3: 0
MSAVE4: 0

;ASCII CHARACTER USED IN FULL SCREEN PATTERN

;BIT 0-4 SELECT RELAY CONTACTS IN "BURN-IN" MODE
;BUS ADDRESS OF COAX SWITCH


```

439 001564 005237 001544      BURNIN: INC      BURN          ;SET BURN-IN MODE FLAG
440 001570 000402              BR          CONT
441 001572 005037 001544      BEGIN: CLR      BURN          ;CLEAR "BURN-IN " MODE FLAG
442 001576              CONT:
443
444      .SBTTL INITIALIZE THE COMMON TAGS
      ;;CLEAR THE COMMON TAGS ($CMTAG) AREA
445 001576 012706 001100      MOV      #SCMTAG,R6      ;;FIRST LOCATION TO BE CLEARED
446 001602 005026              CLR      (R6)+          ;;CLEAR MEMORY LOCATION
447 001604 022706 001140      CMP      #SWR,R6      ;;DONE?
448 001610 001374              BNE      -.6            ;;LOOP BACK IF NO
449 001612 012706 001100      MOV      #STACK,SP     ;;SETUP THE STACK POINTER
450      ;;INITIALIZE A FEW VECTORS
451 001616 012737 017706 000020  MOV      $$SCOPE,@#IOTVEC ;; IOT VECTOR FOR SCOPE ROUTINE
452 001624 012737 000340 000022  MOV      #340,@#IOTVEC+2 ;; LEVEL 7
453 001632 012737 020036 000030  MOV      #ERROR,@#EMTVEC ;; EMT VECTOR FOR ERROR ROUTINE
454 001640 012737 000340 000032  MOV      #340,@#EMTVEC+2 ;; LEVEL 7
455 001646 012737 022630 000034  MOV      #STRAP,@#TRAPVEC ;; TRAP VECTOR FOR TRAP CALLS
456 001654 012737 000340 000036  MOV      #340,@#TRAPVEC+2;LEVEL 7
457 001662 012737 021002 000024  MOV      #SPWRDN,@#PWRVEC ;;POWER FAILURE VECTOR
458 001670 012737 000340 000026  MOV      #340,@#PWRVEC+2 ;;LEVEL 7
459 001676 013737 003252 003244  MOV      $ENDCT,$EOPCT  ;;SETUP END-OF-PROGRAM COUNTER
460 001704 012737 001704 001106  MOV      #,$LPADR      ;;INITIALIZE THE LOOP ADDRESS FOR SCOPE
461      ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
462      ;;EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
463 001712 013746 000004              MOV      @#ERRVEC,-(SF)  ;;SAVE ERROR VECTOR
464 001716 012737 001752 000004  MOV      #64$,@#ERRVEC  ;;SET UP ERROR VECTOR
465 001724 012737 177570 001140  MOV      #DSWR,SWR      ;;SETUP FOR A HARDWARE SWICH REGISTER
466 001732 012737 177570 001142  MOV      #DDISP,DISPLAY ;;AND A HARDWARE DISPLAY REGISTER
467 001740 022777 177777 177172  CMP      #-1,@SWR      ;;TRY TO REFERENCE HARDWARE SWR
468 001746 001012              BNE      66$          ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
469      ;;AND THE HARDWARE SWR IS NOT = -1
470 001750 000403              BR          65$        ;;BRANCH IF NO TIMEOUT
471 001752 012716 001760      64$: MOV      #65$, (SP)   ;;SET UP FOR TRAP RETURN
472 001756 000002              RTI
473 001760 012737 000176 001140  65$: MOV      #SWREG,SWR  ;;POINT TO SOFTWARE SWR
474 001766 012737 000174 001142  MOV      #DISPREG,DISPLAY
475 001774 012637 000004      66$: MOV      (SP)+,@#ERRVEC ;;RESTORE ERROR VECTOR
476
477 002000 005037 001200              CLR      $PASS          ;;CLEAR PASS COUNT
478 002004 132737 000200 001213  BITB     #APTSIZE,$ENVM ;;TEST USER SIZE UNDER APT
479 002012 001403              BEQ      67$          ;;YES,USE NON-APT SWITCH
480 002014 012737 001214 001140  MOV      #$$SWREG,SWR  ;;NO,USE APT SWITCH REGISTER
481 002022
482 002022 005037 001552              CLR      MSAVE0        ;RESET MAP COUNTERS
483 002026 005037 001554              CLR      MSAVE1
484 002032 005037 001556              CLR      MSAVE2        ; AND CONTROLLER
485 002036 005037 001560              CLR      MSAVE3
486 002042 005037 001562              CLR      MSAVE4        ; LOCATIONS
487 002046 005737 000042              TST     @#42          ;TEST IF IN CHAIN MODE
488 002052 001002              BNE     RESTAT
489 002054 104401              TYPE
490 002056 022712              TITLE
491 002060 000005      RESTAT: RESET

```


MO1

MAINDEC-11-DZVSE-A
DZVSEA.P11

02-JUN-76 00:00

VSV01 VIEDO GRAPHIC TEST PROGRAM

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```

492          .SBTTL
493          .SBTTL  DETERMINE THE NUMBER OF CONTROLLERS TO TEST
494
495 002062 013737 001246 001120 DETERO: MOV    $BASE,$GDADR      ;LOAD BASE ADDR.
496 002070 012737 002242 007366      MOV    #4$,RETURN      ;LOAD RETURN ADDR. AFTER TESTING
497 002076 004737 003340      JSR    PC,DETECT      ;DETERMINE # OF MAPS
498 002102 000453      BR     3$             ;BR IF CONTROLLER DOESN'T EXIST
499 002104 004737 003576      JSR    PC,LOADR      ;FIX THE BUS ADDRESSES
500 002110 005737 001254      TST   $CDW2          ;TEST IF THE OPERATOR HAS SELECTED SOME MAPS
501 002114 001407      BEQ   6$             ;BR IF NOT
502 002116 013737 001254 001252      MOV    $CDW2,$CDW1    ;LOAD HIS SELECTION
503 002124 052737 100000 001552      BIS   #BIT15,MSAVE0  ;SET CONTROLLER EXISTS FLAG
504 002132 000430      BR     2$             ;AND TEST THEM
505 002134 105737 001213      6$:  TSTB  $ENVM        ;TEST IF "DO NOT SIZE" IS SET
506 002140 100007      BPL   5$             ;BR IF NOT
507 002142 013737 001250 001252      MOV    $DEVM,$CDW1    ;USE VALUE SUPPLIED BY "APT"
508 002150 052737 100000 001552      BIS   #BIT15,MSAVE0  ;SET CONTROLLER EXISTS FLAG
509 002156 000416      BR     2$
510 002160 005737 001200      5$:  TST   $PASS        ;TEST IF FIRST PASS
511 002164 001006      BNE   1$             ;BR IF NOT
512 002166 013737 001512 001552      MOV    SAVE1,MSAVE0   ;SAVE THE NUMBER OF MAPS
513 002174 052737 100000 001552      BIS   #BIT15,MSAVE0   ;SET "CONTROLLER EXISTS" FLAG
514 002202 123737 001512 001552 1$:  CMPB  SAVE1,MSAVE0   ;COMPARE IF ANY CHANGE IN # OF MAPS
515 002210 001401      BEQ   2$             ;BR IF NOT
516 002212 104016      ERROR 16             ;PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW
517 002214 012737 000001 001546 2$:  MOV    #1,COAXSW      ;LOAD SWITCH VALUE
518 002222 005037 001102      CLR   $STNM          ;RESET TEST #
519 002226 000137 003762      JMP   TST1           ;AND TEST THIS CONTROLLER
520 002232 005737 001552      3$:  TST   MSAVE0        ;TEST IF CONTROLLER EXISTED AT ONE TIME
521 002236 100001      BPL   4$             ;BR IF NO
522 002240 104017      ERROR 17             ;PREVIOUSLY EXISTING CONTROLLER AT 172600 DOES NOT EXIST NOW
523 002242 000240      4$:  NOP
524 002244 005737 001552      TST   MSAVE0        ;TEST IF THE FIRST CONTROLLER EXISTS
525 002250 100413      BMI   10$           ;BR IF IT'S THERE
526 002252 012737 002274 001110      MOV    #7$,$LPERR     ;LOAD LOOP RETURN
527 002260 012737 002274 001106      MOV    #7$,$LPADR     ;LOAD RETURN ADDRESS
528 002266 013737 001246 001126      MOV    $BASE,$BDDAT   ;LOAD BASE ADDRESS FOR TYPEOUT
529 002274 104001      7$:  ERROR 1 ;** FATAL ERROR ** ;VTVO1 AT ADDRESS ($BASE) DOESN'T EXIST
530 002276 000776      BR     7$           ;FATAL ERROR
531 002300 005737 001544      10$: TST   BURN          ;TEST IF "BURN-IN" MODE
532 002304 001002      BNE   DETER1        ;BR AND TEST FOR MORE MAPS
533 002306 000137 003222      JMP   DETEC7        ;REPORT "EOP"
534 002312 012737 171600 001120 DETER1: MOV    #171600,$GDADR ;LOAD BUS ADDRESS
535 002320 012737 002472 007366      MOV    #4$,RETURN      ;LOAD RETURN ADDR. AFTER TESTING
536 002326 004737 003340      JSR    PC,DETECT      ;DETERMINE # OF MAPS
537 002332 000453      BR     3$             ;BR IF CONTROLLER DOESN'T EXIST
538 002334 004737 003576      JSR    PC,LOADR      ;FIX THE BUS ADDRESSES
539 002340 005737 001254      TST   $CDW2          ;TEST IF THE OPERATOR HAS SELECTED SOME MAPS
540 002344 001407      BEQ   6$             ;BR IF NOT
541 002346 013737 001254 001252      MOV    $CDW2,$CDW1    ;LOAD HIS SELECTION
542 002354 052737 100000 001554      BIS   #BIT15,MSAVE1  ;SET CONTROLLER EXISTS FLAG
543 002362 000430      BR     2$             ;AND TEST THEM
544 002364 105737 001213      6$:  TSTB  $ENVM        ;TEST IF "DO NOT SIZE" IS SET
545 002370 100007      BPL   5$             ;BR IF NOT
546 002372 013737 001250 001252      MOV    $DEVM,$CDW1    ;USE VALUE SUPPLIED BY "APT"
547 002400 052737 100000 001554      BIS   #BIT15,MSAVE1  ;SET CONTROLLER EXISTS FLAG

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NO1

MAINDEC-11-DZVSE-A
DZVSEA.P11

02-JUN-76 03:00

VSV01 VIEDO GRAPHIC TEST PROGRAM

DETERMINE THE NUMBER OF CONTROLLERS TO TEST

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```

548 002406 000416          BR      2$
549 002410 005737 001200  5$:  TST      $PASS          ;TEST IF FIRST PASS
550 002414 001006          BNE      1$          ;BR IF NOT
551 002416 013737 001512 001554  MOV     SAVE1,MSAVE1 ;SAVE THE NUMBER OF MAPS
552 002424 052737 100000 001554  BIS     #BIT15,MSAVE1 ;SET "CONTROLLER EXISTS" FLAG
553 002432 123737 001512 001554 1$:  CMPB   SAVE1,MSAVE1 ;COMPARE IF ANY CHANGE IN # OF MAPS
554 002440 001401          BEQ     2$          ;BR IF NOT
555 002442 104016          ERROR   16          ;PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW
556 002444 012737 000002 001546 2$:  MOV     #2,COAXSW    ;LOAD SWITCH VALUE
557 002452 005037 001102          CLR     $TSTNM      ;RESET TEST #
558 002456 000137 003762          JMP     TST1        ;AND TEST THIS CONTROLLER
559 002462 005737 001554          3$:  TST     MSAVE1      ;TEST IF CONTROLLER EXISTED AT ONE TIME
560 002466 100001          BPL     4$          ;BR IF NO
561 002470 104017          ERROR   17          ;PREVIOUSLY EXISTING CONTROLLER AT 171600 DOES NOT EXIST NOW
562 002472 000240          4$:  NOP
563 002474 012737 171300 001120 DETER2: MOV     #171300,$GDADR ;LOAD BUS ADDRESS
564 002502 012737 002654 007366  MOV     #4$,RETURN  ;LOAD RETURN ADDR. AFTER TESTING
565 002510 004737 003340          JSR     PC,DETECT   ;DETERMINE # OF MAPS
566 002514 000453          BR      3$          ;BR IF CONTROLLER DOESN'T EXIST
567 002516 004737 003576          JSR     PC,LOADR    ;FIX THE BUS ADDRESSES
568 002522 005737 001254          TST     $CDW2      ;TEST IF THE OPERATOR HAS SELECTED SOME MAPS
569 002526 001407          BEQ     6$          ;BR IF NOT
570 002530 013737 001254 001252  MOV     $CDW2,$CDW1 ;LOAD HIS SELECTION
571 002536 052737 100000 001556  BIS     #BIT15,MSAVE2 ;SET CONTROLLER EXISTS FLAG
572 002544 000430          BR      2$          ;AND TEST THEM
573 002546 105737 001213          6$:  TSTB   $ENVM       ;TEST IF "DO NOT SIZE" IS SET
574 002552 100007          BPL     5$          ;BR IF NOT
575 002554 013737 001250 001252  MOV     $DEVM,$CDW1 ;USE VALUE SUPPLIED BY "APT"
576 002562 052737 100000 001556  BIS     #BIT15,MSAVE2 ;SET CONTROLLER EXISTS FLAG
577 002570 000416          BR      2$
578 002572 005737 001200          5$:  TST     $PASS      ;TEST IF FIRST PASS
579 002576 001006          BNE     1$          ;BR IF NOT
580 002600 013737 001512 001556  MOV     SAVE1,MSAVE2 ;SAVE THE NUMBER OF MAPS
581 002606 052737 100000 001556  BIS     #BIT15,MSAVE2 ;SET "CONTROLLER EXISTS" FLAG
582 002614 123737 001512 001556 1$:  CMPB   SAVE1,MSAVE2 ;COMPARE IF ANY CHANGE IN # OF MAPS
583 002622 001401          BEQ     2$          ;BR IF NOT
584 002624 104016          ERROR   16          ;PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW
585 002626 012737 000004 001546 2$:  MOV     #4,COAXSW    ;LOAD SWITCH VALUE
586 002634 005037 001102          CLR     $TSTNM      ;RESET TEST #
587 002640 000137 003762          JMP     TST1        ;AND TEST THIS CONTROLLER
588 002644 005737 001556          3$:  TST     MSAVE2      ;TEST IF CONTROLLER EXISTED AT ONE TIME
589 002650 100001          BPL     4$          ;BR IF NO
590 002652 104017          ERROR   17          ;PREVIOUSLY EXISTING CONTROLLER AT 171300 DOES NOT EXIST NOW
591 002654 000240          4$:  NOP
592 002656 012737 172300 001120 DETER3: MOV     #172300,$GDADR ;LOAD BUS ADDRESS
593 002664 012737 003036 007366  MOV     #4$,RETURN  ;LOAD RETURN ADDR. AFTER TESTING
594 002672 004737 003340          JSR     PC,DETECT   ;DETERMINE # OF MAPS
595 002676 000453          BR      3$          ;BR IF CONTROLLER DOESN'T EXIST
596 002700 004737 003576          JSR     PC,LOADR    ;FIX THE BUS ADDRESSES
597 002704 005737 001254          TST     $CDW2      ;TEST IF THE OPERATOR HAS SELECTED SOME MAPS
598 002710 001407          BEQ     6$          ;BR IF NOT
599 002712 013737 001254 001252  MOV     $CDW2,$CDW1 ;LOAD HIS SELECTION
600 002720 052737 100000 001560  BIS     #BIT15,MSAVE3 ;SET CONTROLLER EXISTS FLAG
601 002726 000430          BR      2$          ;AND TEST THEM
602 002730 105737 001213          6$:  TSTB   $ENVM       ;TEST IF "DO NOT SIZE" IS SET
603 002734 100007          BPL     5$          ;BR IF NOT

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604 002736 013737 001250 001252      MOV      $DEVM,$CDW1      ;USE VALUE SUPPLIED BY "APT"
605 002744 052737 100000 001560      BIS      #BIT15,MSAVE3   ;SET CONTROLLER EXISTS FLAG
606 002752 000416                      BR       2$
607 002754 005737 001200          5$:     TST      $PASS          ;TEST IF FIRST PASS
608 002760 001006                      BNE     1$              ;BR IF NOT
609 002762 013737 001512 001560      MOV      SAVE1,MSAVE3    ;SAVE THE NUMBER OF MAPS
610 002770 052737 100000 001560      BIS      #BIT15,MSAVE3   ;SET "CONTROLLER EXISTS" FLAG
611 002776 123737 001512 001560      1$:     CMPB    SAVE1,MSAVE3    ;COMPARE IF ANY CHANGE IN # OF MAPS
612 003004 001401                      BEQ     2$              ;BR IF NOT
613 003006 104016                      ERROR   16              ;PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW
614 003010 012737 000010 001546      2$:     MOV      #10,COAXSW     ;LOAD SWITCH VALUE
615 003016 005037 001102                      CLR     $TSTNM          ;RESET TEST #
616 003022 000137 003762                      JMP     TST1             ;AND TEST THIS CONTROLLER
617 003026 005737 001560      3$:     TST      MSAVE3        ;TEST IF CONTROLLER EXISTED AT ONE TIME
618 003032 100001                      BPL     4$              ;BR IF NO
619 003034 104017                      ERROR   17              ;PREVIOUSLY EXISTING CONTROLLER AT 172300 DOES NOT EXIST NOW
620 003036 000240                      4$:     NOP
621 003040 012737 173200 001120      4$:     DETER4: MOV     #173200,$GDADR   ;LOAD BUS ADDRESS
622 003046 012737 003220 007366      MOV     #4$,$RETURN      ;LOAD RETURN ADDR. AFTER TESTING
623 003054 004737 003340                      JSR     PC,$DETECT       ;DETERMINE # OF MAPS
624 003060 000453                      BR      3$              ;BR IF CONTROLLER DOESN'T EXIST
625 003062 004737 003576                      JSR     PC,$LODADR       ;FIX THE BUS ADDRESSES
626 003066 005737 001254                      TST     $CDW2            ;TEST IF THE OPERATOR HAS SELECTED SOME MAPS
627 003072 001407                      BEQ     6$              ;BR IF NOT
628 003074 013737 001254 001252      MOV     $CDW2,$CDW1      ;LOAD HIS SELECTION
629 003102 052737 100000 001562      BIS     #BIT15,MSAVE4   ;SET CONTROLLER EXISTS FLAG
630 003110 000430                      BR      2$              ;AND TEST THEM
631 003112 105737 001213          6$:     TSTB    $ENVM          ;TEST IF "DO NOT SIZE" IS SET
632 003116 100007                      BPL     5$              ;BR IF NOT
633 003120 013737 001250 001252      MOV     $DEVM,$CDW1      ;USE VALUE SUPPLIED BY "APT"
634 003126 052737 100000 001562      BIS     #BIT15,MSAVE4   ;SET CONTROLLER EXISTS FLAG
635 003134 000416                      BR      2$
636 003136 005737 001200          5$:     TST     $PASS          ;TEST IF FIRST PASS
637 003142 001006                      BNE     1$              ;BR IF NOT
638 003144 013737 001512 001562      MOV     SAVE1,MSAVE4    ;SAVE THE NUMBER OF MAPS
639 003152 052737 100000 001562      BIS     #BIT15,MSAVE4   ;SET "CONTROLLER EXISTS" FLAG
640 003160 123737 001512 001562      1$:     CMPB    SAVE1,MSAVE4    ;COMPARE IF ANY CHANGE IN # OF MAPS
641 003166 001401                      BEQ     2$              ;BR IF NOT
642 003170 104016                      ERROR   16              ;PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW
643 003172 012737 000020 001546      2$:     MOV     #20,COAXSW     ;LOAD SWITCH VALUE
644 003200 005037 001102                      CLR     $TSTNM          ;RESET TEST #
645 003204 000137 003762                      JMP     TST1             ;AND TEST THIS CONTROLLER
646 003210 005737 001562      3$:     TST     MSAVE4        ;TEST IF CONTROLLER EXISTED AT ONE TIME
647 003214 100001                      BPL     4$              ;BR IF NO
648 003216 104017                      ERROR   17              ;PREVIOUSLY EXISTING CONTROLLER AT 173200 DOES NOT EXIST NOW
649 003220 000240                      4$:     NOP

```


DETERMINE THE NUMBER OF CONTROLLERS TO TEST

```

650 003222
651
652
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655
656
657
658
659 003222
660 003222 000240
661 003224 005037 001102
662 003230 005237 001200
663 003234 042737 100000 001200
664 003242 005327
665 003244 000001
666 003246 003022
667 003250 012737
668 003252 000001
669 003254 003244
670 003256 104401 003323
671 003262 013746 001200
672 003266 104405
673 003270 104401 003320
674 003274 013700 000042
675 003300 001405
676 003302 000005
677 003304 004710
678 003306 000240
679 003310 000240
680 003312 000240
681 003314
682 003314 000137
683 003316 002060
684 003320 377 377 000
685 003323 015 042412 042116
686 003330 050040 051501 020123
687 003336 000043
688

```

```

DETEC7:
.SBTTL END OF PASS ROUTINE

:*****
:*INCREMENT THE PASS NUMBER ($PASS)
:*TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
:*IF THERES A MONITOR GO TO IT
:*IF THERE ISN'T JUMP TO RESTAT

$EOP:
NOP
CLR $STNM          ;; ZERO THE TEST NUMBER
INC $PASS          ;; INCREMENT THE PASS NUMBER
BIC #10000,$PASS  ;; DON'T ALLOW A NEG. NUMBER
DEC (PC)+          ;; LOOP?
$EOPCT: .WORD 1
BGT $DOAGN        ;; YES
MOV (PC)+,2(PC)+ ;; RESTORE COUNTER
$ENDCT: .WORD 1
$EOPCT
TYPE $SENDMG      ;; TYPE "END PASS #"
MOV $PASS,-(SP)   ;; SAVE $PASS FOR TYPEOUT
TYPDS             ;; GO TYPE--DECIMAL ASCII WITH SIGN
TYPE $ENULL       ;; TYPE A NULL CHARACTER
$GET42: MOV #42,R0 ;; GET MONITOR ADDRESS
BEQ $DOAGN        ;; BRANCH IF NO MONITOR
RESET            ;; CLEAR THE WORLD
$ENDAD: JSR PC,(R0) ;; GO TO MONITOR
NOP              ;; SAVE ROOM
NOP              ;; FOR
NOP              ;; ACT11
$DOAGN: JMP 2(PC)+ ;; RETURN
$RTNAD: .WORD RESTAT
$ENULL: .BYTE -1,-1,0 ;; NULL CHARACTER STRING
$SENDMG: .ASCIZ <15><12>/END PASS #/

```



```

689          .SBTTL DETERMINE THE NUMBER OF BIT MAP'S ON A VTVD1
690          .SBTTL
691
692 003340 013737 001120 001124 DETECT: MOV      $GDADR,$GDDAT      :GET BASE ADDRESS
693 003346 012737 003570 000004      MOV      #7$,ERRVEC      :LOAD RETURN
694 003354 005777 175544          TST      @SGDDAT        :TEST THE CHARACTER STATUS ADDRESS
695 003360 005037 001504          CLR      TEMP2          :THE CONTROLLER EXISTS NOW TEST THE MAPS
696 003364 005037 001506          CLR      TEMP3          :
697 003370 012737 003434 000004      MOV      #2$,ERRVEC      :LOAD BUS TRAP RETURN
698 003376 013737 001124 001126      MOV      $GDDAT,$BDDAT  :GET THE ADDRESS
699 003404 062737 000020 001126 1$:      ADD      #20,$BDDAT     :UPDATE THE ADDRESS
700 003412 005777 175510          TST      @SBDDAT        :TEST FOR A MAP
701 003416 005237 001504          INC      TEMP2          :YES IT EXISTS UPDATE THE COUNT
702 003422 022737 000006 001504      CMP      #6,TEMP2       :TEST FO LAST
703 003430 001402          BEQ      3$             :
704 003432 000764          BR       1$             :
705 003434 022626          CMP      (SP)+,(SP)+    :POP THE STACK
706 003436 012737 000006 000004 3$:      MOV      #ERRVEC+2,ERRVEC :;RESET BUS TRAP
707 003444 005037 000006          CLR      ERRVEC+2      :
708 003450 005737 000042          TST      @#42          :TEST IF "CHAIN MODE"
709 003454 001020          BNE      4$             :BR IF CHAIN MODE
710 003456 005737 001200          TST      $PASS         :TEST IF FIRST PASS
711 003462 001015          BNE      4$             :BR IF NOT
712 003464 104401 022750          TYPE    VTHDR1         :REPORT # OF MAPS
713 003470 013746 001124          MOV      $GDDAT,-(SP)  :ON THE CONTROLLER ADDRESS
714 003474 104402          TYPOC          :
715 003476 104401 022774          TYPE    VTHDR2         : "HAS"
716 003502 013746 001504          MOV      TEMP2,-(SP)   :#N
717 003506 104403          TYPOS          :
718 003510          001          000          .BYTE    1,0           :ONE DIGIT
719 003512 104401 023002          TYPE    VTHDR3         : "MAPS INSTALLED"
720 003516 013737 001504 001512 4$:      MOV      TEMP2,SAVE1   :SAVE # OF MAPS
721 003524 001413          BEQ      6$             :BR IF NONE
722 003526 012737 000001 001506      MOV      #1,TEMP3      :LOAD INDICATOR
723 003534 005337 001504          DEC      TEMP2          :CONVERT FOR LATER
724 003540 001405          BEQ      6$             :
725 003542 006337 001506          ASL      TEMP3          :MOVE
726 003546 005237 001506          INC      TEMP3          :
727 003552 000770          BR       5$             :
728 003554 013737 001506 001252 6$:      MOV      TEMP3,$CDW1    :LOAD THE DETECTED MAP BIT'S
729 003562 062716 000002          ADD      #2,(SP)       :UPDATE EXIT ADDRESS
730 003566 000207          RTS      PC            :RETURN
731
732 003570 022626          7$:      CMP      (SP)+,(SP)+    :
733 003572 000240          NOP          :
734 003574 000207          RTS      PC            :RETURN
735

```


E02

MAINDEC-11-DZVSE-A
DZVSEA.P11 02-JUN-76

VSVO1 VIEDO GRAPHIC TEST PROGRAM
00:00

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736 003576 012700 001446      LODADR: MOV      #VCCSR,RO      :LOAD ADDRESS POINTER
737 003602 013701 001120      MOV      $GDADR,R1      :LOAD ADDRESS VALUE
738 003606 010120      10$:  MOV      R1,(R0)+    :LOAD THE ADDRESSES
739 003610 022700 001470      CMP      #VCINT,RO      :TEST FOR END
740 003614 001374      BNE      10$
741 003616 062737 000002 001450      ADD      #2,VCHRLO      :ADJUST THE ADDRESSES
742 003624 062737 000003 001452      ADD      #3,VCHRHI
743 003632 062737 000004 001454      ADD      #4,VCPOS
744 003640 062737 000005 001456      ADD      #5,VCPOSH
745 003646 062737 000020 001460      ADD      #20,VGCSR
746 003654 062737 000022 001462      ADD      #22,VGPC
747 003662 062737 000024 001464      ADD      #24,VGBUF
748 003670 062737 000030 001466      ADD      #30,VGCOLR
749 003676 013737 001242 001470      MOV      $VECT1,VCINT    :LOAD BASIC INTR. VECTOR
750 003704 042737 160000 001470      BIC      #160000,VCINT   :MASK OFF BR LEVEL BITS
751 003712 013737 001470 001472      MOV      VCINT,VCINT1
752 003720 062737 000002 001472      ADD      #2,VCINT1
753 003726 000207      RTS      PC              :EXIT
754
755 003730 062737 000020 001460      MOVADR: ADD      #20,VGCSR    :UPDATE BUS ADDRESSES TO NEXT MAP
756 003736 062737 000020 001462      ADD      #20,VGPC
757 003744 062737 000020 001464      ADD      #20,VGBUF
758 003752 062737 000020 001466      ADD      #20,VGCOLR
759 003760 000207      RTS      PC              :EXIT
760
761
762
763
764 003762 000004      :*****
765 003764 012737 177777 001204      :*TEST 1 VERIFY ALL BUS ADDRESSES OF THE CHARACTER GEN.
766 003772 012737 004030 001110      :*****
767 004000 013737 001120 001124      †ST1:  SCOPE
768 004006 062737 000006 001124      MOV      #-1,$UNIT      :INDIACTE A SYNC/ CHARACTER GENERATOR LOGIC ERRO
769 004014 013737 001120 001126      MOV      #1,$SLPERR     :LOAD ERROR RETURN
770 004022 012737 004054 000004      MOV      $GDADR,$GDDAT  :LOAD BASE ADDR.
771 004030 005777 175072      ADD      #6,$GDDAT      :ADJUST TO LAST ADDRESS
772 004034 023737 001124 001126      MOV      $GDADR,$BDDAT  :LOAD TEST ADDRESS
773 004042 001407      MOV      #2,$J#ERRVEC   :LOAD BUS TRAP RETURN
774 004044 062737 000002 001126      1$:  TST      @BDDAT      :TEST IF ADDRESS EXISTS
775 004052 000766      CMP      $GDDAT,$BDDAT  :TEST IF FINISHED ALL ADDR.
776      BEQ      3$           ;;BR IF ALL DONE
777      ADD      #2,$BDDAT   :ADJUST ADDRESS POINTER
778      BR      1$         :TRY AGAIN
779
780      2$:  CMP      (SP)+,(SP)+ :CLEAN STACK
781      ERROR 1           :BUS ERROR WITH CHARACTER ADDRESS
782      BR      1$
783
784      3$:  MOV      #ERRVEC+2,@ERRVEC :RESET BUS TRAP
785      CLR      @ERRVEC+2

```


G02

MAINDEC-11-DZVSE-A
DZVSEA.F11 02-JUN-76 00:00

VSV01 VIEDO GRAPHIC TEST PROGRAM
T3 TEST THE CHAR CURSOR DISABLE BIT

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```

0036
0037
0038
0039 004362 000004
0040 004364 012737 177777 001204
0041 004372 012737 002000 001124
0042 004400 013777 001124 175040
0043 004406 017737 175034 001126
0044 004414 042737 140000 001126
0045 004422 023737 001124 001126
0046 004430 001401
0047 004432 104003
0048
0049
0050
0051 004434 000004
0052 004436 012737 004000 001124
0053 004444 013777 001124 174774
0054 004452 017737 174770 001126
0055 004460 042737 140000 001126
0056 004466 023737 001124 001126
0057 004474 001401
0058 004476 104003
0059
0060
0061
0062
0063
0064 004500 000004
0065 004502 012737 010000 001124
0066 004510 013777 001124 174730
0067 004516 017737 174724 001126
0068 004524 042737 140000 001126
0069 004532 023737 001124 001126
0070 004540 001401
0071 004542 104003
0072
0073
0074
0075
0076 004544 000004
0077 004546 012746 000340
0078 004552 012746 004560
0079 004556 000002
0080 004560 012737 020000 001124 1$:
0081 004566 013777 001124 174652
0082 004574 017737 174646 001126
0083 004602 042737 140000 001126
0084 004610 023737 001124 001126
0085 004616 001401
0086 004620 104003
0087 004622 042777 020000 174616 2$:
0088

```

```

*****
:*TEST 3 TEST THE CHAR CURSOR DISABLE BIT
*****
TST3: SCOPE
MOV #-1,$UNIT ;INDICATE A CONTROLLER ERROR
MOV #BIT10,$GDDAT ;LOAD EXPECTED VALUE
MOV $GDDAT,$VCCSR ;LOAD THE REGISTER
MOV $VCCSR,$BDDAT ;READ THE REGISTER
BIC #140000,$BDDAT ;MASK TO BITS 15-14
CMP $GDDAT,$BDDAT ;COMPARE FOR SAME
BEQ TST4 ;;BR IF EXPECTED
ERROR 3 ;'CHAR CURSOR DISABLE' FAILED TO SET

*****
:*TEST 4 TEST THE Y CROSS HAIR ENABLE BIT
*****
TST4: SCOPE
MOV #BIT11,$GDDAT ;LOAD EXPECTED VALUE
MOV $GDDAT,$VCCSR ;LOAD THE REGISTER
MOV $VCCSR,$BDDAT ;READ THE REGISTER
BIC #140000,$BDDAT ;MASK TO BITS 15-14
CMP $GDDAT,$BDDAT ;COMPARE FOR SAME
BEQ TST5 ;;BR IF EXPECTED
ERROR 3 ;'Y CROSS HAIR ENABLE' FAILED TO SET

*****
:*TEST 5 TEST THE X CROSS HAIR ENABLE BIT
*****
TST5: SCOPE
MOV #BIT12,$GDDAT ;LOAD EXPECTED VALUE
MOV $GDDAT,$VCCSR ;LOAD THE REGISTER
MOV $VCCSR,$BDDAT ;READ THE REGISTER
BIC #140000,$BDDAT ;MASK TO BITS 15-14
CMP $GDDAT,$BDDAT ;COMPARE FOR SAME
BEQ TST6 ;;BR IF EXPECTED
ERROR 3 ;'X CROSS HAIR ENABLE' FAILED TO SET

*****
:*TEST 6 TEST THE VERT SYNC INTR. ENABLE BIT
*****
TST6: SCOPE
MOV #340,-(SP) ;RAISE PRIORITY
MOV #1$,-(SP)
RTI
MOV #BIT13,$GDDAT ;LOAD EXPECTED VALUE
MOV $GDDAT,$VCCSR ;LOAD THE REGISTER
MOV $VCCSR,$BDDAT ;READ THE REGISTER
BIC #140000,$BDDAT ;MASK TO BITS 15-14
CMP $GDDAT,$BDDAT ;COMPARE FOR SAME
BEQ 2$ ;;BR IF EXPECTED
ERROR 3 ;'VERT SYNC INTR. ENABLE' FAILED TO SET
BIC #BIT13,$VCCSR ;DISABLE INTR.

```


H02

MAINDEC-11-DZVSE-A VSVD1 VIEDO GRAPHIC TEST PROGRAM MACY11 27(1006) 02-DEC-76 17:43 PAGE 21
DZVSEAR.P11 02-JUN-76 00:00 T7 TEST THAT "RESET" CLEARS CHARACTER STATUS REG.

```
009          ::*****
010          ::*TEST 7      TEST THAT "RESET" CLEARS CHARACTER STATUS REG.
011          ::*****
012          †ST7:  SCOPE
013          CLR      $GDDAT          ;CLEAR EXPETED
014          MOV      #BIT13!BIT12!BIT11!BIT10, @VCCSR ;LOAD THE REGISTER
015          RESET    ;CLEAR THE WORLD
016          MOV      @VCCSR, $BDDAT  ;READ CHARACTER STATUS
017          BIC      #141777, $BDDAT ;MASK TO OTHER BITS
018          BEQ      TST10           ;;BR IF ALL BITS CLEARED
019          ERROR    3               ;RESET FAILED TO CLEAR CHARACTER STATUS REGISTER
020          ::*****
021          ::*TEST 10     TEST THAT 'CLEAR LOW BYTE' DOES NOT CLEAR HIGH BYTE
022          ::*****
023          †ST10: SCOPE
024          MOV      #16000, $GDDAT  ;LOAD EXPECTED READ/WRITE RESULT
025          MOV      $GDDAT, @VCCSR  ;LOAD THE REGISTER BITS
026          CLRB    @VCCSR           ;CLEAR LOW BYTE
027          MOV      @VCCSR, $BDDAT  ;READ THE RESULT OF 'CLRB'
028          BIC      #BIT15!BIT14, $BDDAT ;MASK TO TOP TWO BITS
029          CMP     $GDDAT, $BDDAT   ;COMPARE FOR SAME
030          BEQ     TST11           ;;BR IF EXPECTED
031          ERROR    3               ;CLR LOW BYTE CHANGED THE
032          ;HIGH BYTE OF THE CHAR STATUS REG.
```



```

912          ::*****
914          :*TEST 11      TEST THAT 'VERT SYNC' FLAG CHANGES STATES
915          :*****
916 004736 000004          TST11: SCOPE
917 004740 005077 174502 CLR          3VCCSR          ;ENSURE CLEAR REGISTER
918 004744 004737 015672 JSR          PC,COUNTA      ;GO WAIT FOR A TRANSITION OF THE VERT SYNC FLAG
919          ;NOW TEST THE RELATIVE TIME DIFFERENCE BETWEEN 'OFF' AND 'ON' STATES
920 004750 004737 015676 JSR          PC,COUNT
921 004754 010137 001510 MOV          R1,SAVE0      ;SAVE THE FIRST TIME
922 004760 004737 015676 JSR          PC,COUNT
923 004764 010137 001514 MOV          R1,SAVE2      ;SAVE THE SECOND TIME
924 004770 013737 001514 001126 MOV          SAVE2,$BDDAT  ;LOAD THE BIGGER TIME
925 004776 163737 001510 001126 SUB          SAVE0,$BDDAT  ;SUB THE FIRST TIME
926 005004 001001          BNE          1$          ;;BR IF NOT SAME
927 005006 104004          ERROR          4          ;NO TIME DIFFERENCE BETWEEN
928 005010 013737 001514 001126 1$: MOV          SAVE2,$BDDAT  ;LOAD BIGGER TIME
929 005016 006237 001126 ASR          $BDDAT      ;DIVIDE BY 8
930 005022 006237 001126 ASR          $BDDAT
931 005026 006237 001126 ASR          $BDDAT
932 005032 023737 001126 001510 CMP          $BDDAT,SAVE0  ;TEST THAT MAGITUDE OF AT LEAST 8 BETWEEN THE TW
933 005040 101016          BHI          2$          ;;BR IF GREATER
934 005042 013737 001510 001126 MOV          SAVE0,$BDDAT ;LOAD FIRST VALUE
935 005050 006237 001126 ASR          $BDDAT      ;ADJUST VALUE
936 005054 006237 001126 ASR          $BDDAT
937 005060 006237 001126 ASR          $BDDAT
938 005064 023737 001126 001514 CMP          $BDDAT,SAVE2  ;COMPARE TO SECOND READ
939 005072 101001          BHI          2$          ;BR IF MAGITUDE IS GREATER
940 005074 104004          ERROR          4          ;TIME DIFFERENCE BETWEEN OFF AND ON STATE WRONG
941 005076 000240          2$: NOP

```



```

969
970
971
972 005202 000004
973 005204 012746 000340
974 005210 012746 005216
975 005214 000002
976 005216 012777 020000 174222 10$:
977 005224 004737 015672
978 005230 012777 005274 174232
979 005236 012777 000340 174226
980 005244 012700 000010
981 005250 005046
982 005252 012746 005260
983 005256 000002
984 005260 005300 1$:
985 005262 001376
986 005264 005077 174156
987 005270 104005
988 005272 000421
989
990 005274 022626
991 005276 012777 005332 174166 2$:
992 005304 012700 000100
993 005310 005046
994 005312 012746 005320
995 005316 000002
996 005320 005300 3$:
997 005322 001376
998 005324 005077 174116
999 005330 000402
1000 005332 022626
1001 005334 104005 4$:

```

```

*****
*TEST 13 TEST FOR 'VERT SYNC' INTERRUPT
*****
TST13: SCOPE
MOV #340,-(SP) ;LOAD PSW RETURN TO LEVEL 7
MOV #10$,-(SP) ;LOAD RETURN P.C.
RTI ;DO THIS TO BE LSI-11 COMPAT.
MOV #BIT13,@VCCSR ;SET INTR ENABLE
JSR PC COUNTA ;WAIT FOR A COMPLETE 'VERT SYNC' CYCLE
MOV #2$,@VCINT ;LOAD INTR RETURN VECTOR
MOV #340,@VCINT1
MOV #10,R0 ;LOAD A SHORT DELAY COUNTER
CLR -(SP) ;LOAD PSW RETURN TO LEVEL 0
MOV #1$,-(SP) ;LOAD RETURN P.C.
RTI
1$: DEC R0 ;DELAY
BNE 1$ ;BR IF NOT DONE
CLR @VCCSR
ERROR 5 ;'VERT SYNC' FAILED TO CAUSE AN INTR.
BR TST14 ;:BR OVER INTR HANDLER
;RETURN HERE WITH PSW=7, LOWER PRIORITY AGAIN AND TEST FOR NO INTERRUPT AGAIN
2$: CMP (SP)+,(SP)+ ;ADJUST THE STACK BY 4
MOV #4$,@VCINT1 ;RELOAD INTR VECTOR TO FALSE INTR HANDLER
MOV #100,R0 ;LOAD DELAY COUNTER
CLR -(SP) ;LOAD PSW RETURN LEVEL TO 0
MOV #3$,-(SP) ;LOAD RETURN P.C.
RTI
3$: DEC R0 ;DELAY
BNE 3$
CLR @VCCSR ;CLEAR INTR ENABLE
BR TST14 ;:
4$: CMP (SP)+,(SP)+ ;ADJUST STACK
ERROR 5 ;INTR DONE FAILED TO CLEAR INTR REQUEST

```


L02

```

1002          ;:*****
1003          ;*TEST 14      INTERRUPT LEVEL TEST FOR 'VERT SYNC' INTERRUPT
1004          ;:*****
1005 005336 000004          TST14: SCOPE
1006 005340 012746 000340      MOV      #340,-(SP)          ;LOAD PSW RETURN TO LEVEL 7
1007 005344 012746 005352      MOV      #10$,-(SP)         ;LOAD RETURN P.C.
1008 005350 000002          RTI          ;DO THIS TO BE LSI-11 COMPAT.
1009 005352 012777 020000 174066 10$: MOV      #BIT13,@VCCSR      ;SET INTR ENABLE
1010 005360 004737 015672      JSR      PC,COUNTA        ;WAIT FOR A COMPLETE 'VERT SYNC' CYCLE
1011 005364 012777 005444 174076  MOV      #2$,@VCINT        ;LOAD INTR RETURN VECTOR
1012 005372 012777 000340 174072  MOV      #340,@VCINT1
1013 005400 012700 000010      MOV      #10,R0
1014 005404 113737 001243 001516  MOVB    $VECT1+1,SAVE3    ;LOAD A SHORT DELAY COUNTER
1015 005412 042737 177437 001516  BIC     #177437,SAVE3     ;GET BR LEVEL
1016 005420 013746 001516      MOV      SAVE3,-(SP)      ;MASK OUT BITS
1017 005424 012746 005432      MOV      #1$,-(SP)        ;LOAD BR LEVEL
1018 005430 000002          RTI          ;LOAD RETURN P.C.
1019 005432 005300          1$: DEC     R0              ;DELAY
1020 005434 001376          BNE     1$                ;BR IF NOT DONE
1021 005436 005077 174004      CLR     @VCCSR
1022 005442 000404          BR      5$                ;:BR OVER INTR HANDLER
1023          ;RETURN HERE IF AN INTERRUPT OCCURRED ** ERROR **
1024 005444 022626          2$: CMP     (SP)+,(SP)+    ;ADJUST THE STACK BY 4
1025 005446 005077 173774      CLR     @VCCSR
1026 005452 104005          ERROR  5
1027          ;VERT SYNC INTERRUPTED IN ERROR
1028 005454 012777 005512 174006 5$: MOV      #4$,@VCINT      ;IS BR LEVEL CORRECT ?
1029 005462 012700 000100      MOV      #100,R0         ;RELOAD INTR VECTOR
1030 005466 005046          CLR     -(SP)            ;LOAD DELAY COUNTER
1031 005470 012746 005476      MOV      #3$,-(SP)       ;LOAD PSW RETURN LEVEL TO 0
1032 005474 000002          RTI          ;LOAD RETURN P.C.
1033 005476 005300          3$: DEC     R0              ;DELAY
1034 005500 001376          BNE     3$
1035 005502 005077 173740      CLR     @VCCSR          ;CLEAR INTR ENABLE
1036 005506 104005          ERROR  5                ;LOWERING PROIORITY FAILED TO CAUSE AN INTERRUPT
1037 005510 000401          BR      6$
1038 005512 022626          4$: CMP     (SP)+,(SP)+    ;ADJUST STACK
1039 005514 005046          6$: CLR     -(SP)
1040 005516 012746 005524      MOV      #7$,-(SP)
1041 005522 000002          RTI
1042 005524 000240          7$: NOP
    
```


M02

```

1043
1044
1045
1046 005526 000004
1047 005530 005737 001544
1048 005534 001406
1049 005536 013777 001546 174004
1050 005544 004537 016666
1051 005550 000012
1052 005552
1053 005552 005737 001200
1054 005556 001427
1055 005560 032777 002000 173352
1056 005566 001023
1057 005570 012777 010000 173650
1058 005576 005037 001124
1059 005602 005077 173642
1060 005606 113777 001124 173634
1061 005614 004537 016666
1062 005620 000001
1063 005622 005237 001124
1064 005626 023737 001124 001536
1065 005634 001364
1066
1067
1068
1069 005636 000004
1070 005640 005737 001200
1071 005644 001431
1072 005646 032777 002000 173264
1073 005654 001025
1074 005656 012777 004000 173562
1075 005664 005037 001124
1076 005670 005077 173554
1077 005674 113777 001124 173550
1078 005702 004537 016666
1079 005706 000001
1080 005710 105237 001124
1081 005714 123737 001540 001124
1082 005722 001364
1083 005724 005077 173516
1084

::*****
:*TEST 15 DYNAMIC TESTING OF THE X CROSS HAIR POSITION REGISTERS
::*****
TST15: SCOPE
        TST BURN ;TEST IF RUNNING IN "BURN-IN" MODE
        BEQ 3$ ;BR IF NOT
        MOV COAXSW, @COAX ;LOAD THE RELAY
        JSR R5, DELAY ;LET THE RELAY SETTLE
        IO.
3$:
        TST $PASS ;TEST IF FIRST PASS OF PROGRAM
        BEQ TST16 ;;BR IF FIRST PASS
        BIT #SW10, @SWR ;TEST IF INHIBIT VISUAL CHAR. PATTERNS
        BNE TST16 ;;BR IF INHIBIT IS SET
        MOV #BIT12, @VCCSR ;ENABLE X CROSS HAIRS
        CLR $GDDAT ;CLEAR EXPECTED VALUE
        CLR @VCHRLO ;CLEAR POS.
1$:
        MOVB $GDDAT, @VCHRLO ;LOAD THE LOW BYTE (X POSITION)
        JSR R5, DELAY
        I
        INC $GDDAT ;UPDATE X POSITION
        CMP $GDDAT, XHAIR ;TEST IF MORE LINES?
        BNE 1$

::*****
:*TEST 16 DYNAMIC TESTING OF THE Y CROSS HAIR POSITION REGISTERS
::*****
TST16: SCOPE
        TST $PASS ;TEST IF FIRST PASS OF PROGRAM
        BEQ TST17 ;;BR IF FIRST PASS
        BIT #SW10, @SWR ;TEST IF INHIBIT VISUAL CHAR. PATTERNS
        BNE TST17 ;;BR IF INHIBIT IS SET
        MOV #BIT11, @VCCSR ;ENABLE Y CROSS HAIRS
        CLR $GDDAT ;CLEAR POSITION
        CLR @VCHRLO
1$:
        MOVB $GDDAT, @VCHRHI
        JSR R5, DELAY
        I
        INCB $GDDAT
        CMPB YHAIR, $GDDAT
        BNE 1$
        CLR @VCCSR ;DISABLE CROSS HAIRS
    
```



```

1085      ;:*****
1086      ;*TEST 17      FULL SCREEN OF A CHARACTER
1087      ;:*****
1088      TST17:  SCOPE
1089      TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
1090      BEQ      TST20      ;;BR IF FIRST PASS
1091      BIT      #SW10, @SWR ;TEST IF INHIBIT VISUAL CHAR. PATTERNS
1092      BNE      TST20      ;;BR IF INHIBIT IS SET
1093      JSR      PC, HOME    ;HOME AND ERASE THE SCREEN
1094      JSR      PC, FILLWC  ;FILL SCREEN WITH A 'E'S
1095      JSR      R5, DELAY
1096      100.
1097
1098      ;:*****
1099      ;*TEST 20      SINGLE CHARACTER ACROSS ALL COLS. <ALL CHARACTERS>
1100      ;:*****
1101      TST20:  SCOPE
1102      TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
1103      BEQ      TST21      ;;BR IF FIRST PASS
1104      BIT      #SW10, @SWR ;TEST IF INHIBIT VISUAL CHAR. PATTERNS
1105      BNE      TST21      ;;BR IF INHIBIT IS SET
1106      JSR      PC, HOME    ;HOME AND ERASE THE SCREEN
1107      MOV      #40, STCHAR ;SET-UP STARTING CHARACTER
1108
1109      MOV      VHD, TEMPO   ;LOAD COUNT
1110      MOV      STCHAR, R1  ;LOAD R1= TO CHARACTER
1111      JSR      PC, FILBUF  ;LOAD A BUFFER WITH THAT CHARACTER
1112
1113      JSR      PC, XPRNT   ;DISPLAY A FULL LINE FROM THE BUFFER
1114
1115      DEC      TEMPO       ;DONE ?
1116      BPL      2$         ;FINISHED
1117      JSR      R5, DELAY
1118      100.
1119      JSR      PC, HOME    ;HOME AND ERASE THE SCREEN
1120      MOV      VHD, TEMPO
1121      MOV      STCHAR, R1
1122      INC      STCHAR     ;UPDATE THE CHARACTER
1123      CMP      LASTCH, STCHAR ;TEST FOR FINAL CHARACTER
1124      BNE      1$         ;BRANCH IF NOT COMPLETED
1125      JSR      R5, DELAY
1126      100.
1127

```


B03

MAINDEC-11-DZVSE-A
DZVSEB.P11 02-JUN-76 00:00

VSVO: VIEDO GRAPHIC TEST PROGRAM
T21

MACY11 27(1006) 02-DEC-76 17:43 PAGE 29
ROTATING CHARACTERS ACROSS ALL COLS. (ALL CHARACTERS)

```

11300
11301
11302
11303 006112 000004
11304 006114 005737 001200
11305 006120 001451
11306 006122 032777 002000 173010
11307 006130 001045
11308 006132 004737 017256
11309 006136 012737 000040 001530 3$:
11310
11311 006144 004737 017256 JSR PC,HOME ;HOME AND ERASE THE SCREEN
11312 006150 013737 001524 001534 MOV VHO,TEMPO ;LOAD TEMP
11313 006156 013701 001530 1$: MOV STCHAR,R1 ;LOAD R1=TO CHARACTER
11314 006162 004537 016166 JSR RS,LIC ;LOAD A BUFFER STARTING WITH
11315 006166 001522 WIDTH ; THAT CHARACTER AND WIDTH (BYTE)
11316
11317 006170 004737 016032 JSR PC,XPRNT ;DISPLAY A FULL LINE FROM THE BUFFER
11318
11319 006174 005337 001534 DEC TEMPO ;DONE ?
11320 006200 100010 BPL 2$ ;BR IF YES
11321 006202 004537 016666 JSR RS,DELAY
11322 006206 000144 100.
11323 006210 004737 017256 JSR PC,HOME ;HOME AND ERASE THE SCREEN
11324 006214 013737 001524 001534 MOV VHO,TEMPO
11325 006222 005237 001530 2$: INC STCHAR ;UPDATE THE STARTING CHARACTER
11326 006226 023737 001532 001530 CMP LASTCH,STCHAR ;TEST FOR FINAL CHARACTER
11327 006234 001350 BNE 1$ ;BRANCH IF NOT COMPLETED
11328
11329 006236 004537 016666 JSR RS,DELAY
11330 006242 000144 100.

```



```

1159
1160
1161
1162 006244 000004
1163 006246 005737 001200
1164 006252 001530
1165 006254 032777 002000 172656
1166 006262 001124
1167 006264 004737 017256
1168 006270 012737 123456 017704 DCATST:
1169 006276 012737 176543 017702
1170 006304 013737 001520 017152
1171 006312 013737 001524 017150
1172 006320 012700 024350
1173 006324 012701 017154 2$:
1174 006330 012120 1$:
1175 006332 022701 017254
1176 006336 001374
1177 006340 005337 017150
1178 006344 100367
1179 006346 012737 177777 024330
1180 006354 004737 017604 GENER:
1181 006360 013700 017702
1182 006364 042700 177740
1183 006370 020037 001524
1184 006374 101367
1185 006376 010037 017144
1186 006402 012737 024350 017150
1187 006410 010001
1188 006412 001405
1189 006414 063737 001522 017150 1$:
1190 006422 005301
1191 006424 001373
1192 006426 004737 017604 GENERX:
1193 006432 013700 017702
1194 006436 042700 177700
1195 006442 010037 017146
1196 006446 060037 017150
1197 006452 013701 017150
1198 006456 105711
1199 006460 100735
1200 006462 113777 017144 172766
1201 006470 113777 017146 172756
1202 006476 111137 024330
1203 006502 152711 000200
1204 006506 004737 016032
1205 006512 104407
1206 006514 005337 017152
1207 006520 001315
1208 006522 004537 016666
1209 006526 000144
1210 006530 004737 017256

```

```

*****
:TEST 22 DIRECT CURSOR ADDRESS TEST
*****
↑ST22: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST23 ;BR IF FIRST PASS
BIT #SW10,JSWR ;TEST IF INHIBIT VISUAL CHAR. PATTERNS
BNE TST23 ;BR IF INHIBIT IS SET
JSR PC,HOME ;HOME AND ERASE THE SCREEN
MOV #123456,$LONUM ;PRIME RANDOM NUMBER GENERATOR
MOV #176543,$SHINUM
MOV TOTALC,OVRAL ;LOAD CHAR COUNT
MOV VHO,SET ;LOAD LINE COUNT
MOV #BUFFER+20,RO ;LOAD DESTINATION BUFFER
MOV #MSGTXT,R1 ;LOAD MESSAGE POINTER
MOV (R1)+,(RO)+ ;LOAD 2 CHARACTERS
CMP #MSGTND,R1 ;TEST FOR LAST CHAR
BNE 1$ ;BR UNTIL DONE 1 LINE
DEC SET ;FINISHED ALL LINES
BPL 2$ ;BR IF NOT
MOV #-1,BUFFER ;LOAD MESSAGE TERMINATOR
JSR PC,$RAND ;GENERATE RANDOM NUMBER
MOV $SHINUM,RO ;GET RANDOM NUMBER
BIC #177740,RO ;RANDOM NO. MUST BE TWO DIGITS
CMP RO,VHO ;NO. MUST NOT BE GREATER THAN VHO
BHI GENER ;GREATER, REGENERATION
MOV RO,YADD5 ;STORE RANDOM Y COORDINATE
MOV #BUFFER+20,SET ;LOAD BASE POINTER
MOV RO,R1
BEQ GENRX ;RESULT, MINIMUM Y COORDINATE
ADD WIDTH,SET ;SETUP Y INDEX LOCATION FOR PRINTOUT
DEC R1
BNE 1$ ;Y COORDINATE IS SET
JSR PC,$RAND ;GENERATE RANDOM NUMBER
MOV $SHINUM,RO ;GET A RANDOM NUMBER
BIC #177700,RO ;RANDOM NO. MAY BE LESS THAN 200
MOV RO,XADD5 ;STORE RANDOM X COORDINATE
ADD RO,SET ;SETUP X COOR, FOR PNTOUT.
MOV SET,R1 ;SETUP CHECK
TSTB (R1) ;HAS CURRENT CHAR, ALREADY BEEN USED?
BMI GENER ;YES, REGENERATE
MOVB YADD5,AVCPOSH ;LOAD Y COORDINATE
MOVB XADD5,AVCPOS ;LOAD X COORDINATE
MOVB (R1),BUFFER ;LOAD CHARACTER TO BE PRINTED
BISB #200,(R1) ;INDICATE USE OF CURSOR POSITION
JSR PC,XPRNT ;EXECUTE AND PRINT CHARACTER
CKSWR ;TEST FOR CTRL G
DEC OVRAL ;MAXIMUM NO. OF COORDINATES
BNE GENER ;BR BACK UNTIL DONE
JSR RS,DELAY
JSR PC,HOME ;HOME AND ERASE THE SCREEN

```



```

1211 1211 *****
1212 1212 *TEST 23 CURSOR MOTION TEST
1213 1213 *****
1214 1214 TST23: SCOPE
1215 1215 TST $PASS ;TEST IF FIRST PASS OF PROGRAM
1216 1216 BEQ TST24 ;;BR IF FIRST PASS
1217 1217 BIT #SW10, @SWR ;TEST IF INHIBIT VISUAL CHAR. PATTERNS
1218 1218 BNE TST24 ;;BR IF INHIBIT IS SET
1219 1219 JSR PC, HOME ;HOME AND ERASE THE SCREEN
1220 1220 BIC #BIT10, @VCCSR ;ENABLE CURSOR
1221 1221
1222 1222 ;ROUTINE TO LOAD REFERENCE LINES FOR CURSOR MOTION TEST
1223 1223
1224 1224 LBMT: MOV #BUFFER, R0 ;LOAD POINTER
1225 1225 MOV VH1, R1 ;LOAD R1
1226 1226 DEC R1
1227 1227 JSR PC, MOVDN1 ;MOVE CURSOR DOWN
1228 1228 JSR PC, MOVRIG ;MOVE RIGHT
1229 1229 MOVB #62, (R0)+ ;LOAD #2
1230 1230 JSR PC, MOVRIG ;MOVE RIGHT
1231 1231 JSR PC, MOVRIG ;MOVE RIGHT
1232 1232 MOVB #40, (R0)+
1233 1233 MOVB #61, (R0)+ ;LOAD #1
1234 1234 JSR PC, MOVDWN ;MOVE DOWN
1235 1235 JSR PC, MOVRIG ;MOVE RIGHT
1236 1236 MOVB #63, (R0)+ ;LOAD #3
1237 1237 JSR PC, MOVRIG
1238 1238 JSR PC, MOVRIG ;MOVE RIGHT
1239 1239 MOVB #64, (R0)+ ;LOAD #4
1240 1240 MOVB #377, (R0) ;TERM
1241 1241 BR LBMT1 ;;BR TO NEXT PART
1242 1242
1243 1243 MOVDWN: MOV VH1, R1
1244 1244 MOVDN1: MOVB #15, (R0)+ ;LOAD CR
1245 1245 MOVB #12, (R0)+ ;LOAD LF
1246 1246 DEC R1
1247 1247 BNE MOVDN1 ;LOOP UNTIL DONE
1248 1248 RTS PC ;EXIT
1249 1249
1250 1250 MOVRIG: MOV #16, R1 ;LOAD R1
1251 1251 IS: MOVB #40, (R0)+ ;LOAD SPACES
1252 1252 DEC R1
1253 1253 BPL IS ;LOOP UNTIL DONE
1254 1254 RTS PC ;EXIT
1255 1255
1256 1256 LBMT1: JSR PC, XPRNT ;DISPLAY THIS LINE
1257 1257 JSR R5, DELAY
1258 1258
1259 1259
1260 1260
1261 1261
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1300 1300

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E03

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1260          :CURSOR MOTION SUBROUTINE
1261 006744 013701 001526 LCM: MOV VHI,R1          :LOAD COUNT
1262 006750 012700 024330 MOV #BUFFER,RO      :LOAD BUFFER POINTER
1263 006754 112720 000032 1$: MOVB #32,(RO)+      :LOAD A CURSOR UP
1264 006760 005301 DEC R1
1265 006762 001374 BNE 1$          :LOOP UNTIL DONE
1266 006764 112720 000130 MOVB #130,(RO)+      :LOAD 'X'
1267 006770 012701 000040 MOV #40,R1          :LOAD COUNT
1268 006774 112720 000010 2$: MOVB #10,(RO)+      :LOAD "BACKSPACE (CURSOR LEFT)"
1269 007000 005301 DEC R1          :DONE ALL ?
1270 007002 100374 BPL 2$          :BR IF NOT
1271 007004 112720 000130 MOVB #130,(RO)+      :LOAD 'X'
1272 007010 112720 000010 MOVB #10,(RO)+      :LOAD BACKSPACE
1273 007014 013701 001526 MOV VHI,R1          :LOAD COUNT
1274 007020 112720 000013 3$: MOVB #13,(RO)+      :LOAD CURSOR DOWN
1275 007024 005301 DEC R1          :DONE ?
1276 007026 001374 BNE 3$          :BR IF NOT
1277 007030 112720 000130 MOVB #130,(RO)+      :LOAD 'X'
1278 007034 112720 000010 MOVB #10,(RO)+      :LOAD BACKSPACE
1279 007040 012701 000036 MOV #36,R1          :LOAD COUNT
1280 007044 112720 000030 4$: MOVB #30,(RO)+      :LOAD 'C' CURSOR RIGHT
1281 007050 005301 DEC R1
1282 007052 100374 BPL 4$          :LOOP UNTIL DONE
1283 007054 112720 000130 MOVB #130,(RO)+      :LOAD 'X'
1284 007060 112720 000377 MOVB #377,(RO)+
1285 007064 004737 016032 JSR PC,XPRNT        :DISPLAY THIS LINE
1286 007070 004537 016666 JSR RS,DELAY
1287 007074 000144 100.
1288 007076 004737 017256 JSR PC,HOME          :HOME AND ERASE THE SCREEN
1289 007102 052777 002000 172336 BYS #BIT10,AVCCSR      :DISABLE CHAR. CURSOR
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1294 007110 000004
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:*****
:*TEST 24
:*****
↑ST24: SCOPE

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F03

MAINDEC-11-DZVSE-A
DZVSEA.P11 02-JUN-76

VSVO1 VIEDO GRAPHIC TEST PROGRAM
00:00

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1295	007112	000240			NOCHAR: NOP			
1296	007114	013737	001120	001502	MOV	\$GDADR,TEMP1	:GET BASE ADDRESS	
1297	007122	062737	000020	001502	ADD	#20,TEMP1	:UPDATE TO THE MAP ADDRESS	
1298	007130	013737	001252	004360	MOV	\$CDW1,DEVTMP	:GET THE NUMBER OF MAPS SELECTED	
1299	007136	042737	177700	004360	BIC	#177700,DEVTMP	:MASK TO LOWER SIX BITS	
1300	007144	001506			BEG	MAPRTA	:BR IF NO MAPS SELECTED	
1301	007146	005037	001204		CLR	\$UNIT	:CLEAR MAP INDICATOR	
1302	007152	012737	000001	001506	MOV	#BIT0,TEMP3	:LOAD TESTING BIT	
1303	007160	033737	001506	004360	MAPRTB: BIT	TEMP3,DEVTMP	:TEST IF THIS BIT MAP IS SELECTED	
1304	007166	001402			BEG	MAPRET	:BR IF NOT	
1305	007170	000137	007370		JMP	NUMBR	:SELECTED - TEST THIS MAP	
1306	007174	062737	000020	001502	MAPRET: ADD	#20,TEMP1	:UPDATE TO NEXT BUS ADDRESS	
1307	007202	005237	001204		INC	\$UNIT	:UPDATE MAP INDICATOR	
1308	007206	006337	001506		ASL	TEMP3	:MOVE THE TEST BIT	
1309	007212	022737	000100	001506	CMP	#100,TEMP3	:TEST IF ANY MORE POSSIBLE MAPS	
1310	007220	001357			BNE	MAPRTB	:BR IF MORE	
1311					.SBTTL	MULTIPLE MAP PATTERN		
1312	007222	005737	001200		TST	\$PASS	:TEST IF 1ST PASS	
1313	007226	001455			BEG	MAPRTA	:BR IF FIRST PASS	
1314	007230	004737	003576		JSR	PC,LODADR	:LOAD THE BUS ADDRESS	
1315	007234	013737	001252	007342	MOV	\$CDW1,10\$:GET THE NUMBER OF MAPS	
1316	007242	042737	177700	007342	BIC	#177700,10\$:MASK	
1317	007250	012700	007346		MOV	#DUALPL,RO	:LOAD POINTER	
1318	007254	005737	007342		1\$: TST	10\$:TEST IF ANY EXIST	
1319	007260	001424			BEQ	4\$:BR IF DONE	
1320	007262	006237	007342		ASR	10\$:TEST FOR NEXT MAP	
1321	007266	103015			BCC	2\$:BR IF NEXT NOT SELECTED	
1322	007270	004737	016234		JSR	PC,CLRMAP	:CLEAR THE MAP	
1323	007274	112037	007344		MOVB	(RO)+,11\$:GET COLOR VALUE	
1324	007300	013777	007344	172160	MOV	11\$,2\$VGCOR	:LOAD THE COLOR FOR THIS MAP	
1325	007306	112077	172146		MOVB	(RO)+,2\$VGCOR	:LOAD THE ORGIN FOR THIS MAP	
1326	007312	052777	000400	172140	BIS	#BITS,2\$VGCOR	:ENABLE MAP	
1327	007320	000401			BR	3\$		
1328	007322	005720			2\$: TST	(RO)+	:ADJUST POINTER	
1329	007324	004737	003730		3\$: JSR	PC,MOVADR	:LOAD ADR OF NEXT MAP	
1330	007330	000751			BR	1\$		
1331	007332	004537	016666		4\$: JSR	R5,DELAY		
1332	007336	000144			100.			
1333	007340	000410			BR	MAPRTA		
1334	007342	000000			10\$: 0			
1335	007344	000000			11\$: 0			
1336								
1337	007346	003	005		DUALPL: .BYTE	3,5	:MAP #0 COLOR AND ORGIN	
1338	007350	014	006		.BYTE	14,6	:MAP #1 COLOR AND ORGIN	
1339	007352	050	001		.BYTE	60,1	:MAP #2	
1340	007354	033	002		.BYTE	33,2	:MAP #3	
1341	007356	017	015		.BYTE	17,15	:MAP #4	
1342	007360	063	016		.BYTE	63,16	:MAP #5	
1343								
1344	007362	000177	000000		MAPRTA: JMP	\$RETURN		
1345								
1346	007366	000000			RETURN: 0		:RETURN CONTROL	

:THE BIT MAP ADDRESS IS IN "TEMP1" - LOAD THE ADDRESS AND
: START TESTING THAT BIT MAP

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007370 012737 001502 001460
007376 012737 001502 001462
007404 012737 001502 001464
007410 012737 001502 001466
007420 062737 000002 001462
007426 062737 000004 001464
007434 062737 000010 001466
007442 012737 000024 001102

NUMBR: MOV TEMP1,VGCSR ;LOAD BUS ADDRESSES
MOV TEMP1,VGPC
MOV TEMP1,VGBUF
MOV TEMP1,VGCOLR
ADD #2,VGPC
ADD #4,VGBUF
ADD #10,VGCOLR
MOV #STN-1,\$STNM ;LOAD TEST NUMBER

::*****
:*TEST 25 TEST THAT 'EXPAND' BIT CAN BE SET
:*****

007450 000004
007456 012737 004000 001124
007460 012777 001124 171772
007466 017737 171766 001126
007474 042737 171360 001126
007502 023737 001124 001126
007510 001401
007512 104006

TST25: SCOPE
MOV #BIT11,\$GDDAT ;LOAD THE EXPECTED
MOV \$GDDAT,\$VGCSR ;LOAD THE REGISTER
MOV \$VGCSR,\$BDDAT ;READ THE REGISTER
BIC #171360,\$BDDAT ;MASK TO BITS
CMP \$GDDAT,\$BDDAT ;COMPARE THE EXPECTED TO RECVD
BEQ TST26 ;;BR IF SET
ERROR 6 ;'EXPAND' BIT FAILED TO SET

::*****
:*TEST 26 TEST THAT 'MONO' BIT CAN BE SET
:*****

007514 000004
007516 012737 002000 001124
007524 012777 001124 171726
007532 017737 171722 001126
007540 042737 171360 001126
007546 023737 001124 001126
007554 001401
007556 104006

TST26: SCOPE
MOV #BIT10,\$GDDAT ;LOAD THE EXPECTED
MOV \$GDDAT,\$VGCSR ;LOAD THE REGISTER
MOV \$VGCSR,\$BDDAT ;READ THE REGISTER
BIC #171360,\$BDDAT ;MASK TO BITS
CMP \$GDDAT,\$BDDAT ;COMPARE THE EXPECTED TO RECVD
BEQ TST27 ;;BR IF SET
ERROR 6 ;'MONO' BIT FAILED TO SET

::*****
:*TEST 27 TEST THAT 'MAP ENABLE' BIT CAN BE SET
:*****

007560 000004
007562 012737 000400 001124
007570 012777 001124 171662
007576 017737 171656 001126
007604 042737 171360 001126
007612 023737 001124 001126
007620 001401
007622 104006

TST27: SCOPE
MOV #BIT8,\$GDDAT ;LOAD THE EXPECTED
MOV \$GDDAT,\$VGCSR ;LOAD THE REGISTER
MOV \$VGCSR,\$BDDAT ;READ THE REGISTER
BIC #171360,\$BDDAT ;MASK TO BITS
CMP \$GDDAT,\$BDDAT ;COMPARE THE EXPECTED TO RECVD
BEQ TST30 ;;BR IF SET
ERROR 6 ;MAP ENABLE FAILED TO SET

H03

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1396 ::*****  
1397 :*TEST 30 TEST THAT 'ORIGIN POINT' BITS CAN BE SET  
1398 :******  
1399 TST30: SCOPE  
1400 MOV #15,$LPERR ;LOAD THE EXPECTED  
1401 MOV #BIT0,$GDDAT ;LOAD THE REGISTER  
1402 15: MOV $GDDAT,$VGCSCR ;LOAD THE REGISTER  
1403 MOV $VGCSCR,$BDDAT ;READ THE REGISTER  
1404 BIC #171360,$BDDAT ;MASK TO BITS  
1405 CMP $GDDAT,$BDDAT ;COMPARE THE EXPECTED TO RCVD  
1406 BEQ 25 ;:BR IF SET  
1407 ERROR 6 ;'ORIGIN POINT' BIT FAILED TO SET  
1408 25: INC $GDDAT ;UPDATE EXPECTED VALUE OF 'ORIGIN POINT'  
1409 CMP #20,$GDDAT ;TEST IF VALID VALUE FOR 'ORIGIN'  
1410 BNE 15 ;BR IF MORE TO TEST  
1411 :******  
1412 :*TEST 31 TEST THAT CLEAR LOW BYTE DOES NOT CLEAR HIGH BYTE  
1413 :******  
1414 TST31: SCOPE  
1415 MOV #BIT11!BIT10!BITS,$GDDAT  
1416 MOV #BIT11!BIT10!BITS+17,$VGCSCR  
1417 CLRB $VGCSCR ;CLEAR LOW BYTE  
1418 MOV $VGCSCR,$BDDAT ;READ THE REGISTER  
1419 BIC #170360,$BDDAT ;MASK TO BITS  
1420 CMP $GDDAT,$BDDAT ;TEST IF SAME  
1421 BEQ TST32 ;:BR IF SAME  
1422 ERROR 6 ;CLEARING LOW BYTE OF STATUS CHANGED THE HIGH BY  
1423 :******  
1424 :*TEST 32 TEST THAT CLEAR HIGH BYTE DOES NOT CLEAR LOW BYTE  
1425 :******  
1426 TST32: SCOPE  
1427 MOV #17,$GDDAT ;LOAD EXPECTED READ VALUE  
1428 MOV #BIT11!BIT10!BITS+17,$VGCSCR ;LOAD STATUS REGISTER  
1429 MOV $VGCSCR,RO ;MAKE ADDRESS  
1430 INC RO ;OF HIGH BYTE  
1431 CLRB (RO) ;CLEAR HIGH BYTE  
1432 MOV $VGCSCR,$BDDAT ;READ REGISTER  
1433 BIC #170360,$BDDAT ;MASK OUT BITS  
1434 CMP $GDDAT,$BDDAT ;COMPARE EXPECT TO RCVD  
1435 BEQ TST33 ;:BR IF SAME  
1436 ERROR 6 ;CLEARING THE HIGH BYTE OF STATUS CHANGED  
1437 ;THE LOW BYTE  
1438 :******  
1439 :*TEST 33 TEST THAT "RESET" CLEARS THE BIT MAP STATUS REGISTER  
1440 :******  
1441 TST33: SCOPE  
1442 CLR $GDDAT ;LOAD EXPECTED  
1443 MOV #BIT11!BIT10!BITS!17,$VGCSCR ;LOAD THE STATUS REGISTER  
1444 RESET ;RESET THE WORLD  
1445 MOV $VGCSCR,$BDDAT ;READ THE REGISTER  
1446 BIC #171360,$BDDAT ;MASK TO OTHER BITS  
1447 BEQ TST34 ;:BR IF CLEARED  
1448 ERROR 6 ;RESET FAILED TO CLEAR BIT MAP STATUS REGISTER
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010074 000004
010076 005037 001124
010102 005037 001126
010106 004537 016666
010112 100001
010114 052777 001000 171336
010122 117737 171332 001126
010130 100001
010132 104014
010134 012737 001000 001124
010142 004537 016666
010146 100001
010150 017737 171304 001126
010156 033737 001124 001126
010164 001001
010166 104014
010170 005037 001124
010174 105737 001126
010200 100001
010202 104014
010204 004537 016666
010210 100002
010212 012737 000200 001124
010220 017737 171234 001126
010226 033737 001124 001126
010234 001001
010236 104014
010240 005037 001124
010244 032737 001000 001126
010252 001401
010254 104014

*****
*TEST 34 TEST MAP READY AND CLEAR MAP BITS
*****
TST34: SCOPE
;TEST THAT WRITTING BIT 9 (CLEAR MAP) CLEARS MAP READY (BIT 7)
CLR $GDDAT ;CLEAR EXPECTED
CLR $BDDAT ;CLEAR RESULTS
JSR R5,DELAY ;WAIT FOR VERT SYNC
BIT15!1
BIS #BIT9,$VGCSR ;SET "CLEAR MAP" BIT
MOVB $VGCSR,$BDDAT ;READ VG STATUS REG
BPL 1$ ;;BR IF CLEARED
ERROR 14 ;BIT MAP READY NOT SET

;TEST "CLEAR MAP" BIT SETS
1$: MOV #BIT9,$GDDAT ;LOAD EXPECTED
JSR R5,DELAY ;WAIT FOR VERT SYNC
BIT15!BIT0
MOV $VGCSR,$BDDAT ;READ MAP STATUS REG.
BIT $GDDAT,$BDDAT ;TEST IF BIT IS SET
BNE 2$ ;;BR IF SET
ERROR 14 ;CLEAR MAP BIT FAILED TO SET

;TEST THAT "MAP READY" CLEARED
2$: CLR $GDDAT ;CLEARED EXPECTED
TSTB $BDDAT ;TEST BIT 7
BPL 3$ ;;BR IF CLEARED
ERROR 14 ;SETTING "CLEAR MAP" BIT FAILED TO CLEAR "MAP RE

;TEST THAT MAP READY RETURNS AFTER 2 VERT SYNC TIMES
3$: JSR R5,DELAY ;WAIT FOR VERT SYNC
BIT15!2
MOV #BIT7,$GDDAT ;LOAD EXPECTED
MOV $VGCSR,$BDDAT ;READ STATUS
BIT $GDDAT,$BDDAT ;TEST IF BIT IS SET
BNE 4$ ;;BR IS SET
ERROR 14 ;MAP READY FAILED TO SET AFTER CLEAR MAP

;TEST THAT "CLEAR MAP" BIT WAS CLEARED
4$: CLR $GDDAT ;CLEAR EXPECTED
BIT #BIT9,$BDDAT ;TEST FOR CLEARED BIT
BEQ TST35 ;;BR IF CLEARED
ERROR 14 ;MAP READY SET BUT "CLEAR MAP" BIT FAILED TO CLE

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J03

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1501 010256 000004
1502 010260 012737 010276 001110
1503 010266 005037 001124
1504 010272 005037 001474
1505 010276 013777 001474 171156 1$:
1506 010304 017737 171150 001126
1507 010312 042737 177617 001126
1508 010320 023737 001124 001126
1509 010326 001401
1510 010330 104007
1511
1512 010332 062737 000020 001124 2$:
1513 010340 005237 001474
1514 010344 022737 000010 001474
1515 010352 001351
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1520 010354 000004
1521 010356 012737 010376 001110
1522 010364 012737 000020 001124
1523 010372 005077 171064
1524 010376 012777 177777 171060 1$:
1525 010404 012700 000100
1526 010410 105777 171044 3$:
1527 010414 100403
1528 010416 005300
1529 010420 001373
1530 010422 104014
1531 010424 017737 171030 001126 4$:
1532 010432 042737 177617 001126
1533 010440 023737 001124 001126
1534 010446 001401
1535 010450 104010
1536
1537 010452 062737 000020 001124 2$:
1538 010460 022737 000200 001124
1539 010466 001343
1540

::*****
:*TEST 35 READ-WRITE TEST OF THE LOW 3 BITS OF THE MAP P.C.
::*****
†ST35: SCOPE
MOV #1$, $LPERR
CLR $GDDAT ;CLEAR EXPECTED READ VALUE
CLR $TEMP ;CLEAR WRITE VALUE
MOV $TEMP, @VGPC ;LOAD BIT MAP P.C.
MOV @VGCSR, $BDDAT ;READ STATUS (BIT MAP)
BIC #177617, $BDDAT ;MASK TO UNUSED BITS
CMP $GDDAT, $BDDAT ;TEST IF SAME
BEQ 2$ ;;BR IF SAME
ERROR 7 ;BIT MAP P.C. DID NOT LOAD
CORRECTLY
ADD #20, $GDDAT ;UPDATE EXPECTED READ VALUE
INC $TEMP ;UPDATE EXPECTED WRITE VALUE
CMP #10, $TEMP ;TEST FOR FIRST NON-VALID
BNE 1$ ;BRANCH IF STILL OK VALUES.

::*****
:*TEST 36 STATIC BIT MAP P.C. INCREMENT
::*****
†ST36: SCOPE
MOV #1$, $LPERR
MOV #20, $GDDAT ;LOAD EXPECTED READ VALUE
CLR @VGPC ;CLEAR MAP P.C.
MOV #-1, @VGBUF ;LOAD A PIXEL
MOV #BIT6, R0 ;LOAD DELAY COUNT
TSTB @VGCSR ;TEST IF MAP IS READY
BMI 4$ ;BR IF YES
DEC R0 ;DELAY
BNE 3$ ;BR IN NOT EXCEEDED
ERROR 14 ;MAP READY FAILED TO SET
MOV @VGCSR, $BDDAT ;READ RESULT VALUE FROM BITS 4-6 OF CSR
BIC #177617, $BDDAT ;MASK TO UNUSED BITS
CMP $GDDAT, $BDDAT ;TEST IF SAME
BEQ 2$ ;;BR IF SAME
ERROR 10 ;BIT MAP P.C. FAILED TO INCREMENT
;BY LOADING BUFFER WITHOUT LOADING P.C.
ADD #BIT4, $GDDAT ;UPDATE EXPECTED READ VALUE
CMP #BIT7, $GDDAT ;TEST IF MORE BITS TO INCREMENT
BNE 1$ ;;BR IF MORE BITS
  
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K03

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1541
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1544 010470 000004
1545 010472 012737 010514 001110
1546 010500 012737 010000 001124
1547 010506 012737 000001 001474
1548 010514 005077 170742 1$:
1549 010520 005077 170740
1550 010524 005077 170732
1551 010530 105777 170724 3$:
1552 010534 100375
1553 010536 013777 001474 170720
1554 010544 105777 170710 4$:
1555 010550 100375
1556 010552 017737 170702 001126
1557 010560 042737 007777 001126
1558 010566 023737 001124 001126
1559 010574 001401
1560 010576 104011
1561 010600 006337 001474 2$:
1562 010604 006337 001474
1563 010610 006337 001474
1564 010614 006337 001474
1565 010620 006337 001124
1566 010624 001333
1567

::*****
:*TEST 37 READ-WRITE WORD TEST OF PIXEL 0,1,2 AND 3 -- BIT 0
:*****
†T37: SCOPE
MOV #1$, $LPERR
MOV #BIT12, $GDDAT ;LOAD EXPECTED READ VALUE
MOV #BIT0, $TEMP ;LOAD EXPECTED WRITE VALUE
1$: CLR @VGPC ;RESET P.C.
CLR @VGBUF ;CLEAR DATA
CLR @VGPC ;RESET PC
3$: TSTB @VGCSCR ;WAIT FOR MAP READY
BPL 3$
MOV $TEMP, @VGBUF ;LOAD WRITE VLUE
4$: TSTB @VGCSCR ;WAIT FOR READY
BPL 4$
MOV @VGCSCR, $BDDAT ;READ RESULT VALUE
BIC #7777, $BDDAT ;MASK TO UNWANTED BITS
CMP $GDDAT, $BDDAT ;TEST IF SAME
BEQ 2$ ;;BR IF SAME
ERROR 11 ;BIT0 OF A PIXEL FAILED TO SET
2$: ASL $TEMP ;ADJUST
ASL $TEMP ; THE
ASL $TEMP ; WRITE
ASL $TEMP ; VALUE TO THE NEXT PIXEL
ASL $GDDAT ;ADJUST THE EXPECTED READ VALUE
BNE 1$ ;BR IF; MORE BITS TO TEST
  
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1568
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1570      ;:*****
1571      ;*TEST 40      BASIC LOOK UP TABLE (L.U.T.) TEST
1572      ;:*****
1572      TST40: SCOPE
1573      TST      BURN      ;TEST IF IN "BURN-IN" MODE
1574      BEQ      2$      ;BR IF NOT
1575      MOV      COAXSW,@COAX ;CHANGE THE RELAY
1576      JSR      RS,DELAY ;WAIT FOR RELAY TO SETTLE
1577      IO.
1578      2$:
1579      TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
1580      BEQ      TST41      ;;BR IF FIRST PASS
1581      BIT      #SW09,@SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
1582      BNE      TST41      ;;BR IF INHIBIT IS SET
1583      1$:
1584      JSR      PC,TESTID ;DISPLAY MAP # AND TEST #
1585
1586      JSR      PC,CLRMAP ;CLEAR THE BIT MAP
1587
1588      JSR      RS,LODSTA ;LOAD THE SEQUENCE TABLE
1589      0 ;LOC 0 WITH 14 - LOC 1-17 WITH 63
1590      TABLE3
1591
1592      MOV      #BIT10!BIT8,@VGC SR ;ENABLE THE MAP
1593
1594      JSR      RS,DELAY
1595      100.
1596
1597      ;:*****
1598      ;*TEST 41      STATIC INTENSITY LEVEL TEST (L.U.T. 0)
1599      ;:*****
1600      TST41: SCOPE
1601      TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
1602      BEQ      TST42      ;;BR IF FIRST PASS
1603      BIT      #SW09,@SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
1604      BNE      TST42      ;;BR IF INHIBIT IS SET
1605      JSR      PC,TESTID ;DISPLAY MAP # AND TEST #
1606
1607      JSR      PC,CLRMAP ;ENSURE CLEAR MAP
1608
1609      MOV      #BIT10!BIT8,@VGC SR ;ENABLE 'MONO' AND 'MAP'
1610      MOV      #17,$TEMP
1611
1612      MOV      $TEMP,@VGC CLR 1$: ;LOAD LOC. 0 WITH A INTENSITY LEVEL
1613      JSR      RS,DELAY
1614      IO.
1615      DEC      $TEMP ;REDUCE THE INTENSITY LEVEL
1616      BNE      1$      ;;BR IF MORE LEVELS TO TEST
1617
    
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M03

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1621 011012 000004
1622 011014 005737 001200
1623 011020 001432
1624 011022 032777 001000 170110
1625 011030 001026
1626 011032 004737 017306
1627
1628 011036 004737 016234
1629 011042 012777 002000 170410
1630
1631 011050 004537 017062
1632 011054 000000
1633 011056 016526
1634 011060 005077 170376
1635 011064 012777 000001 170372
1636 011072 052777 000400 170360
1637 011100 004537 016666
1638 011104 000144
1639
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1643 011106 000004
1644 011110 005737 001200
1645 011114 001432
1646 011116 032777 001000 170014
1647 011124 001026
1648 011126 004737 017306
1649
1650 011132 004737 016234
1651 011136 012777 002000 170314
1652
1653 011144 004537 017062
1654 011150 000000
1655 011152 016526
1656 011154 005077 170302
1657 011160 012777 000020 170276
1658 011166 052777 000400 170264
1659 011174 004537 016666
1660 011200 000144
1661

::*****
:*TEST 42 DYNAMIC WORD TESTING OF PIXEL 0 (FIRST MAP LOCATION)
:*****
TST42: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST43 ;;BR IF FIRST PASS
BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST43 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #

JSR PC,CLRMAP ;CLEAR THE MAP
MOV #BIT10,AVGCSR ;SET 'MONO'

JSR RS,LODSTA ;LOAD 4 INTO L.U.T. 0 AND
D ;LOAD #17 INTO L.U.T. 1
TABLE4
CLR AVGPC ;CLEAR MAP P.C.
MOV #1,AVGBUF ;LOAD PIXEL0 BIT0
BIS #BIT8,AVGCSR ;ENABLE THE MAP
JSR RS,DELAY
100.

::*****
:*TEST 43 DYNAMIC WORD TESTING OF PIXEL 1 (FIRST MAP LOCATION)
:*****
TST43: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST44 ;;BR IF FIRST PASS
BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST44 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #

JSR PC,CLRMAP ;CLEAR THE MAP
MOV #BIT10,AVGCSR ;SET 'MONO'

JSR RS,LODSTA ;LOAD 4 INTO L.U.T. 0 AND
D ;LOAD #17 INTO L.U.T. 1
TABLE4
CLR AVGPC ;CLEAR MAP P.C.
MOV #20,AVGBUF ;LOAD PIXEL0 BIT0
BIS #BIT8,AVGCSR ;ENABLE THE MAP
JSR RS,DELAY
100.
  
```


NO3

```

1662                                     ;:*****
1663                                     ;:TEST 44      DYNAMIC WORD TESTING OF PIXEL 2 (FIRST MAP LOCATION)
1664                                     ;:*****
1665 011202 000004                               †TST44: SCOPE
1666 011204 005737 001200                       TST      $PASS                ;TEST IF FIRST PASS OF PROGRAM
1667 011210 001432                               BEQ      TST45                ;;BR IF FIRST PASS
1668 011212 032777 001000 167720               BIT      #SW09,2JSR          ;TEST IF INHIBIT VISUAL MAP PATTERNS
1669 011220 001026                               BNE     TST45                ;;BR IF INHIBIT IS SET
1670 011222 004737 017306                       JSR     PC,TESTID           ;DISPLAY MAP # AND TEST #
1671
1672 011226 004737 016234                       JSR     PC,CLRMAP           ;CLEAR THE MAP
1673 011232 012777 002000 170220               MOV     #BIT10,2VGCSPR     ;SET 'MONO'
1674
1675 011240 004537 017062                       JSR     R5,LODSTA          ;LOAD 4 INTO L.U.T. 0 AND
1676 011244 000000                               O        ;LOAD #17 INTO L.U.T. 1
1677 011246 016526                               TABLE4
1678 011250 005077 170206                       CLR     2VGPC              ;CLEAR MAP P.C.
1679 011254 012777 000400 170202               MOV     #400,2VGBUF        ;LOAD PIXEL2 BIT0
1680 011262 052777 000400 170170               BIS     #BIT8,2VGCSPR     ;ENABLE THE MAP
1681 011270 004537 016666                       JSR     R5,DELAY
1682 011274 000144                               100.
1683
1684                                     ;:*****
1685                                     ;:TEST 45      DYNAMIC WORD TESTING OF PIXEL 3 (FIRST MAP LOCATION)
1686                                     ;:*****
1687 011276 000004                               †TST45: SCOPE
1688 011300 005737 001200                       TST      $PASS                ;TEST IF FIRST PASS OF PROGRAM
1689 011304 001432                               BEQ      TST46                ;;BR IF FIRST PASS
1690 011306 032777 001000 167624               BIT      #SW09,2JSR          ;TEST IF INHIBIT VISUAL MAP PATTERNS
1691 011314 001026                               BNE     TST46                ;;BR IF INHIBIT IS SET
1692 011316 004737 017306                       JSR     PC,TESTID           ;DISPLAY MAP # AND TEST #
1693
1694 011322 004737 016234                       JSR     PC,CLRMAP           ;CLEAR THE MAP
1695 011326 012777 002000 170124               MOV     #BIT10,2VGCSPR     ;SET 'MONO'
1696
1697 011334 004537 017062                       JSR     R5,LODSTA          ;LOAD 4 INTO L.U.T. 0 AND
1698 011340 000000                               O        ;LOAD #17 INTO L.U.T. 1
1699 011342 016526                               TABLE4
1700 011344 005077 170112                       CLR     2VGPC              ;CLEAR MAP P.C.
1701 011350 012777 010000 170106               MOV     #10000,2VGBUF      ;LOAD PIXEL3 BIT0
1702 011356 052777 000400 170074               BIS     #BIT8,2VGCSPR     ;ENABLE THE MAP
1703 011364 004537 016666                       JSR     R5,DELAY
1704 011370 000144                               100.
1705
  
```



```

1706
1707
1708
1709 011372 000004
1710 011374 005737 001200
1711 011400 001441
1712 011402 032777 001000 167530
1713 011410 001035
1714 011412 004737 017306
1715 011416 004737 016234
1716 011422 004537 017062
1717 011426 000000
1718 011430 016466
1719
1720 011432 005077 170024
1721 011436 013700 001464
1722 011442 013701 001542
1723 011446 105777 170006
1724 011452 100375
1725 011454 012710 000001
1726 011460 005301
1727 011462 001371
1728 011464 012777 002400 167766
1729 011472 004737 017554
1730 011476 004537 016666
1731 011502 000144
1732
1733
1734
1735
1736 011504 000004
1737 011506 005737 001200
1738 011512 001441
1739 011514 032777 001000 167416
1740 011522 001035
1741 011524 004737 017306
1742 011530 004737 016234
1743 011534 004537 017062
1744 011540 000000
1745 011542 016466
1746
1747 011544 005077 167712
1748 011550 013700 001464
1749 011554 013701 001542
1750 011560 105777 167674
1751 011564 100375
1752 011566 012710 000020
1753 011572 005301
1754 011574 001371
1755 011576 012777 002400 167654
1756 011604 004737 017554
1757 011610 004537 016666
1758 011614 000144
1759

```

```

*****
*TEST 46 DYNAMIC WORD TESTING OF 'PIXELO' (INCREMENT MAP P.C.) TEST L.U.T. 1
*****

```

```

†ST46: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST47 ;;BR IF FIRST PASS
BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST47 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLMAP
JSR RS,LODSTA ;LOAD COLOR TABLE
D ;LOC 0 AND 2 THRU 17 WITH 0
TABLE2 ;LOC 1 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
†S: TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #1,(R0) ;LOAD BIT0 OF PIXEL0
DEC R1 ;FINISHED ALL PIXEL0'S
BNE 1$ ;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR RS,DELAY
100.

```

```

*****
*TEST 47 DYNAMIC WORD TESTING OF 'PIXEL1' (INCREMENT MAP P.C.) TEST L.U.T. 1
*****

```

```

†ST47: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST50 ;;BR IF FIRST PASS
BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST50 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLMAP
JSR RS,LODSTA ;LOAD COLOR TABLE
D ;LOC 0 AND 2 THRU 17 WITH 0
TABLE2 ;LOC 1 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
†S: TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #20,(R0) ;LOAD BIT0 OF PIXEL1
DEC R1 ;FINISHED ALL PIXEL1'S
BNE 1$ ;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR RS,DELAY
100.

```



```

1760
1761
1762
1763 011616 000004
1764 011620 005737 001200
1765 011624 001441
1766 011626 032777 001000 167304
1767 011634 001035
1768 011636 004737 017306
1769 011642 004737 016234
1770 011646 004537 017062
1771 011652 000000
1772 011654 016466
1773
1774 011656 005077 167600
1775 011662 013700 001464
1776 011666 013701 001542
1777 011672 105777 167562
1778 011676 100375
1779 011700 012710 000400
1780 011704 005301
1781 011706 001371
1782 011710 012777 002400 167542
1783 011716 004737 017554
1784 011722 004537 016666
1785 011726 000144
1786
1787
1788
1789
1790 011730 000004
1791 011732 005737 001200
1792 011736 001441
1793 011740 032777 001000 167172
1794 011746 001035
1795 011750 004737 017306
1796 011754 004737 016234
1797 011760 004537 017062
1798 011764 000000
1799 011766 016466
1800
1801 011770 005077 167466
1802 011774 013700 001464
1803 012000 013701 001542
1804 012004 105777 167450
1805 012010 100375
1806 012012 012710 010000
1807 012016 005301
1808 012020 001371
1809 012022 012777 002400 167430
1810 012030 004737 017554
1811 012034 004537 016666
1812 012040 000144

```

```

*****
*TEST 50 DYNAMIC WORD TESTING OF 'PIXEL2' (INCREMENT MAP P.C.) TEST L.U.T. 1
*****

```

```

TST50: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST51 ;;BR IF FIRST PASS
BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST51 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLMAP
JSR RS,LODSTA ;LOAD COLOR TABLE
O ;LOC 0 AND 2 THRU 17 WITH 0
TABLE2 ;LOC 1 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,RO ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #400,(RO) ;LOAD BIT0 OF PIXEL2
DEC R1 ;FINISHED ALL PIXEL2'S
BNE 1$ ;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR RS,DELAY
100.

```

```

*****
*TEST 51 DYNAMIC WORD TESTING OF 'PIXEL3' (INCREMENT MAP P.C.) TEST L.U.T. 1
*****

```

```

TST51: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST52 ;;BR IF FIRST PASS
BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST52 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLMAP
JSR RS,LODSTA ;LOAD COLOR TABLE
O ;LOC 0 AND 2 THRU 17 WITH 0
TABLE2 ;LOC 1 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,RO ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #10000,(RO) ;LOAD BIT0 OF PIXEL3
DEC R1 ;FINISHED ALL PIXEL3'S
BNE 1$ ;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR RS,DELAY
100.

```



```

1813
1814
1815
1816 012042 000004
1817 012044 005737 001200
1818 012050 001441
1819 012052 032777 001000 167060
1820 012060 001035
1821 012062 004737 017306
1822 012066 004737 016234
1823 012072 004537 017062
1824 012076 000000
1825 012100 016606
1826
1827 012102 005077 167354
1828 012106 013700 001464
1829 012112 013701 001542
1830 012116 105777 167336
1831 012122 100375
1832 012124 012710 000002
1833 012130 005301
1834 012132 001371
1835 012134 012777 002400 167316
1836 012142 004737 017554
1837 012146 004537 016666
1838 012152 000144
1839
1840
1841
1842
1843 012154 000004
1844 012156 005737 001200
1845 012162 001441
1846 012164 032777 001000 166746
1847 012172 001035
1848 012174 004737 017306
1849 012200 004737 016234
1850 012204 004537 017062
1851 012210 000000
1852 012212 016606
1853
1854 012214 005077 167242
1855 012220 013700 001464
1856 012224 013701 001542
1857 012230 105777 167224
1858 012234 100375
1859 012236 012710 000040
1860 012242 005301
1861 012244 001371
1862 012246 012777 002400 167204
1863 012254 004737 017554
1864 012260 004537 016666
1865 012264 000144
1866

```

```

*****
*TEST 52 DYNAMIC WORD TESTING OF 'PIXELO' (INCREMENT MAP P.C.) TEST L.U.T. 2
*****

```

```

TST52: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST53 ;;BR IF FIRST PASS
BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST53 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLMAP
JSR RS,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0,1 AND 3 THRU 17 WITH 0
TABLE6 ;LOC 2 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,RO ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #2,(RO) ;LOAD BIT1 OF PIXEL0
DEC R1 ;FINISHED ALL PIXEL0'S
BNE 1$ ;BR IF NOT
MOV #BIT10:BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR RS,DELAY
100.

```

```

*****
*TEST 53 DYNAMIC WORD TESTING OF 'PIXEL1' (INCREMENT MAP P.C.) TEST L.U.T. 2
*****

```

```

TST53: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST54 ;;BR IF FIRST PASS
BIT #SW09,JSWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST54 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLMAP
JSR RS,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0,1 AND 3 THRU 17 WITH 0
TABLE6 ;LOC 2 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,RO ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #40,(RO) ;LOAD BIT1 OF PIXEL1
DEC R1 ;FINISHED ALL PIXEL1'S
BNE 1$ ;BR IF NOT
MOV #BIT10:BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR RS,DELAY
100.

```


E04

```

1867
1868
1869
1870 012266 000004
1871 012270 005737 001200
1872 012274 001441
1873 012276 032777 001000 166634
1874 012304 001035
1875 012306 004737 017306
1876 012312 004737 016234
1877 012316 004537 017062
1878 012322 000000
1879 012324 016606
1880
1881 012326 005077 167130
1882 012332 013700 001464
1883 012336 013701 001542
1884 012342 105777 167112
1885 012346 100375
1886 012350 012710 001000
1887 012354 005301
1888 012356 001371
1889 012360 012777 002400 167072
1890 012366 004737 017554
1891 012372 004537 016666
1892 012376 000144
1893
1894
1895
1896
1897 012400 000004
1898 012402 005737 001200
1899 012406 001441
1900 012410 032777 001000 166522
1901 012416 001035
1902 012420 004737 017306
1903 012424 004737 016234
1904 012430 004537 017062
1905 012434 000000
1906 012436 016606
1907
1908 012440 005077 167016
1909 012444 013700 001464
1910 012450 013701 001542
1911 012454 105777 167000
1912 012460 100375
1913 012462 012710 020000
1914 012466 005301
1915 012470 001371
1916 012472 012777 002400 166760
1917 012500 004737 017554
1918 012504 004537 016666
1919 012510 000144

```

```

*****
*TEST 54 DYNAMIC WORD TESTING OF 'PIXEL2' (INCREMENT MAP P.C.) TEST L.U.T. 2
*****

```

```

†ST54: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST55 ;;BR IF FIRST PASS
BIT #SW09, $SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST55 ;;BR IF INHIBIT IS SET
JSR PC, TESTID ;DISPLAY MAP # AND TEST #
JSR PC, CLRMAP
JSR RS, LODSTA ;LOAD COLOR TABLE
O ;LOC 0,1 AND 3 THRU 17 WITH 0
TABLE6 ;LOC 2 WITH A 17
CLR $VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF, R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX, R1 ;LOAD # OF PIXELS
1$: TSTB $VGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #1000, (R0) ;LOAD BIT1 OF PIXEL2
DEC R1 ;FINISHED ALL PIXEL2'S
BNE 1$ ;BR IF NOT
MOV #BIT10!BIT8, $VGCSR ;ENABLE THE MAP
JSR PC, LDLIN ;LOAD REFERENCE LINE
JSR RS, DELAY
100.

```

```

*****
*TEST 55 DYNAMIC WORD TESTING OF 'PIXEL3' (INCREMENT MAP P.C.) TEST L.U.T. 2
*****

```

```

†ST55: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST56 ;;BR IF FIRST PASS
BIT #SW09, $SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST56 ;;BR IF INHIBIT IS SET
JSR PC, TESTID ;DISPLAY MAP # AND TEST #
JSR PC, CLRMAP
JSR RS, LODSTA ;LOAD COLOR TABLE
O ;LOC 0,1 AND 3 THRU 17 WITH 0
TABLE6 ;LOC 2 WITH A 17
CLR $VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF, R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX, R1 ;LOAD # OF PIXELS
1$: TSTB $VGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #20000, (R0) ;LOAD BIT1 OF PIXEL3
DEC R1 ;FINISHED ALL PIXEL3'S
BNE 1$ ;BR IF NOT
MOV #BIT10!BIT8, $VGCSR ;ENABLE THE MAP
JSR PC, LDLIN ;LOAD REFERENCE LINE
JSR RS, DELAY
100.

```


F04

```

1920
1921
1922
1923 012512 000004
1924 012514 005737 001200
1925 012520 001441
1926 012522 032777 001000 166410
1927 012530 001035
1928 012532 004737 017306
1929 012536 004737 016234
1930 012542 004537 017062
1931 012546 000000
1932 012550 016626
1933
1934 012552 005077 166704
1935 012556 013700 001464
1936 012562 013701 001542
1937 012566 105777 166666
1938 012572 100375
1939 012574 012710 000004
1940 012600 005301
1941 012602 001371
1942 012604 012777 002400 166646
1943 012612 004737 017554
1944 012616 004537 016666
1945 012622 000144
1946
1947
1948
1949
1950 012624 000004
1951 012626 005737 001200
1952 012632 001441
1953 012634 032777 001000 166276
1954 012642 001035
1955 012644 004737 017306
1956 012650 004737 016234
1957 012654 004537 017062
1958 012660 000000
1959 012662 016626
1960
1961 012664 005077 166572
1962 012670 013700 001464
1963 012674 013701 001542
1964 012700 105777 166554
1965 012704 100375
1966 012706 012710 000100
1967 012712 005301
1968 012714 001371
1969 012716 012777 002400 166534
1970 012724 004737 017554
1971 012730 004537 016666
1972 012734 000144
1973
  
```

```

*****
*TEST 56      DYNAMIC WORD TESTING OF 'PIXELO' (INCREMENT MAP P.C.) TEST L.U.T. 4
*****
TST56:    SCOPE
          TST      $PASS                    ;TEST IF FIRST PASS OF PROGRAM
          BEQ      TST57                    ;;BR IF FIRST PASS
          BIT      #SW09,ASWR               ;TEST IF INHIBIT VISUAL MAP PATTERNS
          BNE      TST57                    ;;BR IF INHIBIT IS SET
          JSR      PC,TESTID               ;DISPLAY MAP # AND TEST #
          JSR      PC,CLMAP
          JSR      RS,LODSTA               ;LOAD COLOR TABLE
          0                                ;LOC 0,1,2,3 AND 5 THRU 17 WITH 0
          TABLE7                          ;LOC 4 WITH A 17

          CLR      @VGPC                    ;LOAD MAP P.C. TO 0
          MOV      VGBUF,RO               ;LOAD PIXEL ADDRESS
          MOV      NUMPIX,R1               ;LOAD # OF PIXELS
1$:        TSTB     @VGCSR                   ;WAIT FOR MAP READY
          BPL      1$
          MOV      #4,(RO)                ;LOAD BIT2 OF PIXEL0
          DEC      R1                      ;FINISHED ALL PIXEL0'S
          BNE      1$                      ;BR IF NOT
          MOV      #BIT10!BIT8,@VGCSR     ;ENABLE THE MAP
          JSR      PC,LDLIN               ;LOAD REFERENCE LINE
          JSR      RS,DELAY
          100.

*****
*TEST 57      DYNAMIC WORD TESTING OF 'PIXEL1' (INCREMENT MAP P.C.) TEST L.U.T. 4
*****
TST57:    SCOPE
          TST      $PASS                    ;TEST IF FIRST PASS OF PROGRAM
          BEQ      TST60                    ;;BR IF FIRST PASS
          BIT      #SW09,ASWR               ;TEST IF INHIBIT VISUAL MAP PATTERNS
          BNE      TST60                    ;;BR IF INHIBIT IS SET
          JSR      PC,TESTID               ;DISPLAY MAP # AND TEST #
          JSR      PC,CLMAP
          JSR      RS,LODSTA               ;LOAD COLOR TABLE
          0                                ;LOC 0,1,2,3 AND 5 THRU 17 WITH 0
          TABLE7                          ;LOC 4 WITH A 17

          CLR      @VGPC                    ;LOAD MAP P.C. TO 0
          MOV      VGBUF,RO               ;LOAD PIXEL ADDRESS
          MOV      NUMPIX,R1               ;LOAD # OF PIXELS
1$:        TSTB     @VGCSR                   ;WAIT FOR MAP READY
          BPL      1$
          MOV      #100,(RO)              ;LOAD BIT2 OF PIXEL1
          DEC      R1                      ;FINISHED ALL PIXEL1'S
          BNE      1$                      ;BR IF NOT
          MOV      #BIT10!BIT8,@VGCSR     ;ENABLE THE MAP
          JSR      PC,LDLIN               ;LOAD REFERENCE LINE
          JSR      RS,DELAY
          100.
  
```


G04

```

1974
1975
1976
1977 012736 000004
1978 012740 005737 001200
1979 012744 001441
1980 012746 032777 001000 166164
1981 012754 001035
1982 012756 004737 017306
1983 012762 004737 016234
1984 012766 004537 017062
1985 012772 000000
1986 012774 016626
1987
1988 012776 005077 166460
1989 013002 013700 001464
1990 013006 013701 001542
1991 013012 105777 166442
1992 013016 100375
1993 013020 012710 002000
1994 013024 005301
1995 013026 001371
1996 013030 012777 002400 166422
1997 013036 004737 017554
1998 013042 004537 016666
1999 013046 000144
2000
2001
2002
2003
2004 013050 000004
2005 013052 005737 001200
2006 013056 001441
2007 013060 032777 001000 166052
2008 013066 001035
2009 013070 004737 017306
2010 013074 004737 016234
2011 013100 004537 017062
2012 013104 000000
2013 013106 016626
2014
2015 013110 005077 166346
2016 013114 013700 001464
2017 013120 013701 001542
2018 013124 105777 166330
2019 013130 100375
2020 013132 012710 040000
2021 013136 005301
2022 013140 001371
2023 013142 012777 002400 166310
2024 013150 004737 017554
2025 013154 004537 016666
2026 013160 000144

```

```

*****
*TEST 60 DYNAMIC WORD TESTING OF 'PIXEL2' (INCREMENT MAP P.C.) TEST L.U.T. 4
*****
TST60: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST61 ;;BR IF FIRST PASS
BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST61 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0,1,2,3 AND 5 THRU 17 WITH 0
TABLE7 ;LOC 41 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #2000,(R0) ;LOAD BIT2 OF PIXEL2
DEC R1 ;FINISHED ALL PIXEL2'S
BNE 1$ ;BR IF NOT
MOV #BIT10:BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

*****
*TEST 61 DYNAMIC WORD TESTING OF 'PIXEL3' (INCREMENT MAP P.C.) TEST L.U.T. 4
*****
TST61: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST62 ;;BR IF FIRST PASS
BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST62 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0,1,2,3 AND 5 THRU 17 WITH 0
TABLE7 ;LOC 4 WITH A 17
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #40000,(R0) ;LOAD BIT2 OF PIXEL3
DEC R1 ;FINISHED ALL PIXEL3'S
BNE 1$ ;BR IF NOT
MOV #BIT10:BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

```


H04

```

2027
2028
2029
2030 013162 000004
2031 013164 005737 001200
2032 013170 001441
2033 013172 032777 001000 165740
2034 013200 001035
2035 013202 004737 017306
2036 013206 004737 016234
2037 013212 004537 017062
2038 013216 000000
2039 013220 016646
2040
2041 013222 005077 166234
2042 013226 013700 001464
2043 013232 013701 001542
2044 013236 105777 166216
2045 013242 100375
2046 013244 012710 000010
2047 013250 005301
2048 013252 001371
2049 013254 012777 002400 166176
2050 013262 004737 017554
2051 013266 004537 016666
2052 013272 000144
2053
2054
2055
2056
2057 013274 000004
2058 013276 005737 001200
2059 013302 001441
2060 013304 032777 001000 165626
2061 013312 001035
2062 013314 004737 017306
2063 013320 004737 016234
2064 013324 004537 017062
2065 013330 000000
2066 013332 016646
2067
2068 013334 005077 166122
2069 013340 013700 001464
2070 013344 013701 001542
2071 013350 105777 166104
2072 013354 100375
2073 013356 012710 000200
2074 013362 005301
2075 013364 001371
2076 013366 012777 002400 166064
2077 013374 004737 017554
2078 013400 004537 016666
2079 013404 000144
2080

```

```

*****
*TEST 62 DYNAMIC WORD TESTING OF 'PIXELO' (INCREMENT MAP P.C.) TEST L.U.T. 10
*****
TST62: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST63 ;;BR IF FIRST PASS
BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST63 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0,1,2,3,4,5,6,7 AND 11 THRU 17 WITH 0
TABLE8 ;LOC 10 WITH A 17
CLR AVGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
1$: TSTB AVGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #10,(R0) ;LOAD BIT3 OF PIXEL0
DEC R1 ;FINISHED ALL PIXEL0'S
BNE 1$ ;BR IF NOT
MOV #BIT10!BIT8,AVGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

```

```

*****
*TEST 63 DYNAMIC WORD TESTING OF 'PIXEL1' (INCREMENT MAP P.C.) TEST L.U.T. 10
*****
TST63: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST64 ;;BR IF FIRST PASS
BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST64 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0,1,2,3,4,5,6,7 AND 11 THRU 17 WITH 0
TABLE8 ;LOC 10 WITH A 17
CLR AVGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
1$: TSTB AVGCSR ;WAIT FOR MAP READY
BPL 1$
MOV #200,(R0) ;LOAD BIT3 OF PIXEL1
DEC R1 ;FINISHED ALL PIXEL1'S
BNE 1$ ;BR IF NOT
MOV #BIT10!BIT8,AVGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

```



```

2081 ::*****
2082 ::*TEST 64 DYNAMIC WORD TESTING OF 'PIXEL2' (INCREMENT MAP P.C.) TEST L.U.T. 10
2083 ::*****
2084 TST64: SCOPE
2085 013406 000004 TST $PASS ;TEST IF FIRST PASS OF PROGRAM
2086 013410 005737 001200 BEQ TST65 ;;BR IF FIRST PASS
2087 013414 001441 BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
2088 013416 032777 001000 165514 BNE TST65 ;;BR IF INHIBIT IS SET
2089 013424 001035 JSR PC,TESTID ;DISPLAY MAP # AND TEST #
2090 013426 004737 017306 JSR PC,CLRMAP
2091 013432 004737 016234 JSR RS,LODSTA ;LOAD COLOR TABLE
2092 013436 004537 017062 D ;LOC 0,1,2,3,4,5,6,7 AND 11 THRU 17 WITH 0
2093 013442 000000 TABLEB ;LOC 10 WITH A 17
2094 013444 016646
2095 013446 005077 166010 CLR @VGPC ;LOAD MAP P.C. TO 0
2096 013452 013700 001464 MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
2097 013456 013701 001542 MOV NUMPIX,R1 ;LOAD # OF PIXELS
2098 013462 105777 165772 1$: TSTB @VGCSR ;WAIT FOR MAP READY
2099 013466 100375 BPL 1$
2100 013470 012710 004000 MOV #4000,(R0) ;LOAD BIT3 OF PIXEL2
2101 013474 005301 DEC R1 ;FINISHED ALL PIXEL2'S
2102 013476 001371 BNE 1$ ;BR IF NOT
2103 013500 012777 002400 165752 MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
2104 013506 004737 017554 JSR PC,LDLIN ;LOAD REFERENCE LINE
2105 013512 004537 016666 JSR RS,DELAY
2106 013516 000144 100.

```

```

2108 ::*****
2109 ::*TEST 65 DYNAMIC WORD TESTING OF 'PIXEL3' (INCREMENT MAP P.C.) TEST L.U.T. 10
2110 ::*****
2111 TST65: SCOPE
2112 013520 000004 TST $PASS ;TEST IF FIRST PASS OF PROGRAM
2113 013522 005737 001200 BEQ TST66 ;;BR IF FIRST PASS
2114 013526 001441 BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
2115 013530 032777 001000 165402 BNE TST66 ;;BR IF INHIBIT IS SET
2116 013536 001035 JSR PC,TESTID ;DISPLAY MAP # AND TEST #
2117 013540 004737 017306 JSR PC,CLRMAP
2118 013544 004737 016234 JSR RS,LODSTA ;LOAD COLOR TABLE
2119 013550 004537 017062 D ;LOC 0,1,2,3,4,5,6,7 AND 11 THRU 17 WITH 0
2120 013554 000000 TABLEB ;LOC 10 WITH A 17
2121 013556 016646
2122 013560 005077 165676 CLR @VGPC ;LOAD MAP P.C. TO 0
2123 013564 013700 001464 MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
2124 013570 013701 001542 MOV NUMPIX,R1 ;LOAD # OF PIXELS
2125 013574 105777 165660 1$: TSTB @VGCSR ;WAIT FOR MAP READY
2126 013600 100375 BPL 1$
2127 013602 012710 100000 MOV #100000,(R0) ;LOAD BIT3 OF PIXEL3
2128 013606 005301 DEC R1 ;FINISHED ALL PIXEL3'S
2129 013610 001371 BNE 1$ ;BR IF NOT
2130 013612 012777 002400 165640 MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
2131 013620 004737 017554 JSR PC,LDLIN ;LOAD REFERENCE LINE
2132 013624 004537 016666 JSR RS,DELAY
2133 013630 000144 100.

```



```

2134
2135
2136
2137 013632 000004
2138 013634 005737 001200
2139 013640 001441
2140 013642 032777 001000 165270
2141 013650 001035
2142 013652 004737 017306
2143 013656 004737 016234
2144 013662 004537 017062
2145 013666 000000
2146 013670 016466
2147 013672 005077 165564
2148 013676 013700 001464
2149 013702 013701 001542
2150 013706 105777 165546
2151 013712 100375
2152 013714 112710 000001
2153 013720 005301
2154 013722 001371
2155 013724 012777 002400 165526
2156 013732 004737 017554
2157 013736 004537 016666
2158 013742 000144
2159
2160
2161
2162
2163 013744 000004
2164 013746 005737 001200
2165 013752 001442
2166 013754 032777 001000 165156
2167 013762 001036
2168 013764 004737 017306
2169 013770 004737 016234
2170 013774 004537 017062
2171 014000 000000
2172 014002 016466
2173 014004 005077 165452
2174 014010 013700 001464
2175 014014 005200
2176 014016 013701 001542
2177 014022 105777 165432
2178 014026 100375
2179 014030 112710 000001
2180 014034 005301
2181 014036 001371
2182 014040 012777 002400 165412
2183 014046 004737 017554
2184 014052 004537 016666
2185 014056 000144

```

```

*****
*TEST 66 DYNAMIC BYTE TESTING OF 'PIXELO'
*****
TST66: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST67 ;;BR IF FIRST PASS
BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST67 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLRMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
D ;LOC 0 AND 2 THRU 17 WITH 17
TABLE2 ;LOC 1 WITH A 4
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
1$: TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1$
MOVB #1,(R0) ;LOAD BIT0 OF PIXEL0
DEC R1 ;FINISHED ALL PIXEL 0'S
BNE 1$ ;BR IF NOT
MOV #BIT10:BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

```

```

*****
*TEST 67 DYNAMIC BYTE TESTING OF 'PIXEL1'
*****
TST67: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST70 ;;BR IF FIRST PASS
BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST70 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLRMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
D ;LOC 0 AND 2 THRU 17 WITH 17
TABLE2 ;LOC 1 WITH A 4
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
INC R0 ;MAKE PIXEL 1 BYTE ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
1$: TSTB @VGCSR ;WAIT FOR MAP READY
BPL 1$
MOVB #1,(R0) ;LOAD BIT0 OF PIXEL 1
DEC R1 ;FINISHED ALL PIXEL 1'S
BNE 1$ ;BR IF NOT
MOV #BIT10:BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

```


K04

```

2186
2187
2188
2189 014060 000004
2190 014062 005737 001200
2191 014066 001442
2192 014070 032777 001000 165042
2193 014076 001036
2194 014100 004737 017306
2195 014104 004737 016234
2196 014110 004537 017062
2197 014114 000000
2198 014116 016466
2199 014120 005077 165336
2200 014124 013700 001464
2201 014130 005720
2202 014132 013701 001542
2203 014136 105777 165316 1$:
2204 014142 100375
2205 014144 112710 000001
2206 014150 005301
2207 014152 001371
2208 014154 012777 002400 165276
2209 014162 004737 017554
2210 014166 004537 016666
2211 014172 000144
2212
2213
2214
2215
2216 014174 000004
2217 014176 005737 001200
2218 014202 001446
2219 014204 032777 001000 164726
2220 014212 001042
2221 014214 004737 017306
2222 014220 004737 016234
2223 014224 004537 017062
2224 014230 000000
2225 014232 016466
2226 014234 005077 165222
2227 014240 013700 001464
2228 014244 062700 000003
2229 014250 013701 001542
2230 014254 105777 165200 1$:
2231 014260 100375
2232 014262 112710 000001
2233 014266 005301
2234 014270 001371
2235 014272 012777 002400 165160
2236 014300 004737 017554
2237 014304 004537 016666
2238 014310 000144
2239 014312 042777 010000 165126

```

```

*****
*TEST 70 DYNAMIC BYTE TESTING OF 'PIXEL2'
*****

```

```

TST70: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST71 ;;BR IF FIRST PASS
BIT #SW09,@SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST71 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLRMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0 AND 2 THRU 17 WITH 17
TABLE2 ;LOC 1 WITH A 4
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
TST (R0)+ ;MAKE PIXEL 2 BYTE ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
1$:
BPL 1$
MOVB #1,(R0) ;LOAD BIT0 OF PIXEL 2
DEC R1 ;FINISHED ALL PIXEL 2'S
BNE 1$ ;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.

```

```

*****
*TEST 71 DYNAMIC BYTE TESTING OF 'PIXEL3'
*****

```

```

TST71: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST72 ;;BR IF FIRST PASS
BIT #SW09,@SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST72 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLRMAP
JSR R5,LODSTA ;LOAD COLOR TABLE
0 ;LOC 0 AND 2 THRU 17 WITH 17
TABLE2 ;LOC 1 WITH A 4
CLR @VGPC ;LOAD MAP P.C. TO 0
MOV VGBUF,R0 ;LOAD PIXEL ADDRESS
ADD #3,R0 ;MAKE PIXEL 3 BYTE ADDRESS
MOV NUMPIX,R1 ;LOAD # OF PIXELS
TSTB @VGCSR ;WAIT FOR MAP READY
1$:
BPL 1$
MOVB #1,(R0) ;LOAD BIT0 OF PIXEL 3
DEC R1 ;FINISHED ALL PIXEL 3'S
BNE 1$ ;BR IF NOT
MOV #BIT10!BIT8,@VGCSR ;ENABLE THE MAP
JSR PC,LDLIN ;LOAD REFERENCE LINE
JSR R5,DELAY
100.
BIC #BIT12,@VCCSR ;DISABLE REF. LINE

```



```

2240
2241
2242
2243 014320 000004
2244 014322 005737 001200
2245 014326 001427
2246 014330 032777 001000 164602
2247 014336 001023
2248 014340 004737 017306
2249 014344 004737 016234
2250 014350 012777 000017 165110
2251 014356 012777 002400 165074
2252 014364 004537 016666
2253 014370 000144
2254 014372 052777 004000 165060
2255 014400 004537 016666
2256 014404 000144
2257
2258
2259
2260 014406 000004
2261 014410 005737 001200
2262 014414 001476
2263 014416 032777 001000 164514
2264 014424 001072
2265 014426 004737 017306
2266 014432 005000
2267 014434 012701 073567
2268 014440 004737 015770
2269 014444 005200 1$:
2270 014446 022700 000040
2271 014452 001403
2272 014454 004737 016002
2273 014460 000771
2274 014462 012700 007740 2$:
2275 014466 004737 015770
2276 014472 005200 3$:
2277 014474 022700 010000
2278 014500 001403
2279 014502 004737 016002
2280 014506 000771
2281 014510 012700 000040 4$:
2282 014514 012701 000007 5$:
2283 014520 004737 015770
2284 014524 012701 070000
2285 014530 062700 000037
2286 014534 004737 015770
2287 014540 005200
2288 014542 022700 007740
2289 014546 001362
2290 014550 005077 164712
2291 014554 012777 003417 164704
2292
2293 014562 012777 002400 164670
2294 014570 004537 016666
2295 014574 000144

```

```

*****
*TEST 72 TEST THE 'EXPAND' FUNCTION
*****
TST72: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST73 ;;BR IF FIRST PASS
BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST73 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
JSR PC,CLRMAP
MOV #17,AVGCOLR ;LOAD MAX INTENSITY INTO LOCATION 0
MOV #BIT10:BIT8,AVGCSR ;SET "MONO" AND ENABLE BIT MAP
JSR R5,DELAY
BIS #BIT11,AVGCSR ;SET THE 'EXPAND' BIT
JSR R5,DELAY
100.

*****
*TEST 73 DRAW A BOX UP ON VSV01 SCREEN
*****
TST73: SCOPE
TST $PASS ;TEST IF FIRST PASS OF PROGRAM
BEQ TST74 ;;BR IF FIRST PASS
BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
BNE TST74 ;;BR IF INHIBIT IS SET
JSR PC,TESTID ;DISPLAY MAP # AND TEST #
CLR R0 ;SET UP BIT MAP ADRS - TOP LINE
MOV #73567,R1 ;SET UP PIXEL DATA IN R1 - INT 7
JSR PC,DISPY ;GO LOAD BIT MAP
1$: INC R0 ;ADVANCE BIT MAP ADRS
CMP #40,R0 ;TOP LINE DONE?
BEQ 2$ ;BR IF SO
JSR PC,DCONT ;LOAD NEXT PIXEL
BR 1$ ;NEXT MAP LOAD
2$: MOV #7740,R0 ;SET UP BIT MAP ADRS - BOTTOM LINE
JSR PC,DISPY ;GO LOAD BIT MAP
3$: INC R0 ;ADVANCE ADRS
CMP #10000,R0 ;BOTTOM LINE DONE?
BEQ 4$ ;BR IF SO
JSR PC,DCONT ;LOAD NEXT PIXEL WORD
BR 3$ ;NEXT MAP LOAD
4$: MOV #40,R0 ;SET UP BIT MAP ADRS SIDE LINES
5$: MOV #7,R1 ;SET UP PIXEL 0 DATA IN R1 - INT 7
JSR PC,DISPY ;GO LOAD BIT MAP
MOV #70000,R1 ;SET UP PIXEL 3 DATA IN R1 - INT 7
ADD #37,R0 ;OFFSET TO RIGHT SIDE LINE
JSR PC,DISPY ;GO LOAD BIT MAP
INC R0 ;GO TO NEXT ROW ON LEFT
CMP #7740,R0 ;TO BOTTOM YET?
BNE 5$ ;BR IF NOT
CLR AVGCOLR ;LOAD L.U.T. 0
MOV #3417,AVGCOLR ;LOAD L.U.T. 7
;NOW DISPLAY NORMAL SIZE BOX
MOV #BIT10:BIT8,AVGCSR ;ENABLE MAP
JSR R5,DELAY ;DELAY FOR OPERATOR
100.

```


M04

```

2296 ;NOW DISPLAY EXPANDED SIZE BOX
2297 014576 052777 004000 164654 BIS #BIT11,AVGCSR ;EXPAND MAP
2298 014604 004537 016666 JSR R5,DELAY ;DELAY FOR OPERATOR
2299 014610 000144 100.
2300 ;*****
2301 ;*TEST 74 TEST THE 'ORIGIN' FUNCTION
2302 ;*****
2303 †ST74: SCOPE
2304 014612 000004 TST $PASS ;TEST IF FIRST PASS OF PROGRAM
2305 014614 005737 001200 BEQ TST75 ;;BR IF FIRST PASS
2306 014620 001434 BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
2307 014622 032777 001000 164310 BNE TST75 ;;BR IF INHIBIT IS SET
2308 014630 001030 JSR PC,TESTID ;DISPLAY MAP # AND TEST #
2309 014632 004737 017306 JSR PC,CLRMAP ;CLEAR THE MAP
2310 014636 004737 016234 MOV #17,AVGCOLR ;LOAD MAX INTENSITY INTO TABLE LOC. 0
2311 014642 012777 000017 164616 MOV #BIT10!BIT8,AVGCSR ;ENABLE THE BIT MAP
2312 014646 012777 002400 164602 CLR $TEMP
2313 014650 012777 000017 164616 1$: MOVB $TEMP,AVGCSR ;LOAD ORIGIN REGISTER
2314 014654 005037 001474 164570 JSR R5,DELAY
2315 014656 005037 001474 164570 1$: 100.
2316 014662 113777 001474 164570 INCB $TEMP ;UPDATE THE ORIGIN POSITION
2317 014666 105237 001474 164570 CMPB #20,$TEMP ;TEST FOR A NON-VALID ORIGIN
2318 014702 122737 000020 001474 BNE 1$ ;BR IF A VALID ORIGIN
2319 014710 001364 1$
2320 ;*****
2321 ;*TEST 75 INTENSITY LEVEL TEST-MONOCROME
2322 ;*****
2323 †ST75: SCOPE
2324 014712 000004 TST $PASS ;TEST IF FIRST PASS OF PROGRAM
2325 014714 005737 001200 BEQ TST76 ;;BR IF FIRST PASS
2326 014720 001435 BIT #SW09,ASWR ;TEST IF INHIBIT VISUAL MAP PATTERNS
2327 014722 032777 001000 164210 BNE TST76 ;;BR IF INHIBIT IS SET
2328 014730 001031 JSR PC,TESTID ;DISPLAY MAP # AND TEST #
2329 014732 004737 017306 JSR PC,CLRMAP ;CLEAR THE BIT MAP
2330 014736 004737 016234 JSR PC,LODSEQ ;LOAD TWO WORDS PER LOOK TABLE LOCATION
2331 014742 004737 016774 CLR 2$ ;CLEAR STARTING INTENSITY LEVEL
2332 014746 005037 014764 MOV #BIT11!BIT10!BIT8,AVGCSR ;SET MONOCROME + ENABLE MAP
2333 014752 012777 006400 164500 1$: JSR R5,LODSTA ;LOAD SEQUENCE
2334 014756 012777 006400 164500 2$: 0 ;INDEX INTO DATA VALVE TABLE
2335 014760 005037 014764 TABLE1
2336 014764 005037 014764 JSR R5,DELAY
2337 014770 004537 016666 100.
2338 014774 000144 ADD #1,2$ ;UPDATE INDEX INTO TABLE FOR LOCATION 0'S VALUE
2339 014776 062737 000001 014764 CMP #20,2$ ;TEST IF LAST VLUE FOR MONOCROME
2340 015004 022737 000020 014764 BNE 1$ ;BR IF MORE LEVELS FOR LOCATION 0

```



```

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2342
2343
2344 015014 000004
2345 015016 005737 001200
2346 015022 001435
2347 015024 032777 001000 164106
2348 015032 001031
2349 015034 004737 017306
2350 015040 004737 016234
2351 015044 012777 004400 164406
2352 015052 012777 000004 164406
2353 015060 004537 016666
2354 015064 000144
2355 015066 012777 000010 164372
2356 015074 004537 016666
2357 015100 000144
2358 015102 012777 000014 164356
2359 015110 004537 016666
2360 015114 000144
2361
2362
2363
2364
2365 015116 000004
2366 015120 005737 001200
2367 015124 001435
2368 015126 032777 001000 164004
2369 015134 001031
2370 015136 004737 017306
2371 015142 004737 016234
2372 015146 012777 004400 164304
2373 015154 012777 000001 164304
2374 015162 004537 016666
2375 015166 000144
2376 015170 012777 000002 164270
2377 015176 004537 016666
2378 015202 000144
2379 015204 012777 000003 164254
2380 015212 004537 016666
2381 015216 000144
2382

```

```

*****
*TEST 76      TEST THE GREEN BITS
*****
TST76:  SCOPE
        TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
        BEQ      TST77      ;;BR IF FIRST PASS
        BIT      #SW09,@SWR  ;TEST IF INHIBIT VISUAL MAP PATTERNS
        BNE      TST77      ;;BR IF INHIBIT IS SET
        JSR      PC,TESTID   ;DISPLAY MAP # AND TEST #
        JSR      PC,CLRMAP   ;CLEAR THE MAP
        MOV      #BIT11!BIT8,@VGCSPR ;ENABLE MAP AND EXPAND
        MOV      #4,@VGCCLR  ;LOAD TABLE POINTER0 - LOW GREEN LEVEL
        JSR      R5,DELAY
        100.
        MOV      #10,@VGCCLR ;SET THE MEDIUM GREEN LEVEL
        JSR      R5,DELAY
        100.
        MOV      #14,@VGCCLR ;SET THE BRIGHTEST BREEN LEVEL
        JSR      R5,DELAY
        100.

*****
*TEST 77      TEST THE RED BITS
*****
TST77:  SCOPE
        TST      $PASS      ;TEST IF FIRST PASS OF PROGRAM
        BEQ      TST100     ;;BR IF FIRST PASS
        BIT      #SW09,@SWR  ;TEST IF INHIBIT VISUAL MAP PATTERNS
        BNE      TST100     ;;BR IF INHIBIT IS SET
        JSR      PC,TESTID   ;DISPLAY MAP # AND TEST #
        JSR      PC,CLRMAP   ;CLEAR THE MAP
        MOV      #BIT11!BIT8,@VGCSPR ;ENABLE MAP AND EXPAND
        MOV      #1,@VGCCLR  ;SET THE DIMMEST RED LEVEL
        JSR      R5,DELAY
        100.
        MOV      #2,@VGCCLR  ;SET THE MEDIUM RED LEVEL
        JSR      R5,DELAY
        100.
        MOV      #3,@VGCCLR  ;SET THE BRIGHTEST RED LEVEL
        JSR      R5,DELAY
        100.

```


B05

MAINDEC-11-DZVSE-A
DZVSEB.P11

02-JUN-76 00:00

VSV01 VIDEO GRAPHIC TEST PROGRAM

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T100 TEST THE BLUE COLOR BITS AT TABLE ADDRESS 0

000000
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000102

015220 000004
015222 005737 001200
015226 001435
015230 032777 001000 163702
015236 001031
015240 004737 017306
015244 004737 016234
015250 012777 004400 164202
015256 012777 000020 164202
015264 004537 016666
015270 000144
015272 012777 000040 164166
015300 004537 016666
015304 000144
015306 012777 000060 164152
015314 004537 016666
015320 000144

```
*****  
*TEST 100 TEST THE BLUE COLOR BITS AT TABLE ADDRESS 0  
*****  
TST100: SCOPE  
TST $PASS ;TEST IF FIRST PASS OF PROGRAM  
BEQ TST101 ;;BR IF FIRST PASS  
BIT #SW09,%SWR ;TEST IF INHIBIT VISUAL MAP PATTERNS  
BNE TST101 ;;BR IF INHIBIT IS SET  
JSR PC,TESTID ;DISPLAY MAP * AND TEST *  
JSR PC,CLMAP  
MOV #BIT11!BIT0,%VGCOR ;ENABLE MAP AND EXPAND  
MOV #20,%VGCOR ;SET THE DIMMEST BLUE LEVEL  
JSR R5,DELAY  
100.  
MOV #40,%VGCOR ;SET THE MEDIUM BLUE LEVEL  
JSR R5,DELAY  
100.  
MOV #60,%VGCOR ;SET THE BRIGHTEST BLUE LEVEL  
JSR R5,DELAY  
100.
```


.SBTTL CROSS HATCH TEST PATTERN

```

015442 012706 001100 HATCH: MOV #STACK, SP ;LOAD SP
015446 004737 003576 JSR PC, LODADR ;LOAD BUS ADDRESSES FOR MAP #1
015450 004737 015510 JSR PC, HATCHO ;LOAD MAP #0
015456 012777 004400 163774 MOV #BIT11!BIT8, @VGC SR ;ENABLE MAP EXPAND AND ORGIN =0
015464 004737 003730 JSR PC, MOVADR ;ADJUST BUS ADDRESSES TO MAP #1
015470 004737 015510 JSR PC, HATCHO ;LOAD MAP #1
015474 012777 004402 163756 MOV #BIT11!BIT8!BIT1, @VGC SR ;ENABLE MAP, EXPAND AND ORIGIN =2
015502 000000 HALT
015504 000137 001572 JMP BEGIN

;ACTUAL LOADING OF CROSS HATCH INTO BIT MAP SUB-ROUTINE
015510 004737 016234 HATCHO: JSR PC, CLRMAP ;ENSURE CLEAR MAP
015514 012737 000022 015636 MOV #22, 10$ ;LOAD VERT. COUNTER
015522 005077 163734 CLR @VGPC ;CLEAR MAP P.C.
015526 012701 000040 1$: MOV #32, R1 ;LOAD WIDTH COUNTER
015532 105777 163722 2$: TSTB @VGC SR ;TEST IF MAP READY SET
015536 100375 BPL 2$ ;BR IF NOT READY
015540 012777 177777 163716 MOV #-1, @VGBUF ;LOAD #17 INTO ALL PIXELS
015546 005301 DEC R1
015550 001370 BNE 2$ ;BR UNTIL FINISHED
015552 012737 000140 015640 MOV #140, 11$ ;LOAD VERT COUNT
015560 105777 163674 3$: TSTB @VGC SR ;TEST IF MAP READY IS SET
015564 100375 BPL 3$ ;BR IF NOT
015566 012777 000017 163670 4$: MOV #17, @VGBUF ;LOAD VERT LINE PIXEL
015574 105777 163660 TSTB @VGC SR ;TEST IF READY
015600 100375 BPL 4$ ;WAIT IF NOT
015602 005077 163656 CLR @VGBUF ;CLEAR PIXELS

015606 005337 015640 DEC 11$
015612 001362 BNE 3$ ;BR UNTIL FUNISHED SEVERAL ROWS OF PIXELS

015614 005337 015636 DEC 10$ ;TEST IF FINISHED THE SCREEN
015620 001342 BNE 1$ ;BR IF NOT

015622 005077 163640 CLR @VGC CLR ;CLEAR L.U.T. 0
015626 012777 007477 163632 MOV #7477, @VGC CLR ;LOAD "WHITE" INTO L.U.T. #17
015634 000207 RTS PC ;EXIT

015636 000000 10$: 0
015640 000000 11$: 0

.SBTTL SWR CONTROL OF X AND Y CROSS HAIRS LOOP
015642 012706 001100 CROSS: MOV #STACK, SP ;LOAD STACK
015646 004737 003576 JSR PC, LODADR ;LOAD DEVICE ADDRESS
015652 012777 014000 163566 MOV #BIT12!BIT11, @VCC SR ;ENABLE CROSS HAIRS
015660 104407 1$: CKSWR ;TEST FOR CTRL G
015662 017777 163252 163560 MOV @SWR, @VCHRLO ;LOAD THE CROSS HAIR POSITION REGISTERS
015670 000773 BR 1$ ;LOOP PACK

```



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25190 .SBTTL
25191 .SBTTL MISC SUBROUTINES
25192 .SBTTL
25193 .SBTTL SUBROUTINE TO TEST THAT 'VERT SYNC' BIT CHANGES, REPORT ERROR IF
25194 :BIT DOES NOT CHANGE STATES (0 TO 1 OR 1 TO 0)
25195 015672 005237 015766 COUNTA: INC CNTSAV ;SET EXIT FLAG
25196 015676 012702 040000 COUNT: MOV #BIT14,R2 ;LOAD BIT 14
25197 015702 005001 CLR R1 ;CLEAR COUNT LOC.
25198 015704 013700 001446 MOV VCCSR,R0 ;LOAD BUS ADDRESS
25199 015710 030210 BIT R2,(R0) ;TEST IF BIT IS SET
25200 015712 001411 BEQ 3$ ;BR IF SET
25201 :COME HERE IF BIT 14 WAS INITIALLY SET - NOW WAIT FOR IT TO CLEAR
25202 015714 005201 1$: INC R1 ;COUNT TIME
25203 015716 001002 BNE 2$ ;BR IF NOT OVERFLOW
25204 015720 104005 ERROR 5 ;VERTICAL SYNC FLAG ERROR
25205 015722 000207 RTS PC ;EXIT
25206 015724 030210 2$: BIT R2,(R0) ;TEST IF BIT 14 IS CLEARED
25207 015726 001372 BNE 1$ ;BR IF STILL SET
25208 015730 005037 015766 CLR CNTSAV ;CLEAR EXIT FLAG
25209 015734 000207 RTS PC ;EXIT
25210 :COME HERE IF BIT 14 WAS INITIALLY CLEARED - NOW WAIT FOR IT TO SET
25211 015736 005201 3$: INC R1 ;COUNT TIME
25212 015740 001002 BNE 4$ ;BR IF NOT OVERFLOW
25213 015742 104005 ERROR 5 ;VERTICAL SYNC FLAG ERROR
25214 015744 000207 RTS PC ;EXIT
25215 015746 030210 4$: BIT R2,(R0) ;TEST IF BIT 14 IS SET
25216 015750 001772 BEQ 3$ ;BR IF CLEARED
25217 015752 005737 015766 TST CNTSAV ;TEST IF EXIT FLAG IS SET
25218 015756 001347 BNE COUNT ;BR IF SET
25219 015760 005037 015766 CLR CNTSAV ;CLEAR AGAIN
25220 015764 000207 RTS PC ;EXIT
25221 015766 000000 CNTSAV: 0
25222
25223 015770 042777 000400 163462 DISPY: BIC #400,AVGCSR ;DISABLE MAP
25224 015776 010077 163460 MOV R0,AVGPC ;LOAD MAP P.C.
25225 016002 042777 000400 163450 DCONT: BIC #400,AVGCSR ;DISABLE MAP
25226 016010 105777 163444 1$: TSTB AVGCSR ;TEST IF MAP IS READY
25227 016014 100375 BPL 1$
25228 016016 010177 163442 MOV R1,AVGBUF ;LOAD A PIXEL
25229 016022 052777 000400 163430 BIS #400,AVGCSR ;ENABLE MAP
25230 016030 000207 RTS PC ;EXIT

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F05

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2531                                     .SBTTL DISPLAY BUFFER ON VTVO1 SCREEN SUB-ROUTINE
2532
2533 016032 012700 024330 XPRNT: MOV #BUFFER,R0 ;LOAD BUFFER POINTER
2534 016036 000240 XPRNTA: NOP
2535 016040 000240 NOP
2536 016042 000240 NOP
2537 016044 005777 163376 1$: TST @VCCSR ;TEST IF READY IS SET
2538 016050 100375 BPL 1$ ;WAIT TILL DONE
2539 016052 112077 163370 MOV# (R0)+,@VCCSR ;LOAD THE CHARACTER
2540 016056 122710 000377 CMP# #377,(R0) ;TEST FOR TERM
2541 016062 001370 BNE 1$ ;BR IF NOT TERM.
2542 016064 000240 NOP
2543 016066 000240 NOP
2544 016070 000240 NOP
2545 016072 000240 NOP
2546 016074 000207 RTS PC ;EXIT
2547
2548 016076 013701 001500 FILLWC: MOV KE,R1 ;LOAD BASIC CHARACTER
2549 016102 004737 016132 JSR PC,FILBUF ;LOAD BUFFER WITH THE CHARACTER
2550 016106 013737 001524 016130 MOV VHD,10$ ;LOAD VERTICAL COUNTER
2551 016114 004737 016032 1$: JSR PC,XPRNT ;DISPLAY A LINE
2552 016120 005337 016130 DEC 10$ ;FINISHED ALL VERT LINES ?
2553 016124 100373 BPL 1$ ;BR IF NOT
2554 016126 000207 RTS PC ;EXIT
2555 016130 000000 10$: 0
2556
2557 016132 013702 001522 FILBUF: MOV WIDTH,R2 ;LOAD WIDTH VALUE
2558 016136 012700 024330 MOV #BUFFER,R0 ;SET-UP BUFFER POINTER
2559 016142 110120 1$: MOV# R1,(R0)+ ;SAVE THE CHARACTER IN THE BUFFER
2560 016144 005302 DEC R2 ;FINISHED?
2561 016146 001375 BNE 1$ ;BRANCH IF NOT COMPLETED
2562 016150 112720 000015 MOV# #15,(R0)+ ;LOAD "CR"
2563 016154 112720 000012 MOV# #12,(R0)+ ;LOAD "LF"
2564 016160 112710 000377 MOV# #377,(R0) ;LOAD TERM.
2565 016164 000207 RTS PC ;EXIT
2566
2567 ;LOAD A INCREMENTING CHARACTER ACROSS THE SCREEN WIDTH
2568 ;ONLY 40 THRU 177 ARE LEGAL CHARACTERS
2569
2570 016166 012700 024330 LIC: MOV #BUFFER,R0 ;SET-UP BUFFER POINTER
2571 016172 013502 MOV @5)+,R2 ;SET-UP WIDTH
2572 016174 110120 1$: MOV# R1,(R0)+ ;SAVE A CHARACTER IN THE BUFFER
2573 016176 005201 INC R1 ;UPDATE THE CHARACTER
2574 016200 023701 001532 CMP LASTCH,R1 ;TEST FOR
2575 016204 001002 BNE 2$ ;BRANCH IF NOT
2576 016206 012701 000040 MOV #40,R1 ;MAKE A LEGAL CHARACTER
2577 016212 005302 2$: DEC R2 ;DECREMENT COUNT
2578 016214 001367 BNE 1$ ;BRANCH IF NOT COMPLETED
2579 016216 112720 000015 MOV# #15,(R0)+ ;LOAD "CR"
2580 016222 112720 000012 MOV# #12,(R0)+ ;LOAD "LF"
2581 016226 112710 000377 MOV# #377,(R0) ;LOAD TERM
2582 016232 000205 RTS R5 ;EXIT
2583
  
```


G05

MAINDEC-11-DZVSE-A
DZVSEA.P11

02-JUN-76 00:00

VSV01 VIEDO GRAPHIC TEST PROGRAM
DISPLAY BUFFER ON VTVD1 SCREEN SUB-ROUTINE

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2584                                     :SUBROUTINE TO CLEAR THE BIT MAP
2585                                     :
2586 016234 012737 000100 016316 CLRMAP: MOV     #BIT6,10$      ;LOAD BASE DELAY
2587 016242 005037 016320          CLR     11$           ;CLEAR COUNTER
2588 016246 004537 016666          JSR     RS,DELAY
2589 016252 100001          BIT15!1
2590 016254 000240          NOP
2591 016256 000240          NOP
2592 016260 000240          NOP
2593 016262 012777 001000 163170 MOV     #BIT9,AVGCSR      ;SET 'CLEAR MAP' BIT
2594 016270 105777 163164 1$:   TSTB   AVGCSR          ;TEST READY FLAG
2595 016274 100407          BMI    2$           ;BR IF READY SET
2596 016276 005237 016320          INC    11$          ;UPDATE COUNTER
2597 016302 001372          BNE   1$           ;BR IF NO OVERFLOW
2598 016304 005337 016316          DEC    10$          ;DECREMENT BASE DELAY
2599 016310 001367          BNE   1$           ;BR IF NO OVERFLOW
2600 016312 104015          ERROR  15          ;MAP READY FAILED TO SET AFTER A GROSS
2601                                     ;TIME DELAY
2602 016314 000207          2$:   RTS     PC           ;EXIT
2603
2604 016316 000000          10$:  0           ;BASE DELAY
2605 016320 000000          11$:  0           ;COUNT LOCATION
2606
2607                                     .SBTTL  CROSS HAIRS WITH 2 BIT MAPS ENABLED
2608
2609 016322 012706 001100          FRANKD: MOV    #STACK,SP
2610 016326 004737 003576          JSR    PC,LODADR        ;LOAD DEVICE ADDRESS
2611 016332 004737 016234          JSR    PC,CLRMAP
2612 016336 012777 000014 163122 MOV    #14,AVGCOLR      ;LOAD LUT TO GREEN
2613 016344 012777 000400 163106 MOV    #BIT8,AVGCSR     ;ENABLE THE MAP
2614 016352 004737 003730          JSR    PC,MOVADR        ;UPDATE TO NEXT MAP ADDRESS
2615 016356 004737 016234          JSR    PC,CLRMAP
2616 016362 012777 000014 163076 MOV    #14,AVGCOLR      ;LOAD LUT TO GREEN
2617 016370 012777 004404 163062 MOV    #BIT11!BIT8!4,AVGCSR ;ENABME MAP - EXPAND - ORIGIN 4
2618 016376 012777 014000 163042 MOV    #BIT12!BIT11,AVGCSR ;ENABLE CROSS HAIRS
2619 016404 104407          1$:   CKSWR          ;TEST FOR CTRL G
2620 016406 017777 162526 163034 MOV    AVSWR,AVCHARLO   ;LOAD CROSS HAIR OGIGIN
2621 016414 000773          BR    1$
2622
2623                                     .SBTTL  BUR TIME-OUT LOOP
2624
2625 016416 012706 001100          BUSOUT: MOV    #STACK,SP ;LOAD SP
2626 016422 012737 016440 000004 MOV    #2$,ERRVEC       ;LOAD BUS TRAP RETURN
2627 016430 005777 162612 1$:   TST   AV$BASE        ;REFERENCE THE ADDRESS
2628 016434 000775          BR    1$
2629 016436 000240          NOP
2630 016440 022626          2$:   CMP    (SP)+,(SP)+ ;CLEAN THE STACK
2631 016442 000772          BR    1$
2632 016444 000240          NOP

```

2633	016446	000	001	002	TABLE1: .BYTE	0,1,2,3,4,5,6,7,10,11,12,13,14,15,16,17	
2634	016451	003	004	005			
2635	016454	006	007	010			
2636	016457	011	012	013			
2637	016462	014	015	016			
2638	016465	017					
2639	016466	000	017	000	TABLE2: .BYTE	0,17,0,0,0,0,0,0,0,0,0,0,0,0,0	:L.U.(.1 = 17 OTHERS =0
2640	016471	000	000	000			
2641	016474	000	000	000			
2642	016477	000	000	000			
2643	016502	000	000	000			
2644	016505	000					
2645	016506	014	063	063	TABLE3: .BYTE	14,63,63,63,63,63,63,63,63,63,63,63,63,63,63	
2646	016511	063	063	063			
2647	016514	063	063	063			
2648	016517	063	063	063			
2649	016522	063	063	063			
2650	016525	063					
2651	016526	004	017	004	TABLE4: .BYTE	4,17,4,4,4,4,4,4,4,4,4,4,4,4	
2652	016531	004	004	004			
2653	016534	004	004	004			
2654	016537	004	004	004			
2655	016542	004	004	004			
2656	016545	004					
2657	016546	001	002	003	TABLE5: .BYTE	1,2,3,4,10,14,20,40,60,63,74,17,33,25,52,77	
2658	016551	004	010	014			
2659	016554	020	040	060			
2660	016557	063	074	017			
2661	016562	033	025	052			
2662	016565	077					
2663	016566	001	002	003	.BYTE	1,2,3,4,10,14,20,40,60,63,74,17,33,25,52,77	
2664	016571	004	010	014			
2665	016574	020	040	060			
2666	016577	063	074	017			
2667	016602	033	025	052			
2668	016605	077					
2669	016606	000	000	017	TABLE6: .BYTE	0,0,17,0,0,0,0,0,0,0,0,0,0,0	:L.U.T. 2 = 17 OTHERS = 0
2670	016611	000	000	000			
2671	016614	000	000	000			
2672	016617	000	000	000			
2673	016622	000	000	000			
2674	016625	000					
2675	016626	000	000	000	TABLE7: .BYTE	0,0,0,0,17,0,0,0,0,0,0,0,0,0	:L.U.T. 4 = 17 OTHERS = 0
2676	016631	000	017	000			
2677	016634	000	000	000			
2678	016637	000	000	000			
2679	016642	000	000	000			
2680	016645	000					
2681	016646	000	000	000	TABLE8: .BYTE	0,0,0,0,0,0,0,0,17,0,0,0,0,0	:L.U.T. 10 = 17 OTHERS = 0
2682	016651	000	000	000			
2683	016654	000	000	017			
2684	016657	000	000	000			
2685	016662	000	000	000			
2686	016665	000					
2687					.EVEN		


```

2688
2689
2690          .SBTTL  DELAY SUBROUTINE -- WAIT FOR "VERTICAL SYNC INTERRUPT"
2691 016666 012777 016772 162574 DELAY: MOV    #5$,AVCINT          ;LOAD INTR. RETURN ADDRESS
2692 016674 012777 000200 162570      MOV    #APRIOR,AVCINT1      ;LOAD RETURN PRIORITY
2693 016702 012537 016770      MOV    (R5)+,10$          ;GET THE ARG.
2694 016706 100003      BPL    2$                ;BR IF POSITIVE
2695
2696 016710 005737 016770      1$:   TST    10$          ;TEST IF DELAY CANNOT BE INHIBITED
2697 016714 100404      BMI    3$                ;BR IF IT CANT
2698 016716 032777 010000 162214 2$:   BIT    #BIT12,AVSWR      ;TEST IF INHIBIT DELAY SWITCH IS SET
2699 016724 001013      BNE    4$                ;BR IF INHIBIT IS SET
2700 016726 052777 020000 162512 3$:   BIS    #BIT13,AVCCSR      ;ENABLE SYNC INTR.
2701 016734 000001      WAIT                   ;WAIT FOR INTERRUPT TO HAPPEN
2702 016736 042777 020000 162502      BIC    #BIT13,AVCCSR      ;DISABLE INTR.
2703 016744 104407      CKSWR                   ;TEST FOR CTRL G
2704 016746 105337 016770      DECB   10$              ;FINISHED DELAY ?
2705 016752 001356      BNE    1$                ;BR IF NOT
2706 016754 013777 001472 162506 4$:   MOV    VCINT1,AVCINT      ;RESET INTR. VECTOR
2707 016762 005077 162504      CLR    AVCINT1
2708 016766 000205      RTS    R5                ;EXIT
2709
2710 016770 000000      10$:  0
2711 016772 000002      5$:   RTI                ;CONTINUE
2712
2713
2714          ;LOAD MAP WITH TWO WORDS OF THE SAME VALUE
2715          ;          INTO THE ENTIRE BITMAP STARTING AT LOC. 0
2716
2717 016774 010046      LODSEQ: MOV    R0,-(SP)      ;SAVE R0
2718 016776 010146      MOV    R1,-(SP)      ;SAVE R1
2719 017000 005077 162456      CLR    AVGPC          ;ENSURE CLEAR PC
2720 017004 013701 001542      MOV    NUMPIX,R1     ;LOAD COUNTER
2721 017010 006201      ASR    R1             ;ADJUST COUNT
2722
2723 017012 005000      1$:   CLR    R0            ;START WITH PIXEL VALUE OF 0
2724 017014 105777 162440      2$:   TSTB   AVGCSR       ;ENSURE MAP IS READY
2725 017020 100375      BPL    2$
2726 017022 010077 162436      MOV    R0,AVGBUF     ;LOAD A PIXEL WORD
2727 017026 105777 162426      3$:   TSTB   AVGCSR       ;ENSURE MAP IS READY
2728 017032 100375      BPL    3$
2729 017034 010077 162424      MOV    R0,AVGBUF     ;LOAD 2ND PIXEL WORD
2730 017040 005301      DEC    R1             ;FINISHED ALL PIXELS ?
2731 017042 100404      BMI    4$            ;BR IF FINISHED
2732 017044 062700 010421      ADD    #10421,R0      ;UPDATE PIXEL DATA TO NEXT L.U.T. ADDRESS
2733 017050 103361      BCC    2$            ;BR IF MORE ADDRESSES TO LOAD
2734 017052 000757      BR     1$            ;BR TO RESET PIXEL DATA
2735
2736 017054 012601      4$:   MOV    (SP)+,R1     ;RESTORE R1
2737 017056 012600      MOV    (SP)+,R0     ;RESTORE R0
2738 017060 000207      RTS    PC            ;EXIT
2739

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J05

MAINDEC-11-DZVSE-A VSV01 VIEDO GRAPHIC TEST PROGRAM MACY11 27(1006) 02-DEC-76 17:43 PAGE 62
 DZVSEA.P11 02-JUN-76 00:00 SUBROUTINE TO LOAD THE L.U.T. WITH THE VALUE OF A TABLE

```

2740 .SBTTL SUBROUTINE TO LOAD THE L.U.T. WITH THE VALUE OF A TABLE
2741
2742 017062 010046 LODSTA: MOV RO, -(SP) ;SAVE RO
2743 017064 012537 017136 MOV (R5)+, 10$ ;GET STARTING LUT ADDRESS
2744 017070 012500 MOV (R5)+, RO ;GET TABLE ADDRESS
2745 017072 012737 000020 017140 MOV #16., 11$ ;LOAD COUNTER
2746
2747 017100 113737 017136 017143 MOVB 10$, 12$+1 ;LOAD HIGH BYTE WITH LUT ADDRESS
2748 017106 112037 017142 1$: MOVB (RO)+, 12$ ;LOAD VALUE INTO LOW BYTE
2749 017112 013777 017142 162346 MOV 12$, SVGCCLR ;LOAD THE TABLE
2750 017120 105237 017143 INCB 12$+1 ;UPDATE THE LUT POINTER
2751 017124 005337 017140 DEC 11$ ;FINISHED ALL L.U.T. ADDRESSES
2752 017130 001366 BNE 1$ ;BR IF NOT
2753 017132 012600 MOV (SP)+, RO ;RESTORE RO
2754 017134 000205 RTS R5 ;EXIT
2755
2756 017136 000000 10$: 0
2757 017140 000020 11$: 16. ;COUNTER OF L.U.T.
2758 017142 000000 12$: 0 ;TEMP LOCATION
2759
2760 017144 000000 YADD: 0
2761 017146 000000 XADD: 0
2762 017150 000000 SET: 0
2763 017152 000000 OVRAL: 0
2764 017154 051526 030126 026461 MSGTXT: .ASCII \VSV01-DYNAMIC-CURSOR-ADDRESS-TEST-\
2765 017162 054504 040516 044515
2766 017170 026503 052503 051522
2767 017176 051117 040455 042104
2768 017204 042522 051523 052055
2769 017212 051505 026524
2770 017216 042055 043511 052111 .ASCII \-DIGITAL-EQUIPMENT-CORPORATION\
2771 017224 046101 042455 052521
2772 017232 050111 042515 052116
2773 017240 041455 051117 047520
2774 017246 040522 044524 047117
2775 017254 100000 MSGTND: BIT15
  
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2776 .SBTTL HOME THE DISPLAY SUBROUTINE
2777 017256 004537 016666 HOME: JSR R5,DELAY ;WAIT FOR VERT SYNC
2778 017262 100001 BIT15:BIT0
2779 017264 012737 177435 024330 MOV #177435,BUFFER ;LOAD "HOME" AND TERMINATOR
2780 017272 004737 016032 JSR PC,XPRNT ;EXECUTE
2781 017276 005777 162144 1$: TST @VCCSR ;WAIT FOR CHAR READY
2782 017302 100375 BPL 1$
2783 017304 000207 RTS PC ;RETURN
2784
2785 .SBTTL DISPLAY BIT MAP # AND TEST #
2786 017306 032777 010000 161624 TESTID: BIT #BIT12,@SWR ;TEST IF INHIBIT DELAY IS SET
2787 017314 001071 BNE 3$ ;BR AND DONT DISPLAY THIS STUFF
2788 017316 112737 000060 017525 MOVB #'0,MAPIND-1 ;LOAD 1 DIGIT VALUE
2789 017324 013737 001546 017502 MOV COAXSW,10$ ;SAVE COAX SWITCH VALUE
2790 017332 006237 017502 4$: ASR 10$ ;CONVERT TO A ASCII DIGIT
2791 017336 103403 BCS 5$
2792 017340 105237 017525 INCB MAPIND-1
2793 017344 000772 BR 4$
2794 017346 012737 000060 017502 5$: MOV #'0,10$ ;LOAD MAP # INDICATOR
2795 017354 063737 001204 017502 ADD $UNIT,10$ ;ADD CURRENT MAP I.D.
2796 017362 113737 017502 017526 MOVB 10$,MAPIND ;SAVE IN A MESSAGE
2797 017370 013700 001102 1$: MOV $TSTNM,R0 ;LOAD TEST #
2798 017374 112737 000060 017550 MOVB #'0,TSTIND ;LOAD 0
2799 017402 032700 000100 BIT #BIT6,R0 ;TEST IF TEST # > 100
2800 017406 001402 BEQ 2$
2801 017410 105237 017550 INCB TSTIND ;MAKE TEST # 100
2802 017414 010001 2$: MOV R0,R1
2803 017416 042701 177770 BIC #177770,R1 ;MASK LOWER BITS
2804 017422 062701 000060 ADD #60,R1 ;MAKE IT ASCII
2805 017426 110137 017552 MOVB R1,TSTIND+2 ;LOAD MESSAGE
2806 017432 006200 ASR R0
2807 017434 006200 ASR R0
2808 017436 006200 ASR R0
2809 017440 042700 177770 BIC #177770,R0 ;MASK LOWER BITS
2810 017444 062700 000060 ADD #60,R0 ;UPDATE TO ASCII
2811 017450 110037 017551 MOVB R0,TSTIND+1 ;UPDATE MESSAGE
2812 017454 112777 000014 161772 MOVB #14,@VCPOS ;LOAD X POSITION
2813 017462 112777 000030 161766 MOVB #24,@VCPOSH ;LOAD Y POSITION
2814 017470 012700 017504 MOV #BRIAN,R0 ;LOAD MESSAGE POINTER
2815 017474 004737 016036 JSR PC,XPRNTA ;DISPLAY MESSAGE
2816 017500 000207 3$: RTS PC ;EXIT
2817 017502 000000 10$: 0
2818 017504 042524 052123 047111 BRIAN: .ASCII /TESTING BIT MAP #/
2819 017512 020107 044502 020124
2820 017520 040515 020120 043
2821 017525 060 .BYTE 60 ;1 DIGIT OF MAP #
2822 017526 060 .BYTE 60 ;2 DIGIT OF MAP #
2823 017527 040 020040 052522 .ASCII / RUNNING TEST #/
2824 017534 047116 047111 020107
2825 017542 042524 052123 021440
2826 017550 060 060 TSTIND: .BYTE 60,60,60,377
2827 017553 377
2828 .EVEN

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2829
2830 017554 012777 000202 161666 LDLIN: MOV #202, @VCHRLO ;LOAD X CROSS HAIR LINE
2831 017562 052777 010000 161656 BIS #BIT12, @VCCSR ;ENABLE X CROSS HAIR
2832 017570 000207 RTS PC ;EXIT
2833
2834 017572 050000 BYTST: BIT14!BIT12
2835 017574 120000 BIT15!BIT13
2836 017576 050000 BIT14!BIT12
2837 017600 120000 BIT15!BIT13
2838 017602 000000 0
2839
2840 .SBTTL RANDOM NUMBER GENERATOR ROUTINE
2841
2842 ;*****
2843 ;*THIS ROUTINE IS A DOUBLE PRECISION PSEUDO RANDOM NUMBER GENERATOR
2844 ;*WITH A RANGE OF 0 TO 2(+33)-1.
2845 ;*CALL:
2846 ;* JSR PC, $RAND ;:CALL THE ROUTINE
2847 ;* RETURN ;:RETURN HERE THE RANDOM
2848 ;* ;:NUMBER WILL BE IN
2849 ;* ;:$HINUM, $LONUM
2850
2851 $RAND:
2852 017604 010046 MOV RO, -(SP) ;:PUSH RO ON STACK
2853 017606 010146 MOV R1, -(SP) ;:PUSH R1 ON STACK
2854 017610 010246 MOV R2, -(SP) ;:PUSH R2 ON STACK
2855 017612 013700 017704 MOV $LONUM, RO ;:SET RO WITH LOW
2856 017616 013701 017702 MOV $HINUM, R1 ;:SET R1 WITH HIGH
2857 017622 012702 177771 MOV #-7, R2 ;:SET SHIFT COUNT
2858 017626 006300 1$: ASL RO ;:SHIFT RO LEFT AND
2859 017630 006101 ROL R1 ;:ROTATE CARRY INTO R1 AND
2860 017632 005202 INC R2 ;:CHECK FOR DONE
2861 017634 001374 BNE 1$ ;:CONTINUE SHIFT LOOP
2862 017636 063700 017704 ADD $LONUM, RO ;:ADD NUMBER TO MAKE X 129
2863 017642 005501 ADC R1 ;:PROPOGATE CARRY
2864 017644 063701 017702 ADD $HINUM, R1 ;:ADD NUMBER TO MAKE X 129
2865 017650 062700 001057 ADD #1057, RO ;:ADD LOW CONSTANT
2866 017654 005501 ADC R1 ;:PROPOGATE CARRY
2867 017656 062701 047401 ADD #47401, R1 ;:ADD HIGH CONSTANT
2868 017662 010037 017704 MOV RO, $LONUM ;:SAVE RO
2869 017666 010137 017702 MOV R1, $HINUM ;:SAVE R1
2870 017672 012602 MOV (SP)+, R2 ;:POP STACK INTO R2
2871 017674 012601 MOV (SP)+, R1 ;:POP STACK INTO R1
2872 017676 012600 MOV (SP)+, RO ;:POP STACK INTO RO
2873 017700 000207 RTS PC ;:RETURN
2874 017702 176543 $HINUM: .WORD 176543
2875 017704 123456 $LONUM: .WORD 123456

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.SBTTL SCOPE HANDLER ROUTINE

*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
*AND LOAD THE TEST NUMBER(\$STSTM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
*AND LOAD THE ERROR FLAG (\$ERFLG) INTO DISPLAY<15:08>
*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
*SW14=1 LOOP ON TEST
*SW08=1 LOOP ON TEST IN SWR<7:0>
*CALL
* SCOPE ;:SCOPE=IOT

\$SCOPE:
CKSWR ;:TEST FOR CHANGE IN SOFT-SWR
CKSWR
1\$: BIT #BIT14,\$SWR ;:LOOP ON PRESENT TEST?
BNE \$OVER ;:YES IF SW14=1
*****START OF CODE FOR THE XOR TESTER*****
\$XTSTR: BR 6\$;:IF RUNNING ON THE "XOR" TESTER CHANGE
;:THIS INSTRUCTION TO A "NOP" (NOP=240)
;:SAVE THE CONTENTS OF THE ERROR VECTOR
MOV @#ERRVEC, -(SP) ;:SET FOR TIMEOUT
MOV #5,\$@#ERRVEC ;:TIME OUT ON XOR?
TST @#177060 ;:RESTORE THE ERROR VECTOR
MOV (SP)+, @#ERRVEC ;:GO TO THE NEXT TEST
BR \$SVLAD ;:CLEAR THE STACK AFTER A TIME OUT
5\$: CMP (SP)+, (SP)+ ;:RESTORE THE ERROR VECTOR
MOV (SP)+, @#ERRVEC ;:LOOP ON THE PRESENT TEST
BR \$OVER
6\$; *****END OF CODE FOR THE XOR TESTER*****
BIT #BIT08,\$SWR ;:LOOP ON SPEC. TEST?
BEQ \$SVLAD ;:BR IF NO
CMPB @SWR,\$STSTM ;:ON THE RIGHT TEST? SWR<7:0>
BEQ \$OVER ;:BR IF YES
\$SVLAD: INCB \$STSTM ;:COUNT TEST NUMBERS
MOVB \$STSTM,\$TESTN ;:SET TEST NUMBER IN APT MAILBOX
MOV (SP),\$LPADR ;:SAVE SCOPE LOOP ADDRESS
CLRB \$ERFLG ;:ZERO THE ERROR FLAG
\$OVER: MOV \$STSTM,\$DISPLAY ;:DISPLAY TEST NUMBER
MOV \$LPADR,(SP) ;:FUDGE RETURN ADDRESS
RTI ;:FIXES PS

017706
017706 104407
017710 104407
017712 032777 040000 161220
017720 001040
017722 000416
017724 013746 000004
017730 012737 017750 000004
017736 005737 177060
017742 012637 000004
017746 000414
017750 022626
017752 012637 000004
017756 000421
017760
017760 032777 000400 161152
017766 001404
017770 127737 161144 001102
017776 001411
020000 105237 001102
020004 113737 001102 001176
020012 011637 001106
020016 105037 001103
020022 013777 001102 161112
020030 013716 001106
020034 000002

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.SBTTL ERROR HANDLER ROUTINE

*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
*AND GO TO \$ERRTYP ON ERROR
*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
*SW15=1 HALT ON ERROR
*SW13=1 INHIBIT ERROR TYPEOUTS
*CALL
* ERROR N ;;ERROR=EMT AND N=ERROR ITEM NUMBER

\$ERROR:

CKSWR ;;TEST FOR CHANGE IN SOFT-SWR
7\$: INCB \$ERFLG ;;SET THE ERROR FLAG
BEQ 7\$;;DON'T LET THE FLAG GO TO ZERO
MOV \$TSTNM, @DISPLAY ;;DISPLAY TEST NUMBER AND ERROR FLAG
INC \$ERTTL ;;INC THE ERROR COUNT
MOV (SP), \$ERRPC ;;GET ADDRESS OF ERROR INSTRUCTION
SUB #2, \$ERRPC
MOVB @ \$ERRPC, \$ITEMB ;;STRIP AND SAVE THE ERROR ITEM CODE
BIT #BIT13, @SWR ;;SKIP TYPEOUT IF SET
BNE 20\$;;SKIP TYPEOUTS
JSR PC, \$ERRTYP ;;GO TO USER ERROR ROUTINE
TYPE , \$CRLF
20\$: CMPB #APTENV, \$ENV ;;RUNNING IN APT MODE
BNE 2\$;;NO, SKIP APT ERROR REPORT
MOVB \$ITEMB, 21\$;;SET ITEM NUMBER AS ERROR NUMBER
JSR PC, \$ATY4 ;;REPORT FATAL ERROR TO APT
21\$: .BYTE 0
.BYTE 0
22\$: BR 22\$;;APT ERROR LOOP
2\$: TST @SWR ;;HALT ON ERROR
BPL 3\$;;SKIP IF CONTINUE
HALT ;;HALT ON ERROR!
CKSWR ;;TEST FOR CHANGE IN SOFT-SWR
3\$: CMP # \$ENDAD, @#42 ;;ACT-11 AUTO-ACCEPT?
BNE 6\$;;BRANCH IF NO
HALT ;;YES
6\$: RTI ;;RETURN

020036
020036 104407
020040 105237 001103
020044 001775
020046 013777 001102 161066
020054 005237 001112
020060 011637 001116
020064 162737 000002 001116
020072 117737 161020 001114
020100 032777 020000 161032
020106 001004
020110 004737 020174
020114 104401 001167
020120
020120 122737 000001 001212
020126 001007
020130 113737 001114 020142
020136 004737 021522
020142 000
020143 000
020144 000777
020146 005777 160766
020152 100002
020154 000000
020156 104407
020160
020160 022737 003304 000042
020166 001001
020170 000000
020172
020172 000002

.SBTTL ERROR MESSAGE TYPEOUT ROUTINE

*THIS ROUTINE USES THE "ITEM CONTROL BYTE" (\$ITEMB) TO DETERMINE WHICH
*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" (\$ERRTB),
*AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.

\$ERRTYP:

2961									
2962									
2963									
2964									
2965									
2966									
2967									
2968									
2969	020174								
2970	020174	104401	001167						
2971	020200	010046							
2972	020202	005000							
2973	020204	153700	001114						
2974	020210	001004							
2975									
2976	020212	013746	001116						
2977									
2978	020216	104402							
2979	020220	000426							
2980	020222	005300		1\$:					
2981	020224	006300							
2982	020226	006300							
2983	020230	006300							
2984	020232	062700	001256						
2985	020236	012037	020246						
2986	020242	001404							
2987	020244	104401							
2988	020246	000000		2\$:					
2989	020250	104401	001167						
2990	020254	012037	020264	3\$:					
2991	020260	001404							
2992	020262	104401							
2993	020264	000000		4\$:					
2994	020266	104401	001167						
2995	020272	011000		5\$:					
2996	020274	001004							
2997	020276	012600		6\$:					
2998	020300	104401	001167						
2999	020304	000207							
3000	020306			7\$:					
3001	020306	013046							
3002	020310	104402							
3003	020312	005710							
3004	020314	001770							
3005	020316	104401	020324						
3006	020322	000771							
3007	020324	020040	000	8\$:					
3008		020330							

```

TYPE $SCRLF          ;; "CARRIAGE RETURN" & "LINE FEED"
MOV  RO,-(SP)        ;; SAVE RO
CLR  RO              ;; PICKUP THE ITEM INDEX
BISB @($ITEMB,RO)    ;; IF ITEM NUMBER IS ZERO, JUST
BNE  IS              ;; TYPE THE PC OF THE ERROR
MOV  $ERRPC,-(SP)    ;; SAVE $ERRPC FOR TYPEOUT
                                ;; ERROR ADDRESS
                                ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
                                ;; GET OUT
                                ;; ADJUST THE INDEX SO THAT IT WILL
                                ;; WORK FOR THE ERROR TABLE
                                ;;
                                ;; FORM TABLE POINTER
                                ;; PICKUP "ERROR MESSAGE" POINTER
                                ;; SKIP TYPEOUT IF NO POINTER
                                ;; TYPE THE "ERROR MESSAGE"
                                ;; "ERROR MESSAGE" POINTER GOES HERE
                                ;; "CARRIAGE RETURN" & "LINE FEED"
                                ;; PICKUP "DATA HEADER" POINTER
                                ;; SKIP TYPEOUT IF 0
                                ;; TYPE THE "DATA HEADER"
                                ;; "DATA HEADER" POINTER GOES HERE
                                ;; "CARRIAGE RETURN" & "LINE FEED"
                                ;; PICKUP "DATA TABLE" POINTER
                                ;; GO TYPE THE DATA
                                ;; RESTORE RO
                                ;; "CARRIAGE RETURN" & "LINE FEED"
                                ;; RETURN
                                ;;
                                ;; SAVE @($RO)+ FOR TYPEOUT
                                ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
                                ;; IS THERE ANOTHER NUMBER?
                                ;; BR IF NO
                                ;; TYPE TWO(2) SPACES
                                ;; LOOP
                                ;; TWO(2) SPACES

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3009
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.SBTTL BINARY TO OCTAL (ASCII) AND TYPE

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*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
*OCTAL (ASCII) NUMBER AND TYPE IT.
*STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPOS   ;;CALL FOR TYPEOUT
*   .BYTE  N              ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*   .BYTE  M              ;;M=1 OR 0
*                               ;;1=TYPE LEADING ZEROS
*                               ;;0=SUPPRESS LEADING ZEROS
*STYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
*STYPOS OR $TYPOC
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPON   ;;CALL FOR TYPEOUT
*STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPOC   ;;CALL FOR TYPEOUT
$TYPOS: MOV     3(SP),-(SP)  ;;PICKUP THE MODE
        MOVVB  1(SP),$OFILL ;;LOAD ZERO FILL SWITCH
        MOVVB  (SP)+,$OMODE+1 ;;NUMBER OF DIGITS TO TYPE
        ADD    #2,(SP)     ;;ADJUST RETURN ADDRESS
        BR     $TYPON
$TYPOC: MOVVB  #1,$OFILL   ;;SET THE ZERO FILL SWITCH
        MOVVB  #6,$OMODE+1 ;;SET FOR SIX(6) DIGITS
$TYPON: MOVVB  #5,$OCNT    ;;SET THE ITERATION COUNT
        MOV    R3,-(SP)    ;;SAVE R3
        MOV    R4,-(SP)    ;;SAVE R4
        MOV    R5,-(SP)    ;;SAVE R5
        MOVVB  $OMODE+1,R4 ;;GET THE NUMBER OF DIGITS TO TYPE
        NEG    R4          ;;SUBTRACT IT FOR MAX. ALLOWED
        ADD    #6,R4      ;;SAVE IT FOR USE
        MOVVB  R4,$OMODE  ;;GET THE ZERO FILL SWITCH
        MOVVB  $OFILL,R4  ;;PICKUP THE INPUT NUMBER
        MOV    12(SP),R5  ;;CLEAR THE OUTPUT WORD
1$:     CLR    R3          ;;ROTATE MSB INTO "C"
        ROL   R5          ;;GO DO MSB
        BR    3$         ;;FORM THIS DIGIT
2$:     ROL   R5
        ROL   R5
        ROL   R5
        MOV    R5,R3
3$:     ROL   R3          ;;GET LSB OF THIS DIGIT
        DECB  $OMODE     ;;TYPE THIS DIGIT?
        BPL   7$         ;;BR IF NO
        BIC   #177770,R3 ;;GET RID OF JUNK
        BNE   4$         ;;TEST FOR 0
        TST  R4          ;;SUPPRESS THIS 0?

```

020330	017646	000000	
020334	116637	000001	020553
020342	112637	020555	
020346	062716	000002	
020352	000406		
020354	112737	000001	020553
020362	112737	000006	020555
020370	112737	000005	020552
020376	010346		
020400	010446		
020402	010546		
020404	113704	020555	
020410	005404		
020412	062704	000006	
020416	110437	020554	
020422	113704	020553	
020426	016605	000012	
020432	005003		
020434	006105		1\$:
020436	000404		
020440	006105		2\$:
020442	006105		
020444	006105		
020446	010503		
020450	006103		3\$:
020452	105337	020554	
020456	100016		
020460	042703	177770	
020464	001002		
020466	005704		


```

3065 020470 001403          BEQ      5$          ;;BR IF YES
3066 020472 005204          4$: INC      R4          ;;DON'T SUPPRESS ANYMORE 0'S
3067 020474 052703 000060     BIS      #'0,R3      ;;MAKE THIS DIGIT ASCII
3068 020500 052703 000040     5$: BIS      #'0,R3      ;;MAKE ASCII IF NOT ALREADY
3069 020504 110337 020550     MOVB    R3,6$        ;;SAVE FOR TYPING
3070 020510 104401 020550     TYPE    6$          ;;GO TYPE THIS DIGIT
3071 020514 105337 020552     7$: DECB   $OCNT      ;;COUNT BY 1
3072 020520 003347          BGT      2$          ;;BR IF MORE TO DO
3073 020522 002402          BLT      6$          ;;BR IF DONE
3074 020524 005204          INC      R4          ;;INSURE LAST DIGIT ISN'T A BLANK
3075 020526 000744          BR       2$          ;;GO DO THE LAST DIGIT
3076 020530 012605     6$: MOV     (SP)+,R5    ;;RESTORE R5
3077 020532 012604          MOV     (SP)+,R4    ;;RESTORE R4
3078 020534 012603          MOV     (SP)+,R3    ;;RESTORE R3
3079 020536 016666 000002 000004  MOV     2(SP),4(SP)  ;;SET THE STACK FOR RETURNING
3080 020544 012616          MOV     (SP)+,(SP)
3081 020546 000002          RTI          ;;RETURN
3082 020550          8$: .BYTE  0          ;;STORAGE FOR ASCII DIGIT
3083 020551          .BYTE  0          ;;TERMINATOR FOR TYPE ROUTINE
3084 020552          $OCNT: .BYTE 0       ;;OCTAL DIGIT COUNTER
3085 020553          $OFILL: .BYTE 0     ;;ZERO FILL SWITCH
3086 020554          $OMODE: .WORD 0    ;;NUMBER OF DIGITS TO TYPE
3087          .SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
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3100 020556 010046          $TYPDS: MOV     R0,-(SP)  ;;PUSH R0 ON STACK
3101 020556 010146          MOV     R1,-(SP)  ;;PUSH R1 ON STACK
3102 020562 010246          MOV     R2,-(SP)  ;;PUSH R2 ON STACK
3103 020564 010346          MOV     R3,-(SP)  ;;PUSH R3 ON STACK
3104 020566 010546          MOV     R5,-(SP)  ;;PUSH R5 ON STACK
3105 020570 012746 020200     MOV     #20200,-(SP) ;;SET BLANK SWITCH AND SIGN
3106 020574 016605 000020     MOV     20(SP),R5  ;;GET THE INPUT NUMBER
3107 020600 100004          BPL     1$          ;;BR IF INPUT IS POS.
3108 020602 005405          NEG     R5          ;;MAKE THE BINARY NUMBER POS.
3109 020604 112766 000055 000001  MOVB   #'-,1(SP)   ;;MAKE THE ASCII NUMBER NEG.
3110 020612 005000          1$: CLR     R0          ;;ZERO THE CONSTANTS INDEX
3111 020614 012703 020772     MOV     #$DBLK,R3  ;;SETUP THE OUTPUT POINTER
3112 020620 112723 000040     MOVB   #' ,(R3)+   ;;SET THE FIRST CHARACTER TO A BLANK
3113 020624 005002          2$: CLR     R2          ;;CLEAR THE BCD NUMBER
3114 020626 016001 020762     MOV     $DTBL(R0),R1 ;;GET THE CONSTANT
3115 020632 160105          3$: SUB     R1,R5    ;;FORM THIS BCD DIGIT
3116 020634 002402          BLT     4$          ;;BR IF DONE
3117 020636 005202          INC     R2          ;;INCREASE THE BCD DIGIT BY 1
3118 020640 000774          BR      3$
3119 020642 060105          4$: ADD     R1,R5    ;;ADD BACK THE CONSTANT
3120 020644 005702          TST     R2          ;;CHECK IF BCD DIGIT=0

```

E06

MAINDEC-11-DZVSE-A
DZVSEA.F11

VSV01 VIEDO GRAPHIC TEST PROGRAM
02-JUN-76 00:00

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CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

0121	020646	001002		BNE	5\$:: FALL THROUGH IF 0
0122	020650	105716		TSTB	(SP)	:: STILL DOING LEADING 0'S?
0123	020652	100407		BMI	7\$:: BR IF YES
0124	020654	106316	5\$:	ASLB	(SP)	:: MSD?
0125	020656	103003		BCC	6\$:: BR IF NO
0126	020660	116663	000001 177777	MOVB	1(SP),-1(R3)	:: YES--SET THE SIGN
0127	020666	052702	000060	BIS	#'0,R2	:: MAKE THE BCD DIGIT ASCII
0128	020672	052702	000040	BIS	#',R2	:: MAKE IT A SPACE IF NOT ALREADY A DIGIT
0129	020676	110223		MOVB	R2,(R3)+	:: PUT THIS CHARACTER IN THE OUTPUT BUFFER
0130	020700	005720		TST	(R0)+	:: JUST INCREMENTING
0131	020702	020027	000010	CMP	R0,#10	:: CHECK THE TABLE INDEX
0132	020706	002746		BLT	2\$:: GO DO THE NEXT DIGIT
0133	020710	003002		BGT	8\$:: GO TO EXIT
0134	020712	010502		MOV	R5,R2	:: GET THE LSD
0135	020714	000764		BR	6\$:: GO CHANGE TO ASCII
0136	020716	105726	8\$:	TSTB	(SP)+	:: WAS THE LSD THE FIRST NON-ZERO?
0137	020720	100003		BPL	9\$:: BR IF NO
0138	020722	116663	177777 177776	MOVB	-1(SP),-2(R3)	:: YES--SET THE SIGN FOR TYPING
0139	020730	105013	9\$:	CLRB	(R3)	:: SET THE TERMINATOR
0140	020732	012605		MOV	(SP)+,R5	:: POP STACK INTO R5
0141	020734	012603		MOV	(SP)+,R3	:: POP STACK INTO R3
0142	020736	012602		MOV	(SP)+,R2	:: POP STACK INTO R2
0143	020740	012601		MOV	(SP)+,R1	:: POP STACK INTO R1
0144	020742	012600		MOV	(SP)+,R0	:: POP STACK INTO R0
0145	020744	104401	020772	TYPE	\$DBLK	:: NOW TYPE THE NUMBER
0146	020750	016666	000002 000004	MOV	2(SP),4(SP)	:: ADJUST THE STACK
0147	020756	012616		MOV	(SP)+,(SP)	
0148	020760	000002		RTI		:: RETURN TO USER
0149	020762	023420	\$DTBL:	10000.		
0150	020764	001750		1000.		
0151	020766	000144		100.		
0152	020770	000012		10.		
0153	020772	000004	\$DBLK:	.BLKW 4		

F06

MAINDEC-11-DZVSE-A
DZVSEA.F11

02-JUN-76 00:00

VSV01 VIEDO GRAPHIC TEST PROGRAM

MACY11 27(1006) 02-DEC-76 17:43 PAGE 71

CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

.SBTTL POWER DOWN AND UP ROUTINES

POWER DOWN ROUTINE

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```

$PWRDN: MOV    $SILLUP, @#PWRVEC    ;; SET FOR FAST UP
        MOV    #340, @#PWRVEC+2    ;; PRIO:7
        MOV    RD, -(SP)           ;; PUSH RD ON STACK
        MOV    R1, -(SP)           ;; PUSH R1 ON STACK
        MOV    R2, -(SP)           ;; PUSH R2 ON STACK
        MOV    R3, -(SP)           ;; PUSH R3 ON STACK
        MOV    R4, -(SP)           ;; PUSH R4 ON STACK
        MOV    R5, -(SP)           ;; PUSH R5 ON STACK
        MOV    @SWR, -(SP)         ;; PUSH @SWR ON STACK
        MOV    SP, $SAVR6          ;; SAVE SP
        MOV    $PWRUP, @#PWRVEC    ;; SET UP VECTOR
        HALT
        BR     .-2                ;; HANG UP
    
```

POWER UP ROUTINE

0175
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0200
0201
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0203
0204

```

$PWRUP: MOV    $SILLUP, @#PWRVEC    ;; SET FOR FAST DOWN
        MOV    $SAVR6, SP          ;; GET SP
        CLR    $SAVR6             ;; WAIT LOOP FOR THE TTY
1$:      INC    $SAVR6             ;; WAIT FOR THE INC
        BNE    1$                 ;; OF WORD
        MOV    (SP)+, @SWR         ;; POP STACK INTO @SWR
        MOV    (SP)+, R5           ;; POP STACK INTO R5
        MOV    (SP)+, R4           ;; POP STACK INTO R4
        MOV    (SP)+, R3           ;; POP STACK INTO R3
        MOV    (SP)+, R2           ;; POP STACK INTO R2
        MOV    (SP)+, R1           ;; POP STACK INTO R1
        MOV    (SP)+, R0           ;; POP STACK INTO R0
        MOV    $PWRDN, @#PWRVEC    ;; SET UP THE POWER DOWN VECTOR
        MOV    #340, @#PWRVEC+2    ;; PRIO:7
        TYPE   PWRMSG              ;; REPORT THE POWER FAILURE
        MOV    (PC)+, (SP)         ;; POWER FAIL MESSAGE POINTER
$PWRMG: .WORD   BEGIN            ;; RESTART AT BEGIN
$PWRAD: .WORD   BEGIN            ;; RESTART ADDRESS
$SILLUP: HALT                    ;; THE POWER UP SEQUENCE WAS STARTED
        BR     .-2                ;; BEFORE THE POWER DOWN WAS COMPLETE
$SAVR6: 0                          ;; PUT THE SP HERE
PWRMSG: .ASCIZ <15><12>/RESTARTING AFTER A POWER FAILURE/<15><12>
    
```

.EVEN

.SBTTL TYPE ROUTINE

```

*****
*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
*NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
*NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
*NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.

```

```

*CALL:
*1) USING A TRAP INSTRUCTION
* TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
*OR
* TYPE
* MESADR
*

```

```

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$TYPE: TSTB $TFFLG ;; IS THERE A TERMINAL?
BPL 1$ ;; BR IF YES
HALT ;; HALT HERE IF NO TERMINAL
BR 3$ ;; LEAVE
1$: MOV RO, -(SP) ;; SAVE RO
MOV 32(SP), RO ;; GET ADDRESS OF ASCIZ STRING
CMPB #APTENV, $ENV ;; RUNNING IN APT MODE
BNE 62$ ;; NO, GO CHECK FOR APT CONSOLE
BITB #APTPOOL, $ENVM ;; SPOOL MESSAGE TO APT
BEQ 62$ ;; NO, GO CHECK FOR CONSOLE
MOV RO, 61$ ;; SETUP MESSAGE ADDRESS FOR APT
JSR PC, $ATY3 ;; SPOOL MESSAGE TO APT
61$: .WORD 0 ;; MESSAGE ADDRESS
62$: BITB #APTCSUP, $ENVM ;; APT CONSOLE SUPPRESSED
BNE 60$ ;; YES, SKIP TYPE OUT
2$: MOVB (RO)+, -(SP) ;; PUSH CHARACTER TO BE TYPED ONTO STACK
BNE 4$ ;; BR IF IT ISN'T THE TERMINATOR
TST (SP)+ ;; IF TERMINATOR POP IT OFF THE STACK
60$: MOV (SP)+, RO ;; RESTORE RO
3$: ADD #2, (SP) ;; ADJUST RETURN PC
RTI ;; RETURN
4$: CMPB #HT, (SP) ;; BRANCH IF <HT>
BEQ 8$
CMPB #CRLF, (SP) ;; BRANCH IF NOT <CRLF>
BNE 5$
TST (SP)+ ;; POP <CR><LF> EQUIV
TYPE ;; TYPE A CR AND LF
$CRLF
CLRB $CHARCNT ;; CLEAR CHARACTER COUNT
BR 2$ ;; GET NEXT CHARACTER
5$: JSR PC, $TYPEC ;; GO TYPE THIS CHARACTER
6$: CMPB $FILLC, (SP)+ ;; IS IT TIME FOR FILLER CHARS.?
BNE 2$ ;; IF NO GO GET NEXT CHAR.
MOV $NULL, -(SP) ;; GET # OF FILLER CHARS. NEEDED
AND THE NULL CHAR.
7$: DECB 1(SP) ;; DOES A NULL NEED TO BE TYPED?
BLT 6$ ;; BR IF NO--GO POP THE NULL OFF OF STACK
JSR PC, $TYPEC ;; GO TYPE A NULL
DECB $CHARCNT ;; DO NOT COUNT AS A COUNT

```



```

3261 021406 000770          BR      7$          ;;LOOP
3262
3263          :HORIZONTAL TAB PROCESSOR
3264
3265 021410 112716 000040      8$:      MOVB      #' (SP)          ;;REPLACE TAB WITH SPACE
3266 021414 004737 021434      9$:      JSR      PC,$TYPEC          ;;TYPE A SPACE
3267 021420 132737 000007 021500      BITB      #7,$CHARCNT          ;;BRANCH IF NOT AT
3268 021426 001372          BNE      9$          ;;TAB STOP
3269 021430 005726          TST      (SP)+          ;;POP SPACE OFF STACK
3270 021432 000724          BR      2$          ;;GET NEXT CHARACTER
3271 021434 105777 157510      $TYPEC: TSTB      0$TPS          ;;WAIT UNTIL PRINTER IS READY
3272 021440 100375          BPL      $TYPEC
3273 021442 116677 000002 157502      MOVB      2(SP),0$TPB          ;;LOAD CHAR TO BE TYPED INTO DATA REG.
3274 021450 122766 000015 000002      CMPB      #CR,2(SP)          ;;IS CHARACTER A CARRIAGE RETURN?
3275 021456 001003          BNE      1$          ;;BRANCH IF NO
3276 021460 105037 021500      CLRB      $CHARCNT          ;;YES--CLEAR CHARACTER COUNT
3277 021464 000406          BR      $TYPEX          ;;EXIT
3278 021466 122766 000012 000002 1$:      CMPB      #LF,2(SP)          ;;IS CHARACTER A LINE FEED?
3279 021474 001402          BEQ      $TYPEX          ;;BRANCH IF YES
3280 021476 105227          INCB      (PC)+          ;;COUNT THE CHARACTER
3281 021500 000000      $CHARCNT: .WORD 0          ;;CHARACTER COUNT STORAGE
3282 021502 000207      $TYPEX: RTS      PC
3283
3284          .SBTTL  APT COMMUNICATIONS ROUTINE
3285
3286          ;;*****
3287 021504 112737 000001 021750  $ATY1:  MOVB      #1,$FFLG          ;;TO REPORT FATAL ERROR
3288 021512 112737 000001 021746  $ATY3:  MOVB      #1,$MFLG          ;;TO TYPE A MESSAGE
3289 021520 000403          BR      $ATYC
3290 021522 112737 000001 021750  $ATY4:  MOVB      #1,$FFLG          ;;TO ONLY REPORT FATAL ERROR
3291 021530          $ATYC:
3292 021530 010046          MOV      R0,-(SP)          ;;PUSH R0 ON STACK
3293 021532 010146          MOV      R1,-(SP)          ;;PUSH R1 ON STACK
3294 021534 105737 021746          TSTB      $MFLG          ;;SHOULD TYPE A MESSAGE?
3295 021540 001450          BEQ      5$          ;;IF NOT: BR
3296 021542 122737 000001 001212      CMPB      #APTENV,$ENV          ;;OPERATING UNDER APT?
3297 021550 001031          BNE      3$          ;;IF NOT: BR
3298 021552 132737 000100 001213      BITB      #APTPOOL,$ENVM          ;;SHOULD SPOOL MESSAGES?
3299 021560 001425          BEQ      3$          ;;IF NOT: BR
3300 021562 017600 000004          MOV      04(SP),R0          ;;GET MESSAGE ADDR.
3301 021566 062766 000002 000004          ADD      #2,4(SP)          ;;BUMP RETURN ADDR.
3302 021574 005737 001172 1$:      TST      $MSGTYPE          ;;SEE IF DONE W/ LAST XMISSION?
3303 021600 001375          BNE      1$          ;;IF NOT: WAIT
3304 021602 010037 001206          MOV      R0,$MSGAD          ;;PUT ADDR IN MAILBOX
3305 021606 105720          2$:      TSTB      (R0)+          ;;FIND END OF MESSAGE
3306 021610 001376          BNE      2$
3307 021612 163700 001206          SUB      $MSGAD,R0          ;;SUB START OF MESSAGE
3308 021616 006200          ASR      R0          ;;GET MESSAGE LNGTH IN WORDS
3309 021620 010037 001210          MOV      R0,$MSGGLT          ;;PUT LENGTH IN MAILBOX
3310 021624 012737 000004 001172      MOV      #4,$MSGTYPE          ;;TELL APT TO TAKE MSG.
3311 021632 000413          BR      5$
3312 021634 017637 000004 021660 3$:      MOV      04(SP),4$          ;;PUT MSG ADDR IN JSR LINKAGE
3313 021642 062766 000002 000004          ADD      #2,4(SP)          ;;BUMP RETURN ADDRESS
3314 021650 013746 177776          MOV      177776,-(SP)          ;;PUSH 177776 ON STACK
3315 021654 004737 021222          JSR      PC,$TYPE          ;;CALL TYPE MACRO
3316 021660 000000          4$:      .WORD 0

```

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3317 021662          55:
3318 021662 105737 021750 105:  TSTB  $FFLG      :: SHOULD REPORT FATAL ERROR?
3319 021666 001416          BEQ  12$      :: IF NOT: BR
3320 021670 005737 001212          TST  $ENV     :: RUNNING UNDER APT?
3321 021674 001413          BEQ  12$      :: IF NOT: BR
3322 021676 005737 001172          11$:  TST  $MSGTYPE  :: FINISHED LAST MESSAGE?
3323 021702 001375          BNE  11$      :: IF NOT: WAIT
3324 021704 017637 000004 001174  MOV  24(SP), $FATAL  :: GET ERROR #
3325 021712 062766 000002 000004  ADD  #2, 4(SP)      :: BUMP RETURN ADDR.
3326 021720 005237 001172          INC  $MSGTYPE      :: TELL APT TO TAKE ERROR
3327 021724 105037 021750          12$:  CLRB $FFLG      :: CLEAR FATAL FLAG
3328 021730 105037 021747          CLRB $LFLG      :: CLEAR LOG FLAG
3329 021734 105037 021746          CLRB $MFLG      :: CLEAR MESSAGE FLAG
3330 021740 012601          MOV  (SP)+, R1     :: POP STACK INTO R1
3331 021742 012600          MOV  (SP)+, R0     :: POP STACK INTO R0
3332 021744 000207          RTS  PC           :: RETURN
3333 021746          000          $MFLG: .BYTE 0    :: MESSG. FLAG
3334 021747          000          $LFLG: .BYTE 0    :: LOG FLAG
3335 021750          000          $FFLG: .BYTE 0    :: FATAL FLAG
3336          021752          .EVEN
3337          000200  APTSIZE=200
3338          000001  APTENV=001
3339          000100  APTSPOOL=100
3340          000040  APTCSUP=040

```


.SBTTL TTY INPUT ROUTINE

::*****
:ENABL LSB

::*****
:*SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
:*ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
:*SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
:*WHEN OPERATING IN TTY FLAG MODE.

3351	021752	022737	000176	001140	\$CKSWR:	CMP	#SWREG, SWR	:: IS THE SOFT-SWR SELECTED?
3352	021760	001074				BNE	15\$:: BRANCH IF NO
3353	021762	105777	157156			TSTB	2\$TKS	:: CHAR THERE?
3354	021766	100071				BPL	15\$:: IF NO, DON'T WAIT AROUND
3355	021770	117746	157152			MOVB	2\$TKB, -(SP)	:: SAVE THE CHAR
3356	021774	042716	177600			BIC	#1C177, (SP)	:: STRIP-OFF THE ASCII
3357	022000	022726	000007			CMP	#7, (SP)+	:: IS IT A CONTROL G?
3358	022004	001062				BNE	15\$:: NO, RETURN TO USER
3359	022006	123727	001134	000001		CMPB	\$AUTOB, #1	:: ARE WE RUNNING IN AUTO-MODE?
3360	022014	001456				BEQ	15\$:: BRANCH IF YES
3361								
3362	022016	104401	022477			TYPE	,\$CNTLG	:: ECHO THE CONTROL-G (↑G)
3363	022022	104401	022504		\$GTSWR:	TYPE	\$MSWR	:: TYPE CURRENT CONTENTS
3364	022026	013746	000176			MOV	\$WREG, -(SP)	:: SAVE SWREG FOR TYPEOUT
3365	022032	104402				TYPOC		:: GO TYPE--OCTAL ASCII(ALL DIGITS)
3366	022034	104401	022515			TYPE	,\$MNEW	:: PROMPT FOR NEW SWR
3367	022040	005046			19\$:	CLR	-(SP)	:: CLEAR COUNTER
3368	022042	005046				CLR	-(SP)	:: THE NEW SWR
3369	022044	105777	157074		7\$:	TSTB	2\$TKS	:: CHAR THERE?
3370	022050	100375				BPL	7\$:: IF NOT TRY AGAIN
3371								
3372	022052	117746	157070			MOVB	2\$TKB, -(SP)	:: PICK UP CHAR
3373	022056	042716	177600			BIC	#1C177, (SP)	:: MAKE IT 7-BIT ASCII
3374								
3375								
3376								
3377	022062	021627	000025		9\$:	CMP	(SP), #25	:: IS IT A CONTROL-U?
3378	022066	001005				BNE	10\$:: BRANCH IF NOT
3379	022070	104401	022472			TYPE	,\$CNTLU	:: YES, ECHO CONTROL-U (↑U)
3380	022074	062706	000006		20\$:	ADD	#6, SP	:: IGNORE PREVIOUS INPUT
3381	022100	000757				BR	19\$:: LET'S TRY IT AGAIN
3382								
3383								
3384	022102	021627	000015		10\$:	CMP	(SP), #15	:: IS IT A <CR>?
3385	022106	001022				BNE	16\$:: BRANCH IF NO
3386	022110	005766	000004			TST	4(SP)	:: YES, IS IT THE FIRST CHAR?
3387	022114	001403				BEQ	11\$:: BRANCH IF YES
3388	022116	016677	000002	157014		MOV	2(SP), 2\$SWR	:: SAVE NEW SWR
3389	022124	062706	000006		11\$:	ADD	#6, SP	:: CLEAR UP STACK
3390	022130	104401	001167		14\$:	TYPE	,\$CRLF	:: ECHO <CR> AND <LF>
3391	022134	123727	001135	000001		CMPB	\$INTAG, #1	:: RE-ENABLE TTY KBD INTERRUPTS?
3392	022142	001003				BNE	15\$:: BRANCH IF NOT
3393	022144	012777	000100	156772		MOV	#100, 2\$TKS	:: RE-ENABLE TTY KBD INTERRUPTS
3394	022152	000002			15\$:	RTI		:: RETURN
3395	022154	004737	021434		16\$:	JSR	PC, \$TYPEC	:: ECHO CHAR
3396	022160	021627	000060			CMP	(SP), #60	:: CHAR < 0?

```

3397 022164 002420          BLT      18$          ;;BRANCH IF YES
3398 022166 021627 000067    CMP      (SP),#67     ;;CHAR > 7?
3399 022172 003015          BGT      18$          ;;BRANCH IF YES
3400 022174 042726 000060    BIC      #60,(SP)+    ;;STRIP-OFF ASCII
3401 022200 005766 000002    TST      2(SP)        ;;IS THIS THE FIRST CHAR
3402 022204 001403          BEQ      17$          ;;BRANCH IF YES
3403 022206 006316          ASL      (SP)         ;;NO, SHIFT PRESENT
3404 022210 006316          ASL      (SP)         ;;CHAR OVER TO MAKE
3405 022212 006316          ASL      (SP)         ;;ROOM FOR NEW ONE.
3406 022214 005266 000002    17$: INC      2(SP)        ;;KEEP COUNT OF CHAR
3407 022220 056616 177776    BIS      -2(SP),(SP)  ;;SET IN NEW CHAR
3408 022224 000707          BR       7$           ;;GET THE NEXT ONE
3409 022226 104401 001166    18$: TYPE   $QUES     ;;TYPE ?<CR><LF>
3410 022232 000720          BR       20$         ;;SIMULATE CONTROL-U
3411          .DSABL  LSB
3412
3413
3414
3415          ;;*****
3416          ;;THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
3417          ;;CALL:
3418          ;;          RDCHR          ;; INPUT A SINGLE CHARACTER FROM THE TTY
3419          ;;          RETURN HERE   ;; CHARACTER IS ON THE STACK
3420          ;;          WITH PARITY BIT STRIPPED OFF
3421
3422          $RDCHR: MOV      (SP),-(SP)  ;;PUSH DOWN THE PC
3423 022234 011646 000004 000002    MOV      4(SP),2(SP)  ;;SAVE THE PS
3424 022236 016666 000004 156674    1$: TST      @STKS     ;;WAIT FOR
3425 022244 105777 156674          BPL      1$           ;;A CHARACTER
3426 022250 100375          MOV      @STKB,4(SP)  ;;READ THE TTY
3427 022252 117766 156670 000004    BIC      #1C<177>,4(SP) ;;GET RID OF JUNK IF ANY
3428 022260 042766 177600 000004    CMP      4(SP),#23    ;;IS IT A CONTROL-S?
3429 022266 026627 000004 000023    BNE      3$           ;;BRANCH IF NO
3430 022274 001013          TST      @STKS     ;;WAIT FOR A CHARACTER
3431 022276 105777 156642    2$: BPL      2$           ;;LOOP UNTIL ITS THERE
3432 022302 100375          MOV      @STKB,-(SP)  ;;GET CHARACTER
3433 022304 117746 156636          BIC      #1C177,(SP)  ;;MAKE IT 7-BIT ASCII
3434 022310 042716 177600          CMP      (SP)+,#21    ;;IS IT A CONTROL-Q?
3435 022314 022627 000021          BNE      2$           ;;IF NOT DISCARD IT
3436 022320 001366          BR       1$           ;;YES, RESUME
3437 022322 000750          CMP      4(SP),#140  ;;IS IT UPPER CASE?
3438 022324 026627 000004 000140    3$: BLT      4$           ;;BRANCH IF YES
3439 022332 002407          CMP      4(SP),#175  ;;IS IT A SPECIAL CHAR?
3440 022334 026627 000004 000175    BGT      4$           ;;BRANCH IF YES
3441 022342 003003          BIC      #40,4(SP)   ;;MAKE IT UPPER CASE
3442 022344 042766 000040 000004    4$: RTI              ;;GO BACK TO USER
3443 022352 000002
3444          ;;*****
3445          ;;THIS ROUTINE WILL INPUT A STRING FROM THE TTY
3446          ;;CALL:
3447          ;;          RDLIN         ;; INPUT A STRING FROM THE TTY
3448          ;;          RETURN HERE   ;; ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
3449          ;;          TERMINATOR WILL BE A BYTE OF ALL 0'S
3450          $RDLIN: MOV      R3,-(SP)    ;;SAVE R3
3451 022354 010346 000004 000140    1$: MOV      #$TTYIN,R3  ;;GET ADDRESS
3452 022356 012703 022462 022472    2$: CMP      #$TTYIN+8.,R3 ;;BUFFER FULL?

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3453 022366 101405      BLOS      4$          ;;BR IF YES
3454 022370 104410      RDCHR                    ;;GO READ ONE CHARACTER FROM THE TTY
3455 022372 112613      MOV8      (SP)+,(R3)   ;;GET CHARACTER
3456 022374 122713 000177 10$:  CMPB      #177,(R3)   ;;IS IT A RUBOUT
3457 022400 001003      BNE       3$          ;;SKIP IF NOT
3458 022402 104401 001166 4$:   TYPE      $QUES   ;;TYPE A '?'
3459 022406 000763      BR        1$          ;;CLEAR THE BUFFER AND LOOP
3460 022410 111337 022460 3$:   MOV8      (R3),9$     ;;ECHO THE CHARACTER
3461 022414 104401 022460      TYPE      9$
3462 022420 122723 000015      CMPB      #15,(R3)+  ;;CHECK FOR RETURN
3463 022424 001356      BNE       2$          ;;LOOP IF NOT RETURN
3464 022426 105063 177777      CLRB      -1(R3)     ;;CLEAR RETURN (THE 15)
3465 022432 104401 001170      TYPE      $LF        ;;TYPE A LINE FEED
3466 022436 012603      MOV       (SP)+,R3    ;;RESTORE R3
3467 022440 011646      MOV       (SP),-(SP)  ;;ADJUST THE STACK AND PUT ADDRESS OF THE
3468 022442 016666 000004 000002      MOV       4(SP),2(SP) ;;FIRST ASCII CHARACTER ON IT
3469 022450 012766 022462 000004      MOV       #STTYIN,4(SP)
3470 022456 000002      RTI                    ;;RETURN
3471 022460 000          9$:   .BYTE     0          ;;STORAGE FOR ASCII CHAR. TO TYPE
3472 022461 000          .BYTE     0          ;;TERMINATOR
3473 022462 000010      $TTYIN:  .BLKB     8.   ;;RESERVE 8 BYTES FOR TTY INPUT
3474 022472 052536 005015 000      $CNTLU:  .ASCIZ   /↑U/<15><12> ;;CONTROL "U"
3475 022477 136 006507 000012      $CNTLG:  .ASCIZ   /↑G/<15><12> ;;CONTROL "G"
3476 022504 005015 053523 020122      $MSWR:   .ASCIZ   <15><12>/SWR = /
3477 022512 020075 000
3478 022515 040 047040 053505      $MNEW:   .ASCIZ   / NEW = /
3479 022522 036440 000040
3480      .SBTTL  READ AN OCTAL NUMBER FROM THE TTY
3481
3482      ;;*****
3483      ;;*THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
3484      ;;*CHANGE IT TO BINARY.
3485      ;;*CALL:
3486      ;;*      RDOCT
3487      ;;*      RETURN HERE
3488      ;;*      ;;READ AN OCTAL NUMBER
3489      ;;*      ;;LOW ORDER BITS ARE ON TOP OF THE STACK
3490      ;;*      ;;HIGH ORDER BITS ARE IN $HIOCT
3490 022526 011646 000004 000002      $RDOCT:  MOV       (SP),-(SP) ;;PROVIDE SPACE FOR THE
3491 022530 016666      MOV       4(SP),2(SP) ;;INPUT NUMBER
3492 022536 010046      MOV       R0,-(SP)    ;;PUSH R0 ON STACK
3493 022540 010146      MOV       R1,-(SP)    ;;PUSH R1 ON STACK
3494 022542 010246      MOV       R2,-(SP)    ;;PUSH R2 ON STACK
3495 022544 104411 1$:   RDLIN                    ;;READ AN ASCIZ LINE
3496 022546 012600      MOV       (SP)+,R0    ;;GET ADDRESS OF 1ST CHARACTER
3497 022550 005001      CLR       R1          ;;CLEAR DATA WORD
3498 022552 005002      CLR       R2
3499 022554 112046 2$:   MOV8      (R0)+,-(SP) ;;PICKUP THIS CHARACTER
3500 022556 001412      BEQ      3$          ;;IF ZERO GET OUT
3501 022560 006301      ASL      R1          ;;*2
3502 022562 006102      ROL      R2
3503 022564 006301      ASL      R1          ;;*4
3504 022566 006102      ROL      R2
3505 022570 006301      ASL      R1          ;;*8
3506 022572 006102      ROL      R2
3507 022574 042716 177770      BIC      #↑C7,(SP)   ;;STRIP THE ASCII JUNK
3508 022600 062601      ADD      (SP)+,R1    ;;ADD IN THIS DIGIT

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M06

MAINDEC-11-DZVSE-A
DZVSEA.P11

02-JUN-76 00:00

VSV01 VIEDO GRAPHIC TEST PROGRAM

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READ AN OCTAL NUMBER FROM THE TTY

3509	022602	000764		BR	2\$::LOOP
3510	022604	005726		3\$: TST	(SP)+	:::CLEAN TERMINATOR FROM STACK
3511	022606	010166	000012	MOV	R1,12(SP)	:::SAVE THE RESULT
3512	022612	010237	022626	MOV	R2,\$HIOCT	
3513	022616	012602		MOV	(SP)+,R2	:::POP STACK INTO R2
3514	022620	012601		MOV	(SP)+,R1	:::POP STACK INTO R1
3515	022622	012600		MOV	(SP)+,R0	:::POP STACK INTO R0
3516	022624	000002		RTI		:::RETURN
3517	022626	000000		\$HIOCT: .WORD	0	:::HIGH ORDER BITS GO HERE

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.SBTTL TRAP DECODER

*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
*GO TO THAT ROUTINE.

\$TRAP: MOV RO, -(SP) ;; SAVE RO
MOV 2(SP), RO ;; GET TRAP ADDRESS
TST -(RO) ;; BACKUP BY 2
MOVB (RO), RO ;; GET RIGHT BYTE OF TRAP
ASL RO ;; POSITION FOR INDEXING
MOV \$TRPAD(RO), RO ;; INDEX TO TABLE
RTS RO ;; GO TO ROUTINE

;; THIS IS USE TO HANDLE THE "GETPRI" MACRO

\$TRAP2: MOV (SP), -(SP) ;; MOVE THE PC DOWN
MOV 4(SP), 2(SP) ;; MOVE THE PSW DOWN
RTI ;; RESTORE THE PSW

.SBTTL TRAP TABLE

*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
*BY THE "TRAP" INSTRUCTION.

ROUTINE

\$TRPAD: .WORD \$TRAP2
\$TYPE ;; CALL=TYPE TRAP+1(104401) TTY TYPEOUT ROUTINE
\$TYPOC ;; CALL=TYPOC TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
\$TYPOS ;; CALL=TYPOS TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
\$TYPON ;; CALL=TYPON TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
\$TYPDS ;; CALL=TYPDS TRAP+5(104405) TYPE DECIMAL NUMBER (WITH SIGN)
\$GTSWR ;; CALL=GTSWR TRAP+6(104406) GET SOFT-SWR SETTING
\$CKSWR ;; CALL=CKSWR TRAP+7(104407) TEST FOR CHANGE IN SOFT-SWR
\$RDCHR ;; CALL=RDCHR TRAP+10(104410) TTY TYPEIN CHARACTER ROUTINE
\$RDLIN ;; CALL=RDLIN TRAP+11(104411) TTY TYPEIN STRING ROUTINE
\$RDOCT ;; CALL=RDOCT TRAP+12(104412) READ AN OCTAL NUMBER FROM TTY


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3562          .SBTTL ASCII MESSAGES
3563 022712 005015 046412 026504 TITLE: .ASCIZ <15><12><12>/MD-11-DZVSE-A0 AUG. 1976/<15><12>
3564 022720 030461 042055 053132
3565 022726 042523 040455 004460
3566 022734 052501 027107 030440
3567 022742 033471 006466 000012
3568 022750      015      012 VTHDR1: .BYTE 15,12
3569 022752 052126 030126 020061 .ASCIZ /VIVO! AT ADDRESS /
3570 022760 052101 040440 042104
3571 022766 042522 051523 000040
3572 022774 044040 051501 000040 VTHDR2: .ASCIZ / HAS /
3573 023002 041040 052111 046440 VTHDR3: .ASCIZ / BIT MAP(S) INSTALLED/<15><12>
3574 023010 050101 051450 020051
3575 023016 047111 052123 046101
3576 023024 042514 006504 000012
3577 023032 047516 041040 051525 EM1: .ASCIZ /NO BUS RESPONSE FROM A CHARACTER BUS REGISTER ** FATAL ERROR **/
3578 023040 051040 051505 047520
3579 023046 051516 020105 051106
3580 023054 046517 040440 041440
3581 023062 040510 040522 052103
3582 023070 051105 041040 051525
3583 023076 051040 043505 051511
3584 023104 042524 020122 025040
3585 023112 020052 040506 040524
3586 023120 020114 051105 047522
3587 023126 020122 025052      000
3588 023133      116 020117 052502 EM2: .ASCIZ /NO BUS RESPONSE FROM A SELECTED BIT MAP REGISTER/
3589 023140 020123 042522 050123
3590 023146 047117 042523 043040
3591 023154 047522 020115 020101
3592 023162 042523 042514 052103
3593 023170 042105 041040 052111
3594 023176 046440 050101 051040
3595 023204 043505 051511 042524
3596 023212 000122
3597 023214 044103 051101 041501 EM3: .ASCIZ /CHARACTER STATUS REGISTER ERROR/
3598 023222 042524 020122 052123
3599 023230 052101 051525 051040
3600 023236 043505 051511 042524
3601 023244 020122 051105 047522
3602 023252 000122
3603 023254 042526 052122 041511 EM4: .ASCIZ /VERTICAL SYNC ERROR/
3604 023262 046101 051440 047131
3605 023270 020103 051105 047522
3606 023276 000122
3607 023300 042526 052122 041511 EM5: .ASCIZ /VERTICAL SYNC INTERRUPT ERROR/
3608 023306 046101 051440 047131
3609 023314 020103 047111 042524
3610 023322 051122 050125 020124
3611 023330 051105 047522 000122
3612 023336 044502 020124 040515 EM6: .ASCIZ /BIT MAP STATUS REGISTER ERROR/
3613 023344 020120 052123 052101
3614 023352 051525 051040 043505
3615 023360 051511 042524 020122
3616 023366 051105 047522 000122
3617 023374 044502 020124 040515 EM7: .ASCIZ /BIT MAP P.C. REGISTER ERROR/

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DZVSEA.P11

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VSV01 VIEDO GRAPHIC TEST PROGRAM
ASCII MESSAGES

3618	023402	020120	027120	027103	
3619	023410	051040	043505	051511	
3620	023416	042524	020122	051105	
3621	023424	047522	000122		
3622	023430	044502	020124	040515	EM10: .ASCIZ /BIT MAP P.C. INCREMENT ERROR/
3623	023436	020120	027120	027103	
3624	023444	044440	041516	042522	
3625	023452	042515	052116	042440	
3626	023460	051122	051117	000	
3627	023465	127	051117	020104	EM11: .ASCIZ /WORD TEST OF PIXEL BIT 0 ERROR/
3628	023472	042524	052123	047440	
3629	023500	020106	044520	042530	
3630	023506	020114	044502	020124	
3631	023514	020060	051105	047522	
3632	023522	000122			
3633	023524	054502	042524	052040	EM12: .ASCIZ /BYTE TEST OF PIXEL BIT 0 ERROR/
3634	023532	051505	020124	043117	
3635	023540	050040	054111	046105	
3636	023546	041040	052111	030040	
3637	023554	042440	051122	051117	
3638	023562	000			
3639	023563	103	040510	040522	EM13: .ASCIZ /CHARACTER READY ERROR/
3640	023570	052103	051105	051040	
3641	023576	040505	054504	042440	
3642	023604	051122	051117	000	
3643	023611	102	052111	046440	EM14: .ASCIZ /BIT MAP READY ERROR/
3644	023616	050101	051040	040505	
3645	023624	054504	042440	051122	
3646	023632	051117	000		
3647	023635	120	042522	044526	EM16: .ASCIZ /PREVIOUSLY EXISTING BIT MAP DOES NOT EXIST NOW/
3648	023642	052517	046123	020131	
3649	023650	054105	051511	044524	
3650	023656	043516	041040	052111	
3651	023664	046440	050101	042040	
3652	023672	042517	020123	047516	
3653	023700	020124	054105	051511	
3654	023706	020124	047516	000127	
3655	023714	051120	053105	047511	EM17: .ASCIZ /PREVIOUSLY EXISTING VTVD1 CONTROLLER DOES NOT EXIST NOW/
3656	023722	051525	054514	042440	
3657	023730	044530	052123	047111	
3658	023736	020107	052126	030126	
3659	023744	020061	047503	052116	
3660	023752	047522	046114	051105	
3661	023760	042040	042517	020123	
3662	023766	047516	020124	054105	
3663	023774	051511	020124	047516	
3664	024002	000127			
3665	024004	051105	050122	004503	DH1: .ASCIZ /ERRPC DEVICE ADDRESS THAT CAUSED A BUS TRAP/
3666	024012	042504	044526	042503	
3667	024020	040440	042104	042522	
3668	024026	051523	052040	040510	
3669	024034	020124	040503	051525	
3670	024042	042105	040440	041040	
3671	024050	051525	052040	040522	
3672	024056	000120			
3673	024060	051105	050122	004503	DH3: .ASCIZ /ERRPC VCCSR GOOD BAD/

MAINDEC-11-DZVSE-A VSVO: VIEDO GRAPHIC TEST PROGRAM
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3674	024066	041526	051503	004522					
3675	024074	047507	042117	041011					
3676	024102	042101	000						
3677	024105	105	051122	041520	DH4:	.ASCIZ	/ERRPC	VCCSR	TIME1 TIME2/
3678	024112	053011	041503	051123					
3679	024120	052011	046511	030505					
3680	024126	052011	046511	031105					
3681	024134	000							
3682	024135	105	051122	041520	DH5:	.ASCIZ	/ERRPC	VCCSR/	
3683	024142	053011	041503	051123					
3684	024150	000							
3685	024151	105	051122	041520	DH6:	.ASCIZ	/ERRPC	VGCSR	GOOD BAD/
3686	024156	053011	041507	051123					
3687	024164	043411	047517	004504					
3688	024172	040502	000104						
3689	024176	051105	050122	004503	DH15:	.ASCIZ	/ERRPC	VGCSR/	
3690	024204	043526	051503	000122					
3691	024212	051105	050122	004503	DH16:	.ASCIZ	/ERRPC	VTVD1/	
3692	024220	052126	030126	000061					
3693									
3694	024226	001116	001126	000000	DT1:	.EVEN			
3695	024234	001116	001446	001124	DT3:	\$ERRPC,\$BDDAT,0			
3696	024242	001126	000000						
3697	024246	001116	001446	001510	DT4:	\$ERRPC,VCCSR,SAVE0,SAVE2,0			
3698	024254	001514	000000						
3699	024260	001116	001446	000000	DT5:	\$ERRPC,VCCSR,0			
3700	024266	001116	001460	001124	DT6:	\$ERRPC,VGCSR,\$GDDAT,\$BDDAT,0			
3701	024274	001126	000000						
3702	024300	001116	001460	000000	DT15:	\$ERRPC,VGCSR,0			
3703	024306	001116	001120	000000	DT16:	\$ERRPC,\$GDADR,0,0,0,0			
3704	024314	000000	000000	000000					
3705	024322	000	000	000	DF0:	.BYTE 0,0,0,0,0,0			
3706	024325	000	000	000					
3707	024330	000000			BUFFER:	0			
3708		000001				.END			

VGPC	001462	399#	746*	756*	1352*	1355*	1505*	1523*	1548*	1550*	1634*	1656*	1678*	1700*
		1720*	1747*	1774*	1801*	1827*	1854*	1881*	1908*	1934*	1961*	1988*	2015*	2041*
		2068*	2095*	2122*	2147*	2173*	2195*	2226*	2454*	2524*	2719*			
VHO	001524	423#	1109	1120	1140	1152	1171	1183	2550					
VH1	001526	424#	1225	1243	1261	1273								
VTHDR1	022750	712	3568#											
VTHDR2	022774	715	3572#											
VTHDR3	023002	719	3573#											
WIDTH	001522	422#	1143	1189	2557									
XADDS	017146	1195*	1201	2761#										
XHAIR	001536	429#	1064											
XPRNT	016032	1113	1145	1204	1256	1285	2533#	2551	2780					
XPRNTA	016036	2534#	2815											
YADDS	017144	1185*	1200	2760#										
YHAIR	001540	429#	1081											
\$APTHD	001000	173	179#											
\$ASTAT=	***** U	3318	3333											
\$ATYC	021530	3289	3291#											
\$ATY1	021504	3287#												
\$ATY3	021512	3233	3288#											
\$ATY4	021522	2947	3290#											
\$AUTOB	001134	210#	3359	3480										
\$BASE	001246	275#	495	528	2627									
\$BDADR	001122	205#												
\$BDAT	001126	207#	528*	698*	699*	700	769*	771	772	774*	811*	812	813*	814
		815*	816	817*	818	819*	820	821*	822	823*	824	825*	826	827*
		843*	844*	845	855*	856*	857	867*	868*	859	882*	883*	884	896*
		897*	907*	908*	909	924*	925*	928*	929*	930*	931*	932	934*	935*
		936*	937*	938	948*	956*	962*	1366*	1367*	1368	1378*	1379*	1380	1390*
		1391*	1392	1403*	1404*	1405	1418*	1419*	1420	1432*	1433*	1434	1445*	1446*
		1456*	1460*	1469*	1470	1477	1486*	1487	1494	1506*	1507*	1508	1531*	1532*
		1533	1556*	1557*	1558	3694	3695	3700						
\$CDW1	001252	277#	502*	507*	541*	546*	570*	575*	599*	604*	628*	633*	728*	791
		1298	1315											
\$CDW2	001254	278#	500	502	539	541	568	570	597	599	626	628		
\$CHARC	021500	3250*	3260*	3267	3276*	3281#								
\$CKSWR	021752	3351#	3557											
\$CMTAG	001100	193#	444	445	453	459	460							
\$CM1 =	000002	225#	226#	227#										
\$CM2 =	000004	225#	226#	227#										
\$CM3 =	000002	223#	225											
\$CNTLG	022477	3362	3475#											
\$CNTLU	022472	3379	3474#											
\$CPUOP	001220	249#												
\$CRLF	001167	228#	2942	2961	2970	2989	2994	2998	3249	3284	3390	3474		
\$DBLK	020772	3111	3145	3153#										
\$DEVCT	001202	240#												
\$DEVM	001250	276#	507	546	575	604	633							
\$DOAGN	003314	666	675	681#										
\$DTBL	020762	3114	3149#											
\$ENDAD	003304	159	677#	2956										
\$ENDCT	003252	459	668#											
\$ENDMG	003323	670	685#											
\$ENULL	003320	673	684#											
\$ENV	001212	245#	2944	3228	3296	3320								
\$ENVM	001213	246#	478	505	544	573	602	631	3230	3235	3298			

\$MTYP4	001237	271#												
\$NULL	001154	219#	3255	3284										
\$NWTST=	000001	761#	784#	836#	849#	861#	873#	889#	900#	913#	942#	969#	1002#	1043#
		1066#	1085#	1098#	1128#	1159#	1211#	1291#	1360#	1372#	1384#	1396#	1411#	1423#
		1438#	1450#	1498#	1517#	1541#	1569#	1597#	1618#	1640#	1662#	1684#	1706#	1733#
		1760#	1787#	1813#	1840#	1867#	1894#	1920#	1947#	1974#	2001#	2027#	2054#	2081#
		2108#	2134#	2160#	2186#	2213#	2240#	2257#	2300#	2320#	2341#	2362#	2383#	2404#
		2429#												
\$OCNT	020552	3042*	3071*	3084#										
\$OMODE	020554	3037*	3041*	3046	3049*	3060*	3086#							
\$OVER	020022	2893	2904	2909	2914#									
\$PASS	001200	239#	477*	510	549	578	607	636	662*	663*	671	684	710	1053
		1070	1089	1102	1132	1163	1215	1312	1579	1601	1622	1644	1666	1688
		1710	1737	1764	1791	1817	1844	1871	1898	1924	1951	1978	2005	2031
		2058	2085	2112	2138	2164	2190	2217	2244	2261	2304	2324	2345	2366
		2387	2408											
\$PASTM	001006	183#												
\$PWRAD	021142	3192#												
\$PWRDN	021002	457	3159#	3187										
\$PWRMG	021136	3190#												
\$PWRUP	021054	3169	3175#											
\$QUES	001166	227#	2961	3284	3409	3458	3474							
\$RAND	017604	1180	1192	2851#										
\$RDCHR	022234	3422#	3558											
\$RDDEC=	***** U	3561												
\$RDLIN	022354	3450#	3559											
\$RDOCT	022526	3490#	3560											
\$RDSZ =	000010	3443#												
\$REGAD	001160	223#												
\$REGO	001162	225#												
\$REG1	001164	226#												
\$RTNAD	003316	683#												
\$R2A =	***** U	3561												
\$SAVRE=	***** U	3561												
\$SAVR6	021152	3168*	3176	3177*	3178*	3196#								
\$SCOPE	017706	451	2889#											
\$SETUP=	000137	441#	450	451	453	455	457	459	460	661	2890	2931	2954	2956
		3346	3480											
\$STUP =	177777	441#												
\$SVLAD	020000	2901	2907	2910#										
\$SVPC =	000230	157#	162											
\$SWR =	160400	1#	11	130	131	132	133	134	135	227	460	461	656	662
		676	682	684	765	788	840	853	865	877	893	904	917	946
		973	1006	1047	1070	1089	1102	1132	1163	1215	1295	1364	1376	1388
		1400	1415	1427	1442	1454	1502	1521	1545	1573	1601	1622	1644	1666
		1688	1710	1737	1764	1791	1817	1844	1871	1898	1924	1951	1978	2005
		2031	2058	2085	2112	2138	2164	2190	2217	2244	2261	2304	2324	2345
		2366	2387	2408	2433	2883	2884	2885	2892	2904	2906	2907	2910	2913
		2917	2924	2925	2926	2927	2935	2939	2951	2955	2961	3193		
\$SWREG	001214	247#	480											
\$SWRMK=	000000	135	136	2885	2886	2908								
\$TEMP	001474	411#	1504*	1505	1513*	1514	1547*	1553	1561*	1562*	1563*	1564*	1610*	1612
		1615*	2312*	2313	2316*	2317								
\$TEMPO	001476	412#												
\$TESTN	001176	238#	2911*											
\$TKB	001146	216#	3344	3355	3372	3426	3432							

MACRO	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
TESTID	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
TRMTRB	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
TYPBIN	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
TYPDEC	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
TYPNAM	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
TYPNUM	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
TYPOCS	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
TYPOCT	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
TYPTXT	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
USCMRE	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
USCMTM	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
USFCR	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
USFWT	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
USSET	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
USSETM	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
USSKIP	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
EQUAT	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
HEADE	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
KT11	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SETUP	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SWRHT	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SWRLO	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
ACT1	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
APT8	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
APTH	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
APTY	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
ASTA	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SCATC	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SCMTA	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SOB20	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SOB20	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SDIV	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SEOP	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SERRC	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SERRT	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SMLT	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SARM	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974
SONE	17087	17089	18113	18115	18140	18142	1867	1869	1894	1896	1920	1922	1947	1949	1974

..	..	2840
..	..	2840
..	..	2841
..	..	2877
..	..	2818
..	..	28087
..	..	28005
..	..	28010

. ABS. 024332 000

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

. DZVSEA.SEG/CRF/SOL/NL:TOC=DSKZ:SYSMAC.SML,DZVSEA.P11
RUN-TIME: 68 65 SECONDS
RUN-TIME RATIO: 286/138=2.0
CORE USED: 34K (67 PAGES)

