

TM03/TE16

BASIC FUNCTION TESTS
MD-11-DZTEC-A

EP-DZTEC-A-DL-A
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FICHE 1 OF 1

JUN 1977
digital
MADE IN USA

This microfiche card contains a grid of frames, each representing a page of test data. The frames are arranged in approximately 10 rows and 6 columns. Each frame contains a table with multiple columns and rows of text, likely representing test results or configuration parameters. The text is too small to be legible in this image.



B01

ECF1DZRSQASEQ

00010000

770526

POP10 411

DRMDR1DZTECASEQ

00010000

770526

CO1

TMD3/TE16 BASIC FUNCTION TEST MACY11 27(1006) 31-MAR-77 17:23 PAGE 1
C.DOC 24-MAR-77 15:54

.REM %

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZTEC-A-D
PRODUCT TITLE: TMD3/TE16 BASIC FUNCTION TEST
DATE CREATED: FEB 77
MAINTAINER: DIAGNOSTIC ENGINEERING
AUTHOR: J. G. ADAMS

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TABLE OF CONTENTS

PARAGRAPH	SUBJECT	PAGE
1.	ABSTRACT	3
2.	REQUIREMENTS	3
3.	LOADING PROCEDURE	3
4.	STARTING PROCEDURE	3
5.	SWITCH SETTINGS	5
6.	ERROR PRINTOUTS	6
7.	OPERATION	7
8.	SUBTEST SUMMARIES	8
9.	LISTING	16

1. ABSTRACT

THIS PROGRAM IS INTENDED TO TEST ALL OF THE BASIC FUNCTIONAL LEVEL OPERATIONS OF THE TM03/TE16 MAG TAPE SYSTEM. ALL FUNCTIONS: WRITE, READ, SPACE, ERASE, REWIND, ETC; WILL BE TESTED. IN ADDITION TO THE TM03/TE16 TESTS, THE RM WILL BE TESTED SEPARATELY IN SO FAR AS IT IS POSSIBLE TO SEPARATE THE RM FROM THE TM03/TE16 ITSELF.

2. REQUIREMENTS (HARDWARE)

- A. ANY PDP11 PROCESSOR
- B. 8K OF CORE
- C. CONSOLE TTY
- D. TM03 MAGTAPE CONTROLLER
- E. MASS BUS CONTROLLER
- F. TE16 MAG TAPE TRANSPORT

3. LOADING PROCEDURE

USE STANDARD BINARY LOADING PROCEDURE

4. STARTING PROCEDURE

THERE ARE TWO (2) STARTING ADDRESSES THAT MAYBE USED: 200(8) AND 210(8)

- A. 200(8): STARTING AT THIS ADDRESS WILL CAUSE THE PROGRAM IDENTIFICATION TO BE PRINTED FOLLOWED BY REQUESTS FOR THE VARIOUS PARAMETERS NEEDED BY THE PROGRAM.
- B. 210(8): THIS ADDRESS IS INTENDED FOR USE AS A RESTART ONLY AND WILL USE THE CURRENT PARAMETER VALUES.

**NOTE SEE ALSO SECTION 5-CONSOLE SWITCH SETTINGS
** TYPE IC TO RESTART PROGRAM (2200)

4.1 AUTOMATIC MODE OPERATION

IF THIS PROGRAM IS LOADED AND RUN IN AUTOMATIC (CHAIN) MODES
DEFAULT RESPONSES TO OPERATOR REQUESTS ARE USED, AND ALL AVAIL-
ABLE TMO3/TE16 COMBINATIONS ARE TESTED. ADDITIONALLY THE SOFTWARE
SWR IS INVOKED WITH A SWITCH SETTING OF 100000 (HALT ON ERROR)
IF LOADED VIA ACT11 CHAIN MODE.

**EXCEPTION: IF THIS PROGRAM IS LOADED VIA TMDP CHAIN MODE THE
PROGRAM WILL NOT TEST TMO3 DRIVE #0, TE16 SLAVE #0.

** NOTE: THIS PROGRAM CONTAINS AN OPERATOR ASSISTED SUBTEST. THIS
SUBTEST IS NOT EXECUTED IN CHAIN MODE. TO RUN LOAD THE
PROGRAM IN DUMP MODE.

4.2 SAMPLE START AT 200

NOTE: DEFAULT RESPONSES ARE SHOWN IN ANGLE BRACKETS (<>)
OPERATOR RESPONSES ARE SHOWN IN PARENTHESES () AND
LOCATIONS CONTAINING THE DEFAULT ARE SHOWN IN [].
TO INVOKE THE DEFAULT RESPONSE TYPE (CR).

PARAMETER REQUEST: <DEFAULT> (RESPONSE) [LOCATION:]

TMO3-TE16 BASIC FUNCTIONS TEST (DZTEC-A)
TYPE ↑C TO RESTART

REGISTER START: <172440> (CR) [REGS:]
VECTOR ADDRESS: <224> (CR) [VECT:]
DRIVE NUMBER: <0> (CR) [DRVN:]
SLAVE NUMBER: <0> (CR) [SLVN:]
SERIAL NO: 12345
RH ONLY (NO=0, YES=1): <0> (0) [RHOF:]
IF THE SOFTWARE SWR IS INVOKED:
SWR = <000000> NEW = (CR)

5. CONSOLE SWITCH SETTING

CONTROL:

1) CONTROL G (<1G>):
SELECTS THE SOFTWARE SWR AND ALLOWS THE USER TO SELECT NEW SWITCH SETTINGS.

THE MACHINE WILL THEN TYPE: SWR=XXXXXXXXNEW=
WHERE: XXXXXX IS THE OCTAL CONTENTS OF THE SOFTWARE SWR.
AFTER THE "NEW=" HAS BEEN TYPED THEN THE OPERATOR CAN DO ONE
OF THE FOLLOWING AT THE TTY:

- A) TYPE A NEW SWITCH SETTING
- B) IF A <CR> IS THE FIRST KEY DEPRESSED THE SOFTWARE SWITCH REGISTER CONTENTS WILL NOT BE CHANGED.

2) CONTROL A (<1A>):
ALTERNATES USAGE OF SWF FROM HARDWARE TO SOFTWARE & VICE VERSA,

3) CONTROL C (<1C>):
RESTARTS PROGRAM AT 200

4) CONTROL U (<1U>):
DELETES ALL CHARACTERS TYPED IN RESPONSE TO A REQUEST.

ALL SWITCHES EXCEPT 5-9 ARE USED AND THE NORMAL, OR DEFAULT,
RUN IS DONE WITH ALL SWITCHES SET TO ZERO (0).
ALL HARDWARE SWITCHES ARE DYNAMIC, AND MAY BE CHANGED AT ANY TIME.

SW15(100000): 1=HALT ON ERROR
0=CONTINUE
SW14(040000): 1=LOP ON ERROR (SCOPE: RH TESTS ONLY)
0=CONTINUE
SW13(020000): 1=DO NOT PRINT ERRORS
0=PRINT ALL ERRORS
SW12(010000): 1=CONTINUOUS CYCLE
0=HALT AT END OF PASS
SW11(004000): 1=INHIBIT ITERATION
0=DO ALL ITERATIONS PER TEST
SW10(002000): 1=HALT AT END OF CURRENT TEST
0=CONTINUE
SW9-5: N/A
SW4-0: SELECT TEST NUMBER::00=ALL TESTS

THE USE OF SW0-4 IS TO ALLOW SELECTION AND CONTINUOUS
EXECUTION OF ANY TEST. THE TEST SELECTION MAY BE CHANGED AT
ANY TIME. HOWEVER IT IS ADVISABLE TO USE SW10 TO STOP THE
PROGRAM AT THE END OF THE CURRENT TEST BEFORE SELECTING A TEST.

6. ERROR PRINTOUTS

THE ERROR PRINTOUTS FOR EACH TEST WILL APPEAR IN THE SAME GENERAL FORMAT. THE FIRST LINE WILL ALWAYS SHOW THE TEST NUMBER AND ITS TITLE. THE SECOND LINE WILL BE AN EXPLANATION OF THE ERROR. THE FOLLOWING LINES WILL SHOW THE APPROPRIATE REGISTER OR ADDRESS VALUES THAT ARE APPLICABLE TO THE INDIVIDUAL TEST

EXAMPLES:

1. THIS EXAMPLE SHOWS A TYPICAL ERROR PRINTOUT FOR THE WRITE READ TEST: A WRITE CRC ERROR OCCURRED ON SLAVE 6.

FT13: WRITE-READ TEST
WRITE ERROR NRZ

CS1	MC	BA	FC	CS2	DS	ER	TC
144260	000000	015650	000000	000103	150600	100000	101306

2. THIS EXAMPLE SHOWS A TYPICAL SPACE ERROR:
THE FC IS NOT ZERO AT THE END OF THE OPERATION.

FT14: SPACE TEST
SPACE REVERSE ERROR NRZ

CS1	MC	BA	FC	CS2	DS	ER	TC
144230	177700	017162	177740	000114	150600	001000	161700

3. THIS EXAMPLE SHOWS A SPACE OPERATION WHICH RESULTED IN INCORRECT POSITIONING. SHOULD BE AT RECORD 20, IS AT RECORD 22.

FT14: SPACE TEST
POSITION ERROR:
REVERSE ERROR EXPT:20 RCVD:22

7. OPERATION

THE PROCEDURES FOR OPERATING THIS PROGRAM ARE QUITE SIMPLE AND REQUIRE ONLY A FEW STEPS:

1. LOAD ADDRESS 200 OR 210
2. SET SWITCHES FOR DESIRED TEST CYCLE
REFER TO SECTION 5 FOR DYNAMIC LOADING OF SOFTWARE SWITCH REGISTER.
3. PRESS START
4. ENTER APPROPRIATE RESPONSES TO THE TTY REQUESTS

ALL HARDWARE SWITCHES ARE DYNAMIC AND MAY BE CHANGED AT ANY TIME. THE NORMAL, OR DEFAULT, OPERATING SEQUENCE IS ALL SWITCHES DOWN (ZERO). THE END OF EACH PASS IS NOTED BY A MESSAGE STATING END OF PASS AND THE NUMBER OF THAT PASS.
FOR THE DYNAMIC LOADING OF THE SOFTWARE SWITCH REGISTER REFER TO SECTION 5 **

SINGLE TEST SELECTION: (SW0-SW4)

WHEN SW0-4 ARE SET TO ZERO (00) THE SCHEDULAR WILL EXECUTE ALL OF THE TESTS IN SEQUENCE. IF SW0-4 IS SET TO SOME SPECIFIC TEST NUMBER THAT PARTICULAR TEST WILL BE EXECUTED CONTINUOUSLY. ANY TEST MAY BE SINGLE SELECTED IN ANY ORDER; HOWEVER, THE BEST WAY TO AFFECT THE CHANGE IS TO USE SW10 TO HALT THE CURRENT TEST, THEN CHANGE NUMBER AND PRESS CONTINUE.

8. SUBTEST SUMMARIES

THE FOLLOWING IS A LIST OF ALL TESTS IN THEIR PROPER SEQUENCE. A BASIC DESCRIPTION OF EACH TEST IS PROVIDED TO AID IN UNDERSTANDING OF THE ERROR MESSAGES ASSOCIATED WITH EACH ONE.

A. RH TESTS: THE FIRST TEN (10) TESTS WILL PERFORM BASIC RH OPERATIONS AS FAR AS IS POSSIBLE WITHOUT REQUIRING THE TMD3/TE16 ITSELF. (SEE RH ONLY OPTION; PAR 7)

FT1: RH ADDRESSING: THIS TEST WILL ASSURE THAT THE RH WILL RESPOND WITHOUT CAUSING A BUS TRAP TO ALL TMD2 REGISTER ADDRESS IN SEQUENCE STARTING AT THE ADDRESS OF CS1 ENTERED BY THE OPERATOR.

FT2: RH REGISTER BITS READ/WRITE: THIS TEST WILL ASSURE THAT ALL BITS OF THE RH WRITE/READ REGISTERS CAN BE SET AND RESET.

FT3: RH INITIALIZE: THIS TEST WILL ASSURE THAT A RH INITIALIZE (BIT 5 OF CS2=1) WILL INDEED CLEAR THE RH ERRORS.

* FT4: SILO TEST 1: THIS TEST WILL ASSURE THAT A READ FROM AN EMPTY SILO WILL CAUSE DLT TO SET.

* FT5: SILO TEST 2: THIS TEST WILL ASSURE THAT BOTH THE IR AND OR BITS WILL CORRECTLY RESPOND TO LOADING OF THE SILO WITH ALL ZEROS AND THEN A WORD OF ALL ONES.

* FT6: SILO TEST 3: THIS TEST WILL WRITE AND THEN READ THE ENTIRE SILO TO ASSURE THAT DATA CAN BE PROPERLY FILLED AND READ. ALSO THE PROPER STATUS OF IR AND OR ARE CHECKED.

* FT7: SILO TEST 4: THIS TEST WILL ASSURE PROPER RH11 RESPONSE TO SILO OVERFLOW.

* FT10: SILO TEST 5: THIS TEST WILL ASSURE SILO RESET BY RH11 INITIALIZE.

**** NOTE: SILO TESTS (FT4-FT10) ARE FOR THE RH11 ONLY. ****

B. TM03/TE16 BASIC FUNCTIONS: THE FOLLOWING FOURTEEN (14) TESTS WILL ASSURE OPERATION OF THE MAG TAPE BASIC FUNCTIONS.

FT11: NOP TEST: THIS TEST WILL ASSURE THAT THE NOP FUNCTION EXECUTES WITH NO ERROR.

FT12: REWIND TEST: THIS TEST WILL ASSURE THAT THE REWIND FUNCTION WILL POSITION THE TAPE TO BOT WITH NO ERROR.

1. ISSUE A REWIND COMMAND
2. AWAIT PIP RESET (MOTION STOPPED)
3. ASSURE THAT NO ERROR OCCURED
4. END

FT13: WRITE/READ TEST: THIS TEST WILL ASSURE THAT THE UNIT UNDER TEST CAN WRITE AND READ IN ALL DENSITIES (FOR BOTH PE AND NRZ).

1. REWIND TO BOT
2. WRITE 100 RECORDS
 - A, ALL ONES DATA
 - B, 200 FRAMES
 - C, 200 BPI; 000
3. CHECK FOR ERRORS ON EACH RECORD
4. READ REVERSE THEN FORWARD ALL 100 RECORDS
5. CHECK FOR ERRORS ON EACH RECORD
6. REPEAT STEPS 2 THRU 5 FOR 556,800,1600 BPI
7. END.

DATA READ IS NOT CHECKED; ONLY THE FUNCTION IS TESTED, NOT THE MEDIUM.

FT14: SPACE TEST: THIS TEST WILL ASSURE THAT PROPER POSITIONING IS MAINTAINED BY BOTH SPACE FORWARD AND REVERSE.

1. REWIND TO BOT
2. WRITE 100 RECORDS
 - A. EACH RECORD IS ONE FRAME LARGER THAN THE LAST. THIS WILL ALLOW FOR POSITION CHECKING BY RECORD SIZE.
3. EACH RECORD IS FORWARD CHECKED.
4. DATA RELATED ERRORS ARE IGNORED.
5. NOW SPACE REVERSE 77 RECORDS AND READ REVERSE 1, THE FRAME COUNT SHOULD BE 100. THIS IS THE SIZE OF THE FIRST RECORD.
6. NOW SPACE FORWARD 76 RECORDS AND READ FORWARD 1, THE FRAME COUNT SHOULD BE 177. THIS IS THE SIZE OF THE NEXT TO LAST RECORD.
7. CONTINUE THE SPACE AND READ (DECREMENTING THE RECORD COUNT EACH TIME) UNTIL ALL POSITIONS HAVE BEEN CHECKED. IF POSITION IS LOST; TEST ENDS.
8. REPEAT STEPS 1 THRU 7 FOR PE.
9. END

FT15: ERASE TEST: THIS TEST WILL ASSURE THAT THE ERASE FUNCTION WILL INDEED ERASE TAPES.

1. REWIND TO BOT
2. ISSUE 200 ERASE COMMANDS.
3. ASSURE NO ERRORS FOR EACH COMMAND.
4. REWIND TO BOT
5. ISSUE A READ FORWARD COMMAND.
6. THE TAPE SHOULD MOVE FORWARD UNTIL STOPPED BY OPI (APPROX 25 FT).
7. ASSURE NO ERRORS OTHER THAN OPI.
8. END

FT16: TAPE MARK WRITE/READ: THIS TEST WILL ASSURE THAT A TAPE MARK CAN BE WRITTEN AND READ IN BOTH PE AND NRZ.

1. REWIND TO BOT.
2. ISSUE A WRITE TAPE MARK COMMAND.
3. ASSURE NO ERRORS.
4. ASSURE THAT TAPE MARK STATUS IS SET IN DRIVE STATUS (BIT 2).
5. READ REVERSE.
6. ASSURE THAT TAPE MARK IS SET.
7. ASSURE THAT NO ERRORS OTHER THAN FCE OCCURED.
8. READ FORWARD.
9. REPEAT STEPS 6 AND 7
10. REPEAT STEPS 1 THRU 9 FOR PE.
11. END

FT17: TAPE MARK SPACE TEST: THIS TEST WILL ASSURE THAT SPACING WILL BE TERMINATED BY RECOGNITION OF TAPE MARK BOTH IN PE AND NRZ.

1. REWIND TO BOT.
2. WRITE THE FOLLOWING PATTERN OF TAPE MARKS AND DATA RECORDS:

TM:20 RECS:TM:40 RECS:TM:60 RECS:TM:100 RECS:TM:

3. ASSURE NO ERRORS.
4. ASSURE THAT TAPE MARK STATUS IS SET FOR TM WRITES.
5. NOW SPACE REVERSE 200 RECORDS.
6. THE SPACE OPERATION SHOULD STOP ON EACH TAPE MARK IT FINDS. THEREFOR 5 SPACE COMMANDS ARE ISSUED TO COVER THE ENTIRE PATTERN WRITTEN ON TAPE. BOT SHOULD NEVER BE REACHED AND THE FRAME COUNT WILL REFLECT THE NUMBER OF RECORDS BETWEEN TAPE MARKS.
7. REPEAT STEP 6 IN THE FORWARD DIRECTION.
8. ASSURE NO ERRORS OTHER THAN FCE.
9. REPEAT STEPS 1 THRU 8 FOR PE
10. END

FT20: WRITE CHECK TEST: BOTH WRITE CHECK FORWARD AND REVERSE ARE TESTED IN BOTH PE AND NRZ.

1. REWIND TO BOT.
2. WRITE A 400 FRAME RECORD USING DATA PATTERN 3 (125125).
3. ASSURE NO ERRORS OCCURED.
4. ISSUE A REVERSE WRITE CHECK COMMAND.
5. ASSURE NO ERRORS OCCURED.
6. REPEAT STEP 5 FOR A FORWARD WRITE CHECK.
7. REPEAT STEPS 1 THRU 6 FOR PE.
8. END

FT21: ERASE HEAD TEST: THIS TEST WILL ASSURE THAT THE ERASE HEAD ITSELF IS OPERATING.

1. REWIND TO BOT.
2. WRITE 2 RECORDS OF 800(10) FRAMES EACH. EACH RECORD WILL BE 1 INCH OF TAPE. DATA IS NOT ALL ONES.
3. REWIND TO BOT.
4. NOW WRITE A 400(10) FRAME RECORD. THIS RECORD WILL BE ONE HALF INCH OF TAPE. THE ERASE HEAD SHOULD CLEAR THE REMAINDER OF THE FIRST RECORD (ONE HALF INCH).
5. REWIND TO BOT.
6. NOW READ THE SHORT FIRST RECORD. IT SHOULD BE 400(10) FRAMES.
7. NOW READ THE SECOND RECORD. IT SHOULD BE STILL 800(10) FRAMES.
8. IF THE SECOND RECORD IS TOO LONG, THE ERASE HEAD DID NOT FUNCTION OR IT IS IN THE WRONG POLARITY.
10. END

FT22: BUFFERED COMMAND: THIS TEST WILL ASSURE THAT THE TMO2 WILL ACCEPT AND EXECUTE ANOTHER COMMAND WHILE ITS SELECTED SLAVE IS REWINDING.

1. REWIND TO BOT.
2. ISSUE 3 LONG WRITE COMMANDS TO ASSURE BEING OFF BOT.
3. ISSUE A REWIND COMMAND.
4. AS SOON AS DRIVE READY BECOMES SET, ISSUE ANOTHER WRITE COMMAND.
5. THE NEXT DRIVE READY SHOULD BE AFTER THE TAPE HAS REACHED BOT AND EXECUTED THE BUFFERED WRITE COMMAND.
6. ASSURE NO ERRORS OCCURED.
7. END

FT23: READ IN PRESET: THIS TEST WILL ASSURE THAT UNIT 0
IS REWOUND AND SET TO 800 BPI NORMAL.
(ONLY IF SLAVE 0 IS SELECTED).

1. ISSUE A WRITE COMMAND TO ASSURE
BEING OFF BOT.
2. ISSUE THE READ-IN PRESET COMMAND.
3. ABORT MOTION STOP.
4. ASSURE THAT BOT WAS REACHED.
5. ASSURE THAT THE TAPE CONTROL REGISTER
IS SET TO 800 BPI, NORMAL, 000.
6. END

(THIS TEST IS ONLY PERFORMED IF THE SELECTED SLAVE IS ZERO (0)).

FT24: AUTOMATIC DENSITY SELECTION -WRITE NRZ, READ PE:
THIS TEST ASSURES THAT AN NRZ WRITTEN
TAPE WHEN READ AS PE WILL SWITCH THE
SLAVE TO NRZ MODE.

1. REWIND SLAVE
2. WRITE AN NRZ RECORD
3. REWIND SLAVE
4. READ RECORD IN PE MODE
5. CHECK DS REG PES BIT=0
6. END

FT25: AUTOMATIC DENSITY SELECTION -WRITE PE, READ NRZ:
THIS TEST ASSURES THAT A PE WRITTEN
TAPE WHEN READ AS NRZ WILL SWITCH
THE SLAVE TO PE MODE.

1. REWIND SLAVE
2. WRITE A PE RECORD
3. REWIND A SLAVE
4. READ RECORD IN NRZ MODE
5. CHECK DS REG PES BIT=1
6. END.

FT26: REWIND: OFF LINE THIS TEST WILL ASSURE
THAT THE UNIT WILL REWIND AND
GO OFF LINE. (NOT IF IN CONTINUOUS CYCLE)

1. ISSUE THE REWIND OFF-LINE COMMAND.
2. ASSURE THAT MOL (BIT 12 OF DRIVE STATUS)
IS RESET INDICATING THE UNIT WENT OFF LINE.
3. END

(THIS TEST IS NOT PERFORMED WHEN CONTINUOUS CYCLE OPERATION IS SELECTED: SW 12 = 1)

530
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545

.TITLE TM03/TE16 BASIC FUNCTION TEST
:MAINDEC-11-DZTEC-A-D
:FEB 77
:R. BARNES
:MCALL .SACT11,.SEOP,SCATCH,SSAVE,SRESTORE,SCHAIN,SCNMODE
:MLIST MC
:LIST ME
:ENABLE ABS,AMA

:CONSOLE SWITCHES*****

:SW15(100000): 1=HALT ON ERROR
: 0=CONTINUE
:SW14(040000) 1=LOOP ON ERROR (SCOPE(040000) RH TESTS ONLY)
: 0=CONTINUE
:SW13(02000): 1=DO NOT PRINT ERRORS
: 0=PRINT ERRORS
:SW12(010000): 1=CONTINUOUS CYCLE
: 0=HALT AT END OF PASS
:SW11(40000): 1=INHIBIT ITERATIONS
: 0=DO ITERATIONS
:SW10(002000): 1=HALT AT END OF EACH TEST
: 0=CONTINUE
:SW0-4: SELECT TEST NUMBER :: 00=ALL TESTS

;USE SOFTWARE SWR IF HARDWARE SWR <15::00> = 177777 OR NOT AVAIL.

598
599
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604
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608 000046
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610 000052
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612
613
614
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616 000060 012656
617 000062 000340
618
619
620
621
622
623 000176 000000
624
625
626
627
628 000200 000137 001600
629
630
631
632 000210 000137 002542
633
634
635
636
637 000224 000224
638 000224 012646
639 000226 000340
640

```

;REGISTER EQUIVS*****
R0=X0
R1=X1
R2=X2
R3=X3
R4=X4
R5=X5
SP=X6
PC=X7

;ACT11 HOOK *****
$SVPC=.;SAVE CURRENT LOCATION CTR
.=46
.WORD SENDAD;SET LOCATION 46
.=52
.WORD 0;SET LOCATION 52 = 0
.=SVPC;RESTORE LOCATION CTR

;TTY INTERRUPT VECTOR*****
.=60
.WORD TTINT;TTY INTERRUPT HEADER ADDRESS
.WORD 340;PRIORITY LEVEL 7

;SOFTWARE SWITCH REGISTER*****
;USED IF HARDWARE SWR <15:00> = 177777 OR NOT AVAIL.
SWREG: 0;SOFTWARE SWITCH REGISTER
.=176

;START ADDRESS*****
.=200
JMP START;PROGRAM START

;RESTART ADDRESS*****
.=210
JMP ST4

;TMD3 INTERRUPT VECTOR*****
.=224
MTINT;TAPE INTERRUPT HANDLER ADDRESS
340

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000510
000512
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000524
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000528
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000532
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000538
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000542
000544
000546
000550
000552
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000556
000560
000562
000564
000566
000570
000572
000574
000576

172440
172442
172444
172446
172450
172452
172454
172456
172460
172462
172464
172466
172470
172472
172474
177776
177570
177560
177562
177564
177566
177777
000011
000010
000224
172440
000004
000006

```

.=510
;MASS BUS REGISTER EQUIVS*****
CI: 172440
IC: 172442
BA: 172444
FC: 172446
CS: 172450
OS: 172452
ER: 172454
AS: 172456
CC: 172460
DB: 172462
MR: 172464
DT: 172466
SN: 172470
TC: 172472
BAE: 172474

;CONSTANTS*****
PSW: 177776      ;PROCESSOR STATUS
SWR: 177570      ;SWITCH REGISTER
TKS: 177560      ;TTY READER STATUS
TKB: 177562      ;TTY READ BUFFER
TPS: 177564      ;TTY PUNCH STATUS
TPB: 177566      ;TTY PUNCH BUFFER
SERNUM: 177777   ;SERIAL NUMBER
DRVTP: 011       ;DRIVE TYPE
ITAMT: 10        ;ITERATION AMOUNT
VECT: 224        ;INTERRUPT VECTOR(RH)
REGS: 172440     ;STARTING REGISTER ADDRESS
BTRP: 4          ;BUS TRAP ADDRESS
BTRP2: 6         ;BUS TRAP PRIORITY LEVEL 7

```

676		
677		
678	000500	000000
679	000502	000000
680	000504	000000
681	000506	000000
682	000510	000000
683	000512	000000
684	000514	000000
685	000516	000000
686	000520	000000
687	000522	000000
688	000524	000000
689	000526	000000
690	000530	000000
691	000532	000000
692	000534	000000
693	000536	000000
694	000540	000000
695	000542	000000
696	000544	000000
697	000546	000000
698	000550	000000
699	000552	000000
700	000554	000000
701	000556	000000
702	000560	000000
703	000562	000000
704	000564	000000
705	000566	000000
706	000570	000000
707	000572	000000
708	000574	000000
709	000576	000000
710	000700	000000
711	000702	000000
712	000704	000000
713	000706	000000
714	000710	000000
715	000712	000000
716	000714	000000
717	000716	000000
718	000720	000000
719	000722	000000
720	000724	000000
721	000726	000000
722	000730	000000
723	000732	000000
724	000734	000000
725	000736	000000
726		

;FLAGS AND COUNTERS*****

T08:	0
T18:	00
RH17F:	00
HDRFL:	00
EMADOR:	00
DAVN:	00
SLVN:	00
BADOR:	00
FCNT:	00
WCNT:	00
RCNT:	00
ERRP:	00
ERRP1:	00
RRO:	00
RFD:	00
ROYDX:	00
OPYX:	00
SCNT:	00
PFLG:	00
RTRN:	00
ERAOO:	00
TEMP1:	00
TEMP2:	00
TEMP3:	00
STMSK:	00
ITCNT:	00
DSAV:	00
SAV1:	00
SAV2:	00
SAV3:	00
SCOLP:	00
ITRLP:	00
EXFL:	00
PEXFL:	00
STFLG:	00
LTADD:	00
FUN:	00
SERFL:	00
CRCNT:	00
UIDES:	00
PATRN:	00
RHTF:	00
MRZOF:	00
RHOF:	00
PCNTR:	00
TEMPST:	00
COUNT:	00
RDSW:	0

G02

TM03/TE16 BASIC FUNCTION TEST MACY11 27(1006) 31-MAR-77 17:23 PAGE 19
C 29-MAR-77 09:54

727
728
729
730 000740 000000
731 000742 012440
732 000744 012460
733 000746 012464
734 000750 012472

;DATA PATTERN GENERATORS*****

DATB: 0
DATA0: DAT1 : ALL ONE BITS
DATA1: DAT2 : ALL ZERO BITS
DATA2: DAT3 : ALTERNATING ONE/ZERO BITS
DATA3: DAT4 : ALL BITS 0-377

,LOGIC TEST ENTRY TABLE*****

735		
736		
737		
738	000752	000000
739	000754	000000
740	000756	003216
741	000760	003216
742	000762	003316
743	000764	003316
744	000766	003640
745	000770	003640
746	000772	004060
747	000774	004060
748	000776	004206
749	001000	004206
750	001002	004400
751	001004	004400
752	001006	004652
753	001010	004652
754	001012	004746
755	001014	004746
756	001016	005102
757	001020	005102
758	001022	005220
759	001024	005220
760	001026	005332
761	001030	005332
762	001032	005644
763	001034	005644
764	001036	006516
765	001040	006516
766	001042	006716
767	001044	006716
768	001046	007144
769	001050	007144
770	001052	007546
771	001054	007546
772	001056	007772
773	001060	007772
774	001062	010324
775	001064	010324
776	001066	010530
777	001070	010530
778	001072	010750
779	001074	010750
780	001076	011142
781	001100	011142
782	001102	011334
783	001104	011334
784	001106	003110
785	001110	000026

TSTTBL: 0

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FT26

TLAST: .WORD TEND
.WORD 26

;CONTAINS # OF TESTS

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786      001600      . = 1600
787      ;PROGRAM START AND HOUSEKEEPING*****
788
789 001600 012706 000500 START: MOV #500, SP ;SET STACK POINTER
790 001604 013746 000006 MOV #6, -(SP) ;SAVE VECTORS
791 001610 013746 000004 MOV #4, -(SP)
792 001614 012737 001640 000004 MOV #15, #4 ;SET UP FOR TIMEOUT
793 001622 005037 000006 CLR #6 ;REFERENCE HARDWARE SWITCH REGISTER
794 001626 022777 177777 176714 CMP #1, #SWR
795 001634 001402 BEQ #25
796 001636 000404 BR #35
797 001640 022626 15: CMP (SP)+, (SP)+ ;ADJUST STACK
798 001642 012737 000176 000550 25: MOV #SWREG, SWR ;POINT TO SOFTWARE SWITCH REG
799 001644 012637 000004 35: MOV (SP)+, #4 ;RESTORE VECTORS
800 001654 012637 000006 MOV (SP)+, #6
801 001660 00127 CLR (PC)+ ;CLEAR CHAIN INDICATOR
802 001662 000000 CHNFLG: .WORD 0 ;CHAIN MODE INDICATOR
803 ;1/0 = CHAIN/NOT CHAIN MODE
804 001664 022737 003154 000042 CMP #SENDAD, #42 ;BRANCH IF LOADED VIA ACT11 CHAIN MODE
805 001672 001404 BEQ #505 ;BRANCH IF IN DUMP MODE
806 001674 005737 000042 TST #42
807 001700 001413 BEQ #525
808 001702 000406 BR #515
809 001704 012737 000176 000550 505: MOV #SWREG, SWR ;INVOKE SOFTWARE SWR
810 001712 012777 100000 176630 MOV #100000, #SWR ;WITH HALT ON ERROR SET
811 001720 005237 001662 515: INC CHNFLG ;SET CHNFLG = CHAIN MODE
812 001724 000137 002556 JMP TSCD ;GO TO CHAIN ADDRESS
813 001730
814 001730 122737 000006 000041 525: CMPB #6, #41 ;BRANCH IF LOADED VIA TMDP (DUMP MODE)
815 001736 001004 BNE #55 ;ADVISE USER TO REMOVE TMDP FROM UUT
816 001740 012704 016675 MOV #MSG69, R4
817 001744 004737 013442 JSR PC, TTOUT
818 001750 012704 014470 55: MOV #MSG3, R4 ;PRINT TITLE
819 001754 004737 013442 JSR PC, TTOUT ;DO NOT PRINT TITLE ON RESTART
820 001760 112737 000043 014470 MOVB #MSG3, R4
821 001766 012704 014623 STOB: MOV #MSG4, R4 ;REQUEST REGISTER ADDRESS
822 001772 004737 013442 JSR PC, TTOUT
823 001776 013703 000572 MOV REGS, R3 ;PRINT CURRENT ADDRESS
824 002002 004737 013572 JSR PC, OCTP ;SET ADDRESS SAVE LOC
825 002006 012705 000572 MOV #REGS, R5 ;SET SIZE OF RESPONSE
826 002012 012701 000007 MOV #7, R1 ;SET UPPER LIMIT
827 002016 012702 176400 MOV #176400, R2 ;SET LOWER LIMIT
828 002022 012703 172300 MOV #172300, R3 ;GO GET RESPONSE
829 002026 004737 013120 JSR PC, TTR
830 002032 012704 014646 MOV #MSG5, R4 ;REQUEST VECTOR
831 002036 004737 013442 JSR PC, TTOUT
832 002042 013703 000570 MOV VECT, R3 ;PRINT CURRENT VECTOR
833 002046 004737 013572 JSR PC, OCTP ;SET ADDRESS SAVE LOC
834 002052 012705 000570 MOV #VECT, R5 ;SET SIZE OF RESPONSE
835 002056 012701 000004 MOV #4, R1 ;SET UPPER LIMIT
836 002062 012702 000224 MOV #224, R2 ;SET LOWER LIMIT
837 002066 012703 000150 MOV #150, R3 ;GO GET RESPONSE
838 002072 004737 013120 JSR PC, TTR ;GET VECTOR
839 002076 013700 000570 MOV VECT, R0 ;LOAD INTERRUPT ADDRESS IN VECTOR
840 002102 012720 012646 MOV #MTINT, (R0)+ ;LOAD PRIORITY
841 002106 012710 000340 MOV #340, (R0)

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8742	002112	013700	000572		MOV	REGS, R0	: GET START OF REGS	
8743	002116	012701	000017		MOV	#17, R1	: SET NUMBER OF REGS	
8744	002120	013701	000510		MOV	#C1, R2	: GET START OF TABLE	
8745	002124	013701	000002		STO:	MOV	RO, (R2)+	: BUILD TABLE
8746	002128	013701	000002		ADD	#2, RO	: BUMP ADDRESS	
8747	002132	013701	000002		DEC	R1	: SEE IF DONE	
8748	002136	001373			BNE	STO	: IF NOT: BR	
8749	002140	012702	000600		MOV	#T08, R2		
8750	002144	012700	000054		MOV	#54, R0		
8751	002148	013022			ST1:	CLR	(R2)+	: CLEAR FLAGS + COUNTERS
8752	002152	005300			DEC	R0		
8753	002154	001375			BNE	ST1		
8754	002156	012737	000001	000722	MOV	#1, RHTF	: SET ADDRESS TEST FLAG	
8755	002164	000137	002750		JMP	TSRH	: GO DO INITIAL ADDRESS TEST PASS	
8756	002170	012704	014725		ST1A:	MOV	#MSG10, R4	
8757	002174	004737	013442		JSR	PC, TTOUT	: REQUEST DRIVE NUMBER	
8758	002178	013703	000612		MOV	DRVN, R3	: GET CURRENT DRIVE #	
8759	002182	004737	013572		JSR	PC, OCTP	: AND TYPE IT	
8760	002186	012705	000612		MOV	#DRVN, R5	: SET ADDRESS OF DRIVE NUMBER SAVE	
8761	002190	012701	000002		MOV	#2, R1	: SET SIZE OF RESPONSE	
8762	002194	012702	000007		MOV	#7, R2	: SET UPPER LIMIT	
8763	002198	012703	000000		MOV	#0, R3	: SET LOWER LIMIT	
8764	002202	004737	013120		JSR	PC, TTR	: GO GET RESPONSE	
8765	002206	012777	000040	176256	MOV	#40, ACS	: SET INIT	
8766	002210	053777	000612	176250	BIS	DRVN, ACS	: SET DRIVE NUMBER	
8767	002214	005777	176234		TST	AC1	: ACCESS DRIVE	
8768	002218	032777	010000	176236	BIT	#10000, ACS	: SEE IF NED	
8769	002222	001405			BEQ	ST2	: IF NOT: BR	
8770	002226	012704	015657		MOV	#MSG41, R4		
8771	002230	004737	013442		JSR	PC, TTOUT	: PRINT NOT AVAIL	
8772	002234	000735			BR	ST1A	: REDO DRIVE REQUEST	
8773	002238	012704	014745		ST2:	MOV	#MSG11, R4	
8774	002242	004737	013442		JSR	PC, TTOUT	: REQUEST SLAVE NUMBER	
8775	002246	013703	000614		MOV	SLVN, R3	: GET CURRENT SLAVE #	
8776	002250	004737	013572		JSR	PC, OCTP	: AND TYPE IT	
8777	002254	012705	000614		MOV	#SLVN, R5	: SET ADDRESS OF SLAVE SAVE	
8778	002258	012701	000002		MOV	#2, R1	: SET SIZE OF RESPONSE	
8779	002262	012702	000007		MOV	#7, R2	: SET UPPER LIMIT	
8780	002266	012703	000000		MOV	#0, R3	: SET LOWER LIMIT	
8781	002270	004737	013120		JSR	PC, TTR	: GO GET RESPONSE	
8782	002274	012777	000040	176150	MOV	#40, ACS	: INIT	
8783	002278	053777	000612	176142	BIS	DRVN, ACS	: SET DRIVE NUMBER	
8784	002282	013777	000614	176156	MOV	SLVN, ATC	: LOAD SLAVE NUMBER	
8785	002286	032777	002000	176144	BIT	#2000, ATC	: SEE IF SLAVE PRESENT	
8786	002290	001005			BNE	ST3	: IF SO: BR	
8787	002294	012704	015700		MOV	#MSG42, R4		
8788	002298	004737	013442		JSR	PC, TTOUT	: PRINT NON-EXIST SLAVE	
8789	002302	000734			BR	ST2	: REDO SLAVE REQUEST	
8790	002306	012704	015721		ST3:	MOV	#MSG43, R4	
8791	002310	004737	013442		JSR	PC, TTOUT	: PRINT SERIAL NUMBER TAG	
8792	002314	017703	176116		MOV	ASH, R3		
8793	002318	004737	014120		JSR	PC, SMT	: PRINT SERIAL NUMBER	
8794	002322	005037	000604		CLR	RH17F	: SET RH INDICATOR = RH11	
8795	002326	013746	000004		MOV	#4, -(SP)	: SAVE ERROR TRAP VECTORS	
8796	002330	013746	000006		MOV	#6, -(SP)	: AND PRIORITY	
8797	002334	012737	002466	000004	MOV	#15, #4	: SET TIME OUT TRAP TO 15 BELOW	

898	002450	005037	000006		CLR	#6	
899	002451	005777	176064		TST	#6	: REFERENCE BAE REGISTER
900	002460	012737	000001	000604	MOV	#1, RH17F	: SET FLAG = RH70
901	002466	012637	000006	18:	MOV	(SP)+, #6	: RESTORE ERROR TRAP
902	002472	012637	000004		MOV	(SP)+, #4	
903	002476	012704	016152		MOV	#5, G62, R4	: GET REQUEST
904	002502	004737	013442		JSR	PC, TOUT	: REQUEST RH11 ONLY RESPONSE
905	002506	013703	000726		MOV	RHOF, R3	: GET CURRENT FLAG SETTING
906	002512	004737	013572		JSR	PC, OCTP	: AND TYPE IT
907	002516	012705	000726		MOV	#RHOF, RS	: SET FLAG ADDRESS
908	002522	012701	000002		MOV	#2, R1	: SET SIZE OF RESPONSE
909	002526	012702	000001		MOV	#1, R2	: SET UPPER LIMIT
910	002532	012703	000000		MOV	#0, R3	: SET LOWER LIMIT
911	002536	004737	013120		JSR	PC, TTR	: GO GET RESPONSE
912							
913				: START 210			
914	002542	012706	000500	ST4:	MOV	#500, SP	: SET STACK PTR
915	002546	005037	000730		CLR	PCNTR	: CLEAR PASS COUNTER
916	002552	004737	014222		JSR	PC, GTSWR	: GET SWITCHES

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;TEST SCHEDULAR*****
917 002556 052777 000100 175766 TSCD: BIS #100, @TKS ;SET KEYBOARD IE BIT
918 005037 000704 CLR STFLG ;CLEAR SINGLE TEST FLAG
919 017700 175754 MOV @SWR, RO
920 042700 177740 BIC #177740, RO
921 001125 001125 BNE STSCD ;GO SELECT SINGLE TEST
922 005737 001662 TST CHNFLG ;;BRANCH IF NOT IN CHAIN MODE
923 001457 001457 BEQ TSCDA
924 012737 177777 000612 MOV #-1, DRVN ;:INITIALIZE DRIVE #
925 012737 177777 000614 NXTDRV: MOV #-1, SLVN ;:INITIALIZE SLAVE #
926 012777 000040 175666 IS: MOV #40, @CS ;:INIT CONTROLLER
927 000037 000010 000612 INC DRVN ;:STEP DRIVE #
928 000037 000010 000612 CMP #10, DRVN ;:EXIT IF ALL DRIVES TESTED
929 001006 001006 BEQ $OOOE ;:FOR AVAILABILITY
930 013077 000612 175644 MOV DRVN, @CS ;:LOAD DRIVE #
931 000077 175630 TST @C1 ;:ACCESS DRIVE
932 000077 010000 175632 BIT #10000, @CS ;:BRANCH IF DRIVE NON EXISTANT
933 001356 001356 BNE IS ;:(MED = 1)
934 002237 000614 NXTSLV: INC SLVN ;:STEP SLAVE # AND BRANCH
935 001011 001011 BNE IS ;:IF NOT SLAVE 0
936 005737 000612 TST DRVN ;:BRANCH IF NOT DRIVE # 0
937 001006 001006 BNE IS
938 122737 000006 000041 CMPB #6, @#41 ;;BRANCH IF NOT TMDP
939 001002 001002 BNE IS
940 005237 000614 INC SLVN ;:STEP TO SLAVE # 1
941 022737 000010 000614 IS: CMP #10, SLVN ;:BRANCH IF ALL SLAVES TESTED
942 001733 001733 BEQ NXTDRV ;:FOR AVAILABILITY
943 013777 000614 175604 MOV SLVN, @TC ;:LOAD SLAVE UNIT #
944 032777 002000 175572 BIT #2000, @T ;:BRANCH IF SLAVE NOT
945 001751 001751 BEQ NXTSLV ;:PRESENT (SPR = 0)
946 000240 000240 TSCDA: NOP
947 012737 000752 000706 TSDR: MOV #TSTTBL, LTADD
948 062737 000706 000706 TSCD0: ADD #4, LTADD
949 013737 000706 000676 TSCD1: MOV LTADD, ITRLP
950 062737 000706 000676 ADD #2, ITRLP ;SET ITERATION ADDRESS
951 000037 000037 CLR STACK
952 000037 000037 CLR EP
953 003010 003010 CLR HDRFL ;CLEAR PRINT HEADER FLAG
954 017700 175666 MOV @LTADD, RO ;SET POINTER TO TEST
955 000110 000110 JMP (RO) ;GO TO TEST
956 032777 002000 175520 TSCD2: BIT #2000, @SWR ;SEE IF HALT ON TEST
957 001401 001401 BEQ TSCD3 ;IF NOT: BR
958 000000 000000 HALT
959 003034 003034 TSCD3: TST STFLG ;SE IF SINGLE TEST
960 001746 001746 BEQ TSCD0 ;IF NOT: BR
961 017700 175502 MOV @SWR, RO
962 042700 177740 BIC #177740, RO ;BRANCH IF ALL TESTS SELECTED
963 001641 001641 BEQ TSCD
964 012737 000001 000704 STSCD: MOV #1, STFLG ;SET SINGLE TEST FLAG
965 023700 001110 CMP TLAST, RO ;SEE IF EXCEEDED TESTS
966 002410 002410 BLT TEND ;IF SO: BR
967 006300 006300 ASL RO
968 006100 006100 ROL RO ;SET TABLE MODIFIER
969 012737 000752 000706 MOV #TSTTBL, LTADD
970 060037 000706 ADD RO, LTADD ;SET TEST POINTER

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973	003106	000726			BR	TSCD1	
974	003110	005737	001662	TEND:	TST	CHNFLG	;BRANCH IF IN CHAIN MODE
975	003114	001265			BNE	NXTSLV	
976	003116	012704	014661	SDONE:	MOV	#MSG6,R4	
977	003122	004737	013442		JSR	PC,TIOUT	;PRINT END OF PASS
978	003126	013703	000730		MOV	PCNTR,R3	
979	003132	004737	013572		JSR	PC,OC1P	;PRINT PASS NUMBER
980	003136	005000			CLR	RO	
981	003140	005300		IS:	DEC	RO	
982	003142	001376			BNE	IS	
983	003144	013700	000042		MOV	#42,RO	;GET ACT11 RETURN ADDRESS
984	003150	001405			BEQ	HERE	;BRANCH IF NOT ACT11
985	003152	000005			RESET		
986	003154	004710		SENDAD:	JSR	PC,(RO)	
987	003156	000240			NOP		
988	003160	000240			NOP		
989	003162	000240			NOP		
990	003164	000240		HERE:	NOP		
991	003166	005737	001662		TST	CHNFLG	;BRANCH IF IN CHAIN MODE
992	003172	001005			BNE	TENDX	
993	003174	032777	010000 175346		BIT	#10000,#SWR	;SEE IF HALT ON PASS
994	003202	001001			BNE	TENDX	;IF NOT: BR
995	003204	000000			HALT		
996	003206	005237	000730	TENDX:	INC	PCNTR	;BUMP PASS COUNTER
997	003212	000137	002556		JMP	TSCD	;RESTART


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998
999 ;RM ADDRESSING TEST*****
1000
1001 003216 012737 016771 000610 FT1: MOV #MSFT1,EMADR ;SET HEADER
1002 003224 012777 013004 175342 MOV #TRAP,TRP ;SET TRAP HANDLER ADDRESS
1003 003232 012777 000340 175336 MOV #340,TRP2
1004 003240 012700 000016 MOV #16,R0 ;SET NUMBER OF REGISTERS
1005 003244 013701 000510 MOV C1,R1 ;GET FIRST ADDRESS (C1)
1006 003250 005711 FT1A: TST (R1) ;REFERENCE REGISTER
1007 003254 000240 NOP ;IF ADDRESS IS BAD, BUS TRAP WILL OCCUR
1008 003254 005300 FT1B: DEC R0 ;SEE IF DONE ALL
1009 003256 001403 BEQ FT1X ;IF SO: BR
1010 003260 062701 000002 ADD #2,R1 ;BUMP ADDRESS POINTER
1011 003264 000771 BR FT1A ;CONTINUE
1012 003266 012777 000006 175300 FT1X: MOV #6,TRP ;RESET TRAP CATCHER
1013 003274 005737 000722 TST R0TF ;SEE IF INITIAL ADDRESS TEST PASS
1014 003300 001404 BEQ FT1XX ;IF NOT: BR
1015 003302 005037 000722 CLR R0TF ;CLEAR FLAG
1016 003306 000137 002170 JMP ST1A ;RETURN
1017 003312 000137 003022 FT1XX: JMP TSCD2 ;RETURN TO SCHEDULAR

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1018
1019 ;R4 REGISTER BITS READ/WRITE*****
1020
1021 003316 012737 017016 000610 FT2: MOV #MSG2, ERADR ;SET TEST HEADER
1022 003324 012701 177777 MOV #1, R1 ;SET ALL ONES PATTERN
1023 003330 004737 012620 FT2A: JSR PC, INIT1 ;GO INIT
1024 003334 013700 000512 MOV MC, R0 ;GET ADDRESS OF WORD COUNT
1025 003340 010102 MOV R1, R2 ;SET EXPT REGISTER BIT PATTERN
1026 003342 010110 MOV R1, (R0) ;LOAD PATTERN
1027 003344 021002 CMP (R0), R2 ;SEE IF EXPT=RCVD
1028 003346 001410 BEQ FT2B ;IF SO: BR
1029 003348 012737 015205 000650 MOV #MSG25, ERADR ;SET CODE
1030 003350 012737 003330 000674 MOV #FT2A, $COLP ;SET SCOPE
1031 003352 004737 003504 JSR PC, FT2ER ;GO DO ERROR
1032 003354 013700 000514 FT2B: MOV BA, R0 ;GET ADDRESS OF BUS ADDRESS
1033 003356 010102 MOV R1, R2
1034 003358 042702 000001 BIC #1, R2 ;SET EXPT PATTERN
1035 003360 010110 MOV R1, (R0) ;LOAD PATTERN
1036 003362 020210 CMP R2, (R0) ;SEE IF EXPT=RCVD
1037 003364 001410 BEQ FT2C ;IF SO: BR
1038 003366 012737 015213 000650 MOV #MSG26, ERADR ;SET ERROR CODE
1039 003368 012737 003370 000674 MOV #FT2B, $COLP ;SET SCOPE ADDRESS
1040 003370 004737 003504 JSR PC, FT2ER ;GO DO ERROR
1041 003372 013700 000532 FT2C: MOV DB, R0 ;GET ADDRESS OF DATA BUFFER
1042 003374 010102 MOV R1, R2
1043 003376 010110 MOV R1, (R0) ;LOAD PATTERN
1044 003378 012703 004000 MOV #4000, R3
1045 003380 005303 FT2D: DEC R3 ;DELAY
1046 003382 001376 BNE FT2D
1047 003384 020210 CMP R2, (R0) ;SEE IF EXPT=RCVD
1048 003386 001410 BEQ FT2E ;IF SO: BR
1049 003388 012737 015221 000650 MOV #MSG27, ERADR ;SET ERROR CODE
1050 003390 012737 003430 000674 MOV #FT2C, $COLP ;SET SCOPE ADDRESS
1051 003392 004737 003504 JSR PC, FT2ER ;GO DO ERROR
1052 003394 005701 FT2E: TST R1 ;SEE IF DONE RESET
1053 003396 001453 BEQ FT2X ;IF SO: BR
1054 003398 005001 CLR R1 ;SET ZERO PATTERN
1055 003400 000712 BR FT2A ;DO ZERO BITS
1056 003402 000240 FT2ER: NOP
1057 003404 032777 020000 175034 BIT #20000, $SWR ;SEE IF PRINT ERROR
1058 003406 001034 BNE FT2ERB ;IF NOT: BR
1059 003408 005737 000606 TST HDRFL ;SEE IF DONE HEADER
1060 003410 001004 BNE FT2ERA ;IF SO: BR
1061 003412 013704 000610 MOV ERADR, R4
1062 003414 004737 013442 JSR PC, TTOUT ;DO HEADER
1063 003416 012737 000001 000606 FT2ERA: MOV #1, HDRFL ;SET FLAG
1064 003418 013704 000650 MOV ERADR, R4
1065 003420 004737 013442 JSR PC, TTOUT ;PRINT ERROR CODE
1066 003422 012704 015151 MOV #MSG22, R4
1067 003424 004737 013442 JSR PC, TTOUT ;PRINT EXPT TAG
1068 003426 010103 MOV R1, R3
1069 003428 004737 013560 JSR PC, OCTPE ;PRINT EXPT
1070 003430 012704 015161 MOV #MSG23, R4
1071 003432 004737 013442 JSR PC, TTOUT ;PRINT RCVD TAG
1072 003434 011003 MOV (R0), R3
1073 003436 004737 013560 JSR PC, OCTPE ;PRINT RCVD
    
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1074	003606	005777	174736	FT2ERB:	TST	JSR		;SEE IF HALT ON ERROR
1075	003612	100001			BPL	FT2ERC		;IF NOT: BR
1076	003614	000000			HALT			
1077	003616	004737	012512	FT2ERC:	JSR	PC,SCOPE		;GO SEE IF SCOPE ON ERROR
1078	003622	000240			NOP			
1079	003624	000207			RTS	PC		;IF NO SCOPE: CONTINUE TEST
1080	003626	000240		FT2X:	NOP			
1081	003630	004737	012546		JSR	PC,ITER		;GO SEE IF ITERATIONS
1082	003634	000137	003022		JMP	TSC02		;RETURN TO SCHEDULAR

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1083
1084 ;RH INITIALIZE TEST*****
1085
1086 003640 012737 017053 000610 FT3: MOV #MSGT3,EMADDR ;SET TEST HEADER
1087 003646 012737 003640 000674 MOV #FT3,SCOLP
1088 01654 044737 012620 JSR PC,INIT1 ;GO INIT
1089 0160 02777 020000 174632 BIS #20000,ACS ;FORCE UPE =1
1090 016 C 0240 NOP
1091 01670 044737 012620 JSR PC,INIT1 ;GO INIT
1092 01674 005777 174610 TST #01 ;SEE IF SC IS RESET
1093 013700 100005 BPL FT3A ;IF SO: BR
1094 003702 012737 015257 000650 MOV #MSG29,ERADD ;SET ERROR CODE
1095 003710 004737 003774 JSR PC,FT3ER ;GO DO ERROR
1096 003714 032777 040000 174566 FT3A: BIT #4000,#01 ;SEE IF TIRE IS RESET
1097 003722 001405 BEQ FT3B ;IF SO: BR
1098 003724 012737 015306 000650 MOV #MSG30,ERADD ;SET ERROR CODE.
1099 003732 004737 003774 JSR PC,FT3ER ;GO DO ERROR
1100 003736 017701 174556 FT3B: MOV #CS,R1 ;GET CS2
1101 003742 042701 000307 BIC #307,R1 ;MARK IR/OR
1102 003746 005701 TST R1 ;SEE IF RESET
1103 003750 001405 BEQ FT3X ;IF SO: BR
1104 003752 012737 015336 000650 MOV #MSG31,ERADD ;SET ERROR CODE
1105 003760 004737 003774 JSR PC,FT3ER ;GO DO ERROR
1106 003764 004737 012546 FT3X: JSR PC,ITER ;GO SEE IF ITERATION
1107 003770 000137 003022 JMP TSCD2 ;RETURN TO SCHEDULAR
1108
1109 ;ERROR REPORT SUBROUTINE
1110 003774 000240 FT3ER: NOP
1111 003776 032777 020000 174544 BIT #20000,#SWR ;SEE IF PRINT ERROR
1112 004004 001015 BNE 2$ ;IF NOT: BR
1113 004006 005737 000606 TST HDRFL ;SEE IF DONE HEADER
1114 004012 001006 BNE 1$ ;IF SO: BR
1115 004014 013704 000610 MOV EMADDR,R4
1116 004020 004737 013442 JSR PC,TTOUT ;PRINT HEADER
1117 004024 005237 000606 INC HDRFL
1118 004030 013704 000650 1$: MOV ERADD,R4
1119 004034 004737 013442 JSR PC,TTOUT ;PRINT ERROR CODE
1120 004040 005777 174504 2$: TST #SWR ;SEE IF HALT ON ERROR
1121 004044 100001 BPL 3$ ;IF NOT: BR
1122 004046 000000 HALT
1123 004050 000240 3$: NOP
1124 004052 004737 012512 JSR PC,SCOPE ;GO SEE IF SCOPE
1125 004056 000207 RTS PC ;IF NOT: BR

```

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1126
1127
1128 ;RH11 SILO TEST 1: EPMTY SILO READ*****
1129 004060 005737 000604 FT4: TST RH17F
1130 004064 001141 BNE FTSX ; IF RH70: BR
1131 004066 012737 017105 000610 MOV #MSGF4,EMADDR ; SET TEST TEST HEADER
1132 004074 012777 000040 174416 MOV #40,2CS ; INIT
1133 004102 017700 174424 MOV 208,R0 ; READ DB
1134 004106 005777 174406 TST 2CS ; SEE IF DLT IS SET
1135 004112 100013 BPL FT4ER ; IF NOT: BR
1136 004114 005777 174370 TST 2C1 ; SEE IF SC IS SET
1137 004120 100014 BPL FT4ERA ; IF NOT: BR
1138 004122 032777 040000 174360 BIT #40,0,2C1 ; SEE IF TFE IS SET
1139 004130 001414 BEQ FT4ERB ; IF NOT: BR
1140 004132 004737 012546 FT4X: JSR PC,ITER ; GO SEE IF ITERATION
1141 004136 000137 003022 JIP TSC02 ; RETURN TO SCHEDULAR
1142 004142 012737 015366 000650 FT4ER: MOV #MSG32,ERADD ; SET ERROR CODE
1143 004150 000407 BR FT4ER
1144 004152 012737 015404 000650 FT4ERA: MOV #MSG33,ERADD ; SET ERROR CODE
1145 004160 000403 BR FT4ERC
1146 004162 012737 015421 000650 FT4ERB: MOV #MSG34,ERADD ; SET ERROR CODE.
1147 004170 000240 FT4ERC: NOP
1148 004172 012737 004060 000674 MOV #FT4,SCOLP ; SET SCOPE ADDRESS
1149 004200 004737 003774 JSR PC,FT3ER ; GO PRINT ERROR
1150 004204 000752 BR FT4X

```

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1151
1152 ;RH11 SILO TEST 2: IR/OR CHECK*****
1153
1154 004206 005737 000604 FTS: TST RH17F ;SEE IF RH70
1155 004212 001066 BNE FTSX ;IF SO: BR
1156 004214 012737 017135 000610 MOV #MSG35, ERADD ;SET TEST HEADER
1157 004222 012737 004230 000674 MOV #FTSA, SCOLP ;SET SCOPE ADDRESS
1158 004230 004737 012620 FTSA: JSR PC, INITI ;GO INIT
1159 004234 032777 000100 174256 BIT #100, ACS ;SEE IF IR IS SET
1160 004242 001005 BNE FTSB ;IF SO: BR
1161 004244 012737 015437 000650 MOV #MSG35, ERADD ;SET ERROR CODE
1162 004252 004737 003774 JSR PC, FT3ER ;GO DO ERROR
1163 004256 032777 000200 174234 FTSB: BIT #200, ACS ;SEE IF OR IS RESET
1164 004264 001405 BEQ FTSX ;IF SO: BR
1165 004266 012737 015464 000650 MOV #MSG36, ERADD ;SET ERROR CODE
1166 004274 004737 003774 JSR PC, FT3ER ;GO DO ERROR
1167 004300 012777 000000 174224 FTSC: MOV #0, R0B ;LOAD ZERO INTO SILO
1168 004306 032777 000200 174204 BIT #200, ACS ;SEE THAT OR RESET
1169 004314 001405 BEQ FTSD ;IF IT DOES: BR
1170 004316 012737 015513 000650 MOV #MSG37, ERADD ;SET ERROR CODE
1171 004324 004737 003774 JSR PC, FT3ER ;GO DO ERROR
1172 004330 012777 177777 174174 FTSD: MOV #-1, R0B ;LOAD SILO WITH -1
1173 004336 012700 004000 MOV #4000, R0
1174 004342 032777 000200 174150 FTSE: BIT #200, ACS ;SEE IF OR IS SET
1175 004350 001007 BNE FTSX ;IF SO: BR
1176 004352 005300 DEC R0
1177 004354 001372 BNE FTSE ;AWAIT OR
1178 004356 012737 015513 000650 MOV #MSG37, ERADD ;SET ERROR CODE
1179 004364 004737 003774 JSR PC, FT3ER ;GO DO ERROR
1180 004370 004737 012546 FTSX: JSR PC, ITER ;GO SEE IF ITERATION
1181 004374 000137 003022 JMP TSCD2 ;RETURN TO SCHEDULAR
    
```

```

1182
1183
1184
1185 004400 005737 000604 FT6: TST RH17F
1186 004404 001052 BNE FT6X ; IF RH70: BR
1187 004406 012737 017165 000610 MOV #MSGT6,EMADDR ; SET TEST HEADER
1188 004414 012737 004422 000674 MOV #FT6A,SCOLP ; SET SCOPE ADDRESS
1189 004422 004737 012620 FT6A: JSR PC,INIT1 ; GO INIT
1190 004426 005000 CLR RO ; PRESET DATA
1191 004430 010077 174076 FT6B: MOV RO,208 ; LOAD SILO
1192 004434 005200 INC RO ; BUMP DATA
1193 004436 022700 000102 CMP #102,RO ; SEE IF FILLED ALL
1194 004442 001372 BNE FT6B ; IF NOT: BR
1195 004444 032777 000100 174046 BIT #100,208 ; SEE IF IR IS RESET.
1196 004452 001405 BEQ FT6C ; IF SO: BR
1197 004454 012737 015624 000650 MOV #MSG40,ERADD ; SET ERROR CODE
1198 004462 004737 003774 JSR PC,FT3ER ; GO DO ERROR
1199 004466 032777 000200 174024 FT6C: BIT #200,208 ; SEE IF OR IS SET
1200 004474 001005 BNE FT6D ; IF SO: BR
1201 004476 012737 015552 000650 MOV #MSG38,ERADD ; SET ERROR CODE
1202 004504 004737 003774 JSR PC,FT3ER ; GO DO ERROR
1203 004510 005000 FT6D: CLR RO ; PRESET DATA
1204 004512 017701 174014 FT6E: MOV 208,R1 ; READ SILO
1205 004516 020001 CMP RO,R1 ; SEE IF EXPT=RCVD
1206 004520 001010 BNE FT6DE ; IF NOT: BR
1207 004522 005200 INC RO ; BUMP DATA
1208 004524 022700 000102 CMP #102,RO ; SEE IF DONE ALL
1209 004530 001370 BNE FT6E ; IF NOT: BR
1210 004532 004737 012546 FT6X: JSR PC,ITER ; GO SEE IF ITERATION
1211 004536 000137 003022 JMP TSCD2 ; RETURN TO SCHEDULAR
1212
1213 004542 000240 FT6DE: NOP
1214 004544 032777 020000 173776 BIT #20000,25WR ; SEE IF PRINT ERROR
1215 004552 001032 BNE FT6DEB ; IF NOT: BR
1216 004554 005737 000606 TST HDIFL ; SEE IF DONE HEADER
1217 004560 013701 000610 MOV EMADDR,R1
1218 004564 004737 013442 JSR PC,TTOUT ; PRINT HEADER
1219 004570 005237 000606 INC HDIFL ; SET FLAG
1220 004574 012704 015604 FT6DEA: MOV #MSG39,R4
1221 004600 004737 013442 JSR PC,TTOUT ; PRINT SILO READ ERROR
1222 004604 012704 015151 MOV #MSG22,R4
1223 004610 004737 013442 JSR PC,TTOUT ; PRINT EXPT TAG
1224 004614 010003 MOV RO,R3
1225 004616 004737 013572 JSR PC,OCTP ; PRINT EXPT
1226 004622 012704 015161 MOV #MSG23,R4
1227 004626 004737 013442 JSR PC,TTOUT ; PRINT RCVD TAG
1228 004632 010103 MOV R1,R3
1229 004634 004737 013572 JSR PC,OCTP ; PRINT RCVD
1230 004640 005777 173704 FT6DEB: TST 25WR ; SEE IF HALT ON ERROR
1231 004644 100001 BPL FT6DEX ; IF NOT: BR
1232 004646 000000 HALT
1233 004650 000207 FT6DEX: RTS PC ; RETURN TO TEST

```

```

1234
1235
1236
1237 004652 005737 000604          FT7:  TST      RH17F
1238 004656 001021                    BNE      FT7X
1239 004660 012737 017215 000610    MOV      #MSG32_ERADDR ; IF RH70: BR
1240 004666 012737 004652 000674    MOV      #FT7_S0LP     ; SET TEST HEADER
1241 004674 004737 012620                    JSR      PC_INIT1     ; SET SCOPE ADDRESS
1242 004700 012700 000103                    MOV      #103,R0      ; GO INIT
1243 004704 010077 173622          FT7A:  MOV      R0,R0      ; SET SIZE OF SILO +1
1244 004710 005300                    DEC      R0           ; LOAD SILO
1245 004712 001374                    BNE      FT7A        ; SEE IF DONE
1246 004714 005777 173600          TST      @CS         ; IF NOT: BR
1247 004720 100004                    BPL      FT7ER       ; SEE IF DLT IS SET
1248 004722 004737 012546          FT7X:  JSR      PC_ITER  ; IF NOT: BR
1249 004726 000137 003022          JMP      TSC02       ; GO SEE IF ITERATION
1250 004732 012737 015366 000650    FT7ER: MOV      #MSG32_ERADD ; RETURN TO SCHEDULAR
1251 004740 004737 003774          JSR      PC_FT3ER   ; SET ERROR CODE
1252 004744 000766                    BR       FT7X        ; GO DO ERROR
    
```

;RH11 SILO TEST 4: SILO OVERFLOW*****


```

1253
1254
1255
1256 004746 005737 000604
1257 004752 001034
1258 004754 012737 017245 000610
1259 004762 012737 004746 000674
1260 004770 012777 000040 173522
1261 004776 012700 000004
1262 004784 010077 173524
1263 004792 000000
1264 004800 001374
1265 004808 000040 173500
1266 004816 012777 177777 173504
1267 004824 017701 173500
1268 004832 017701 173474
1269 004840 005777 173456
1270 004848 100011
1271 005044 004737 012546
1272 005052 005737 000726
1273 005054 001402
1274 005056 000137 003110
1275 005062 000137 003022
1276 005066 012737 015366 000650
1277 005074 004737 003774
1278 005100 000761

;RH11 SILO TEST 5: SILO RESET*****
FT10: TST RH17F
      BNE FT10X
      MOV #MSFT10,EMADDR
      MOV #FT10,SCOLP
      MOV #40,SCS
      MOV #4,RO
      MOV #0,ROB
FT10A: MOV #0,ROB
      DEC RO
      BNE FT10A
      BIS #40,SCS
      MOV #1,ROB
      MOV #0B,RI
      MOV #0B,RI
      TST SCS
      BPL FT10ER
FT10X: JSR PC,ITER
      TST RHOF
      BEQ FT10XX
      JMP TEND
FT10XX: JMP TSC02
FT10ER: MOV #MSG32,ERADD
      JSR PC,FT3ER
      BR FT10X

; IF RH70: BR
; SET TEST HEADER
; SET SCOPE ADDRESS
; INITIALIZE
; SET NUMBER OF SILO WRITER
; WRITE SILO
; SEE IF DONE
; IF NOT: BR
; INITIALIZE
; WRITE SILO
; READ SILO 1
; READ SILO 2
; SEE IF DLT IS SET
; IF NOT: BR
; GO SEE IF ITERATION
; SEE IF RH11 ONLY
; IF NOT: BR
; ELSE GO TO END
; RETURN TO SCHEDULAR
; SET ERROR CODE
; GO DO ERROR

```

```

1279                                     ;NOP TEST*****
1280
1281 005102 000240                                     FT11:  NOP
1282 005104 012737 005102 000674  MOV      #FT11,SCOLP      ;SET SCOPE ADDRESS
1283 005112 004737 012620  JSR      PC,INITI
1284 005116 012737 000300 000716  MOV      #300,LUDES      ;SET TC= ALL NRZ,NORM,ODD
1285 005124 012737 177777 000620  MOV      #-1,FCNT        ;SET FC= ALL OVER
1286 005132 012737 177777 000622  MOV      #-1,WCNT        ;SET WC= ALL OVER
1287 005140 012737 177777 000616  MOV      #-1,BADDR       ;SET BA= ALL OVER
1288 005146 012737 000001 000636  MOV      #1,RODYX        ;SET DELAY
1289 005154 012737 000001 000640  MOV      #1,OPDYX        ;SET OP DELAY
1290 005162 012737 000001 000710  MOV      #1,FUN          ;SET NOP FUNCTIONS CODE
1291 005170 004737 011546  JSR      PC,EXEC         ;GO EXECUTE COMMAND
1292 005174 000240  NOP
1293 005176 012737 017276 000610  MOV      #MSFT11,EMADDR
1294 005204 004737 011776  JSR      PC,ERCHK        ;GO CHECK REGISTER
1295 005210 004737 012546  JSR      PC,ITER         ;GO SEE IF ITERATIONS
1296 005214 000137 003022  JMP      TSC02          ;RETURN TO SCHEDULAR

```

```

1297                                     ;REWIND TEST*****
1298
1299 005220 000240 FT12: NOP
1300 012737 005220 000674 MOV #FT12, SCOLP
1301 012737 011546 JSR PC, INITI ;GO INITIALIZE
1302 012777 001700 173300 BIS #1700, ATC ;SET TO NRZ, NORMAL
1303 012737 177760 000620 MOV #-20, FCNT ;SET FC=20
1304 012737 177770 000622 MOV #-10, WCNT ;SET WC=10
1305 012737 020112 000616 MOV #DATA, BADDR ;SET WRITE BUFFER
1306 012737 000007 000710 MOV #Z, EUN ;SET R WIND OP CODE
1307 004737 011546 JSR PC, EXEC ;GO EXECUTE COMMAND
1308
1309 012777 020000 173214 FT12A: BIT #20000, AOS
1310 001374 BNE FT12B ;AWAIT PIP
1311 012737 017316 000610 MOV #MSFT12, EMADDR
1312 005316 004737 011776 JSR PC, ERCHK ;GO CHECK FOR ERROR
1313 005322 004737 012546 JSR PC, ITER ;GO SEE IF ITERATION
1314 005326 000137 003022 JMP TSCD2 ;RETURN TO SCHEDULAR
1315
    
```

```

;WRITE/READ TEST*****
1316
1317
1318 005332 000240 FT13: NOP
1319 012737 000001 000636 MOV #1,ROYDX
1320 012737 000001 000640 MOV #1,OPYDX
1321 012737 000100 000624 MOV #100,RCNT ;SET RECORD COUNT
1322 012737 017341 000610 MOV #MSG13,EMADDR ;SET TEST HEADER
1323 012737 000001 000720 MOV #1,PATRN
1324 004737 012000 ;SET UP ALL ONES DATA PATTERN
1325 012737 001700 000716 MOV #1700,UDES ;SET TO 800 BPI NORMAL
1326 004737 011700 FT13A: JSR PC,PCND ;GO REWIND
1327 012737 177000 000620 MOV #200,FCNT ;SET FC
1328 012737 177000 000022 MOV #100,WCNT ;SET WC
1329 012737 000000 000016 MOV #DATA,BADDR ;SET BA
1330 012737 000000 000010 MOV #61,FUN ;SET WRITE OP-CODE
1331 000000 000000 000626 MOV #MSG12,ERRP
1332 000000 000000 000006 FT13B: JSR PC,EXEC ;GO EXECUTE COMMAND
1333 000000 000000 000074 CLR SCOLP ;NO SCOPE LOOP
1334 000000 000000 011776 JSR PC,ERCHK ;GO CHECK ERROR
1335 000000 000000 000624 DEC RCNT ;SEE IF DONE ALL
1336 000000 000000 000007 BNE FT13B ;IF NOT: BR
1337 000000 000000 000100 000624 MOV #100,RCNT ;SET RECORD COUNT
1338 012737 001624 000616 MOV #DATA,BADDR
1339 012737 000000 000616 ADD #50,BADDR ;SET BA
1340 012737 000000 000710 MOV #77,FUN ;SET READ REVERSE OP-CODE
1341 012737 015003 000626 MOV #MSG13,ERRP
1342 004737 011546 FT13C: JSR PC,EXEC ;GO EXECUTE COMMAND
1343 004737 011776 JSR PC,ERCHK ;GO CHECK ERROR
1344 005337 000624 DEC RCNT ;SEE IF READ ALL
1345 001371 000000 000007 BNE FT13C ;IF NOT: BR
1346 012737 000000 000616 SUB #200,BADDR ;SET BA
1347 012737 000000 000710 MOV #71,FUN ;SET READ FORWARD OP-CODE
1348 012737 015003 000626 MOV #MSG14,ERRP
1349 012737 000100 000624 MOV #100,RCNT ;SET RECORD COUNT
1350 004737 011546 FT13D: JSR PC,EXEC ;GO EXECUTE COMMAND
1351 004737 011776 JSR PC,ERCHK ;GO CHECK ERRORS
1352 005337 000624 DEC RCNT ;SEE IF DONE ALL
1353 001371 000000 000007 BNE FT13D ;IF NOT: BR
1354 032737 002000 000716 BIT #200,UDES ;SEE IF DONE PE
1355 001007 000000 000007 BNE FT13X ;IF SO: BR
1356 012737 002300 000716 MOV #2300,UDES ;SET PE MODE
1357 012737 000100 000624 MOV #100,RCNT ;RESET RECORD COUNT
1358 005636 000662 BR FT13A ;GO DO NEXT DENSITY
1359 005640 000137 003022 FT13X: JMP TSCD2 ;RETURN TO SCHEDULAR

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```

1360 ;SPACE TEST*****
1361
1362 005644 000240 FT14: NOP
1363 005646 012737 017370 000610 MOV #MSFT14,EMADDR ;SET TEST HEADER
1364 005654 012737 001700 000716 MOV #1700,UDES ;SET NRZ NORMAL
1365 005662 004737 011700 FT14A1: JSR PC,REWD ;GO INITIALIZE
1366 005666 012737 000100 000624 MOV #100,RCNT ;SET NUMBER OF RECORDER
1367 005674 012737 177777 020112 MOV #-1,WDATA ;SET DATA PATTERN
1368 005702 012737 177700 011620 MOV #-100,FCNT ;PRESET FRAME CNT
1369 005710 012737 177740 000622 MOV #-40,WCNT ;PRESET WORD CNT
1370 005716 004737 012620 FT14A: JSR PC,INIT1 ;GO REWIND
1371 005722 012737 001000 000640 MOV #1000,OPDYX
1372 005730 012737 040000 000636 MOV #4000,RODYX
1373 005736 012737 000061 000710 MOV #61,FUN ;SET WRITE OP-CODE
1374 005744 012737 102300 000660 MOV #102300,STMSK ;MASK DATA RELATED ERRORS
1375 005752 052777 000010 172540 BIS #10,STS ;INHIBIT BUS ADDRESS INCREMENT
1376 005760 004737 011546 JSR PC,EXEC ;GO EXECUTE COMMAND
1377 005764 012737 016043 000626 MOV #MSG46,ERRP ;SET ERROR CODE
1378 005772 004737 011776 JSR PC,ERRCHK ;GO CHECK ERROR
1379 005776 005737 000712 TST SERFL ;SEE IF ERROR
1380 005002 001402 BEQ FT14A2 ;IF NOT: BR
1381 005004 000137 JMP FT14X ;ELSE EXIT
1382 005010 005337 000620 FT14A2: DEC FCNT ;BUMP FC
1383 005014 005737 000001 000620 BIT #1,FCNT ;SEE IF SHOULD BUMP WC
1384 005022 001403 BEQ FT14A3 ;IF NOT: BR
1385 005024 162737 000001 000622 SUB #1,WCNT ;BUMP WC
1386 005032 005737 000624 FT14A3: DEC RCNT ;SEE IF DONE ALL
1387 005033 001227 BNE FT14A ;WRITE ALL RECORDS
1388 005035 012737 000100 000632 MOV #100,RRO ;PRESET RECORD POSITION
1389 005036 012737 000176 000634 MOV #176,RFD
1390 005034 012737 177701 000642 MOV #-77,SCNT ;SET SPACE AMOUNT
1391 005032 012737 000033 000710 FT14B: MOV #33,FUN ;SET OP-CODE SPACE REVERSE
1392 005030 004737 011546 JSR PC,EXEC ;GO EXECUTE COMMAND
1393 006074 012737 016114 000626 MOV #MSG48,ERRP ;SET ERROR CODE
1394 006102 004737 011776 JSR PC,ERRCHK ;GO CHECK ERROR
1395 006106 005737 000712 TST SERFL ;SEE IF ERROR
1396 006112 001166 BNE FT14X ;IF SO: BR
1397 006114 004737 006210 JSR PC,FT14RR ;GO READ REVERSE + CHECK DATA
1398 006120 000240 NOP
1399 006122 012737 000031 000710 MOV #31,FUN ;SET SPACE FORWARD OP-CODE
1400 006130 005237 000642 INC SCNT ;SET SPACE AMOUNT
1401 006134 001535 BEQ FT14X ;IF DONE: BR
1402 006136 004737 011546 JSR PC,EXEC ;GO EXECUTE COMMAND
1403 006142 012737 016067 000626 MOV #MSG47,ERRP ;SET ERROR CODE
1404 006150 004737 011776 JSR PC,ERRCHK ;GO CHECK ERROR
1405 006154 005737 000712 TST SERFL ;SEE IF ERROR FLAG
1406 006160 001143 BNE FT14X ;IF NO: BR
1407 006162 004737 006252 JSR PC,FT14RF ;GO READ FORWARD FOR POSITION CHECK
1408 006166 000240 NOP
1409 006170 005237 000642 INC SCNT ;DECREMENT SPACE AMOUNT
1410 006174 001535 BEQ FT14X ;IF DONE: BR
1411 006176 005237 000632 INC RRO ;BUMP DATA EXPT
1412 006202 005337 000634 DEC RFD ;BUMP DATA EXPT
1413 006206 000725 BR FT14B
1414 006210 000240 FT14RR: NOP
1415 006212 012737 021624 000616 MOV #RDATA,BADDR ;SET BA
    
```

1416	006220	012737	000077	000710	MOV	#77,FUN	;SET READ REVERSE OP-CODE
1417	006223	004737	011546		JSR	PC,EXEC	;GO EXECUTE COMMAND
1418	006232	000240			NOP		
1419	006234	013705	000632		MOV	RFD,R5	
1420	006240	020577	172252		CHP	RS,ZFC	;SEE IF CORRECT RECORD
1421	006244	001020			BNE	FT14RER	;IF NOT: BR
1422	006246	000137	006300		JMP	FT14EC	;GO CLEAR RH11 ERROR BIT
1423	006250	000240			NOP		
1424	006253	012737	000071	000710	MOV	#71,FUN	;SET READ FORWARD OP-CODE
1425	006256	004737	011546		JSR	PC,EXEC	;GO EXECUTE COMMAND
1426	006266	013705	000634		MOV	RFD,R5	
1427	006270	020577	172220		CHP	RS,ZFC	;SEE IF CORRECT RECORD
1428	006276	001003			BNE	FT14RER	;IF NOT: BR
1429	006280	004737	012620		JSR	PC,INIT1	;CLEAR RH
1430	006282	000207			RTS	PC	;RETURN
1431	006285	000240			NOP		
1432	006310	032777	020000	172232	BIT	#20000,JSWR	;SEE IF PRINT INHIBITED
1433	006316	001060			BNE	FT14R3	;IF SO: BR
1434	006320	012704	017370		MOV	#MSG14,R4	
1435	006327	004737	013442		JSR	PC,TTOUT	;PRINT HEADER
1436	006330	012704	014703		MOV	#MSG9,R4	
1437	006334	004737	013442		JSR	PC,TTOUT	;PRINT ERROR TYPE
1438	006340	012704	015136		MOV	#MSG20,R4	;SET NRZ TAG POINTER
1439	015344	032737	002000	000716	BIT	#2000,UDES	;SEE IF PE
1440	016352	001402			BEQ	FT14R0	;IF NOT: BR
1441	017374	012704	015144		MOV	#MSG21,R4	;ELSE SET PE TAG POINTER
1442	017360	004737	013442		JSR	PC,TTOUT	;PRINT TAG
1443	017354	032737	000002	000710	BIT	#2,FUN	;SEE IF READ REVERSE
1444	017372	001003			BNE	FT14R1	;IF SO: BR
1445	017374	012704	015116		MOV	#MSG17,R4	
1446	017370	000402			BR	FT14R2	;GO PRINT
1447	017372	012704	015076		MOV	#MSG16,R4	
1448	017376	004737	013442		JSR	PC,TTOUT	;PRINT FRWD/REV
1449	017374	012704	015151		MOV	#MSG22,R4	
1450	017376	004737	013442		JSR	PC,TTOUT	;PRINT EXPT TAG
1451	017372	010503			MOV	RS,R3	
1452	017374	042703	177700		BIC	#177700,R3	;MASK RECORD NUMBER
1453	017376	004737	013572		JSR	PC,OCTP	;PRINT EXPT RECORD NUMBER
1454	017374	012704	015161		MOV	#MSG23,R4	
1455	017376	004737	013442		JSR	PC,TTOUT	;PRINT RCVD TAG
1456	017374	017703	172046		MOV	ZFC,R3	
1457	017376	042703	177700		BIC	#177700,R3	;MASK RECORD NUMBER
1458	017374	004737	013572		JSR	PC,OCTP	;PRINT ACTUAL RECORD NUMBER
1459	017376	005777	172064		TST	JSWR	;SEE IF HALT ON ERROR
1460	017374	100001			BPL	FT14X	;IF NOT: BR
1461	006466	000000			HALT		
1462	006470	032737	002000	000716	BIT	#2000,UDES	;SEE IF DONE PE
1463	006476	001005			BNE	FT14XX	;IF SO: BR
1464	006500	012737	002300	000716	MOV	#2300,UDES	;SET TO PE
1465	006506	000137	005662		JMP	FT14A1	;DO IN PE
1466	006512	000137	003022		JMP	TSCD2	;RETURN TO SCHEDULAR

```

1467                                     ;ERASE TEST####
1468
1469 006516 000240 FT15: NOP
1470 006520 005037 CLR STMSK
1471 006524 012737 000100 000636 MOV #100,ROYDX
1472 006528 012737 000010 000640 MOV #10,OPDYX
1473 006530 012737 017412 000610 MOV #MSFT15,EMADDR ;SET TEST HEADER
1474 006536 004737 011700 JSR PC, RIND ;REWIND
1475 006538 012737 021624 000616 MOV #RODATA,BADDR ;SET BA
1476 006540 012737 001700 000716 MOV #1700,LDIS ;SET NRZ, NORMAL
1477 006542 012737 000025 000710 FT15A: MOV #25,FUN ;SET ERASE OP-CODE
1478 006574 012737 000200 000624 FT15B: MOV #200,RCNT ;SET TO ERASE 129 TIMES
1479 006602 004737 011546 JSR PC, EXEC ;GO EXECUTE COMMAND
1480 006606 012737 016043 000626 MOV #MSG46,ERRP ;SET ERROR CODE
1481 006614 004737 011776 JSR PC, ERCHK ;GO CHECK ERRORS
1482 006620 005737 000712 TST SERFL ;SEE IF ANY ERRORS
1483 006624 001032 BNE FT15X ;IF SO EXIT
1484 006626 005337 000624 DEC RCNT ;SEE IF DONE ERASING
1485 006630 001363 BNE FT15B ;IF NOT: BR
1486 006634 000240 NOP
1487 006636 004737 011700 JSR PC, RIND ;REWIND
1488 006642 012737 177600 000622 MOV #-200,WCNT ;SET WC
1489 006644 012737 000071 000710 MOV #71,FUN ;SET READ FORWARD OP-CODE
1490 006646 012737 000040 000636 MOV #40,ROYDX ;SET DELAY
1491 006648 004737 011546 JSR PC, EXEC ;GO EXECUTE COMMAND
1492 006670 000240 NOP
1493 006672 012737 016503 000626 MOV #MSG60,ERRP ;SET ERROR CODE
1494 006700 012737 020000 000660 MOV #20000,STMSK ;GO CHECK ERRORS
1495 006706 004737 011776 JSR PC, ERCHK ;RETURN TO SCHEDULAR
1496 006712 000137 0C3022 FT15X: JMP TSCD2

```

```

1497                                     ;TAPE MARK WRITE/READ TEST*****
1498
1499 006716 000240 FT16: NOP
1500 006720 012737 000001 000636 MOV #1,ROYDX
1501 006726 012737 001000 000640 MOV #1000,OPDYX
1502 006734 012737 017434 000610 MOV #MSFT16,EMADDR ;SET HEADER
1503 006742 012737 001700 000716 MOV #1700,UDES ;SET TO NRZ,NORMAL,000
1504 006750 004737 011700 FT16A: JSR PC,RWHD ;INIT AND REWIND SLAVE
1505 006754 012737 177760 000620 FT16B: MOV #20,FCNT ;FC=20
1506 006762 012737 177770 000622 MOV #10,MCNT ;MC=10
1507 006770 012737 000027 000710 MOV #27,FUN ;SET WRITE TAPE MARK OP-CODE
1508 006776 004737 011546 JSR PC,EXEC ;GO EXECUTE COMMAND
1509 007002 012737 001000 000660 MOV #1000,STMSK ;SET F R FCE MASK
1510 007010 012737 014765 000626 MOV #MSG12,ERRP ;SET E OR CODE
1511 007016 004737 011776 JSR PC,ERCHK ;GO CHECK ERRORS
1512 007022 004737 012340 JSR PC,THCHK ;GO SEE IF TH SET
1513 007026 012737 000077 000710 MOV #77,FUN ;SET L TO REVERSE OP-CODE
1514 007034 004737 011546 JSR PC,EXEC ;GO EXECUTE COMMAND
1515 007040 012737 001000 000660 MOV #1000,STMSK ;SET F R ERROR MASK
1516 007046 012737 015003 000626 MOV #MSG13,ERRP ;SET E OR CODE
1517 007054 004737 011776 JSR PC,ERCHK ;GO CHECK ERRORS
1518 007060 004737 012340 JSR PC,THCHK ;GO SEE IF TH SET
1519 007064 012737 000071 000710 MOV #71,FUN ;SET F R FORWARD OP-CODE
1520 007072 004737 011546 JSR PC,EXEC ;GO EXECUTE COMMAND
1521 007076 012737 015030 000626 MOV #MSG14,ERRP ;SET E OR CODE
1522 007104 004737 011776 JSR PC,ERCHK ;GO CHECK ERRORS
1523 007110 004737 012340 JSR PC,THCHK ;GO SEE IF TH SET
1524 007114 032737 002000 000716 BIT #2000,UDES ;SEE IF DONE PE
1525 007122 001004 BNE FT16X ;IF SO: BR
1526 007124 012737 002300 000716 MOV #2300,UDES ;SET PE,NORMAL
1527 007132 000706 BR FT16A ;DO IN PE
1528 007134 004737 012546 FT16X: JSR PC,ITER ;DO ITERATIONS
1529 007140 000137 003022 JMP TSC02 ;RETURN TO SCHEDULAR
1530
    
```



```

1531
1532
1533
1534 007144 005037 000624
1535 007150 012737 017475 000610
1536 007156 012737 001700 000716
1537 007164 004737 011700
1538 007170 012737 000027 000710
1539 007176 012737 040000 000636
1540 007204 012737 040000 000640
1541 007212 004737 011546
1542 007216 012737 102300 000660
1543 007224 012737 015055 000626
1544 007232 004737 011776
1545 007236 005737 000712
1546 007242 001137
1547 007244 004737 012340
1548 007250 000240
1549 007252 000240
1550 007254 032737 000100 000624
1551 007262 001045
1552 007264 062737 000020 000624
1553 007272 013737 000624 000652
1554 007300 012737 177600 010622
1555 007306 012737 177400 011620
1556 007314 012737 020112 011516
1557 007322 012737 000061 000710
1558 007330 000240
1559 007332 000240
1560 007334 004737 011546
1561 007340 012737 014765 000626
1562 007346 012737 102300 000660
1563 007354 004737 011776
1564 007360 005737 000712
1565 007364 001066
1566 007366 005337 000652
1567 007372 001356
1568 007374 000675
1569 007376 000240
1570 007400 012737 000033 000710
1571 007406 012737 015076 000626
1572 007414 012737 177600 000642
1573 007422 012737 000005 000624
1574 007430 004737 012620
1575 007434 004737 011546
1576 007440 012737 001000 000660
1577 007446 004737 011776
1578 007452 005737 000712
1579 007456 001031
1580 007460 004737 012340
1581 007464 005337 000624
1582 007470 001357
1583 007472 022737 000031 000710
1584 007500 001407
1585 007502 012737 015116 000626
1586 007510 012737 000031 000710

```

;TAPE MARK SPACE TEST*****

```

FT17: CLR RCNT
      MOV #MSG17,ERROR ;SET HEADER
      MOV #1700,LDIS ;SET TO NRZ
      JSR PC,REWD ;REWIND TAPE
FT17A: MOV #27,FUN
FT17B: MOV #40000,ROYDX ;SET DRY DELAY
      MOV #40000,OPYX ;SET OP DELAY
      JSR PC,EXEC ;GO WRITE TH
      MOV #102300,STMSK ;MASK DATA RELATED ERRORS
      JSR #MSG15,ERRP ;SET ERROR TYPE
      JSR PC,ERCHK ;GO CHECK ERROR
      TST SERFL ;SEE IF ERROR
      BNE FT17X ;IF SO: BR
      JSR PC,TRCHK ;GO SEE IF TH SET
      NOP
      NOP
      BIT #100,RCNT ;SEE IF DONE PATTERN
      BNE FT17D ;IF SO: BR
      ADD #20,RCNT ;ADD 20 TO RECORD COUNT
      MOV RCNT,TEMP1 ;SAVE RECORD COUNT
      MOV #-200,MCNT ;MC=128
      MOV #-400,FCNT ;FC=256
      MOV #INDATA,BADDR ;BA=WRITE BUFFER
      MOV #61,FUN ;SET WRITE OP CODE
FT17C: NOP
      NOP
      JSR PC,EXEC ;GO WRITE
      MOV #MSG12,ERRP ;SET ERROR CODE
      MOV #102300,STMSK ;MASK DATA RELATED ERRORS
      JSR PC,ERCHK ;GO CHECK ERROR
      TST SERFL ;SEE IF ERROR
      BNE FT17X ;IF SO: BR
      DEC TEMP1 ;SEE IF DONE ALL
      BNE FT17C ;IF NOT: BR
      BR FT17B ;ELSE GO DO TH
FT17D: NOP
      MOV #33,FUN ;SET SPACE REVERSE
      MOV #MSG16,ERRP ;SET ERROR CODE
      MOV #-200,SCNT ;SET TO 10 RECORDS
      MOV #5,RCNT ;SET NUMBER OF OPS TO DO
      JSR PC,INIT1 ;GO INIT
      JSR PC,EXEC ;GO SPACE
      MOV #1000,STMSK ;SET ERROR MASK
      JSR PC,ERCHK ;GO CHECK ERROR
      TST SERFL ;SEE IF ERROR
      BNE FT17X ;IF SO: BR
      JSR PC,TRCHK ;GO SEE IF TH SET
      DEC RCNT ;SEE IF DONE SPACES
      BNE FT17E ;IF NOT: BR
      CMP #31,FUN ;SEE IF DONE FORWARD
      BEQ FT17F ;IF SO: BR
      MOV #MSG17,ERRP ;SET ERROR CODE
      MOV #31,FUN ;SET TO SPACE FORWARD

```

E04

1587	007516	000736				BR	FT17D1	:DO FORWARD
1588	007520	032737	002000	000716	FT17F:	BIT	#2000, UDES	:SEE IF DONE PE
1589	007526	001005				BNE	FT17X	:IF SO: BR
1590	007530	012737	002300	000716		MOV	#2300, UDES	:SET TO PE
1591	007536	000137	007164			JMP	FT17A	:GO PE
1592	007542	000137	003022		FT17X:	JMP	TSC02	:RETURN TO SCHEDULAR

```

1593
1594
1595
1596 007546 000240 FT20: NOP
1597 007550 012737 017523 000610 MOV #MSFT20,EMADDR ;SET HEADER
1598 007556 012737 001700 000716 MOV #1700,UDES ;SET UNIT DESCRIPTION
1599 007564 004737 011700 FT20A: JSR PC,RWHD ;INIT AND REWIND SLAVE
1600 007570 012737 000003 000720 MOV #3,PATRN
1601 007576 004737 012400 JSR PC,DSUP ;GO SET PATTERN 3
1602 007602 012737 020112 000616 MOV #NDATA,BADDR ;SET BA
1603 007610 012737 177400 000620 MOV #400,FCNT ;SET FC
1604 007616 012737 177600 000622 MOV #200,WCNT ;SET WC
1605 007624 012737 000061 000710 MOV #61,FUN ;SET WRITE OP CODE
1606 007632 004737 011546 JSR PC,EXEC ;GO WRITE RECORD
1607 007636 012737 016043 000626 MOV #MSG46,ERRP ;SET ERROR CODE
1608 007644 004737 011776 JSR PC,ERCHK ;GO CHECK ERROR
1609 007650 005737 000712 TST SE#FL ;SEE IF ERROR
1610 007654 001042 BNE FT20X ;IF SO: BR
1611 007656 012737 015076 000626 MOV #MSG16,ERRP ;SET REVERSE ERROR TAG
1612 007664 012737 000057 000710 MOV #57,FUN ;SET REVERSE WRITE CHECK OP-CODE
1613 007672 062737 000376 000616 ADD #376,BADDR ;SET BA FOR REVERSE CHECK
1614 007700 004737 011546 JSR PC,EXEC ;GO DO REVERSE CHECK
1615 007704 004737 011776 JSR PC,ERCHK ;GO CHECK ERROR
1616 007710 012737 015116 000626 FT20B: MOV #MSG17,ERRP ;SET FORWARD TAG
1617 007716 012737 000051 000710 MOV #51,FUN ;SET FORWARD CHECK OP CODE
1618 007724 162737 000376 000616 SUB #376,BADDR ;SET BA FOR FORWARD CHECK
1619 007732 004737 011546 JSR PC,EXEC ;GO DO FORWARD CHECK
1620 007736 004737 011776 JSR PC,ERCHK ;GO CHECK ERROR
1621 007742 032737 002000 000716 FT20C: BIT #2000,UDES ;SEE IF DONE PE
1622 007750 001004 BNE FT20X ;IF SO: BR
1623 007752 012737 002300 000716 MOV #2300,UDES ;ELSE SET PE
1624 007760 000701 BR FT20A ;DO IN PE
1625 007762 004737 012546 FT20X: JSR PC,ITER ;DO ITERATIONS
1626 007766 000137 003022 JMP TSCD2 ;RETURN TO SCHEDULAR
    
```

```

1627
1628
1629
1630 007772 012737 017554 000610 FT21: MOV #MSG21,EMADDR ;SET TEST HEADER
1631 010000 004737 011700 FT21A: JSR PC,REWD ;GO REWIND
1632 010004 012737 000003 000720 MOV #3,PATRN
1633 010012 004737 012400 JSR PC,DSUP ;GO SET PATTERN 3
1634 010016 012737 020112 000616 MOV #WDATA,BADDR ;SET BA=WRITE BUFFER
1635 010024 012737 176340 000620 MOV #800,FCNT ;SET FC=800(10)
1636 010032 012737 177160 000622 MOV #400,WCNT ;SET WC=400(10)
1637 010040 012737 001700 000716 MOV #1700,UCES ;SET NRZ NORMAL
1638 010048 012737 000061 000710 MOV #61,FUN ;SET WRITE OP-CODE
1639 010056 004737 011546 JSR PC,EXEC ;GO DO WRITE 1
1640 010060 012737 014765 000626 MOV #MSG12,ERRP ;SET ERROR CODE
1641 010068 004737 011776 JSR PC,ERCHK ;GO CHECK FOR ERROR
1642 010072 004737 011546 JSR PC,EXEC ;YES DO WRITE 2
1643 010076 004737 011776 JSR PC,ERCHK ;YES CHECK FOR ERROR
1644 010102 000240 NOP
1645 010104 004737 011700 JSR PC,REWD ;GO REWIND
1646 010110 012737 177160 000620 MOV #400,FCNT ;SET FC=400(10)
1647 010116 012737 177470 000622 MOV #200,WCNT ;SET WC=200(10)
1648 010124 004737 011546 JSR PC,EXEC ;GO REWRITE RECORD 1-WH TO EH
1649 010130 000240 FT21SCP: NOP
1650 010132 004737 011700 JSR PC,REWD ;REWIND
1651 010136 012737 021624 000616 MOV #WDATA,BADDR ;SET E READ BUFFER
1652 010144 012737 177160 000620 MOV #400,FCNT ;SET FC=400
1653 010152 012737 177470 000622 MOV #200,WCNT ;SET WC=200
1654 010160 012737 000071 000710 MOV #71,FUN ;SET READ OP-CODE
1655 010166 004737 011546 JSR PC,EXEC ;GO READ RECORD 1
1656 010172 012737 015030 000626 MOV #MSG14,ERRP ;SET ERROR CODE
1657 010200 004737 011776 JSR PC,ERCHK ;GO CHECK FOR ERROR
1658 010204 000240 NOP
1659 010206 052777 000010 170304 BIS #10,ACS ;INHIBIT BA INCREMENT
1660 010214 012737 176340 000620 MOV #800,FCNT ;SET FC=800(10)
1661 010222 012737 177160 000622 MOV #400,WCNT ;SET WC=400(10)
1662 010230 004737 011546 JSR PC,EXEC ;GO READ RECORD 2
1663 010234 022777 001440 170254 CMP #800,ZFC ;SEE IF READ RECORD 2 OK
1664 010242 001424 BEQ FT21X ;IF SO: BR
1665 010244 022777 001441 170244 CMP #801,ZFC ;BRANCH IF IN GREY AREA
1666 010252 001420 BEQ FT21X
1667 010254 022777 001440 170234 15: CMP #800,ZFC ;BRANCH IF ERASE HEAD REVERSED
1668 010262 101404 BLOS FT21B ;IF SO: BR
1669 010264 012737 015736 000650 MOV #MSG44,ERADD ;SET ERASE HEAD INOPERATIVE ERROR CODE
1670 010272 000403 BR FT21C
1671 010274 012737 015766 000650 FT21B: MOV #MSG45,ERADD ;SET ERASE HEAD REVERSED ERROR CODE
1672 010302 012737 010130 000674 FT21C: MOV #FT21SCP,SCOLP ;SET SCOPE ADDRESS
1673 010310 004737 003774 JSR PC,FT3ER ;GO PRINT ERROR
1674 010314 004737 012546 FT21X: JSR PC,ITER ;GO SEE IF ITERATION
1675 010320 000137 003022 JMP TSC02 ;RETURN TO SCHEDULAR
1676
1677

```

```

1678 ;BUFFERED COMMAND TEST*****
1679
1680 010324 012737 017603 000610 FT22: MOV #MSG22,EMADDR ;SET TEST HEADER
1681 010332 004737 011700 JSR PC,REWIND ;GO REWIND
1682 010336 012700 000003 MOV #3,RO ;SET NUMBER OF WRITES
1683 010342 012737 001700 000716 MOV #1700,UDES ;SET TO M/Z NORMAL
1684 010350 012737 020112 017616 MOV #WDATA,BADDR ;SET BA=WRITE BUFFER
1685 010356 012737 177770 017620 MOV #-1000,FCNT ;SET FC=1000
1686 010364 012737 177770 017622 MOV #-400,WCNT ;SET WC=400
1687 010372 012737 000061 000710 MOV #61,FUN ;SET WRITE OP-CODE
1688 010400 004737 011546 FT22A: JSR PC,EXEC ;GO DO WRITE
1689 010404 015300 DEC RO ;SEE IF DONE ALL
1690 010406 01374 BNE FT22A ;IF NOT: BR
1691 010410 000240 NOP
1692 010412 012777 000007 170070 MOV #7,RC1 ;START REWIND
1693 010420 032777 000200 170074 FT22B: BIT #200,BOS
1694 010426 001774 BEQ FT22B
1695 010430 004737 012620 JSR PC,INITI ;INITIALIZE
1696 010434 012737 000010 000636 MOV #10,ROYDX ;SET LONG READY DELAY
1697 010442 004737 011546 JSR PC,EXEC ;ISSUE BUFFERED WRITE
1698 010446 000240 NOP
1699 010450 012737 016141 000626 MOV #MSG49,ERRP ;SET ERROR CODE
1700 010456 012737 102300 000660 MOV #102300,STMSK ;MARK DATA ERROR
1701 010464 004737 011776 JSR PC,ERCHK ;GO CHECK ERROR
1702 010470 032777 000002 170024 BIT #2,BOS ;SEE IF BOT IS SET
1703 010476 001410 BEQ FT22X ;IF NOT: BR
1704 010500 012737 016167 000650 MOV #MSG50,ERADD ;SET ERROR CODE
1705 010506 012737 010324 000674 MOV #FT22,SCOLP
1706 010514 004737 003774 JSR PC,FT3ER ;GO DO ERROR
1707 010520 004737 012546 FT22X: JSR PC,ITER ;GO SEE IF ITERATION
1708 010524 000137 003022 JMP TSCD2 ;RETURN TO SCHEDULAR
1709
1710
    
```

```

1711                                     ;READ-IN PRESET TEST*****
1712
1713 010530 005737 000614          FT23: TST      SLVN          ;SEE IF SLAVE SELECT=0
1714 010534 001103                BNE      FT23X        ;IF NOT: BR
1715 010536 012737 017640 000610  MOV      #MSFT23,EMADDR ;SET TEST HEADER
1716 010544 004737 012620          JSR      PC,INIT1     ;GO INIT
1717 010550 012737 001700 000716  MOV      #1700,UDES    ;SET TO NRZ NORMAL
1718 010556 012737 020112 000616  MOV      #WDATA,BADDR  ;SET BA=WRITE BUFFER
1719 010564 012737 177400 000620  MOV      #-400,FCNT    ;SET FC=400
1720 010572 012737 177600 000622  MOV      #-200,WCNT    ;SET WC=200
1721 010600 012737 000061 000710  MOV      #61,FUN       ;SET WRITE OP-CODE
1722 010606 004737 011546          JSR      PC,EXEC      ;GO DO WRITE
1723 010612 000240                NOP
1724 010614 004737 012620          JSR      PC,INIT1     ;INITIALIZE
1725 010620 012737 000021 000710  MOV      #21,FUN       ;SET READ-IN PRESET OP CODE
1726 010626 004737 011546          JSR      PC,EXEC      ;GO DO COMMAND
1727 010632 005000                CLR      R0
1728 010634 012703 000004          MOV      #4,R3        ;SET MULT
1729 010640 032777 020000 167654  FT23A: BIT      #20000,20S   ;SEE IF PIP RESET
1730 010646 001404                BEQ      FT23B        ;IF SO: BR
1731 010650 005300                DEC      R0
1732 010652 001372                BNE      FT23A        ;AWAIT PIP RESET
1733 010654 005303                DEC      R3
1734 010656 001370                BNE      FT23A        ;DELAY
1735 010660 032777 000002 167634  FT23B: BIT      #2,20S   ;SEE IF BOT
1736 010666 001010                BNE      FT23C        ;IF SO: BR
1737 010670 012737 016225 000650  MOV      #MSG51,ERR00  ;SET ERROR CODE
1738 010676 012737 010530 000674  MOV      #FT23,SCOLP   ;
1739 010704 004737 003774          JSR      PC,FT3ER     ;GO DO ERROR
1740 010710 012701 141000          FT23C: MOV      #141000,R1 ;SET EXPT TC
1741 010714 013700 000542          MOV      TC,R0        ;SET TC ADDRESS
1742 010720 020110                CMP      R1,(R0)      ;SEE IF EXPT=RCVD
1743 010722 001410                BEQ      FT23X        ;IF SO: BR
1744 010724 012737 016261 000650  MOV      #MSG52,ERR00  ;SET ERROR CODE
1745 010732 012737 010530 000674  MOV      #FT23,SCOLP   ;CLEAR SCOPE ADDRESS
1746 010740 004737 003504          JSR      PC,FT2ER     ;GO DO ERROR
1747 010744 000137 003022          FT23X: JMP      TSCD2   ;RETURN TO SCHEDULAR
1748
1749

```

```

1750
1751 ;AUTO-DENSITY SELECT TEST: WRITE-NRZ,READ-PE
1752
1753 010750 012737 017727 000610 FT24: MOV #MSG24,EMADDR ;SET ERROR MSG HEADER
1754 010756 004737 011700 JSR PC,RWIND ;REWIND SLAVE
1755 010762 012737 000001 000720 MOV #1,PATRN ;SELECT PATTERN
1756 010770 004737 012400 JSR PC,DSUP ;GO DO DATA SETUP
1757 010774 012737 020112 000616 MOV #BDATA,BADDR ;SET BUS ADDRESS,
1758 011002 012737 177400 000620 MOV #400,FCNT ;FRAME COUNT,
1759 011010 012737 177600 000622 MOV #200,WCNT ;WORD COUNT,
1760 011016 012737 001700 000716 MOV #1700,UDES ;& SLAVE DESC = NRZ NORMAL
1761 011024 012737 000061 000710 MOV #61,FUN ;LOAD OP CODE WRITE FWD
1762 011032 004737 011546 JSR PC,EXEC ;GO EXECUTE COMMAND
1763 011036 012737 016043 000626 MOV #MSG46,ERRP ;SET ERROR MSG ADDRESS
1764 011044 004737 011776 JSR PC,ERCHK ;GO CHECK ERRORS
1765 011050 005737 000712 TST SERFL ;BRANCH IF AN ERROR OCCURRED
1766 011054 001026 BNE FT24X
1767 011056 004737 011700 JSR PC,RWIND ;REWIND SLAVE
1768 011062 012737 021624 000616 MOV #BDATA,BADDR ;SET BUS ADDRESS FOR READ
1769 011070 012737 002300 000716 MOV #2300,UDES ;SET SLAVE DESC = PE,NORMAL
1770 011076 012737 000071 000710 MOV #71,FUN ;SET OP CODE = READ FWD
1771 011104 004737 011546 JSR PC,EXEC ;GO READ RECORD
1772 011110 032777 000040 167404 BIT #40,BDS ;BRANCH IF PES BIT CLEARED
1773 011116 001405 BEQ FT24X
1774 011120 012737 016602 000650 MOV #MSG63,ERRAD ;GO PROCESS ERROR
1775 011126 004737 003774 JSR PC,FT3ER
1776 011132 004737 012546 FT24X: JSR PC,ITER ;RETURN TO SCHEDULER
1777 011136 000137 003022 JMP TSCD2
1778
    
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1779
1780 ;AUTO-DENSITY SELECT TEST: WRITE-PE READ-NRZ
1781 011142 012737 020005 000610 FT25: MOV #MSFT25,EMADDR ;SET ERROR MESSAGE ADDRESS
1782 011150 004737 011700 JSR PC,RIND ;REWIND SLAVE
1783 011154 012737 000001 000720 MOV #1,PATRN ;SELECT PATTERN
1784 011162 004737 012400 JSR PC,DSUP ;GO DO DATA SETUP
1785 011166 012737 000112 000616 MOV #DATA,BADDR ;SET BUS ADDRESS
1786 011174 012737 177400 000620 MOV #-400,FCNT ;SET FRAME COUNT
1787 011178 012737 177200 000622 MOV #-200,WCNT ;SET WORD COUNT
1788 011180 012737 000000 000716 MOV #2300,UDES ;& SLAVE DESC = PE,NORMAL
1789 011216 012737 000001 000710 MOV #61,FUN ;LOAD WRITE OP CODE
1790 011224 004737 011700 JSR PC,EXEC ;GO EXECUTE WRITE
1791 011230 012737 016043 000626 MOV #MSG46,ERRP ;SET ERROR MSG HDR
1792 011236 004737 011776 JSR PC,ERCHK ;GO CHECK FOR ERRORS
1793 011242 005737 000712 TST SERFL ;BRANCH IF ERROR OCCURRED
1794 011246 001076 BNE FT25X
1795 011250 004737 011700 JSR PC,RIND ;REWIND SLAVE
1796 011254 012737 021624 000616 MOV #DATA,BADDR ;SET BUS ADDRESS FOR READ
1797 011262 012737 001700 000716 MOV #1700,UDES ;SET SLAVE DESC = NRZ,NORMAL
1798 011270 012737 000071 000710 MOV #71,FUN ;SET READ FWD OP CODE
1799 011276 004737 011546 JSR PC,EXEC ;GO EXECUTE
1800 011302 032777 000040 167212 BIT #40,ZDS ;BRANCH IF PES BIT GOT SET
1801 011310 001005 BNE FT25X
1802 011312 012737 016633 000650 MOV #MSG64,ERADD
1803 011320 004737 003774 JSR PC,FT3ER ;GO PROCESS ERROR
1804 011324 004737 012546 FT25X: JSR PC,ITER ;ITERATION LOOP
1805 011330 000137 003022 JMP TSC02 ;RETURN TO SCHEDULER
1806
1807 ;REWIND: OFF LINE TEST*****
1808
1809 011334 032777 010000 167206 FT26: BIT #10000,ZSWR ;SEE IF IN CONTINUOUS MODE
1810 011342 001077 BNE FT26XX ;IF SO: BR
1811 011344 005737 001662 TST CHNFLG ;BRANCH IF CHAIN MODE
1812 011350 001074 BNE FT26XX
1813 011352 012737 017673 000610 MOV #MSFT26,EMADDR ;SET TEST HEADER
1814 011360 004737 011700 JSR PC,RIND ;REWIND & SELECT SLAVE
1815 011364 012737 000001 000720 MOV #1,PATRN ;SELECT PATTERN (ALL 1'S)
1816 011372 004737 012400 JSR PC,DSUP ;FILL WRITE BUFFER
1817 011376 012737 000112 000616 MOV #DATA,BADDR ;SET WRITE BUFFER BUS ADDRESS
1818 011404 012737 177400 000620 MOV #-400,FCNT ;SET FRAME COUNT
1819 011412 012737 177600 000622 MOV #-200,WCNT ;SET WORD COUNT
1820 011420 012737 001700 000716 MOV #1700,UDES ;SET UNIT DESCRIPTION = NRZ
1821 011426 012737 000061 000710 MOV #61,FUN ;SET WRITE COMMAND
1822 011434 004737 011546 JSR PC,EXEC ;GO WRITE A RECORD
1823 011440 012777 000003 167042 MOV #3,ZC1 ;ISSUE REWIND: OFF LINE COMMAND
1824 011446 005037 000674 CLR SCOLP ;CLEAR SCOPE LOOP
1825 011452 012700 004000 MOV #4000,RO
1826 011456 005300 18: DEC RO ;DELAY
1827 011460 001376 BNE 18
1828 011462 032777 010000 167032 BIT #10000,ZDS ;SEE IF MOL IS RESET
1829 011470 001406 BEQ 25 ;IF SO: BR
1830 011472 012737 016300 000650 MOV #MSG53,ERADD ;SET ERROR CODE
1831 011500 004737 003774 JSR PC,FT3ER ;GO DO ERROR
1832 011504 001042 BR FT26X
1833 011506 013700 000524 25: MOV ER,RO ;GET ADDRESS OF ERROR REG
1834 011512 005001 CLR R1 ;RESULT SHOULD BE 0
    
```


L04

TMO3/TE16 BASIC FUNCTION TEST MACY11 27(1006) 31-MAR-77 17:23 PAGE 50
C 29-MAR-77 09:54

1835	011514	020110			CMP	R1 (R0)	; BRANCH IF ERROR REG = 0
1836	011516	001405			BEQ	FT26X	
1837	011520	012737	016667	000650	MOV	#MSG67, ERADD	; SET ERROR MSG HEADER
1838	011526	004737	003504		JSR	PC, FT2ER	; GO TYPE ERROR
1839	011532	012704	016325		FT26X: MOV	#MSG54, R4	
1840	011536	004737	013442		JSR	PC, TTOUT	; PRINT ON LINE REQUEST
1841	011542	000137	003022		FT26XX: JMP	TSCD2	; RETURN TO SCHEDULER
1842							


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1871                                     ;REWIND SUBROUTINE*****
1872
1873 011700 000240                                RWIND:  NOP
1874 011702 004737 012620                        JSR    PC, INIT1                ; INIT
1875 011706 012777 000007 166574                MOV    #7, 2C1                  ; START REWIND
1876 011714 012700 040000                        MOV    #40000, R0
1877 011720 000000                                RWINDA: DEC    R0
1878 011722 000000                                BNE    RWINDA                    ; DELAY
1879 011724 032777 020000 166570                RWINDB: BIT    #20000, 20S
1880 011732 001374                                BNE    RWINDB                    ; WAIT PIP
1881 011734 002777 000002 166560                BIT    #2, 20S                   ; SET IF BOT
1882 011742 001012                                BNE    RWINDX                    ; IF SO: BR
1883 011744 013704 000610                        MOV    EM-00R, R4
1884 011750 004737 013442                        JSR    PC, TTOUT                 ; PRINT HEADER
1885 011754 012704 014452                        MOV    #MSG2, R4
1886 011760 004737 013442                        JSR    PC, TTOUT                 ; PRINT REWIND ERROR
1887 011764 000137 003022                        JMP    TSCD2                     ; RETURN TO SECHEDULAR
1888 011770 004737 012620                        RWINDX: JSR    PC, INIT1         ; INIT
1889 011774 000207                                RTS    PC                        ; RETURN TO CALLER
1890

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;ERROR CHECK SUBROUTINE*****
1891
1892 011776 005037 000712 ERCHK: CLR SERFL ;CLEAR FLAG
1893 012002 017737 166514 000664 MOV 205,DSAV ;SAVE DRIVE STATUS REGISTER
1894 012010 032777 040000 166504 BIT #40000,205 ;SEE IF ERROR
1895 012016 001001 BNE ERPT ;IF SO: BR
1896 012020 000207 RTS PC ;RETURN
1897 012024 017704 166476 ERPT: MOV 2ER,R4 ;GET ERROR REGISTER
1898 012028 032737 002000 000716 BIT #2000,LDLDS ;SEE IF PE
1899 012032 001403 BEQ ERPTA1 ;IF SO: BR
1900 012036 042737 000200 000660 BIC #200,STMSK ;RESET PE MASK
1901 012040 043704 000660 ERPTA1: BIC STMSK,R4 ;MASK DONT CARE BITS
1902 012044 001530 BEQ ERPTG ;IF NO UNEXPECTED ERRORS: BR
1903 012048 012737 000001 000712 ERPTG: MOV #1,SERFL ;SET FLAG
1904 012052 032777 020000 166462 BIT #21000,2SMR ;SEE IF SHOULD PRINT ERRORS
1905 012056 001115 BNE ERPTD ;IF NOT: BR
1906 012060 005737 000606 TST HORFL ;SEE IF DONE HEADER
1907 012064 001006 BNE ERPTA ;IF SO: BR
1908 012068 005237 000606 INC HORFL ;SET HEADER FLAG
1909 012102 013704 000610 MOV ENADR,R4 ;PRINT HEADER
1910 012106 004737 013442 JSR PC,TTOUT ;GET ERROR CODE
1911 012110 013704 000626 ERPTA: MOV ERAP,R4 ;IF NONE: BR
1912 012114 001414 BEQ ERPTB ;PRINT ERROR CODE
1913 012118 004737 JSR PC,TTOUT ;SET NRZ TAG
1914 012122 012704 015136 MOV #MSG20,R4 ;SEE IF PE
1915 012126 032777 002000 166404 BIT #2000,ITC ;IF NOT: BR
1916 012130 001402 BEQ ERPT1A ;ELSE SET PE TAG
1917 012134 012704 015144 MOV #MSG21,R4 ;PRINT TAG
1918 012138 004737 JSR PC,TTOUT ;SEE IF CODE 2
1919 012142 013704 000630 ERPT1A: MOV ERAP1,R4 ;IF NOT: BR
1920 012146 001402 BEQ ERPTB1 ;PRINT CODE 2
1921 012150 013704 013442 JSR PC,TTOUT ;SEE IF ITERATION
1922 012154 004737 004000 166360 ERPTB1: BIT #4000,2SMR ;IF NOT: BR
1923 012158 001010 BNE ERPTC ;PRINT ITER TAG
1924 012162 012704 016457 MOV #MSG56,R4 ;PRINT ITERATION
1925 012166 004737 013442 JSR PC,TTOUT ;PRINT REGISTER TAG
1926 012170 013703 000662 MOV ITCNT,R3 ;PRINT CSI
1927 012174 004737 013572 JSR PC,OCTP ;PRINT MC
1928 012178 012704 014364 ERPTC: MOV #MSG1,R4 ;PRINT BA
1929 012182 004737 013442 JSR PC,TTOUT ;PRINT FC
1930 012186 017703 166262 MOV 2C1,R3 ;PRINT CS1
1931 012190 004737 013560 JSR PC,OCTPE ;PRINT WC
1932 012194 017703 166254 MOV 2MC,R3 ;PRINT BA
1933 012198 004737 013560 JSR PC,OCTPE ;PRINT FC
1934 012202 017703 166246 MOV 2BA,R3 ;PRINT CS2
1935 012206 004737 013560 JSR PC,OCTPE ;PRINT DS
1936 012210 017703 166240 MOV 2FC,R3 ;PRINT ER
1937 012214 004737 013560 JSR PC,OCTPE ;PRINT TC
1938 012218 017703 166232 MOV 2CS,R3 ;PRINT DS
1939 012222 004737 013560 JSR PC,OCTPE ;PRINT DS
1940 012226 017703 166224 MOV 2DS,R3 ;PRINT ER
1941 012230 004737 013560 JSR PC,OCTPE ;PRINT ER
1942 012234 017703 166224 MOV 2ER,R3 ;PRINT ER
1943 012238 004737 013560 JSR PC,OCTPE ;PRINT ER
1944 012242 017703 166224 MOV 2TC,R3 ;PRINT TC
1945 012246 004737 013560 JSR PC,OCTPE ;PRINT TC
1946

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1947	012322	005777	166222	ERPTD:	TST	@SWR	;SEE IF HALT ON ERROR
1948	012326	100001			BPL	ERPTX	;IF NOT: BR
1949	012330	000000			HALT		
1950	012332	004737	012620	ERPTX:	JSR	PC,INIT1	;INIT
1951	012336	000207		ERPTXX:	RTS	PC	;RETURN
1952							
1953							

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1954                                     ;TAPE MARK STATUS CHECK*****
1955
1956 012340 032737 000004 000664 TMCHK: BIT      M4,DSAV      ;SEE IF TM SET
1957 012346 001401                    BEQ      TMCHK1     ;IF NOT: BR
1958 012350 000207                    RTS      PC         ;ELSE RETURN
1959 012352 005737 000712                    TST     SERFL      ;SEE IF HAD ERROR
1960 012356 001374                    BNE     TMCHK0     ;IF SO: BR
1961 012360 012737 016467 000630                    MOV     #MSG57,ERRP1 ;SET ERROR CODE 2
1962 012366 004737 012052                    JSR     PC,ERRPG   ;GO PRINT TM ERROR
1963 012372 005037 000630                    CLR     ERRP1     ;CLEAR CODE 2 FLAG
1964 012376 000207                    RTS      PC       ;RETURN
1965
1966                                     ;DATA SETUP ROUTINE*****
1967
1968 012400 000240                    DSUP:  NOP
1969 012402 012703 020112                    DSD:  MOV     #DATA,R3 ;R3 = ADDRS OF WRITE BUFFER
1970 012406 013701 000720                    MOV     PATTERN,R1 ;R1 = PATTERN SELECTOR
1971 012412 006301                    ASL     R1         ;MAKE PATTERN SELECTOR EVEN
1972 012414 004771 000740                    JSR     PC,DATA1(R1) ;GO GENERATE PATTERN
1973 012420 012702 000640                    MOV     #640,R2   ;R2=BUFFER SIZE +2
1974 012424 012701 021624                    MOV     #DATA,R1 ;R1=READ DATA START
1975 012430 005021                    IS:   CLR     (R1)+ ;CLEAR BUFFER
1976 012432 005302                    DEC     R2        ;SEE IF DONE ALL
1977 012434 001375                    BNE     IS       ;IF NOT: BR
1978 012436 000207                    RTS      PC       ;EXIT
1979
1980                                     ;ALL ONES*****
1981
1982 012440 012701 177777                    DAT1:  MOV     #-1,R1 ;R1=DATA
1983 012444 012702 000640                    DAT1A: MOV     #640,R2 ;R2=WORD COUNT +2
1984 012450 010123                    IS:   MOV     R1,(R3)+ ;LOAD BUFFER
1985 012452 005302                    DEC     R2        ;SEE IF DONE
1986 012454 001375                    BNE     IS       ;IF NOT: BR
1987 012456 000207                    RTS      PC
1988
1989                                     ;ALL ZEROS*****
1990
1991 012460 005001                    DAT2:  CLR     R1   ;R1=DATA
1992 012462 000770                    BR     DAT1A     ;LOAD BUFFER
1993
1994                                     ;ONE/ZERO IN ALTERNATING CHARACTERS*****
1995
1996 012464 012701 125125                    DAT3:  MOV     #125125,R1 ;R1=DATA
1997 012470 000765                    BR     DAT1A     ;LOAD BUFFER
1998
1999                                     ;ALL BITS 0-377*****
2000
2001 012472 005001                    DAT4:  CLR     R1   ;R1=STARTING DATA
2002 012474 012702 001500                    MOV     #1500,R2 ;R2=CHARACTER COUNT
2003 012500 110123                    IS:   MOV     R1,(R3)+ ;LOAD BUFFER
2004 012502 105201                    INCB   R1        ;BUMP DATA
2005 012504 005302                    DEC     R2        ;SEE IF DONE
2006 012506 001374                    BNE     IS       ;IF NOT: BR
2007 012510 000207                    RTS      PC
2008
    
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012512	000240		
012514	032777	040000	166026
012522	001001		
012524	000207		
012526	000240		
012530	005737	000674	
012534	001001		
012536	000207		
012540	022626		
012542	000177	166126	
012546	000240		
012550	032777	004000	165772
012556	001403		
012560	005037	000662	
012564	000207		
012566	005737	000730	
012572	001772		
012574	005237	000662	
012600	023737	000662	000566
012606	001764		
012610	005726		
012612	017700	166060	
012616	000110		
012620	000240		
012622	012777	000040	165670
012630	013777	000612	165662
012636	013777	000614	165676
012644	000207		

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;SCOPE LOOP ON ERROR SUBROUTINE*****
SCOPE:  NOP
        BIT      #40000, @SWR      ;SEE IF LOOP ON ERROR
        BNE     1$                  ;IF SO: BR
        RTS     PC                  ;ELSE EXIT
1$:      NOP
        TST     SCOLP              ;SEE IF SCOPE ADDRESS
        BNE     2$                  ;IF NOT: BR
        RTS     PC                  ;ELSE EXIT
2$:      CMP     (SP)+, (SP)+      ;RESET STACK
        JMP     @SCOLP             ;LOOP ON ERROR

;TEST ITERATION SUBROUTINE*****
ITER:   NOP
        BIT     #4000, @SWR       ;SEE IF ITERATIONS
        BEQ     2$                  ;IF SO: BR
        CLR     ITCNT              ;CLEAR ITERATION COUNTER
        RTS     PC                  ;ELSE EXIT
1$:      TST     PCNTR              ;DO SINGLE SUBTEST ITERATION
        BEQ     1$                  ;ON FIRST PASS
        INC     ITCNT              ;BUMP COUNTER
        CMP     ITCNT, ITAMT       ;SEE IF DONE ALL
        BEQ     1$                  ;IF SO: BR
        TST     (SP)+              ;RESET STACK
        MOV     @ITRLP, RO         ;SET ITERATION POINTER
        JMP     (RO)              ;GO ITERATE

;INITIALIZE SUBROUTINE*****
INIT1:  NOP
        MOV     #40, @CS           ;INIT
INIT2:  MOV     DRVN, @CS          ;SELECT DRIVE
        MOV     SLVN, @TC         ;SELECT SLAVE
        RTS     PC                ;RETURN
    
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2049 ;MAG TAPE INTERRUPT HANDLER*****
2050
2051 012646 000240 MTINT: NOP
2052 012650 013716 000646 MOV RTRN,(SP) ;RETURN TO (RTRN)
2053 012654 000002 RTI ;RETURN
2054
2055 ;TTY INTERRUPT HANDLER*****
2056
2057 012656 017746 165672 TTINT: MOV @TKB,-(SP) ;GET CHARACTER
2058 012662 042716 000200 BIC #200,(SP) ;CLEAR PARITY BIT
2059 012666 122716 000003 CMPB #3,(SP) ;BRANCH IF NOT CONTROL C
2060 012672 001010 BNE 1$
2061 012674 015737 001662 TST CHNFLG ;INHIBIT ↑C IF CHAIN MODE
2062 012700 001005 BNE 1$
2063 012702 011077 165640 CLR @PSW
2064 012706 000005 RESET
2065 012710 000137 000 JMP @#200 ;RESTART PROGRAM
2066 012714 122716 000001 1$: CMPB #1,(SP) ;BRANCH IF NOT ↑A
2067 012720 010117 BNE 2$
2068 012722 002737 000176 000550 CMP #SWREG,SWR ;BRANCH IF HARDWARE SWR IS INVOKED
2069 012730 001016 BNE 3$
2070 012732 012737 177570 000550 MOV #177570,SWR ;INVOKe HARDWARE SWR
2071 012740 004737 014320 JSR PC,SAVE ;SAVE REGISTERS ON THE STACK
2072 012744 012704 016743 MOV #MSG70,R4 ;TYPE 'HARDWARE SWR IN USE'
2073 012750 004737 013442 JSR PC,TTOUT
2074 012754 004737 014342 JSR PC,.RESTORE
2075 012760 122716 000007 2$: CMPB #7,(SP) ;BRANCH IF NOT ↑G
2076 012764 001005 BNE 4$
2077 012766 012737 000176 000550 3$: MOV #SWREG,SWR ;INVOKe SOFTWARE SWR
2078 012774 004737 014222 JSR PC,GTSWR ;GET SOFTWARE SWITCHES
2079 013000 005726 4$: TST (SP)+ ;POP CHARACTER OFF THE STACK
2080 013002 000002 RTI
2081
2082 ;BUS ADDRESS TRAP HANDLER*****
2083
2084 013004 000240 TRAP: NOP
2085 013006 032777 020000 165534 BIT #20000,@SWR ;SEE IF SHOULD PRINT ERRORS
2086 013014 001020 BNE TRAP2 ;IF NOT: BR
2087 013016 005737 000606 TST HORFL ;SEE IF DONE HEADER
2088 013022 001006 BNE TRAP1 ;IF SO: BR
2089 013024 005237 000606 INC HORFL ;ELSE SET HEADER FLAG
2090 013030 013704 000610 MOV EMADDR,R4
2091 013034 004737 013442 JSR PC,TTOUT ;PRINT HEADER
2092 013040 012704 015171 TRAP1: MOV #MSG24,R4
2093 013044 004737 013442 JSR PC,TTOUT ;PRINT ERROR
2094 013050 010103 MOV R1,R3 ;GET ADDRESS THAT CAUSED THE TRAP
2095 013052 004737 013572 JSR PC,OCTP ;PRINT ADDRESS OF TRAP
2096 013056 005777 165466 TRAP2: TST @SWR ;SEE IF HALT ON ERROR
2097 013058 100001 BPL TRAPX ;IF NOT: BR
2098 013064 000000 HALT
2099 013066 022626 TRAPX: CMP (SP)+,(SP)+ ;RESET STACK
2100 013070 012737 003250 000674 MOV #FTIA,SCOLP ;SET SCOPE ADDRESS
2101 013076 004737 012512 JSR PC,SCOPE ;GO SEE IF SCOPE LOOP
2102 013102 005737 000722 TST RHTF ;SEE IF INITIAL ADDRESS TEST
2103 013106 001402 BEQ TRAPXX ;IF NOT: BR
2104 013110 000137 001766 JMP STOB ;ELSE REDO ADDRESS REQUEST
    
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G05

TMO3/TE16 BASIC FUNCTION TEST MACY11 27(1006) 31-MAR-77 17:23 PAGE 58
C 29-MAR-77 09:54

2105 013114 000137 003254
2106

TRAPXX: JMP FT1B

;RETURN TO TEST 1

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013120 010146
013122 011601
013124 005037 000652
013130 005000
013132 004737 013400
013136 122737 000003 000602
013144 001003
013146 001005
013150 000137 000200
013154 122737 000015 000602
013162 001004
013164 005737 000652
013170 001471
013172 000457
013174 122737 000025 000602
013202 001005
013204 012704 016663
013210 004737 013442
013214 000742
013216 122737 000177 000602
013224 001012
013 8 000241
013 0 006000
013232 006200
013234 006200
013236 012704 016665
013242 004737 013442
013246 005201
013250 005730
013252 122737 000060 000602
013260 101402
013262 000137 013360
013266 122737 000070 000602
013274 101002
013276 000137 013360
013302 005237 000652
013306 006300
013310 006300
013312 006300

TTY ENTRY SUBROUTINE:

THIS SUBROUTINE IS USED BY THE TEST CONDITION
ENTRY ROUTINE TO READ THE RESPONSE ENTERED
AT THE TTY AND CHECK THEM FOR LEGALITY AND
LIMITS. ALL RESPONSE MUST BE TYPED IN OCTAL
(0-7) AND MUST FALL WITHIN THE LIMITS SET BY
THE CALLING ROUTINE.
IF AN ENTRY IS ILLEGAL OR OUTSIDE THE LIMITS,
A QUESTION MARK IS TYPED (?) AND THE RESPONSE
MAY BE REENTERED.
ENTRIES MAY NOT EXCEED SIX (6) CHARACTERS AND
MAY BE TERMINATED AT LESS THAN SIX BY TYPING A
CARRIAGE RETURN

TTR: MOV R1, -(SP) ;SAVE CHAR COUNT ON STACK
10S: MOV (SP), R1 ;RESTORE CHAR COUNT (FOR ↑U)
CLR TEMP1 ;CLEAR FIRST CHARACTER FLAG
CLR RO
1S: JSR PC, TTIN ;GO READ CHARACTER
CMPB #3, TIB ;BRANCH IF NOT ↑C
BNE 11S
RESET ;RESET
JMP #200 ;RESTART
11S: CMPB #15, TIB ;SEE IF CR
BNE 2S ;IF NOT: BR
TST TEMP1 ;SEE IF FIRST CHARACTER
BEQ 9S ;IF SO: BR
BR 6S ;ELSE GO LOAD VALUE
2S: CMPB #25, TIB ;BRANCH IF NOT CONTROL U
BNE 21S
MOV #MSG65, R4 ;TYPE <CR><LF>
JSR PC, TTOUT
BR 10S ;RESTART
21S: CMPB #177, TIB ;BRANCH IF NOT 'RUBOUT'
BNE 3S
CLC ;REMOVE LAST CHARACTER
ROR RO
ASR RO
ASR RO
MOV #MSG66, R4 ;TYPE '\'
JSR PC, TTOUT
INC R1 ;DECREMENT CHAR RECEIVED COUNT
BR 1S ;GET NEXT CHARACTER
3S: CMPB #60, TIB ;SEE IF CHAR IS LESS THAN 0
BLOS 4S ;IF NOT: BR
JMP T1NER ;ELSE GO TO ERROR
4S: CMPB #70, TIB ;SEE IF CHAR IS GREATER THAN 7
BHI 5S ;IF NOT: BR
JMP T1NER ;ELSE GO TO ERROR
5S: INC TEMP1 ;SET FIRST CHARACTER FLAG
ASL RO
ASL RO ;SHIFT 3 LEFT
ASL RO

2163	013314	042737	177770	000602	BIC	#177770, TIB	: STRIP ASCII
2164	013322	053700	000602		BIS	TIB, R0	: LOAD CHARACTER
2165	013326	005301			DEC	R1	: SEE IF DONE
2166	013330	001300			BNE	1\$: IF NOT: BR
2167	013332	020002		6\$:	CMP	R0, R2	: SEE IF EXCEEDED MAXIMUM LIMIT
2168	013334	101402			BLOS	7\$: IF NOT: BR
2169	013336	000137	013360		JMP	TINER	: ELSE GO TO ERROR
2170	013342	020300		7\$:	CMP	R3, R0	: SEE IF BELOW MINIMUM LIMIT
2171	013344	101402			BLOS	8\$: IF NOT: BR
2172	013346	000137	013360		JMP	TINER	: ELSE GO TO ERROR
2173	013352	010015		8\$:	MOV	R0, (R5)	: LOAD VALUE
2174	013354	005726		9\$:	TST	(SP)+	: POP CHAR COUNT OFF STACK
2175	013356	000207			RTS	PC	: EXIT
2176							
2177							
2178							
2179	013360	012704	014677		TINER:	MOV	#MSG7, R4
2180	013364	004737	013442			JSR	PC, TTOUT
2181	013370	005726				TST	(SP)+
2182	013372	162716	000020			SUB	#20, (SP)
2183	013376	000207				RTS	PC
2184							
2185							
2186							
2187	013400	005277	165146		TTIN:	INC	@TKS
2188	013404	105777	165142		1\$:	TSTB	@TKS
2189	013410	100375				BPL	1\$
2190	013412	117737	165136	000602		MOVB	@TKB, TIB
2191	013420	042737	000200	000602		BIC	#200, TIB
2192	013426	013737	000602	000600		MOV	TIB, TOB
2193	013434	004737	013542			JSR	PC, TOG
2194	013440	000207				RTS	PC
2195							
2196							
2197							
2198	013442	112437	000600		TTOUT:	MOVB	(R4)+, TOB
2199	013446	122737	000043	000600		CMPB	#43, TOB
2200	013454	001440				BEQ	TEX
2201	013456	122737	000045	000600		CMPB	#45, TOB
2202	013464	001403				BEQ	1\$
2203	013466	004737	013542			JSR	PC, TOG
2204	013472	000763				BR	TTOUT
2205	013474	112737	000015	000600	1\$:	MOVB	#15, TOB
2206	013502	004737	013542			JSR	PC, TOG
2207	013506	012703	000004			MOV	#4, R3
2208	013512	005037	000600		2\$:	CLR	TOB
2209	013516	004737	013542			JSR	PC, TOG
2210	013522	005303				DEC	R3
2211	013524	001372				BNE	2\$
2212	013526	112737	000012	000600		MOVB	#12, TOB
2213	013534	004737	013542			JSR	PC, TOG
2214	013540	000740				BR	TTOUT
2215	013542	105777	165010		TOG:	TSTB	@TPS
2216	013546	100375				BPL	TOG
2217	013550	113777	000600	165002		MOVB	TOB, @TPB
2218	013556	000207			TEX:	RTS	PC

; TTY ENTRY ERROR SUBROUTINE*****

: PRINT?
 : POP CHAR COUNT OFF STACK
 : RESET SP TO START OF VALUE ROUTINE
 : REDO VALUE ENTRY

; TTY READ SUBROUTINE*****

: STRIP PARITY BIT
 : MOVE CHAR TO OUTPUT BFR
 : AND TYPE IT

; TTY OUTPUT SUBROUTINE*****

; DO FILLERS

J05

TM03/TE16 BASIC FUNCTION TEST MACY11 27(1006) 31-MAR-77 17:23 PAGE 61
C 29-MAR-77 09:54

2219
2220

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2221 ;OCTAL OUTPUT SUBROUTINE*****
2222
2223 013560 012737 000001 014010 OCTPE: MOV #1,OFL
2224 013566 010304 MOV R3,R4
2225 013570 000410 BR OCTPG
2226 013572 005037 014010 OCTP: CLR OFL ;CLEAR FLAG FOR LEADING ZERO
2227 013576 010304 OCTPE1: MOV R3,R4 ;SEE IF NUMBER IS ZERO
2228 013580 001004 BNE OCTPG ;IF NOT ZERO: BR
2229 013582 004737 013770 JSR PC,OCTPG1 ;ELSE PRINT ZERO
2230 013586 000137 013732 JMP OCTPG3 ;SPACE AND EXIT
2231 013590 000704 100000 OCTPG: BIT #100000,R4 ;SEE IF MSD = 1
2232 013594 001406 BEQ OCTPG1 ;IF NOT: BR
2233 013598 012704 000001 MOV #1,R4
2234 013602 004737 013746 JSR PC,OCTPG ;PRINT 1
2235 013606 000137 013642 JMP OCTPG2
2236 013610 000004 OCTP1: CLR R1 ;PRINT 0
2237 013614 004737 013746 JSR PC,OCTPG
2238 013618 010304 OCTP2: MOV R3,R4
2239 013622 000004 ROR R4,R4
2240 013626 000004 ROR R4,R4 ;POSITION DIGIT
2241 013630 000004 ROR R4,R4
2242 013634 000004 ROR R4,R4
2243 013638 000004 SLAB
2244 013642 000037 013746 JSR PC,OCTPG ;PRINT DIGIT 2
2245 013646 000004 MOV R3,R4
2246 013650 000004 ROR R4,R4
2247 013654 000004 SLAB
2248 013658 004737 013746 JSR PC,OCTPG ;PRINT DIGIT 3
2249 013662 010304 MOV R3,R4
2250 013666 006104 ROL R4,R4
2251 013670 006104 ROL R4,R4
2252 013674 000304 SLAB
2253 013678 004737 013746 JSR PC,OCTPG ;PRINT DIGIT 4
2254 013682 010304 MOV R3,R4
2255 013686 006004 ROR R4,R4
2256 013690 006004 ROR R4,R4
2257 013694 006004 ROR R4,R4
2258 013698 004737 013746 JSR PC,OCTPG
2259 013702 010304 MOV R3,R4
2260 013706 004737 013746 JSR PC,OCTPG ;PRINT DIGIT 5
2261 013710 012737 000240 000600 OCTP3: MOV #240,T08 ;PRINT SPACE
2262 013714 004737 013542 JSR PC,T08 ;EXIT
2263 013718 000207 RTS PC
2264 013722 042704 177770 OCTPG: BIC #177770,R4
2265 013726 001004 BNE OCTPG0
2266 013730 005737 014010 TST OFL
2267 013734 001001 BNE OCTPG0
2268 013738 000207 RTS PC
2269
2270 013742 005237 014010 OCTPG0: INC OFL
2271 013746 052704 000260 OCTPG1: BIS #260,R4
2272 013750 010437 000600 MOV R4,T08
2273 014000 004737 013542 JSR PC,T08
2274 014004 010304 MOV R3,R4
2275 014008 000207 RTS PC
2276 014012 000000 OFL: 0 ;FIRST CHAR FLAG

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2277
2278 ;DATA CHARACTER OUTPUT SUBROUTINE*****
2279
2280 014012 005037 000600 DOUT: CLR T08
2281 014016 012704 000010 MOV #10,R4 ;SET NUMBER TO PRINT
2282 014022 110337 000600 MOVB R3,T08
2283 014026 105777 164524 1S: TSTB #TPS
2284 014032 100375 BPL 1S
2285 014034 132737 000200 000600 BITB #200,T08
2286 014042 001404 BEQ 2S
2287 014044 012777 000061 164506 MOV #061,#TPB
2288 014052 000403 BR 3S
2289 014054 012777 000060 164476 2S: MOV #060,#TPB
2290 014062 006137 000600 3S: ROL T08
2291 014066 005304 DEC R4
2292 014070 001356 BNE 1S
2293 014072 000207 RTS PC
2294
2295 014074 013703 000656 DOUTD: MOV TEMP3,R3
2296 014100 000303 SWAB R3
2297 014102 004737 014012 JSR PC,DOUT
2298 014106 013703 000656 MOV TEMP3,R3
2299 014112 004737 014012 JSR PC,DOUT
2300 014116 000207 RTS PC
2301
2302 ;TE16 SERIAL NUMBER PRINT SUBROUTINE*****
2303
2304 014120 010304 SNPT: MOV R3,R4
2305 014122 000304 SWAB R4
2306 014124 006004 ROR R4
2307 014126 006004 ROR R4
2308 014130 006004 ROR R4
2309 014132 006004 ROR R4 ;GET FIRST DIGIT
2310 014134 004737 014176 JSR PC,SNPG ;GO PRINT
2311 014140 010304 MOV R3,R4
2312 014142 000304 SWAB R4 ;GET SECOND DIGIT
2313 014144 004737 014176 JSR PC,SNPG ;GO PRINT
2314 014150 010304 MOV R3,R4
2315 014152 006004 ROR R4
2316 014154 006004 ROR R4
2317 014156 006004 ROR R4
2318 014160 006004 ROR R4 ;GET THIRD DIGIT
2319 014162 004737 014176 JSR PC,SNPG ;GO PRINT
2320 014166 010304 MOV R3,R4 ;GET FOURTH DIGIT
2321 014170 004737 014176 JSR PC,SNPG ;GO PRINT
2322 014174 000207 RTS PC ;EXIT
2323 014176 012737 000260 000600 SNPG: MOV #260,T08 ;SET BASE = 0
2324 014204 042704 177760 BIC #177760,R4 ;MASK DIGIT
2325 014210 050437 000600 BIS R4,T08 ;SET ASCII
2326 014214 004737 013542 JSR PC,TOG ;TYPE DIGIT
2327 014220 000207 RTS PC ;RETURN
2328

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2329
2330 ;ROUTINE TO LOAD NEW VALUE INTO SWITCHES
2331 014222 022737 000176 000550 GTSWR: CMP #SWREG,SWR ;BRANCH IF SOFTWARE SWR
2332 014220 001032 BNE IS ;NOT INVOKED
2333 014222 004737 014320 JSR PC,SAVE ;SAVE REGISTERS ON THE STACK
2334 014222 012704 020127 MOV #R3,R4
2335 014222 004737 013120 JSR PC,TTOUT
2336 014222 017703 164276 MOV #SWR,R3
2337 014222 004737 013120 JSR PC,OCPE
2338 014222 012704 020127 MOV #RNEW,R4
2339 014222 004737 013120 JSR PC,TTOUT
2340 014222 013705 020127 MOV SWR,RS ;TTR ROUTINE RETURNS NEW VALUE TO (RS)
2341 014272 012701 020127 MOV #7,R1 ;LIMIT RESPONSE TO 7 CHARS
2342 014276 012702 177777 MOV #177777,R2 ;BETWEEN 0 AND 177777
2343 014302 012703 000000 MOV #0,R3
2344 014306 004737 013120 JSR PC,TTR
2345 014312 004737 014342 JSR PC,.RESTORE ;RESTORE REGISTERS
2346 014316 000207 IS: RTS PC
2347
2348 ;:ROUTINE TO SAVE REGISTERS ON THE STACK
2349 014320 010546 .SAVE: MOV #5,-(SP) ;;R5 IS SAVED AT 12(SP)
2350 014322 010446 MOV #4,-(SP) ;;R4 IS SAVED AT 10(SP)
2351 014324 010346 MOV #3,-(SP) ;;R3 IS SAVED AT 6(SP)
2352 014326 010246 MOV #2,-(SP) ;;R2 IS SAVED AT 4(SP)
2353 014330 010146 MOV #1,-(SP) ;;R1 IS SAVED AT 2(SP)
2354 014332 010046 MOV #0,-(SP) ;;R0 IS SAVED AT (SP)
2355 014334 016646 000014 MOV 14(SP),-(SP) ;;PUSH RETURN PC ON THE STACK
2356 014340 000207 RTS PC ;;RETURN TO CALLER
2357
2358 ;:ROUTINE TO RESTORE REGISTERS SAVED ON THE STACK
2359 014342 012666 000014 .RESTORE:MOV (SP)+,14(SP) ;;STORE RETURN PC ON STACK
2360 014346 012600 MOV (SP)+,%0
2361 014350 012601 MOV (SP)+,%1
2362 014352 012602 MOV (SP)+,%2
2363 014354 012603 MOV (SP)+,%3
2364 014356 012604 MOV (SP)+,%4
2365 014360 012605 MOV (SP)+,%5
2366 014362 000207 RTS PC ;;RETURN
2367
2368

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2369
2370
2371 014364 041445 030523 020040
2372 014372 020040 041527 020040
2373 014400 041040 041040 020101
2374 014406 041040 041506
2375 014414 020040 041440
2376 014422 031123 041040
2377 014430 051504 041040
2378 014438 041040 041445
2379 014446 041040 047111
2380 014454 041040 047522
2381 014462 041040
2382 014470 041040
2383 014478 041040
2384 014486 041040
2385 014494 041040
2386 014502 041040
2387 014510 041040
2388 014518 041040
2389 014526 041040
2390 014534 041040
2391 014542 041040
2392 014550 041040
2393 014558 041040
2394 014566 041040
2395 014574 051506 047520 051516
2396 014600 041040 020046 041536
2397 014606 041040 020117 042522
2398 014614 052123 051101 022524
2399 014622 043
2400 014623 043
2401 014630 052123 042522 044507
2402 014636 040524 051105 051440
2403 014644 021446 052122 036440
2404 014646 053044
2405 014654 020122 041505 047524
2406 014661 045 020075 043
2407 014666 043117 047105 020104
2408 014674 020123 050040 051501
2409 014677 040 020077 043
2410 014703 045 047520 044523
2411 014710 044524 047117 042440
2412 014716 051122 051117 020072
2413 014724 043
2414 014725 043
2415 014732 020105 051104 053111
2416 014740 051105 042516 041115
2417 014745 045 040072 043
2418 014752 020105 046123 053101
2419 014760 051105 052516 041115
2420 014765 045 020072 043
2421 014772 020105 051127 052111
2422 015000 020122 043
2423 015010 045 042522 042101
2424 015016 042523 053105 051105
042440 051122

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;MESSAGE TABLE*****

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MSG1: .ASCII /XCSI WC BA FC CS2 /
MSG2: .ASCII /%REWIND ERROR#/
MSG3: .ASCII /%XTMO3-TE16 BASIC FUNCTION TEST (DZTEC-A)%/
MSG4: .ASCII /%REGISTER START = #/
MSG5: .ASCII /%VECTOR = #/
MSG6: .ASCII /%END OF PASS #/
MSG7: .ASCII / ? #/
MSG9: .ASCII /%POSITION ERROR: #/
MSG10: .ASCII /%DRIVE NUMBER: #/
MSG11: .ASCII /%SLAVE NUMBER: #/
MSG12: .ASCII /%WRITE ERROR #/
MSG13: .ASCII /%READ REVERSE ERROR #/

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020104	MSG14: .ASCII	/XREAD FORWARD ERROR #/
051101		
047522		
052111	MSG15: .ASCII	/XWRITE TM ERROR #/
042440		
0521440		
051105	MSG16: .ASCII	/XREVERSE ERROR #/
051122		
040527	MSG17: .ASCII	/XFORWARD ERROR #/
051122		
021440	MSG20: .ASCII	/ NRZ #/
043	MSG21: .ASCII	/ PE #/
052120	MSG22: .ASCII	/ EXPT: #/
042126	MSG23: .ASCII	/ RCVD: #/
020123	MSG24: .ASCII	/XBUS TRAP: #/
020072		
041527	MSG25: .ASCII	/XAC: #/
020072		
040502	MSG26: .ASCII	/XBA: #/
020072		
041104	MSG27: .ASCII	/XDB: #/
020072		
047111	MSG28: .ASCII	/XINIT DID NOT CLEAR RH #/
052111		
042111		
047040		
041440		
042514		
051040		
020110		
047040	MSG29: .ASCII	/XSC NOT RESET BY INIT #/
051505		
020131		
051740		
0517040	MSG30: .ASCII	/XTRE NOT RESET BY INIT #/
051505		
020131		
021440		
047040	MSG31: .ASCII	/XCS2 NOT RESET BY INIT #/
051505		
020131		
021440		
047040	MSG32: .ASCII	/XDLT NOT SET #/
052105		
020103	MSG33: .ASCII	/XSC NOT SET #/
047516		
042523		
020124		
051122	MSG34: .ASCII	/XTRE NOT SET #/
020105		
042523		
043		
051111	MSG35: .ASCII	/XIR NOT SET BY INIT #/
047040		

2623	016566	030075	054454	051505
2624	016571	047516	035051	021440
2625	016580	052117	042111	047040
2626	016610	020117	040440	052125
2627	016616	052103	042523	042514
2628	016624	043	047040	055122
2629	016632	043		
2630	016633	045	044504	020104
2631	016640	047516	020124	0501
2632	016646	047524	051440	046105
2633	016654	041505	020124	042520
2634	016662	043		
2635	016663	043		
2636	016665	045	051105	020072
2637	016674	043		
2638	016675	045	042522	047515
2639	016678	042526	052040	042115
2640	016702	020128	051106	046517
2641	016710	051440	040514	042526
2642	016716	052040	020117	042502
2643	016724	052040	051505	042524
2644	016732	022504	043	
2645	016740	045	040510	042122
2646	016743	045	042522	051440
2647	016750	040527	044440	020116
2648	016756	051127	044440	
2649	016764	051525	022505	043
2620				

MSG63: .ASCII /%DID NOT AUTO SELECT NRZ%/

MSG64: .ASCII /%DID NOT AUTO SELECT PE%/

MSG65: .ASCII /%#/

MSG66: .ASCII /%#/

MSG67: .ASCII /%ER: #/

MSG69: .ASCII /%REMOVE TMDP FROM SLAVE TO BE TESTED%/

MSG70: .ASCII /%HARDWARE SWR IN USE%/

;TEST HEADERS*****

016771	045	043045	030524	MSFT1: .ASCII	/%FT1:RH ADDRESSING #/
016776	051072	020110	042101		
017004	051104	051505	044523		
017012	043516	021440			
017016	022445	052106	035062	MSFT2: .ASCII	/%FT2:RH REGISTER BITS TEST #/
017022	044122	051016	043505		
017027	051511	047514	020122		
017033	044505	051524	020440		
017046	051505	020124	043		
017053	043045	043045	031524	MSFT3: .ASCII	/%FT3:RH INITIALIZE TEST #/
017060	051072	020110	047111		
017068	044523	047514	04514		
017074	042524	052040	051505		
017102	020124	043			
017105	045	043045	032124	MSFT4: .ASCII	/%FT4:RH1 SILO TEST 1 #/
017112	051072	030510	020061		
017120	044523	047514	052040		
017126	051505	020124	020061		
017134	043				
017138	043	043045	032524	MSFT5: .ASCII	/%FT5:RH1 SILO TEST 2 #/
017142	051072	030510	020061		
017150	044523	047514	052040		
017158	051505	020124	020062		
017165	043				
017172	051072	043045	033124	MSFT6: .ASCII	/%FT6:RH1 SILO TEST 3 #/
017200	044523	030510	020061		
017206	051505	047514	052040		
017214	020124	020124	020063		
017215	043				
017222	051072	043045	033524	MSFT7: .ASCII	/%FT7:RH1 SILO TEST 4 #/
017230	044523	030510	020061		
017236	051505	047514	052040		
017244	043	020124	020064		
017245	043				
017253	045	043045	030524	MSFT10: .ASCII	/%FT10:RH1 SILO TEST 5 #/
017258	035062	044122	030461		
017260	051440	046111	020117		
017266	042524	052123	032440		
017274	021440				
017276	022445	052106	030461	MSFT11: .ASCII	/%FT11:NOP TEST#/
017304	047072	050117	052040		
017312	051505	021524			
017316	022445	052106	031061	MSFT12: .ASCII	/%FT12:REWIND TEST#/
017324	051072	053505	047111		
017332	020104	042524	052123		
017340	043				
017341	043				
017346	035062	043045	030524	MSFT13: .ASCII	/%FT13:WRITE-READ TEST#/
017354	026505	051127	052111		
017362	052040	042522	042101		
017370	022445	051505	021524		
017376	051472	052106	032061	MSFT14: .ASCII	/%FT14:SPACE TEST#/
017404	052040	040520	042503		
017412	022445	051505	021524		
		052106	032461	MSFT15: .ASCII	/%FT15:ERASE TEST#/

2677	017420	042472	040522	042523	
2678	017426	052040	051505	021524	
2679	017434	012445	052106	033061	MSFT16: .ASCII /%%FT16:TAPE MARK WRITE-READ TEST#/ #/
2680	017442	01072	050101	020105	
2681	017450	04515	045522	053440	
2682	017456	044522	042524	051055	
2683	017464	040505	020104	042524	
2684	017472	052123	043043		
2685	017475	043043	043043	030524	MSFT17: .ASCII /%%FT17:TM SPACE TEST #/ #/
2686	017502	035062	046224	051440	
2687	017510	040520	042503	052040	
2688	017516	051505	020124	043	
2689	017523	043045	043045	031124	MSFT20: .ASCII /%%FT20:WRITE CHECK TEST #/ #/
2690	017530	035062	051127	052111	
2691	017536	020106	044103	041505	
2692	017544	020113	042524	052123	
2693	017552	021440			
2694	017554	022445	052106	030462	MSFT21: .ASCII /%%FT21:ERASE HEAD TEST#/ #/
2695	017562	042472	043043	042523	
2696	017570	044040	043043	020104	
2697	017576	042524	052123	043	
2698	017603	045	043045	031124	MSFT22: .ASCII /%%FT22:BUFFERED COMMAND TEST#/ #/
2699	017610	035062	04503	043106	
2700	017616	051105	045105	041440	
2701	017624	046517	04515	042116	
2702	017632	052040	051505	021524	
2703	017640	022445	052106	031462	MSFT23: .ASCII /%%FT23:READ IN PRESET TEST#/ #/
2704	017646	051072	04505	020104	
2705	017654	047111	053040	042522	
2706	017662	042523	020124	042524	
2707	017670	052123	043		
2708	017673	045	043043	031124	MSFT26: .ASCII /%%FT26:REWIND-OFF LINE TEST#/ #/
2709	017700	035062	042523	044527	
2710	017706	042116	04745	043106	
2711	017714	046040	047111	020105	
2712	017722	042524	052123	043	
2713	017727	045	043045	031124	MSFT24: .ASCII /%%FT24:AUTO DENSITY SELECT: WRITE-NRZ, READ-PE#/ #/
2714	017734	035062	052501	047524	
2715	017742	042040	047105	044523	
2716	017750	054524	051440	046105	
2717	017756	041505	035124	053440	
2718	017764	044522	042524	047055	
2719	017772	055122	051054	040505	
2720	020000	026504	042520	043	
2721	020005	045	043045	031124	MSFT25: .ASCII /%%FT25:AUTO DENSITY SELECT: WRITE-PE, READ-NRZ#/ #/
2722	020012	035062	052501	047524	
2723	020020	042040	047105	044523	
2724	020026	054524	051440	046105	
2725	020034	041505	035124	053440	
2726	020042	044522	042524	050055	
2727	020050	026105	042522	042101	
2728	020056	047055	055122	043	
2729	020063	045	043536	043	SCNTG: .ASCII /%IG#/ #/
2730	020067	045	053523	036522	SMSWR: .ASCII /%SMR= #/ #/
2731	020074	021440			
2732	020076	020040	042516	036527	SMNEW: .ASCII / NEW= #/ #/

H06

TH03/TE16 BASIC FUNCTION TEST MACY11 27(1006) 31-MAR-77 17:23 PAGE 72
C 29-MAR-77 09:54

2733	020104	021440		
2734	020106	022477	043	\$QUEST: .ASCII /?%#/
2735				
2736				
2737		020112		WDATA: .EVEN
2738	020112	000000		0
2739		021624		.=.+1510
2740	021624	000000		RDATA: 0
2741				
2742		000001		.END

AS	000526	EXECX	011666	FT17D	007376	FT4X	004132	MSFT4	017105
BA	000514	EXECXA	011672	FT17D1	007414	FT5	004206	MSFT5	017135
BADDR	000616	EXECXX	011676	FT17E	007430	FT5A	004230	MSFT6	017165
BAE	000544	EXFL	000700	FT17F	007520	FT5B	004256	MSFT7	017215
BTRP	000574	FC	000516	FT17X	007542	FT5C	004300	MSG1	014364
BTRP2	000576	FCNT	000620	FT2	003316	FT5D	004330	MSG10	014725
CC	000530	FT1	003216	FT2A	003330	FT5E	004342	MSG11	014745
CHNPLG	001662	FT1A	003250	FT2B	003370	FT5X	004370	MSG12	014765
COUNT	000734	FT1B	003254	FT2C	003430	FT6	004400	MSG13	015003
CRCNT	000714	FT1X	003266	FT2D	003444	FT6A	004422	MSG14	015030
CS	000520	FT1XX	003312	FT2E	003474	FT6B	004430	MSG15	015055
C1	000510	FT10	004746	FT2ER	003504	FT6C	004466	MSG16	015076
DATA0	000742	FT10A	005002	FT2ERA	003534	FT6D	004510	MSG17	015116
DATA1	000744	FT10ER	005066	FT2ERB	003606	FT6DE	004542	MSG2	014452
DATA2	000746	FT10X	005044	FT2ERC	003616	FT6DEA	004574	MSG20	015136
DATA3	000750	FT10XX	005062	FT2X	003626	FT6DEB	004640	MSG21	015144
DATBL	000740	FT11	005102	FT20	007546	FT6DEX	004650	MSG22	015151
DAT1	012440	FT12	005220	FT20A	007564	FT6E	004512	MSG23	015161
DAT1A	012444	FT12A	005300	FT20B	007710	FT6X	004532	MSG24	015171
DAT2	012460	FT13	005332	FT20C	007742	FT7	004652	MSG25	015205
DAT3	012464	FT13A	005404	FT20X	007762	FT7A	004704	MSG26	015213
DAT4	012472	FT13B	005446	FT21	007772	FT7ER	004732	MSG27	015221
DB	000532	FT13C	005526	FT21A	010000	FT7X	004722	MSG28	015227
DOUT	014012	FT13D	005574	FT21B	010274	FLN	000710	MSG29	015257
DOUTD	014074	FT13X	005640	FT21C	010302	GTSWR	014222	MSG3	014470
DRVN	000612	FT14	005644	FT21SC	010130	HDR&L	000606	MSG30	015306
DRVTP	000564	FT14A	005716	FT21X	010314	HERE	003164	MSG31	015336
DS	000522	FT14A1	005662	FT22	010324	INIT1	012620	MSG32	015366
DSAV	000664	FT14A2	006010	FT22A	010400	INIT2	012630	MSG33	015404
DSUP	012400	FT14A3	006032	FT22B	010420	ITAMT	000566	MSG34	015421
DSO	012402	FT14B	006062	FT22X	010520	ITCNT	000662	MSG35	015437
DT	000536	FT14EC	006300	FT23	010530	ITER	012546	MSG36	015464
EMADDR	000610	FT14E	006306	FT23A	010640	ITRLP	000676	MSG37	015513
ER	000524	FT14EF	006252	FT23B	010660	LTADD	000706	MSG38	015552
ERADD	000650	FT14ER	006210	FT23C	010710	MR	000534	MSG39	015604
ERCHK	011776	FT14RR	006250	FT23X	010744	MSFT1	016771	MSG4	014623
ERPT	012022	FT14R0	006360	FT24	010750	MSFT10	017245	MSG40	015624
ERPTA	012112	FT14R1	006402	FT24X	011132	MSFT11	017276	MSG41	015657
ERPTA1	012044	FT14R2	006406	FT25	011142	MSFT12	017316	MSG42	015700
ERPTB	012150	FT14R3	006460	FT25X	011324	MSFT13	017341	MSG43	015721
ERPTB1	012162	FT14X	006470	FT26	011334	MSFT14	017370	MSG44	015736
ERPTC	012212	FT14XX	006512	FT26X	011532	MSFT15	017412	MSG45	015766
ERPTD	012322	FT15	006516	FT26XX	011542	MSFT16	017434	MSG46	016043
ERPTG	012052	FT15A	006564	FT3	003640	MSFT17	017475	MSG47	016067
ERPTX	012332	FT15B	006602	FT3A	003714	MSFT2	017016	MSG48	016114
ERPTXX	012336	FT15X	006712	FT3B	003736	MSFT20	017523	MSG49	016141
ERRP	000626	FT16	006716	FT3ER	003774	MSFT21	017554	MSG5	014646
ERRP1	000630	FT16A	006750	FT3X	003764	MSFT22	017603	MSG50	016167
EXEC	011546	FT16B	006754	FT4	004060	MSFT23	017640	MSG51	016225
EXECA	011620	FT16X	007134	FT4ER	004142	MSFT24	017727	MSG52	016261
EXECB	011626	FT17	007144	FT4ERA	004152	MSFT25	020005	MSG53	016300
EXECC	011646	FT17A	007164	FT4ERB	004162	MSFT26	017673	MSG54	016325
		FT17B	007170	FT4ERC	004170	MSFT3	017053	MSG55	016457
		FT17C	007330					MSG56	

MSG57	016467	OCTP3	013732	SCNT	000642	TEMP3	000656	TSCD3	003034
MSG6	014661	OFL	014010	SCOLP	000674	TEND	003110	TSRH	002750
MSG60	016503	OPDYX	000540	SCOPE	012512	TENDX	003206	TSTBL	000752
MSG62	016552	PATRN	000720	SERFL	000712	TEX	013556	TTIN	013400
MSG63	016602	PCNTR	000730	SERNUM	000562	TIB	000602	TTINT	012656
MSG64	016633	PEXFL	000702	SLVN	000614	TINER	013360	TTOUT	013442
MSG65	016663	PFLG	000644	SN	000540	TKB	000554	TTR	013120
MSG66	016665	PSM	000546	SMPG	014176	TKS	000552	UDES	000716
MSG67	016667	RCNT	000624	SNPT	014120	TLAST	001110	VECT	000570
MSG69	016675	ROATA	021624	START	001600	TMCHK	012340	MC	000512
MSG7	014677	RDSM	000736	STFLG	000704	TMCHKO	012350	MCNT	000622
MSG70	016743	ROYDX	000536	STMSK	000660	TMCHKY	012352	MDATA	020112
MSG9	014703	REGS	000572	STSCD	003054	TOB	000600	SCNTG	020063
MTINT	012646	RFD	000534	STO	002126	TOG	013542	SDONE	003116
NR20F	000724	RHOF	000726	STOB	001766	TPB	000560	SENDAD	003154
NXTDRV	002616	RHTF	000722	ST1	002150	TPS	000556	SMNEW	020076
NXTSLV	002670	RH17F	000604	ST1A	002170	TRAP	013004	SMSWR	020067
OCTP	013572	RRO	000632	ST2	002276	TRAPX	013066	SQUEST	020105
OCTPE	013560	RTRN	000646	ST3	002406	TRAPXX	013114	SSVPC =	000764
OCTPE1	013576	RWND	011700	ST4	002542	TRAP1	013040	=	021626
OCTPG	013746	RWNOA	011720	SWR	000550	TRAP2	013056	.RESTO	014342
OCTPG0	013764	RWNOB	011724	SWREG	000176	TSCD	002556	.SAVE	014320
OCTPG1	013770	RWNOX	011770	TC	000542	TSCDA	002746		
OCTPO	013612	SAV1	000666	TEMPST	000732	TSCDB	002756		
OCTP1	013634	SAV2	000670	TEMP1	000652	TSCD1	002764		
OCTP2	013642	SAV3	000672	TEMP2	000654	TSCD2	003022		

. ABS. 021626 000

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

DZTECA,DZTECA/SOL+C.DOC,C
 RUN-TIME: 3 5 .3 SECONDS
 RUN-TIME RATIO: 36/8=4.1
 CORE USED: 6K (11 PAGES)