

# RP04/05/06

## FUNCTION CONTROL PART 1 MD-11-DZRJI-A

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FICHE 1 OF 2 MADE IN US

DZRJI A  
SEQ

The main body of the document consists of a 10x10 grid of small technical diagrams. Each diagram in the grid represents a specific function or control element of the MD-11 aircraft's control system. The diagrams are arranged in a regular, repeating pattern across the page, with each cell containing a small-scale version of a technical drawing. The overall layout is highly organized and systematic, typical of a technical manual or a control panel layout for a complex aircraft system.



# RP04/05/06

FUNCTION CONTROL PART 1  
MD-11-DZRJI-A

EP-DZRJI-A-DL-A

NOV 1976

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FICHE 2 OF 2

MADE IN USA

This microfiche card contains a grid of 100 frames of technical data, arranged in 10 rows and 10 columns. Each frame contains a small, dense table of information, likely related to the function control part of the MD-11-DZRJI-A system. The data is organized into columns and rows, with some frames containing headers and footers. The overall layout is a structured grid of technical specifications.





## IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZRJI-A-D  
PRODUCT NAME: RPO4/5/6 FUNCTIONAL CONTROLLER TEST PART I  
DATE CREATED: MAY 1976  
MAINTAINER: DIAGNOSTIC ENGINEERING  
AUTHOR: PETE BLACKSTONE

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## 1.0 ABSTRACT

THIS DIAGNOSTIC IS USED TO TEST RPO4 5 6 DEVICE CONTROL LOGIC CONNECTED TO AN RH11 OR RH70 CONTROLLER.

IT USES THE DISK SURFACE AND THE DRIVE MECHANICS TO PROVE THE PROPER WORKING OF THE SUBSYSTEM. IT DOES NOT NEED A FORMATTED DISK PACK. A DISK PACK WITH NO VITAL INFORMATION WRITTEN ON IT IS ESSENTIAL. AFTER A SUCCESSFUL RUN (WITH NO ERRORS) OF THIS DIAGNOSTIC IT CAN BE ASSERTED THAT THE DCL IN THE RPO4/5/6 SUBSYSTEM WORKS SUCCESSFULLY WHILE STANDING ALONE. SYSTEMS INTERACTION AND DRIVE TIMING IS LEFT TO OTHER DIAGNOSTICS. THIS IS WITH THE ASSUMPTION THAT PROGRAMS DZRJGA AND DZRJHA HAVE BEEN RUN SUCCESSFULLY.

## 2.0 REQUIREMENTS

## 2.1 EQUIPMENT

POP-11 COMPUTER WITH CONSOLE TELETYPE, AND A RPO4/5/6 DISK SYSTEM. THE RPO4/5/6 DISK SYSTEM WILL CONSIST OF AN RH11 CONTROLLER, A DISK CONTROL LOGIC (DCL), A DEC 733 DISK DRIVE, AND ITS APPROPRIATE DISK PACK. THE DISK PACK NEED NOT BE FORMATTED. USED SECTION OF THE DISK SURFACE SHALL BE GOOD (HOLE FREE). THE SURFACE FOR THE FOLLOWING SECTORS MUST BE GOOD, THAT IS, FREE OF ANY HOLES OR SURFACE IRREGULARITY BEFORE ANY DATA ERROR CAN BE ATTRIBUTED TO THE LOGIC.

CYLINDER 00,	TRACK 00,	SECTOR 00
CYLINDER 00,	TRACK 00,	SECTOR 01
CYLINDER 00,	TRACK 18,	SECTOR 21
CYLINDER 01,	TRACK 00,	SECTOR 00
CYLINDER 02,	TRACK 00,	SECTOR 00
CYLINDER 03,	TRACK 00,	SECTOR 00
CYLINDER 04,	TRACK 00,	SECTOR 00
CYLINDER 05,	TRACK 00,	SECTOR 00
CYLINDER 05,	TRACK 07,	SECTOR 04
CYLINDER 06,	TRACK 00,	SECTOR 00
CYLINDER 07,	TRACK 00,	SECTOR 00
CYLINDER 08,	TRACK 00,	SECTOR 00
CYLINDER 09,	TRACK 18,	SECTOR 21
CYLINDER 410,	TRACK 18,	SECTOR 21

## 2.2 STORAGE

THIS PROGRAM REQUIRES 16K WORDS OF MEMORY

## 2.3 PRELIMINARY PROGRAMS

THIS PROGRAM ASSUMES THAT MAINDEC-11-DZRJG-(LATEST REV) HAS BEEN RUN WITHOUT ERRORS.  
AND IT ASSUMES THAT MAINDEC-11-DZRJH-(LATEST REV) HAS BEEN RUN WITHOUT ERRORS.

## 3.0 LOADING PROCEDURE

USE STANDARD PROCEDURE FOR LOADING .ABS TAPES



## 4.0 STARTING PROCEDURE

SWITCH 12 MUST BE SET WHEN THIS PROGRAM IS TO BE RUN USING AN RMTD CONTROLLER. IT CAN BE SET AT THE FRONT PANEL, OR IN THE SOFTWARE SWITCH REGISTER IF THE OPERATOR SO DESIRES. SEE PARAGRAPH 5.1 FOR A DESCRIPTION OF SOFTWARE SWITCH REGISTER OPERATION.

## 4.1 CONTROL SWITCH SETTINGS

SEE SECTION 5.1

## 4.2 STARTING ADDRESS

START AT ADDRESS 200---FOR NORMAL RUN  
START AT ADDRESS 210---FOR UNIT SELECTION  
START AT ADDRESS 220---FOR NO MANUAL INTERVENTION

200 START  
ALL SWITCHES MUST BE DOWN FOR WORST CASE RUN. WITH THIS STARTING ADDRESS ALL THE RPO4/5/6S ON THE SYSTEM WILL BE TESTED ONE AT A TIME BEFORE "END PASS" IS PRINTED OUT. TESTING WILL START WITH THE LOWEST UNIT NUMBER DRIVE THAT IS POWERED UP (THAT IS THE LOWEST UNIT NUMBER RHAS REGISTER THAT RESPONDS) THEN GO ON TO THE NEXT HIGHER UNIT NUMBER THAT IS POWERED UP.

210 START  
ALL SWITCHES MUST BE DOWN FOR WORST CASE RUN. WITH THIS STARTING ADDRESS THE CONSOLE TELETYPE WILL ASK FOR THE UNIT NUMBER TO BE TESTED. THEN ONLY THAT UNIT WILL BE TESTED FOR EACH PASS OF THE PROGRAM.

220 START  
ALL SWITCHES MUST BE DOWN FOR WORST CASE RUN. WITH THIS STARTING ADDRESS THE PROGRAM WILL NOT RUN THOSE TESTS THAT NEED MANUAL INTERVENTION. THIS IS RECOMMENDED ONLY FOR DEBUGGING WHERE THE ERROR IS NOT IN A TEST THAT REQUIRES MANUAL INTERVENTION

## 4.3 PROGRAM AND/OR OPERATOR ACTION

1. LOAD THE PROGRAM INTO MEMORY.
2. SET STARTING ADDRESS ON THE SWITCH REGISTER
3. PRESS "LOAD ADDRESS".
4. SET "OPERATIONAL SWITCH SETTINGS" (SEE SECTION 5.1)  
WORST CASE IS ALL SWITCHES DOWN.
5. PRESS "START".
6. FOR THE FIRST PASS EACH TEST WILL BE EXECUTED ONCE ON THE DRIVES PRESENT OR DRIVE SELECTED BEFORE "END PASS" IS PRINTED. THE FIRST PASS WILL REQUIRE OPERATOR INTERVENTION IF THE PROGRAM IS NOT RUN UNDER AN "ACT-11" MONITOR. THE SECOND AND SUBSEQUENT PASSES WILL EXECUTE



EACH TEST FOUR TIMES ON EACH DRIVES PRESENT OR DRIVE SELECTED BEFORE "END PASS" IS PRINTED. THE SECOND AND SUBSEQUENT PASSED DO NOT NEED ANY OPERATOR INTERVENTION.

## 5.0 OPERATING PROCEDURE

### 5.1 OPERATIONAL SWITCH SETTINGS

IF THE PROGRAM IS BEIDNG RUN ON A SWITCHLES PROCESSOR (I. E. AN 11/34) IT WILL DETERMINE THAT A HARWARE SWITCH REGISTER IS NOT PRESENT, AND WILL USE "SOFTWARE" SWITCH REGISTER. THE SETTINGS OF THE SWITCHES ARE CONTROLLED THROUGH A KEYBOARD ROUTINE WHICH IS CALED BY TYPING A 'COBNTROL G'. THE PROGRAM WILL RECOGNIZE A 'CONTROL G' AT ANY TIME EXCEPT WHEN IT IS AR A HIGHER PRIORITY PROCESSING AN RPO4/5/6 INTERRLPT. THE "SOFTWARE" SWITCH VALUE S ARE ENTERED AS AN OCTAL NUMBER IN RESPONSE TO PROMPTING FROM THE SWITCH ENTRY ROUTINE:

'SWR = NNNNNN NEW ='

EACH TIME SWITCH SETTINGS ARE ENTERED, THE ENTIRE SWITCH REGISTER IMAGE MUST BE ENTERED. LEADING ZEROS ARE NOT REQUIRED. 'RUBOUT' AND 'CONTROL U' FUNCTIONS MAY BE USED TO CORRECT TYPING ERRORS DURING SWITCH ENTRY.

ON PROCESSORS WITH HARDWARE SWITCH REGISTER, THE "SOFTWARE" SWITCH REGISTER MAY ALSO BE USED. IF THE PROGRAM FINDS ALL 16 SWITCHES IN THE 'UP' POSITION WHEN IT IS STARTED, ALL SWITCH REGISTER REFERENCES WILL BE TO THE "SOFTWARE" REGISTER AND THE PROCEDURES DESCRIBED ABOVE MUST BE FOLLOWED.

SWITCH DEFINITIONS ARE GIVEN IN SECTION 9 "OPERATIONAL SWITCH SETTINGS" HOWEVER THE DETAIL DESCRIPTION ARE GIVEN HERE.

#### SWITCH 15 - HALT ON ERROR

WHEN THIS SWITCH IS SET, IF THE PROGRAM FINDS AN ERROR THEN THE APPROPRIATE INFORMATION WILL BE PRINTED OUT AND THEN THE PROGRAM WILL HALT. AFTER THIS HALT, PRESSING "CONTINUE" WILL CONTINUE WITH THE PROGRAM TILL THE NEXT ERROR IS FOUND WHEN THE SAME THING WILL HAPPEN.

#### SWITCH 14 - LOOP ON TEST

WHEN THIS SWITCH IS SET THE PROGRAM WILL BEGIN TO LOOP ON THE CURRENT TEST BEING EXECUTED. FOR EXAMPLE IF THIS SWITCH IS SET WHEN THE PROGRAM IS IN TEST 10 THEN THE PROGRAM WILL KEEP EXECUTING ALL OF TEST 10 REPEATEDLY. ONE WAY TO BE SURE THAT THE PROGRAM IS IN THE EXPECTED TEST IS TO SET THIS SWITCH DURING AN ERROR PRINTOUT OR DURING A PROGRAM HALT.

#### SWITCH 13 - INHIBIT ERROR TYPEOUTS

WHEN THIS SWITCH IS SET FURTHER ERROR PRINTOUTS WILL CEASE, HOWEVER OPERATOR INSTRUCTIONS SUCH AS "STOP DRIVE X" WILL CONTINUE. AT THE END OF PASS "TOTAL NUMBER OF ERRORS ON THIS PASS ON DRIVE X" WILL BE TRUE, THAT IS, ALTHOUGH PRINTOUTS WERE INHIBITED IF THAT PASS FOUND 6 ERRORS, IT WILL SAY SO.



SWITCH 12 - RH70 CONTROLLER SELECT  
THIS SWITCH MUST BE SET AT THE START OF THE PROGRAM WHEN THE DISK DRIVES TO BE TESTED ARE CONNECTED TO AN RH70 CONTROLLER. IT MUST NOT BE SET WHEN DISK DRIVES TO BE TESTED ARE CONNECTED TO AN RH11 CONTROLLER.

SWITCH 11 - INHIBIT ITERATIONS  
WHEN THIS SWITCH IS SET THE PROGRAM ON SECOND PASS WILL NOT REPEAT EACH TEST FOUR TIMES BUT WILL DO EACH TEST ONCE ONLY.

SWITCH 10 - BELL ON ERROR  
WHEN THIS SWITCH IS SET, IF THE PROGRAM FINDS AN ERROR THE "BELL" OR "ALARM" WILL BE SOUNDED. THIS SWITCH IS USEFUL WHEN SWITCH 11 IS SET YET INFORMATION IS NEEDED WHEN ANY ERROR IS DETECTED. TAKE THE EXAMPLE OF A PROGRAM LOOPING ON A TEST WITH SWITCH 11 SET TO HELP SCOPING. THEN IF THIS SWITCH IS SET AND THE BELL OR ALARM SOUNDS IT MEANS THAT THE ERROR IS PRESENT BUT IF THE BELL OR ALARM STOPS IT MEANS THAT THE ERROR IS NOT PRESENT.

SWITCH 9 - LOOP ON ERROR  
WHEN THIS SWITCH IS SET, IF THE PROGRAM FINDS AN ERROR THEN GENERALLY THE PROGRAM WILL LOOP BACK TO THE LAST EXECUTED "SCOPE" STATEMENT. IF ON THE SECOND TIME THROUGH AN ERROR IS FOUND IT WILL AGAIN LOOP BACK TO THAT "SCOPE" STATEMENT. THIS LOOPING WILL CONTINUE AS LONG AS THE ERROR IS PRESENT AND THIS SWITCH IS SET. HOWEVER IF THE ERROR IS NOT PRESENT AT ANY TIME THEN IT WILL CONTINUE NORMALLY WITH THE PROGRAM. EACH TIME THE ERROR IS ENCOUNTERED PRINTOUT WILL TAKE PLACE UNLESS SWITCH 11 IS ALSO SET. DURING BEGUG, USING A SCOPE, IT IS RECOMMENDED THAT SWITCH 11 IS ALSO SET.

NOTE: SEE SECTION 8.3

SWITCH 8 - LOOP ON TEST IN SWR <7:0>  
THIS IS A SPECIAL SWITCH. WHEN SET SWITCHES 0 THRU 7 HAVE ONE MEANING AND WHEN RESET SWITCHES 0 THRU 7 HAVE ANOTHER MEANING. THIS MEANS THAT ANY SETTING OF SWITCH 0 THRU 7 MUST BE DONE WITH SWITCH 8 IN THE APPROPRIATE POSITION. WHEN THIS SWITCH IS SET THEN SWITCHES 0 THRU 7 GIVE THE TEST NUMBER TO BE LOOPED ON. FOR EXAMPLE WITH SWITCH 8 SET AND SWITCH 3 SET THE PROGRAM WILL LOOP ON TEST 10. HOWEVER THIS SETTING MUST BE DONE AT THE BEGINNING OF THE PROGRAM THEN ALL THE TESTS FROM 1 TO 10 WILL BE EXECUTED AND THEN TEST 10 WILL BE REPEATED OVER AND OVER AGAIN. WHEN THIS SWITCH IS NOT SET THEN SWITCHES 0 THRU 7 HAVE THE MEANING ITS NAME INDICATES. FOR EXAMPLE SWITCH 7 IS "STOP FURTHER COMPARES: THAT IS IF SWITCH 8 IS NOT SET AND SWITCH 7 IS SET THEN WHEN A DATA ERROR IS DETECTED NO FURTHER COMPARES WILL BE DONE. FOR EXAMPLE IN A 256 WORD BUFFER IF ALL THE WORDS ARE IN ERROR THEN AFTER SEEING THE PRINTOUT FOR THE FIRST FEW



WORDS SETTING SWITCH 7 ONLY WILL STOP FURTHER PRINTOUTS OF THIS ERROR AND GO ON WITH THE TEST RATHER THAN PRINT ALL THE 256 WORDS. HOWEVER IF THIS WAS DONE WITH SWITCH 11 THEN THE NEXT ERROR THAT THE PROGRAM DETECTS IN A SUBSEQUENT TEST WILL ALSO BE LOST. BUT WITH SWITCH 7, ONLY THIS GROUP OF DATA ERRORS ARE NOT PRINTED OUT. ANOTHER EXAMPLE OF SWITCH 8 BEING LOW IS WITH SWITCH 6, WHICH IS "ECC TEST-COMPARE END RESULT ONLY". THAT IS IF SWITCH 8 IS NOT SET AND SWITCH 6 IS SET THEN ON ECC TESTS (TEST 120 THRU TEST 134) INSTEAD OF COMPARING CONTENTS OF THE POSITION REGISTER AND PATTERN REGISTER AFTER EVERY CLOCK, COMPARES WILL ONLY BE DONE AT THE END OF ALL THE CLOCKS.

NOTE: SEE SECTION 8.3

SWITCH 7 - STOP FURTHER COMPARES IF SW08 IS LOW. IF SWITCH 8 IS SET AND THIS SWITCH IS ALSO SET THEN THIS SWITCH GIVES THE TEST NUMBER TO BE LOOPED ON AS INDICATED IN THE DESCRIPTION OF SWITCH 8. IF SWITCH 8 IS NOT SET AND THIS SWITCH IS SET THEN THE PROGRAM WILL DO AS THE NAME INDICATES. FOR EXAMPLE IN A 256 WORD BUFFER IF ALL THE WORDS ARE IN ERROR THEN AFTER SEEING THE ERROR PRINTOUTS FOR THE FIRST FEW WORDS THEN SETTING SWITCH 7 WITH SWITCH 8 NOT SET WILL STOP THE PRINTOUT OF ALL 256 WORDS BUT WILL NOT STOP THE PRINTOUT OF ANOTHER ERROR IN ANY SUBSEQUENT TEST. IT IS EXPECTED THAT SWITCH 7 AFTER BEING SET FOR A WHILE TO STOP PRINTING ALL THE 256 WORDS WILL BE RESET AGAIN TO ENABLE THE PRINTING OF OTHER DATA ERRORS.

SWITCH 6 - TYPE ALL REGISTERS WITH ERROR IF SW08 IS LOW IF SWITCH 8 IS SET AND THIS SWITCH IS ALSO SET THEN THIS SWITCH GIVES THE TEST NUMBER TO BE LOOPED ON AS INDICATED IN THE DESCRIPTION OF SWITCH 8. IF SWITCH 8 IS NOT SET AND THIS SWITCH IS SET THEN THE PROGRAM WILL DO AS THE NAME INDICATES. THAT IS ON FINDING AN ERROR INSTEAD OF ONLY GIVING THE ERROR MESSAGE AND RELEVANT REGISTERS AS WILL BE DONE IF SWITCH 11 IS NOT SET BUT WILL ALSO GIVE ALL THE REGISTER CONTENTS (EXCEPT "DATA BUFFER" RHDS).

## 5.2 SJB-ROUTINE ABSTRACTS

SEE SECTION 9 "SUBROUTINES".

## 6.0 ERRORS

ERROR PRINTOUTS CONTAIN THE ERROR ADDRESS AND OTHER PERTINENT INFORMATION CONCERNING THE PARTICULAR FAILURE. THIS INFORMATION MAY BE THE CONTENTS OF RELEVANT RPO4/5/6 REGISTERS OR GOOD/RECEIVED DATA. IF THE ERROR OCCURRED IN A SUBROUTINE, THE ADDRESS OF THE SUBROUTINE CALL IS ALSO GIVEN. REFER TO THE PROGRAM LISTING AT THE STATED ADDRESS TO DETERMINE THE CAUSE OF THE ERROR.

### 6.1 IN THE EVENT THAT THE DISK DRIVE BECOMES UNAVAILABLE TO THE CONTROLLER, POWERS DOWN, OR CERTAIN CRITICAL STATUS BITS



CANNOT BE CLEARED PRIOR TO THE START OF A TEST SEQUENCE - THIS INFORMATION WILL BE COMMUNICATED TO THE OPERATOR. IN ADDITION, THE TTY BELL WILL RING AND THE PROGRAM WILL HALT. IT IS SUGGESTED THAT IF THIS HAPPENS, THE OPERATOR LOAD ADDRESS 200 (210) AND RESTART THE PROGRAM AS A FIRST ATTEMPT TO SOLVE THE PROBLEM. IF THE FAILURE CONTINUES TO OCCUR, THERE ARE TWO OPTIONS OPEN TO THE OPERATOR:

1. LOOK IN THE TEST LISTING FOR THE 'HALT' INSTRUCTION AND REPLACE IT, PLUS THE TWO WORDS ("TYPE CPHALT") ABOVE WITH 'NOP'S. WITH TTY ERROR PRINTOUTS INHIBITED, A SCOPE LOOP CAN BE INITIATED FOR THE TEST IN QUESTION.
2. GO BACK AND RERUN DZRPS, AS IT IS QUITE POSSIBLE THAT A HARD FAILURE HAS OCCURRED IN ONE OF THE HARDWARE REGISTERS.

IT IS ALSO POSSIBLE TO CONTINUE FROM THE 'HALT' POINT, BUT THIS IS NOT RECOMMENDED AS ALL FOLLOWING TESTS WILL EXHIBIT THE SAME SYMPTOMS AND GIVE MISLEADING ERROR PRINTOUTS.

## 7.0 RESTRICTIONS

BEFORE STARTING THE PROGRAM THE OPERATOR MUST HAVE THE DRIVE PORT SWITCH LOCKED EITHER ON PORT A OR PORT B BUT MUST NEVER LEAVE IT IN THE PROGRAMMABLE STATE.

SWITCH 12 MUST BE SET WHEN RUNNING ON AN RH70 CONTROLLER AND IT MUST NOT BE SET WHEN RUNNING ON AN RH11 CONTROLLER. BECAUSE OF THE REQUIREMENT FOR IT TO BE SET WHEN USING AN RH70, THE PROGRAM CANNOT BE RUN IN CHAIN MODE WHEN USING THE SOFTWARE SWITCH REGISTER FEATURE WHILE ON AN RH70. THIS IS BECAUSE THE ROUTINE WHICH GETS "SOFTWARE" SWITCH SETTINGS IS NOT OPERABLE WHEN IN CHAIN MODE.

## 8. MISCELLANEOUS

### 8.1 EXECUTION TIME

THE FIRST PASS OF THE PROGRAM WILL TAKE APPROXIMATELY 1 MINUTES PROVIDED AN OPERATOR IS PRESENT TO CARRY OUT THE TYPED INSTRUCTIONS IMMEDIATELY. SUBSEQUENT PASSES WILL TAKE 30 SECONDS WHETHER AN OPERATOR IS THERE OR NOT.

### 8.2 STACK POINTER

THE STACK IS INITIALLY SET TO 1000

### 8.3 OPERATOR SELECTABLE SCOPE LOOPS

HERE IS A DETAILED EXPLANATION OF HOW THE LOOP ON ERROR WORKS. FOR INSTRUCTIONS REGARDING THE USAGE OF THIS TECHNIQUE, HIT ↑ ANY TIME WHILE THE PROGRAM IS RUNNING. ON HITTING AN ERROR IF THE LOOP ON ERROR SWITCH IS SET, THE PROGRAM GOES BACK - USUALLY BACK TO THE BEGINNING OF THE TEST.

WHEN THIS OPERATOR SELECTABLE SCOPE LOOP IS USED THEN THE POINT THE PROGRAM GOES BACK TO CAN BE CHANGED.



THE RESTRICTIONS TO THE POINT WHERE THE PROGRAM CAN GO ARE: -

1. IT MUST BE WITHIN THE TEST UNDER CONSIDERATION
  2. LOOP ON ERROR SWITCH MUST BE SET
  3. THE ERROR MUST OCCUR WITHIN THE TEST UNDER CONSIDERATION
- IF THE ERROR DOES NOT OCCUR WITHIN THE TEST UNDER CONSIDERATION THE PROGRAM WILL REVERT TO NORMAL OPERATION. HOWEVER, IF LOOP ON TEST SWITCH IS SET AND THIS OPERATOR SELECTABLE SCOPE LOOP IS USED THEN THE PROGRAM WILL LOOP BACK TO THE SELECTED POINT WHEN IT COMES TO THE END OF THE TEST UNDER CONSIDERATION.

AFTER LOOPING FOR SOME TIME IF THE LOOP SWITCH IS PUT DOWN THEN NORMAL OPERATION WILL CONTINUE.

#### 8.4 PROGRAM REVISION HISTORY

#### 9.0 PROGRAM DESCRIPTION

#### 9.1 LOGIC DIVISION IN HARDWARE MODULES

REGISTER BOARD (RG) - ERROR REGISTER 1 STATUS REGISTERS  
MUX FOR REGISTERS GO HANDLING REGISTER  
DECODE COMMAND DECODE EXECUTION OF  
MECH. COMMANDS

SYNC. DATA BOARD (SN) - DATA CONTROL PARALLEL TO SERIAL  
SYNC. BYTE DETECT.

SEEK AND SEARCH (SS) - SEEK LOGIC SEARCH LOGIC HEADER  
HANDLING.

ERROR CORRECTION (EC) - ECC LOGIC ERROR REGISTER 2 & 3  
MUX FOR ERROR REG. 2 & 3 LOOK AHEAD  
REG. SECTOR COUNTER DATA FORMATION  
RING COUNTER.

DUAL PORT (DP) - DUAL PORT ARBITRATION ATTENTION LOGIC  
SERIAL NO REGISTER MASS BUS REGISTER  
STORAGE

#### 9.2 DISK SURFACE USAGE

##### SYMBOLS USED

C = CYLINDER

T = TRACK

S = SECTOR

W = WRITE

R = READ

TT = TEST NUMBER

CO, TO, SO

TT22-W,R, TT23-R, TT24-W,R, TT25-W,R, TT26-W,R, TT35-W,R, TT37-W, TT50-W, TT51-W,R, TT52-W,R, TT55-W,R

CO, TO, S1

TT27-W,R, TT37-W,R, TT40-R, TT41-W,R, TT42-W,R, TT43-W,R

CO, T18, S21

TT30-W, TT31-W,R



C1, TO, SO  
TT30-W,R, TT31-W,R, TT53-W,R, TT54-W,R

C1, T18, S21  
TT31-W

C2, TO, SO  
TT31-W,R

C2, T18, S21  
TT31-W

C3, TO, SO  
TT31-W,R

C3, T18, S21  
TT31-W

C4, TO, SO  
TT31-W,R

C4, T18, S21  
TT31-W

C5, TO, SO  
TT31-W,R

C5, T7, S4  
TT33-W,R, TT34-W,R

C5, T18, S21  
TT31-W

C6, TO, SO  
TT31-W,R

C6, T18, S21  
TT31-W

C7, TO, SO  
TT31-W,R

C7, T18, S18  
TT31-W

C8, TO, SO  
TT31-W,R

C8, T18, S21  
TT31-W

C9, TO, SO  
TT31-W

C9, T18, S21  
TT31-W, TT32-R

C10, TO, SO  
TT31-W,R

C410, T18, S21  
TT36-W,R, TT50-W,R

9.3

THE FOLLOWING SECTION DESCRIBES EACH TEST AND SUBROUTINES  
IN DETAIL AND CAN BE USED AS AN INDEX TO THE LISTING.  
THE LEFT MOST COLUMN IS THE LINE NUMBER WITHIN THE LISTING  
WHERE THAT ITEM WILL BE FOUND.



DOCUMENT  
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MAINDEC-11-DZRJI-A, RPO4/5/6 FUNCT. CONT. TST-PT 1  
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11954	TRAP DECODER
11970	TRAP TABLE
11992	POWER DOWN AND UP ROUTINES

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PROGRAM BY PETE BLACKSTONE

THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC  
PACKAGE (MAINDEC-11-DZQAC-CO), MAR 21, 1976.

DRIVE MUST BE LOCKED ON PORT A OR PORT B

28 INTERNAL PROGRAM MACROS BEGIN HERE  
\*\*\*\*\*

33 \*  
\*NOTE: ALL MACRO CALLS BEGINNING WITH ".S" ARE SUPPLIED FROM AN  
EXTERNAL SYSMAC.SML PACKAGE WHICH MUST BE MADE AVAILABLE  
TO THE SOURCE PROGRAM AT ASSEMBLY TIME.

40

\*\*\*\*\*  
OPERATIONAL SWITCH SETTINGS  
\*\*\*\*\*

41

SWITCH	USE
15	HALT ON ERROR
14	LOOP ON TEST
13	INHIBIT ERROR TYPECUTS
12	RH70 CONTROLLER SELECT
11	INHIBIT ITERATIONS
10	BELL ON ERROR
9	LOOP ON ERROR
8	LOOP ON TEST IN SWR<7:0>

53

7	STOP FURTHER COMPARES IF SW08 IS LOW
6	TYPE ALL REG. WITH ERROR IF SW8 LOW
5	MULT ADDR PLUG TEST IF SW09 IS LOW



57 \*\*\*\*\*  
BASIC DEFINITIONS  
\*\*\*\*\*

59 INITIAL ADDRESS OF THE STACK POINTER \*\*\* 1000 \*\*\*

64 MISCELLANEOUS DEFINITIONS

76 GENERAL PURPOSE REGISTER DEFINITIONS

98 PRIORITY LEVEL DEFINITIONS

98 "SWITCH REGISTER" SWITCH DEFINITIONS

126 DATA BIT DEFINITIONS (BIT00 TO BIT15)

154 BASIC "CPU" TRAP VECTOR ADDRESSES

158 \*\*\*\*\*  
TRAP CATCHER  
\*\*\*\*\*

171 ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2.HALT"  
SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS  
LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

178 \*\*\*\*\*  
ACT11 HOOKS  
\*\*\*\*\*

189 \*\*\*\*\*  
STARTING ADDRESSES  
\*\*\*\*\*

200 STARTING ADDRESS 200 FOR NORMAL STARTS  
THIS WILL TEST ALL DRIVES ON THE SYSTEM A SINGLE DRIVE AT A TIME

STARTING ADDRESS 210 WILL TEST ONLY ONE SPECIFIED DRIVE

STARTING ADDRESS 220 WILL JUMP OVER THE TESTS REQUIRING AN OPERATOR  
AT THE DRIVE

1

209 \*\*\*\*\*  
 MEMORY MANAGEMENT DEFINITIONS  
 \*\*\*\*\*

- 210 KT11 VECTOR ADDRESS
- 214 KT11 STATUS REGISTER ADDRESSES
- 221 KERNEL "I" PAGE DESCRIPTOR REGISTERS
- 232 KERNEL "I" PAGE ADDRESS REGISTERS

245 \*\*\*\*\*  
 COMMON TAGS  
 \*\*\*\*\*

248 THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS  
 USED IN THE PROGRAM.

303 \*\*\*\*\*  
 ERROR POINTER TABLE  
 \*\*\*\*\*

305 THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.  
 THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN  
 LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.  
 NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).  
 NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

- 311 EM ::POINTS TO THE ERROR MESSAGE
- DH ::POINTS TO THE DATA HEADER
- DT ::POINTS TO THE DATA
- DF ::POINTS TO THE DATA FORMAT

- 323 ITEM1
- 335 ITEM2
- 347 ITEM3
- 358 ITEM4
- 371 ITEMS
- 381 ITEM6
- 391 ITEM7
- 399 ITEM10



G02

- 412 ITEM11
- 426 ITEM12
- 440 ITEM13
- 456 ITEM14
- 466 ITEM15
- 478 ITEM16
- 492 ITEM17
- 506 ITEM20
- 519 ITEM 21
- 526 ITEM 22
- 533 ITEM 23
- 543 ITEM 24
- 552 ITEM 25
- 563 ITEM 26
- 575 ITEM 27
- 586 ITEM 30
- 596 ITEM 31
- 607 ITEM 32
- 615 ITEM 33
- 625 ITEM 34
- 631 ITEM 35
- 641 ITEM 36
- 648 ITEM 37
- 659 ITEM 40
- 670 ITEM 41

H02

- 684 ITEM 42
- 696 ITEM 43
- 709 ITEM 44
- 721 ITEM 45
- 734 ITEM 46
- 743 ITEM 47
- 756 ITEM 50
- 767 ITEM 51
- 783 ITEM 52
- 796 ITEM 53
- 803 ITEM 54
- 815 ITEM 55
- 827 ITEM 56
- 839 ITEM 57
- 852 ITEM 60
- 869 ITEM 61
- 878 ITEM 62
- 888 ITEM 63
- 896 ITEM 64
- 908 ITEM 65
- 920 ITEM 66
- 936 ITEM 67
- 947 ITEM 70
- 959 ITEM 71
- 972 ITEM 74

983 ITEM 73  
990 ITEM 74  
1001 ITEM 75  
1009 ITEM 76  
1021 ITEM 77  
1031 ITEM 100  
1040 ITEM 101  
1052 ITEM 102  
1064 ITEM 103  
1076 ITEM 104  
1090 RH11 REGISTERS  
1098 WORD COUNT REGISTER (RHWC)  
EACH BIT IS CALLED BY BIT NUMBER  
1103 BUS ADDRESS REGISTER (RHBA)  
EACH BIT IS CALLED BY BIT NUMBER  
1108 CONTROL AND STATUS REGISTER 2 (RHCS2)  
1127 DATA BUFFER REGISTER (RHDB)  
EACH BIT IS CALLED BY BIT NUMBER  
1132 RPO4 REGISTERS  
1137 CONTROL AND STATUS 1 REGISTER. (#00)  
1150 STATUS REGISTER (RHDS1) (#01)  
1169 ERROR REGISTER #01 (RHER1) (#02)  
1187 MAINTAINABILITY REGISTER (RHMR)(#03)  
1197 ATTENTION SUMMARY PSEUDO-REGISTER (RHAS) (#04)  
1208 DESIRED SECTOR/TRACK ADDRESS REGISTER (RHDST) (#1)  
EACH BIT IS CALLED BY BIT NUMBER  
1215 DRIVE TYPE REGISTER (RHDT) (#06)  
EACH BIT IS CALLED BY BIT NUMBER



- 1222 LOOK-AHEAD REGISTER (RHLA) (#07)
- 1241 ERROR REGISTER #2 (RHER2) (#10)
- 1259 OFFSET REGISTER (RHOF) (#11)
- 1273 DESIRED CYLINDER ADDRESS (RHCA) (#12)  
EACH BIT IS CALLED BY BIT NUMBER.
- 1279 CURRENT CYLINDER ADDRESS (RHCC) (#13)  
EACH BIT IS CALLED BY BIT NUMBER
- 1285 SERIAL NUMBER REGISTER (RHSN) (#14)  
EACH IS CALLED BY BIT NUMBER
- 1291 ERROR REGISTER #03 (RHER3) (#15)
- 1305 ECC POSITION REGISTER (RHEC1) (#16)  
EACH BIT IS CALLED BY BIT NUMBER
- 1311 ECC PATTERN REGISTER (RHEC2) (#17)  
EACH BIT IS CALLED BY BIT NUMBER

1322

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 REGISTER ADDRESSES  
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- 1327 RP04/5/6 DISK I/O REGISTER LOCATED IN THE RH11 CONTROLLER
- 1334 RP04/5/6 DISK I/O REGISTERS LOCATED IN THE DEVICE CONTROL LOGIC (DCL)
- 1353 ADDITIONAL I/O REGISTERS LOCATED IN THE RH70 CONTROLLER LOGIC
- 1359 P-CLOCK (KW11-P) I/O REGISTERS
- 1367 THE FOLLOWING LOCATIONS ARE RESERVED FOR REGISTER SNAPSHOTS  
ANY TIME THERE IS AN ERROR ALL THESE WILL BE FILLED
- 1370 ONLY SOME MAY BE PRINTED BUT ALL WILL BE FILLED TRUE  
FOR THE TIME JUST AFTER THE "ERROR" ERROR COMMAND
- 1375 THIS ASSUMES THAT A REGISTER SNAPSHOT HAS BEEN TAKEN WHICH IS NOT  
ALWAYS THE CASE - IF QUESTIONABLE CONTENTS APPEAR IN THE REGISTER  
PRINTOUTS, CHECK THE INLINE TEST CODE TO SEE IF THE REGISTER SNAPSHOT  
REFLECTS THE CURRENT STATE OF THE MACHINE
- 1407 FUNCTION EQUATES
- 1409 TABLE OF FUNCTIONS FOR RHCS1 THEN "GO" BIT HAS TO BE SET

1432 DATA BUFFERS FOR READ/WRITE

1440 RESERVED CORE LOCATIONS

1461 TABLE FOR ATTENTION BITS  
ATTENTION TABLE

1470 FLAGS AND INTERNAL PROGRAM CONTROL WORDS

1505 \*\*\*\*\*

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1506 \*\*\*\*\*

\*\*DIAGNOSTIC CODE\*\*

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1507 \*\*\*\*\*

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1510 \*\*\*\*\*

SETUP TESTS

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1525 \*\*\*\*\*

INITIALIZE THE COMMON TAGS

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1612 \*\*\*\*\*

GET VALUE FOR SOFTWARE SWITCH REGISTER

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1627 IS THERE A P-CLOCK (KW11-P) ON THE SYSTEM  
IF SO MAKE 'WAT' TRAPS GO TO 'WAIT.P'  
IF SO MAKE RPO4 INTERRUPTS GO TO 'TIME 1'  
IF NOT MAKE 'WAT' TRAPS GO TO 'WAIT.T'  
IF NOT MAKE RPO4 INTERRUPTS GO TO 'TIME 2'

1633 THE NEXT LINE IS TO BE ADDED LATER  
AND THE JUMP AND NOP REMOVED  
FOR NOW NO CLOCK WILL BE USED

1676 TEST 1 REFERENCE EACH REGISTER

1678 REFERENCE EACH REGISTER BY A MOVE INSTRUCTION

1722 TEST 2 PARTIAL TEST OF RHAS FOR UNIT NUMBERS PRESENT

1724 CHECK THAT RHAS CAN BE CLEARED BY MOVING ALL ONES INTO IT

1745 TEST 3 TEST FOR DRIVES PRESENT USING RHAS AND RHCS2

1747 THE NUMBER OF DRIVES PRESENT IS FOUND  
BY MOVING ALL ONES INTO RHER1 WITH UNIT NUMBER  
IN RHCS2 INCREMENTED FROM ZERO TO SEVEN

1751 THE BITS SET IN RHAS SHOULD GIVE DRIVES PRESENT

1753 THE DRIVE TYPE IS CHECKED TO BE AN RPO4/RPO6 AND THEN  
UNITS PRESENT ARE STORED IN A TABLE CALLED 'UNITS'

1813 SET UP UNITS TABLE

1848 NO...IT'S NOT AN RPO4/RPO5/RPO6 DEVICE SO TYPE  
OUT THE DEVICE TYPE

1891 TEST 4 TYPE SERIAL NUMBER AND DRIVE TYPE

1893 SET APPROPRIATE ATTENTION BIT OF UNIT UNDER TEST IN 'ATTENT'  
TYPE UNIT UNDER TEST

1896 READ SERIAL NUMBER AND DRIVE TYPE REGISTERS  
TYPE THEM OUT AND PROCEED

1899 TO LOOP HERE SET SWITCH 8, AND THIS TEST NUMBER ON  
SWITCHES 0 THRU 7 AND RESTART

1925 INCREMENT THE UNITS TABLE TO NEXT DRIVE (IF ANY)  
& DECREMENT THE "NOUNITS" PRESENT (TO BE TESTED)

2052 TEST 5 CHECK MOL TO BE HIGH

2054 MAKE SURE THAT DRIVE IS ON LINE BEFORE STARTING PROGRAM  
IF DRIVE IS OFFLINE, THEN AFTER TYPE OUT THE PROGRAM WILL  
HANG FOR EVER WAITING FOR DRIVE TO GO ON LINE.

2084 TEST 6 PROGRAM INTERRUPT

2086 PROGRAM INTERRUPT IS TESTED BY SETTING RDY AND IE  
IN RHCS1 AT THE SAME TIME  
THIS SHOULD INTERRUPT THROUGH LOCATION 254  
THE PROCESSOR PRIORITY IS SET TO 4

2125 TEST 7 INTERRUPT AT PROCESSOR AND DISK PRIORITY SAME

2127 PROCESSOR PRIORITY IS SET AT 5 (SAME AS THE DISK)  
IE AND RDY IS SET. THIS SHOULD NOT ALLOW INTERRUPT



2165 TEST 10 PACK ACKNOWLEDGE

2167 IF STARTING ADDRESS 220 IS USED THIS TEST WILL NOT BE PERFORMED

IF THE PROGRAM WORKS UNDER ACT-11 MONITOR  
THEN THIS TEST IS NOT PERFORMED

IF NO ACT-11 MONITOR IS PRESENT  
THEN THIS TEST IS PERFORMED ONLY ON THE FIRST PASS  
ON SUBSEQUENT PASSES THIS TEST IS NOT DONE

THIS TESTS THE ACKNOWLEDGE COMMAND=44  
VV BIT - RHDS1 BIT #6  
MOL BIT - RHDS1 BIT #12  
DVA BIT - RHCS1 BIT #11  
THE DRIVE IS STOPED MOL IS CHECKED TO BE 0  
AND DVA SHOULD BE 0  
THE DRIVE IS POWERED UP  
VV SHOULD BE 0, MOL SHOULD BE 1, DVA SHOULD BE 1  
GO SHOULD BE 0  
ALL REGISTERS EXCEPT RHDB, RHLA AND RHCC ARE STORED  
PACK ACKNOWLEDGE IS ISSUED  
ALL STORED REGISTERS SHOULD BE UNCHANGED  
EXCEPT VV

2202 THIS CODE CHECKS TO SEE IF MANUAL INTFRVENTION TESTS ARE OK

2304 NOW SAVE REGISTERS FOR COMPARISON AFTER PACK ACKNOWLEDGE

2345 NOW COMPARE REGISTERS SO THAT NO REGISTERS  
CHANGED EXCEPT VV IN RHDS1 AND IE IN RHCS1

2378 TEST 11 SET VV BIT #6 IN RHDS1  
THIS TEST SETS VV IN RHDS1 INCASE  
ACT-11 MONITOR IS PRESENT AND THE PREVIOUS TEST  
IS NOT PERFORMED  
THERE IS A RESET AT THE BEGINING OF THIS TEST  
FOR ERROR RECOVERY ONLY.

2388 IN CASE THERE IS ANY DRIVE ERRORS DURING POWER UP  
OR POWER DOWN OR ANY PARITY ERRORS A RESET IS GIVEN

2409 NOW SAVE REGISTERS FOR COMPARISON AFTER PACK ACKNOWLEDGE

2444 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
R0 AND R5 IMMEDIATELY AFTER GO

2475 NOW COMPARE REGISTERS BEFORE PACK ACKNOWLEDGE  
WITH AFTER PACK ACKNOWLEDGE

2517 TEST 12 MAKE CURRENT CYLINDER = 377

2536 TEST 13 MAKE CURRENT CYLINDER = 777

2553 TEST 14 ADDRESS PLUG CHANGE ERROR

2555 CHECK PROPER ADDRESS PLUG FUNCTIONALITY  
BY PULLING THE ADDRESS PLUG DURING A COMMAND ISSUED  
IN DIAGNOSTIC MODE (TO GUARANTEE COMMAND IS STILL ACTIVE WHEN  
PLUG IS PULLED) AND VERIFYING THAT THE DRIVE GOES OFF LINE

2560 THE ADDRESS PLUG IS THEN REPLACED AND RETURN OF THE PROPER  
RESPONDING DRIVE IS VERIFIED, AS WELL AS THE FACT THAT  
ATTENTION BITS COME UP IN THE PROPER BIT LOCATION("????"), AND THE  
ADDRESS CHANGE ERROR (ACE) - RHER3 BIT #13 IS SET, AS WELL AS  
SC, TRE, ERR, AND MCPE; VV IS RESET AND RHCC = 000 (DRIVE RECALIBRATED)

2566 IN ADDITION VERIFICATION IS ALSO MADE THAT NO OTHER DRIVE  
NUMBERS APPEAR ON THE BUSS AFTER THE PLUG IS REPLACED. ("?????")

2597 THIS CODE CHECKS TO SEE IF MANUAL INTERVENTION TESTS ARE OK

2611 SET DIAGNOSTIC MODE TO ENABLE A COMMAND ACTIVE WHILE  
THE PLUG IS PULLED

2616 TAKE AN INITIAL REGISTER SNAPSHOT

2626 ISSUE A COMMAND AND THE 'GO' BIT (NOT POSITIONING COMMAND)  
TO VERIFY COMMAND ABORT IF PLUG IS PULLED

2631 ISSUE SOME CLOCKS TO GET THE COMMAND STARTED  
(USE "SEARCH" WITH "DTETST" FLAG UP TO STOP CLOCKING ?)

2648 CHECK THAT THE UNIT NO. UNDER TEST HAS GONE OFFLINE

2659 NOW REPLACE THE ADDRESS PLUG

2674 CHECK THAT THE ORIGINAL UNIT HAS COME BACK ON LINE

2689 AFTER THE PLUG CHANGE

2733 TAKE A NEW REGISTER SNAPSHOT AND  
COMPARE THE REGISTER CONTENTS WITH EXPECTED VALUES

2751 NOW CLEAR OUT THE CONTROLLER AND DRIVE

2760 CHANGE THE REGISTER SNAPSHOT TO EXPECTED VALUES AFTER THE CLEAR

2807 TAKE ANOTHER REGISTER SNAPSHOT AND COMPARE RESULTS  
WITH THE EXPECTED VALUES

- 2824 USE NEW METHOD TO VERIFY THAT ATTENTION BIT COMES UP IN THE PROPER LOCATION ???
- 2830 SET 'VV' IN RHDS1 AFTER RESET FROM THE RECALIBRATE CAUSED BY PULLING THE PLUG
- 2855 TEST 15 CHECK ALL ADDRESS PLUG ADDRESSES
- 2857 CHECK THAT ALL ADDRESS PLUG NUMBERS FUNCTION PROPERLY
- 2859 THIS TEST IS DONE FOR MEMOREX RPO4'S AND RPO6'S ONLY THIS TEST IS SELECTED BY SW#5 AND THE DEFAULT IS NOT TO DO IT
- 2877 CHECK TO SEE IF THIS TEST HAS BEEN SELECTED WITH SW5
- 2895 THIS CODE CHECKS TO SEE IF MANUAL INTERVENTION TESTS ARE OK
- 2927 CHANGE ADDRESS PLUG ON THE UNIT UNDER TEST
- 2946 HOUSEKEEPING
- 2958 ATTEMPT TO ADDRESS THE NEW UNIT NUMBER
- 2979 CHECK IF ALL UNIT NUMBERS HAVE BEEN TRIED
- 2989 SET 'VV' IN RHDS1 AFTER RESET FROM THE RECALIBRATE CAUSED BY PULLING THE ADDRESS PLUGS OUT

3019

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3020

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 \*\*DRIVE COMMAND TESTS\*\*  
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3029 TEST 16 NO OPERATION FUNCTION TEST  
 ALL POSSIBLE REGISTERS ARE CLEARED THEN A "NOP"=0  
 IS GIVEN NO CHANGE SHOULD HAPPEN  
 ALL POSSIBLE REGISTERS ARE FILLED WITH ONES THEN A "NOP"  
 IS GIVEN NO CHANGE SHOULD HAPPEN

3054 NOW SAVE REGISTERS FOR COMPARISON AFTER NO OPERATION

3087 NOW COMPARE REGISTERS BEFORE NO-OP COMMAND WITH AFTER NO-OP COMMAND



3107 NOW REPEAT TEST BY MOVING IN ALL POSSIBLE ONES

3135 NOW SET BITS IN RHAS FOR ALL DRIVES PRESENT

3169 NOW SAVE REGISTERS FOR COMPARISON AFTER A NO-OP

3202 CHANGE REGISTERS TO EXPECTED VALUES

3223 NOW COMPARE REGISTERS BEFORE NO-OP WITH  
AFTER NO-OP COMMAND

3246 TEST 17 DRIVE CLEAR  
ALL WRITE BITS OF ALL REGISTERS ARE FILLED  
WITH ONES EXCEPT GO, CLR, IE, PAT, MCPE, UPE  
THEN A DRIVE CLEAR IS GIVEN  
THEN ALL REGISTERS ARE CHECKED TO HAVE APPROPRIATE VALUE

3266 WRITE ALL WRITABLE REGISTER BITS

3288 NOW SET BITS IN RHAS FOR ALL DRIVES PRESENT

3338 NOW LOAD 'SAVERE' REGISTER SNAPSHOT WITH EXPECTED VALUES

3436 NOW THAT SAVERE TABLE HAS BEEN LOADED WITH  
EXPECTED VALUES, THE REGISTERS WILL BE COMPARED  
WITH SAVERE TABLE

3461 TEST 20 READ-IN-PRESET  
IF STARTING ADDRESS 220 IS USED THIS TEST WILL NOT BE PERFORMED

IF THE PROGRAM WORKS UNDER ACT-11 MONITOR  
THEN THIS TEST IS NOT PERFORMED

IF NO ACT-11 MONITOR IS PRESENT THEN  
THIS TEST IS PERFORMED ONLY ON THE FIRST PASS  
ON SUBSEQUENT PASSES THIS TEST IS NOT DONE

THIS TESTS THE READ-IN-PRESET COMMAND  
FIRST THE DRIVE IS POWERED DOWN AND UP IN ORDER TO  
RESET VV-BIT #6 IN RHDS1  
THEN ALL WRITE BITS OF ALL REGISTERS ARE FILLED  
WITH ONES EXCEPT GO, CLR, IE, PAT, MCPE, UPE  
ATA FOR DRIVE UNDER TEST IS MADE = 0

THEN A READ-IN-PRESET COMMAND = 20 IS GIVEN  
THEN ALL REGISTERS ARE TESTED  
THE FOLLOWING SHOULD BE THE REGISTER CONTENTS  
RHCA = 0, RHDST = 0, RHOF SHOULD HAVE  
FMT22 = 0, ECI = 0, HCI = 0, RHDS1 SHOULD HAVE VV = 1  
ALL OTHER REGISTERS SHOULD BE UNCHANGED

3497 THIS CODE CHECKS TO SEE IF MANUAL INTERVENTION TESTS ARE OK

3544 NOW INITIALIZE ALL THE REGISTERS

3565 NOW SET BITS IN RHAS FOR ALL DRIVES PRESENT

3595 NOW SAVE REGISTERS FOR COMPARISON AFTER READ-IN COMMAND

3643 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE

3670 NOW THAT SAVERE TABLE WITH SAVED REGISTERS HAVE  
THE EXPECTED VALUE AFTER A READ-IN COMMAND  
COMPARISONS ARE MADE

3702 TEST 21 READ-IN-PRESET

3704 THIS TEST IS THE SAME AS THE PREVIOUS TEST EXCEPT  
THAT IT DOES NOT TEST THE SETTING OF VV

THIS TEST IS HERE BECAUSE IF ACT-11 MONITOR IS PRESENT  
THEN THE PREVIOUS TEST WILL NOT BE PERFORMED  
THIS TESTS THE READ-IN-PRESET COMMAND  
ALL WRITE BITS OF ALL REGISTERS ARE FILLED  
WITH ONES EXCEPT GO, CLR, IE, PAT, MCPE, UPE  
ATA FOR DRIVE UNDER TEST IS MADE = 0

THEN A READ-IN-PRESET COMMAND = 20 IS GIVEN  
THEN ALL REGISTERS ARE TESTED  
THE FOLLOWING SHOULD BE THE REGISTER CONTENTS  
RHCA = 0, RHDST = 0, RHOF SHOULD HAVE  
FMT22 = 0, ECI = 0, HCI = 0, RHDS1 SHCJLD HAVE VV = 1  
ALL OTHER REGISTERS SHOULD BE UNCHANGED

3737 INITIALIZE ALL THE REGISTERS

3767 NOW SAVE REGISTERS FOR COMPARISON AFTER READ-IN COMMAND

3815 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE

3842 NOW THAT SAVERE TABLE WITH SAVED REGISTERS HAVE  
THE EXPECTED VALUE AFTER A READ-IN COMMAND  
COMPARISONS ARE MADE

3993 TEST 22 MAKE CURRENT CYLINDER = 777

3902 TEST 23 MAKE CURRENT CYLINDER = 377

3921 TEST 24 RECALIBRATE COMMAND

3923 ALL POSSIBLE REGISTERS ARE FILLED WITH ONES  
THEN A RECALIBRATE = 6 COMMAND IS GIVEN

3926 NO REGISTERS SHOULD CHANGE EXCEPT RHCC = 0

3962 NOW SET BITS IN RHAS FOR ALL DRIVES PRESENT

3991 NOW SAVE REGISTERS FOR COMPARISON AFTER RECALIBRATE

4022 COMPARE CONTENTS OF RHCSI AND RHDSI ALREADY SAVED IN  
RD AND RS IMMEDIATELY AFTER GO

4054 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES

4074 NOW COMPARE REGISTERS AFTER A RECALIBRATE COMMAND

4105 TEST 25 MAKE CURRENT CYLINDER = 777

4124 TEST 26 MAKE CURRENT CYLINDER = 377

4142 TEST 27 RECALIBRATE COMMAND

4144 ALL POSSIBLE REGISTERS ARE FILLED WITH 0  
THEN A RECALIBRATE =6 COMMAND IS GIVEN

4147 NO REGISTERS SHOULD CHANGE EXCEPT RHCC=0

4191 NOW SAVE REGISTERS FOR COMPARISON AFTER RECALIBRATE

4222 COMPARE CONTENTS OF RHCSI AND RHDSI ALREADY SAVED IN  
RD AND RS IMMEDIATELY AFTER GO

4245 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES

4269 NOW COMPARE REGISTERS AFTER A RECALIBRATE COMMAND

4288 TEST 30 UNLOAD COMMAND

4290 IF STARTING ADDRESS 220 IS USED THIS TEST WILL NOT BE PERFORMED  
IF THE PROGRAM WORKS UNDER ACT-11 MONITOR  
THEN THIS TEST IS NOT PERFORMED  
IF NO ACT-11 MONITOR IS PRESENT  
THEN THIS TEST IS PERFORMED ONLY ON THE FIRST PASS  
ON SUBSEQUENT PASSES THIS TEST IS NOT DONE

ALL POSSIBLE REGISTERS ARE FILLED WITH ONES  
THEN AN UNLOAD COMMAND =2 IS GIVEN  
NO REGISTERS SHOULD CHANGE EXCEPT MOL SHOULD=0  
THEN THE DRIVE IS POWERED UP BY OPERATOR  
AND A PACK ACKNOWLEDGE COMMAND (ALREADY TESTED)  
SETS VV-IN RHDSI

4311 THIS CODE CHECKS TO SEE IF MANUAL INTERVENTION TESTS ARE OK

4337 THIS SETTING OF VV IS FOR LOOP ON ERROR ONLY  
WHERE UNLOAD TAKES EFFECT AND CYCLE UP BRINGS VV DOWN

4364 SET VV IN RHDS1 WITH PACK ACKNOWLEDGE

4392 LOAD ALL POSSIBLE REGISTERS WITH ONES

4434 NOW SET BITS IN RHAS FOR ALL DRIVES PRESENT

4465 NOW SAVE REGISTERS FOR COMPARISON AFTER UNLOAD

4499 COMPARE CONTENTS OF RHCS1 AND RHDS1, WHICH WERE SAVED  
DURING THE UNLOAD COMMAND, WITH THE EXPECTED RESULTS

4547 NOW CHANGE REGISTERS SAVED BEFORE UNLOAD COMMAND  
TO EXPECTED VALUES AFTER UNLOAD COMMAND

4550 - AGAIN 'MOL' & 'VV' ARE DON'T CARES

4585 NOW COMPARE REGISTERS AFTER THE UNLOAD COMMAND  
WITH EXPECTED VALUES

4624 SET VV IN RHDS1 AFTER RESET FROM ACTUATING  
THE STANDBY SWITCH AND CYCLING UP (MOL = 1)

4646 TEST 31 OFFSET AND RETURN TO CENTER LINE COMMAND

4648 THIS TESTS TWO COMMANDS: (1)OFFSET. (2)RETURN-TO-CENTER-LINE

4650 ALL POSSIBLE REGISTERS ARE FILLED WITH ONES (EXCEPT RHOF)  
AND AN OFFSET IS GIVEN  
ALL REGISTERS ARE COMPARED. ONLY ATA SHOULD SET  
ALL OTHER REGISTERS SHOULD REMAIN UNCHANGED

4655 THEN A RETURN-TO-CENTER-LINE IS GIVEN  
ALL REGISTERS ARE COMPARED ONLY ATA SHOULD SET  
AND RHOF SHOULD CLEAR (EXCEPT HCI,ECI,FMT22)  
ALL OTHER REGISTERS SHOULD REMAIN UNCHANGED

4660 THE ABOVE PROCESS IS REPEATED FOR OFFSET REGISTER  
VALUES OF 1 TO 377 IE. 377 TIMES

4713 THE OFFSET REGISTER WILL BE INCREMENTED FROM 0 TO 377

4721 NOW SET BITS IN RHAS FOR ALL DRIVES PRESENT

4748 NOW SAVE REGISTERS FOR COMPARISON AFTER OFFSET



4792 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
RD AND RS IMMEDIATELY AFTER GO

4814 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES

4835 NOW COMPARE REGISTERS AFTER AN CFSET COMMAND

4856 NOW A RETURN TO CENTER LINE COMMAND WILL BE GIVEN

4869 NOW REGISTERS ARE SAVED FOR COMPARISON AFTER COMMAND

4902 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
RD AND RS IMMEDIATELY AFTER GO

4934 NOW CHANGE SAVED REGISTER TO EXPECTED VALUE

4958 NOW COMPARE REGISTERS AFTER RETURN-TO-CENTER-LINE

4991 TEST 32 OFFSET COMMAND

4993 THIS TEST WILL ONLY GIVE REPEATED OFFSETS

5056

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READ/WRITE TESTS USING MEDIA  
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5058 TEST 33 WRITE/READ HEADER AND DATA (0'S)

5052 WRITE HEADER AND DATA CYLINDER 0, FORMAT 16 BITS PER WORD  
TRACK 0, SECTOR 0, KEYS=0, NUMBER OF WORDS 256 WORDS  
OF 0  
THEN READ HEADER AND DATA FOR ABOVE.  
WRITE FROM BUFFER AND READ INTO BUFFER ARE FILLED WITH  
10000 0,0,0, AND 256 OF 0  
THE WRITE COMMAND IS THEN LOADED INTO THE REGISTERS EXCEPT  
THE GO BIT, AND ALL THE REGISTERS ARE SAVED  
THEN GO IS GIVEN FOR WRITE HEADER AND DATA

THEN ALL REGISTERS ARE COMPARED TO CHECK FOR IMPROPER CHANGED  
THEN WRITE FROM BUFFER IS CHECKED TO SEE THAT NOTHING CHANGED

NOW FOR THE READ COMMAND READ INTO BUFFER IS FILLED  
WITH ALL ONES, COMMAND IS LOADED INTO REGISTERS EXCEPT  
GO BIT AND ALL REGISTERS ARE SAVED  
GO IS GIVEN FOR THE READ COMMAND

ALL REGISTERS ARE CHECKED FOR IMPROPER CHANGE  
THEN THE READ DATA IS COMPARED.

5092 FILL WRITE FROM SUFFER WITH HEADER

5103 FILL WRITE FROM BUFFER WITH DATA

5111 NOW READ INTO BUFFER WILL BE FILLED WITH SAME DATA  
AS WRITE FROM BUFFER SO THAT AFTER A WRITE COMPARISONS  
CAN BE MADE TO MAKE SURE THAT WRITE DID NOT  
CHANGE WRITE FROM BUFFER

5130 NOW THE WRITE HEADER AND DATA COMMAND WILL BE FILLED

5150 NOW SAVE REGISTERS FOR COMPARISON AFTER WRITE HEADER AND DATA

5182 ONE REVOLUTION=16670 MICRO SEC. ONE SECTOR = 760 MICRO SEC

5192 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
RO AND RS IMMEDIATELY AFTER GO

5216 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES

5228 NOW COMPARE REGISTERS BEFORE WRITE HEADER AND DATA  
WITH REGISTERS AFTER COMMAND

5248 NOW WRITE FROM BUFFER WILL BE CHECKED TO SEE THAT  
NOTHING GOT CHANGED

5264 NOW A READ HEADER AND DATA COMMAND WILL BE GIVEN  
READ INTO BUFFER IS FILLED WITH ONES

5279 NOW FILL COMMAND

5299 NOW SAVE REGISTERS FOR COMPARISON AFTER READ HEADER AND DATA

5339 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
RO AND RS IMMEDIATELY AFTER GO

5363 CHANGE SAVED REGISTERS TO EXPECTED VALUES

5375 COMPARE REGISTER BEFORE READ HEADER AND DATA  
WITH REGISTERS AFTER COMMAND

5394 NOW READ INTO BUFFER WILL BE CHECKED TO SEE  
THE READ WAS GOOD

5415 TEST 34 READ DATA (0'S)

5417 THIS TEST READS DATA WRITTEN BY THE PREVIOUS TEST  
THE WRITE FROM BUFFER IS FILLED WITH ALL 0S  
THE COMMAND IS FILLED, THEN ALL REGISTERS SAVED  
THEN READ DATA COMMAND IS GIVEN  
ALL REGISTERS ARE COMPARED FOR PROPER VALUES  
READ DATA INTO 'READ INTO' BUFFER IS CHECKED

5434 FILL WRITE FROM BUFFER WITH EXPECTED DATA

5442 NOW THE READ DATA COMMAND WILL BE FILLED

5461 NOW SAVE REGISTERS FOR COMPARISON AFTER READ DATA COMMAND

5501 COMPARE CONTENTS OF RHCSI AND RHDSI ALREADY SAVED IN  
RD AND R5 IMMEDIATELY AFTER GO

5525 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES

5537 NOW COMPARE REGISTERS BEFORE READ DATA WITH  
AFTER COMMAND

5553 NOW READ INTO BUFFER WILL BE CHECKED TO SEE THAT READ  
WAS GOOD

5572 TEST 35 WRITE/READ DATA (1'S & 125252)

5574 THIS TEST GIVES A WRITE DATA COMMAND FRO CYLINDER 0  
TRACK 0, SECTOR 0, KEYS 0, 200 WORDS OF ALL ONES  
THIS SECTOR IS FORMATED BY PREVIOUS TEST  
THEN READ DATA COMMAND IS GIVEN FOR 256 WORDS IN  
SAME CYLINDER, TRACK, SECTOR, KEYS  
WRITE FROM BUFFER AND READ INTO BUFFER ARE FILLED WITH  
200 ONES AND 56 125252  
THE WRITE DATA COMMAND IS LOADED EXCEPT GO AND IE  
ALL REGISTERS ARE SAVED AND THEN GO IS GIVEN TO WRITE DATA

THEN ALL REGISTERS ARE COMPARED TO CHECK FOR IMPROPER  
CHANGE, THEN WRITE FROM BUFFER IS CHECKED FOR NO CHANGE

THEN READ INTO BUFFER IS FILLED WITH 200 OF ZEROS  
AND 56 OF 377, WRITE FROM BUFFER IS FILLED WITH 200 ONES  
AND 56 OF 377.  
THE COMMAND EXCEPT GO IS LOADED, ALL REGISTERS ARE SAVED  
GO IS GIVEN

ALL REGISTERS ARE CHECKED  
READ DATA IS CHECKED

5608 NOW FILL WRITE FROM BUFFER -200 OF 1'S AND 56 OF 125252

5621 NOW READ INTO BUFFER WILL BE FILLED WITH SAME DATA AS  
WRITE FROM BUFFER SO THAT AFTER A WRITE COMPARISONS  
CAN BE MADE TO DETERMINE THAT WRITE DID NOT CHANGE BUFFER

5636 NOW WRITE DATA COMMAND WILL BE LOADED

5655 NOW SAVE REGISTERS FOR COMPARISON AFTER WRITE DATA

5687 ONE REVOLUTION = 16670 MICRO SEC, ONE SECTOR=760 MICRO SEC

5697 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
RO AND RS IMMEDIATELY AFTER GO

5721 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE

5733 NOW COMPARE REGISTERS BEFORE WRITE DATA WITH REGISTERS  
AFTER COMMAND

5752 NOW WRITE FROM BUFFER WILL BE CHECKED FOR NO CHANGE

5767 NOW A READ DATA COMMAND WILL BE GIVEN

5769 FILL READ INTO BUFFER WITH 200 ZEROS AND 56 OF 377

5798 FILL WRITE FROM BUFFER WITH 200 ONES AND 56 OF 377

5801 NOW FILL COMMAND

5820 NOW SAVE REGISTERS FOR COMPARISON AFTER READ DATA COMMAND

5860 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
RO AND RS IMMEDIATELY AFTER GO

5884 CHANGE SAVED REGISTERS TO EXPECTED VALUES

5896 COMPARE REGISTERS BEFORE READ DATA COMMAND  
WITH REGISTERS AFTER COMMAND

5914 NOW READ INTO BUFFER IS CHECKED FOR GOOD READ

5934 TEST 36 WRITE/READ DATA (125252)

5936 THIS TEST GIVES A WRITE DATA COMMAND FOR CYLINDER 0  
TRACK 0, SECTOR 0, KEYS 0, 256 WORDS OF 125252  
THIS SECTOR IS FORMATED BY PREVIOUS TEST  
THEN READ DATA COMMAND IS GIVEN FOR 256 WORDS IN  
SAME CYLINDER, TRACK, SECTOR, KEYS  
WRITE FROM BUFFER AND READ INTO BUFFER ARE FILLED WITH  
256 OF 125252  
THE WRITE DATA COMMAND IS LOADED EXCEPT GO AND IE  
ALL REGISTERS ARE SAVED AND THEN GO IS GIVEN TO WRITE DATA

THEN ALL REGISTERS ARE COMPARED TO CHECK FOR IMPROPER  
CHANGE, THEN WRITE FROM BUFFER IS CHECKED FOR NO CHANGE

THEN READ INTO BUFFER IS FILLED WITH 256 OF ZEROS  
WRITE FROM BUFFER IS FILLED WITH



256 OF 125252  
THE COMMAND EXCEPT GO IS LOADED. ALL REGISTERS ARE SAVED  
GO IS GIVEN

ALL REGISTERS ARE CHECKED  
READ DATA IS CHECKED

5970 NOW FILL WRITE FROM BUFFER - 256 OF 125252

5978 NOW READ INTO BUFFER WILL BE FILLED WITH SAME DATA AS  
WRITE FROM BUFFER SO THAT AFTER A WRITE COMPARISONS  
CAN BE MADE TO DETERMINE THAT WRITE DID NOT CHANGE BUFFER

5998 NOW WRITE DATA COMMAND WILL BE LOADED

6007 NOW SAVE REGISTERS FOR COMPARISON AFTER WRITE DATA

6039 ONE REVOLUTION=16670 MICROSEC, ONE SECTOR=760 MICROSEC

6049 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
R0 AND R5 IMMEDIATELY AFTER GO

6073 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE

6085 NOW COMPARE REGISTERS BEFORE WRITE DATA WITH REGISTERS  
AFTER COMMAND

6104 NOW WRITE FROM BUFFER WILL CHECKED FOR NO CHANGE

6119 NOW A READ DATA COMMAND WILL BE GIVEN  
FILL READ INTO BUFFER WITH 256 ZEROS

6134 FILL WRITE FROM BUFFER WITH 256 OF 125252

6142 NOW FILL COMMAND

6161 NOW SAVE REGISTERS FOR COMPARISON AFTER READ DATA COMMAND

6201 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
R0 AND R5 IMMEDIATELY AFTER GO

6225 CHANGE SAVED REGISTERS TO EXPECTED VALUES

6237 COMPARE REGISTERS BEFORE READ DATA COMMAND  
WITH REGISTERS AFTER COMMAND

6255 NOW READ INTO BUFFER IS CHECKED FOR GOOD READ

6273 TEST 37 WRITE/READ DATA (052525)

6275 THIS TEST GIVES A WRITE DATA COMMAND FOR CYLINDER 0  
TRACK 0, SECTOR 0, KEYS 0, 200 WORDS OF 052525  
THIS SECTOR IS FORMATED BY PREVIOUS TEST  
THEN READ DATA COMMAND IS GIVEN FOR 256 WORDS IN  
SAME CYLINDER, TRACK, SECTOR, KEYS  
WRITE FROM BUFFER AND READ INTO BUFFER ARE FILLED WITH  
200 OF 052525 AND 56 OF 377  
THE WRITE DATA COMMAND IS LOADED EXCEPT GO AND IE  
ALL REGISTERS ARE SAVED AND THEN GO IS TO WRITE DATA

THEN ALL REGISTERS ARE COMPARED TO CHECK FOR IMPROPER  
CHANGE, THEN WRITE FROM BUFFER IS CHECKED FOR NO CHANGE

THEN READ INTO BUFFER IS FILLED WITH 200 IF ZEROS  
AND 56 ALL ONES, WRITE FROM BUFFER IS FILLED WITH 200  
WORDS OF 52525 AND 56 WORDS OF 0  
THE COMMAND EXCEPT GO IS LOADED, ALL REGISTERS ARE SAVED  
GO IS GIVEN

ALL REGISTER ARE CHECKED  
READ DATA IS CHECKED

6309 NOW FILL WRITE FROM BUFFER-200 OF 52525 AND 56 OF 377

6322 NOW READ INTO BUFFER WILL BE FILLED WITH SAME DATA AS  
WRITE FROM BUFFER SO THAT AFTER A WRITE COMPARISONS  
CAN BE MADE TO DETERMINE THAT WRITE DID NOT CHANGE BUFFER

6337 NOW WRITE DATA COMMAND WILL BE LOADED

6356 NOW SAVE REGISTER FOR COMPARISON AFTER WRITE DATA

6388 ONE REVOLUTION=16670 MICROSEC, ONE SECTOR=760 MICROSEC

6398 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
R0 AND R5 IMMEDIATELY AFTER GO

6422 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE

6434 NOW COMPARE REGISTERS BEFORE WRITE DATA WITH REGISTERS  
AFTER COMMAND

6453 NOW WRITE FROM BUFFER WILL BE CHECKED FOR NO CHANGE

6468 NOW A READ DATA COMMAND WILL BE GIVEN

6470 FILL READ INTO BUFFER WITH 200 ZEROS AND 56 OF ALL ONES  
6489 FILL WRITE FROM BUFFER WITH 200 OF 52525 AND 56 OF 0  
6502 NOW FILL COMMAND  
6521 NOW SAVE REGISTERS FOR COMPARISON AFTER READ DATA COMMAND  
6561 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
R0 AND R5 IMMEDIATELY AFTER GO  
6585 CHANGE SAVED REGISTERS TO EXPECTED VALUES  
6597 COMPARE REGISTERS BEFORE READ DATA COMMAND  
WITH REGISTERS AFTER COMMAND  
6615 NOW READ INTO BUFFER IS CHECKED FOR GOOD READ  
6636 TEST 40 WRITE/READ DATA USING UNIBUS B  
6638 THIS TEST USES UNIBUS B IF CONNECTED TO THE RH  
IF UNIBUS B IS NOT CONNECTED THEN THIS TEST IS NOT PERFORMED  
THIS TEST GIVES A WRITE DATA COMMAND FOR CYLINDER 0  
TRACK 0, SECTOR 0, KEYS 0, 200 WORDS OF 052525  
THIS SECTOR IS FORMATED BY PREVIOUS TEST  
THEN READ DATA COMMAND IS GIVEN FOR 256 WORDS IN  
SAME CYLINDER, TRACK, SECTOR, KEYS  
THESE COMMANDS USE UNIBUS B FOR DATA  
WRITE FROM BUFFER AND READ INTO BUFFER ARE FILLED WITH  
200 OF 052525 AND 56 OF 377  
THE WRITE DATA COMMAND IS LOADED EXCEPT GO AND IE  
ALL REGISTERS ARE SAVED AND THEN GO IS GIVEN TO WRITE DATA  
  
THEN ALL REGISTERS ARE COMPARED TO CHECK FOR IMPROPER  
CHANGE, THEN WRITE FROM BUFFER IS CHECKED FOR NO CHANGE  
  
THEN READ INTO BUFFER IS FILLED WITH 200 IF ZEROS  
AND 56 ALL ONES, WRITE FROM BUFFER IS FILLED WITH 200  
WORDS OF 52525 AND 56 WORDS OF 0  
THE COMMAND EXCEPT GO IS LOADED, ALL REGISTERS ARE SAVED  
GO IS GIVEN  
  
ALL REGISTER ARE CHECKED  
READ DATA IS CHECKED  
6676 CHECK TO SEE IF THE PROGRAM IS RUNNING WITH AN RH70

6685 NOW FILL WRITE FROM BUFFER-200 OF 52525 AND 56 OF 377

6698 NOW READ INTO BUFFER WILL BE FILLED WITH SAME DATA AS  
WRITE FROM BUFFER SO THAT AFTER A WRITE COMPARISONS  
CAN BE MADE TO DETERMINE THAT WRITE DID NOT CHANGE BUFFER

6713 NOW WRITE DATA COMMAND WILL BE LOADED

6734 NOW SAVE REGISTER FOR COMPARISON AFTER WRITE DATA

6765 ONE REVOLUTION=16670 MICROSEC, ONE SECTOR=760 MICROSEC

6775 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
RD AND RS IMMEDIATELY AFTER GO

6800 CHECK IF NEM NON EXISTANT MEMORY IS SET  
IF SET IT MEANS UNIBUS B IS NOT CONNECTED  
SO THIS TEST IS NOT PERFORMED

6822 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE

6834 NOW COMPARE REGISTERS BEFORE WRITE DATA WITH REGISTERS  
AFTER COMMAND

6854 NOW WRITE FROM BUFFER WILL BE CHECKED FOR NO CHANGE

6870 NOW A READ DATA COMMAND WILL BE GIVEN  
FILL READ INTO BUFFER WITH 200 ZEROS AND 56 OF ALL ONES

6890 FILL WRITE FROM BUFFER WITH 200 OF 52525 AND 56 OF 0

6903 NOW FILL COMMAND

6924 NOW SAVE REGISTERS FOR COMPARISON AFTER READ DATA COMMAND

6963 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
RD AND RS IMMEDIATELY AFTER GO

6987 CHANGE SAVED REGISTERS TO EXPECTED VALUES

6999 COMPARE REGISTERS BEFORE READ DATA COMMAND  
WITH REGISTERS AFTER COMMAND

7018 NOW READ INTO BUFFER IS CHECKED FOR GOOD READ

7043 TEST 41 IMPLIED SEARCH

7045 ONLY NEW ADDITION IN THIS TEST IS AN IMPLIED SEARCH  
A WRITE HEADER AND DATA IS GIVEN FOR MORE THAN ONE SECTOR  
OF WORDS  
WRITE HEADER AND DATA CYLINDER 0, FORMAT 16 BITS PER WORD  
TRACK 0, SECTOR 0, KEYS=0, NUMBER OF WORDS  
266 (4 HEADER 256 DATA=0 4 HEADER 2 DATA=1  
THEN READ HEADER AND DATA FOR ABOVE SECTOR 1 ONLY

WRITE FROM BUFFER AND READ INTO BUFFER ARE FILLED WITH  
 10000,0,0,0 AND 256 OF 0, 10000,1,0,0, AND 2 OF 1  
 THE WRITE COMMAND IS THEN LOADED INTO THE REGISTERS EXCEPT  
 THE GO BIT, AND ALL THE REGISTERS ARE SAVED  
 THEN GO IS GIVEN FOR WRITE HEADER AND DATA

THEN ALL REGISTERS ARE COMPARED TO CHECK FOR IMPROPER CHANGE  
 THEN WRITE FROM BUFFER IS CHECKED TO SEE THAT NOTHING CHANGED

NOW FOR THE READ COMMAND READ INTO BUFFER IS FILLED  
 WITH 377, WRITE FROM BUFFER IS FILLED WITH 10000,1,0,0,1,1  
 AND 254 OF ZEROS COMMAND IS LOADED INTO REGISTERS EXCEPT  
 GO AND IE THEN ALL REGISTERS ARE SAVED  
 GO IS GIVEN FOR THE READ COMMAND, 256 WORDS

ALL REGISTERS ARE CHECKED FOR IMPROPER CHANGE  
 THEN THE READ DATA IS COMPARED

- 7091 FILL WRITE FROM BUFFER WITH HEADER
- 7091 FILL WRITE FROM BUFFER WITH DATA
- 7098 FILL WRITE FROM BUFFER WITH NEXT SECTOR HEADER
- 7109 FILL WRITE FROM BUFFER WITH NEXT SECTOR DATA
- 7115 NOW READ INTO BUFFER WILL BE FILLED WITH SAME DATA  
 AS WRITE FROM BUFFER SO THAT AFTER A WRITE COMPARISONS  
 CAN BE MADE TO MAKE SURE THAT WRITE DID NOT  
 CHANGE WRITE FROM BUFFER.
- 7147 NOW THE WRITE HEADER AND DATA COMMAND WILL BE FILLED
- 7167 NOW SAVE REGISTERS FOR COMPARISON AFTER WRITE HEADER AND DATA
- 7198 ONE REVOLUTION = 16670 MICRO SEC, ONE SECTOR = 760 MICRO SEC
- 7208 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
 RD AND RS IMMEDIATELY AFTER GO
- 7232 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES
- 7244 NOW COMPARE REGISTERS BEFORE WRITE HEADER AND DATA  
 WITH REGISTERS AFTER COMMAND
- 7264 NOW WRITE FROM BUFFER WILL BE CHECKED TO SEE THAT  
 NOTHING GOT CHANGED



- 7290 NOW A READ HEADER AND DATA COMMAND WILL BE GIVEN FOR SECTOR 1, 256 WORDS READ INTO BUFFER IS FILLED WITH ONES
- 7295 WRITE FROM BUFFER IS FILLED WITH EXPECTED DATA
- 7313 NOW FILL COMMAND
- 7333 NOW SAVE REGISTERS FOR COMPARISON AFTER READ HEADER AND DATA
- 7365 ONE REVOLUTION = 16670 MICRO SEC. ONE SECTOR 760 MICRO SECONDS
- 7375 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN RC AND RS IMMEDIATELY AFTER GO
- 7399 CHANGE SAVED REGISTERS TO EXPECTED VALUES
- 7411 COMPARE REGISTERS BEFORE READ HEADER AND DATA WITH REGISTERS AFTER COMMAND
- 7431 NOW READ INTO BUFFER WILL BE CHECKED TO SEE THAT READ WAS GOOD
- 7452 TEST 42 IMPLIED SEEK TO CYL 001

7454

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 SEEK TESTS  
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7456 ONLY NEW ADDITION IN THIS TEST IS AN IMPLIED SEEK FROM CYLINDER C TO CYLINDER 1. A WRITE HEADER AND DATA IS GIVEN FOR MORE THAN ONE SECTOR OF WORDS

WRITE HEADER AND DATA CYLINDER C, FORMAT 16 BITS PER WORD  
 TRACK 18, SECTOR 21, KEYS=0, NUMBER OF WORDS 266 WORDS  
 14 HEADER, 256 DATA=1125, 4 HEADER 2 DATA = 2000  
 THEN READ HEADER AND DATA FOR ABOVE, TRACK 0, SECTOR 0, CYL=1  
 WRITE FROM BUFFER AND READ INTO BUFFER ARE FILLED WITH  
 10000,0,0,0 AND 256 OF 1125, 10001,0,0,0, AND 2 OF 2000  
 THE WRITE COMMAND IS THEN LOADED INTO THE REGISTERS EXCEPT  
 THE GO BIT, AND ALL THE REGISTERS ARE SAVED  
 THEN GO IS GIVEN FOR WRITE HEADER AND DATA

THEN ALL REGISTERS ARE COMPARED TO CHECK FOR IMPROPER CHANGE  
 THEN WRITE FROM BUFFER IS CHECKED TO SEE THAT NOTHING CHANGED

NOW FOR READ COMMAND READ INTO BUFFER IS FILLED  
 WITH ALL ONES, WRITE FROM BUFFER IS LOADED WITH  
 10001,0,0,0 AND 2 OF 2000, 254 OF ZEROS COMMAND IS LOADED  
 INTO REGISTERS EXCEPT GO AND IE ALL REGISTERS ARE SAVED  
 GO IS GIVEN FOR THE READ COMMAND

ALL REGISTERS ARE CHECKED FOR IMPROPER CHANGE  
THEN THE READ DATA IS COMPARED.

7494 FILL WRITE FROM BUFFER WITH HEADER  
7504 FILL WRITE FROM BUFFER WITH DATA  
7511 FILL WRITE FROM BUFFER WITH NEXT TRACK HEADER  
7521 FILL WRITE FROM BUFFER WITH NEXT TRACK DATA  
7529 NOW READ INTO BUFFER WILL BE FILLED WITH SAME DATA  
AS WRITE FROM BUFFER SO THAT AFTER A WRITE COMPARISONS  
CAN BE MADE TO MAKE SURE THAT WRITE DID NOT  
CHANGE WRITE FROM BUFFER.  
7560 NOW THE WRITE HEADER AND DATA COMMAND WILL BE FILLED  
7580 NOW SAVE REGISTERS FOR COMPARISON AFTER WRITE HEADER AND DATA  
7612 ONE REVOLUTION = 16670 MICRO1 SEC, ONE SECTOR = 760 MICRO1 SEC  
7622 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
R0 AND R5 IMMEDIATELY AFTER GO  
7646 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES  
7664 NOW COMARE REGISTERS BEFORE WRITE HEADER AND DATA  
WITH REGISTERS AFTER COMMAND  
7694 NOW WRITE FROM BUFFER WILL BE CHECKED TO SEE THAT  
NOTHING GOT CHANGED  
7700 NOW A READ HEADER AND DATA COMMAND WILL BE GIVEN  
READ INTO BUFFER IS FILLED WITH ONES  
7715 WRITE FROM BUFFER IS FILLED WITH 10001,0,0,0,2000,2000, AND 254 OF 0  
7732 NOW FILL COMMAND  
7751 NOW SAVE REGISTERS FOR COMPARISON AFTER READ HEADER AND DATA  
7791 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
R0 AND R5 IMMEDIATELY AFTER GO  
7815 CHANGE SAVED REGISTERS TO EXPECTED VALUES  
7826 COMPARE REGISTERS BEFORE READ HEADER AND DATA  
WITH REGISTERS AFTER COMMAND

7845 NOW READ INTO BUFFER WILL BE CHECKED TO SEE  
THAT READ WAS GOOD

7967 TEST 43 SEEK & WRT TEST (CYL = 0-10)

7969 THIS TEST GETS THE HEADS OUT TO CYLINDER 10 NOT BY ONE  
SEEK BUT BY TEN IMPLIED SEEKS ONE CYLINDER AT A TIME

THIS TEST STARTS WITH A (ALREADY TESTED) RECALIBRATE  
THAT IS CYLINDER ZERO. THEN ON CYLINDER 0 SECTOR  
#21 TRACK #19 IT WRITES 266 WORDS THERE BY GETTING  
THE HEAD TO CYLINDER 1 THEN IT WRITES 266 WORDS ON  
CYLINDER 1 SECTOR #21 TRACK #18 THERE BY GETTING  
THE HEADS TO CYLINDER 2  
THIS IS REPEATED 10 TIMES GETTING THE  
HEADS TO CYLINDER 10  
THEN A SEEK COMMAND IS GIVEN TO CYLINDER 0  
AND DATA ALREADY WRITTEN IS CHECKED

7993 THE FOLLOWING MOVES ARE TO INITIALIZE TEST FROM  
CYLINDER 0  
THESE LOCATIONS ARE CHANGED DURING TEST TO ENABLE  
GOING TO NEXT CYLINDER

7909 THIS IS TO GET THE HEADS TO CYLINDER 0

7940 FILL WRITE FROM BUFFER WITH HEADER

7951 FILL WRITE FROM BUFFER WITH DATA

7959 FILL WRITE FROM BUFFER WITH NEXT TRACK HEADER

7970 FILL WRITE FROM BUFFER WITH NEXT TRACK DATA

7978 THE WRITE HEADER AND DATA COMMAND WILL BE FILLED

7998 SAVE REGISTERS FOR COMPARISON AFTER WRITE HEADER AND DATA

8030 ONE REVOLUTION = 16670 MICRO SECONDS, ONE SECTOR = 760 MICRO SEC.  
MAX TIME ALLOWED = ONE REVOLUTION + SEEK + 2 SECTORS  
MIN TIME ALLOWED = 2 SECTORS + SEEK

8042 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
R0 AND R5 IMMEDIATELY AFTER GO

8056 NOW CHANGES SAVED REGISTERS TO EXPECTED VALUES

8085 COMPARE REGISTERS BEFORE WRITE HEADER AND DATA  
WITH REGISTERS AFTER COMMAND

8103 SETUP TO READ HEADER AND DATA FOR NEXT TRACK  
FILL READ INTO BUFFER WITH ALL ONES

8118 FILL WRITE FROM BUFFER WITH EXPECTED DATA

8141 FILL COMMAND INTO REGISTERS

8161 SAVE REGISTERS FOR COMPARISON AFTER READ HEADER  
AND DATA

8202 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
RD AND RS IMMEDIATELY AFTER GO

8226 CHANGE SAVED REGISTERS TO EXPECTED VALUES

8238 COMPARE REGISTERS BEFORE READ HEADER AND DATA WITH  
REGISTERS AFTER COMMAND

8257 READ INTO BUFFER IS CHECKED FOR PROPER READ

8276 THE HEADS HAVE ADVANCED ONE CYLINDER BY AN IMPLIED  
SEEK  
CHANGES WILL BE MADE TO ENABLE GOING TO THE NEXT  
CYLINDER AND THEN THE ABOVE WILL BE REPEATED  
TILL CYLINDER 10 IS REACHED

8297 THE HEADS ARE NOW AT CYLINDER 10  
ALL REGISTERS WILL BE SAVED AND A SEEK WILL BE GIVEN  
TO CYLINDER 0  
FILL REGISTERS FOR A SEEK COMMAND

8312 SAVE REGISTERS FOR COMPARISON AFTER SEEK COMMAND

8337 SEEK FOR ONE CYLINDER=7MILI SEC., FOR TEN=70 MILI SEC

8346 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
RD AND RS IMMEDIATELY AFTER GO

8370 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE

8394 COMPARE REGISTERS AFTER A SEEK COMMAND

8411 AT THIS POINT THE CURRENT CYLINDER IS GOOD AND THERE ARE  
NO ERROR BITS  
A READ HEADER AND DATA WILL BE DONE ON CYLINDER 0  
SECTOR 21 TRACK 18. EXPECTED DATA IS 1125  
FOR 10 WORDS  
CLEAR READ INTO BUFFER WITH ALL ONES

8425 FILL WRITE FROM BUFFER WITH EXPECTED DATA  
8445 FILL READ HEADER AND DATA COMMAND FOR 10 WORDS  
8493 CHECK READ WORDS  
8513 TEST 44 SEEK & READ TEST (CYL = 009)  
8515 THIS TEST DEPENDS ON HEADER AND DATA WRITTEN BY THE  
PREVIOUS TEST. AT THIS POINT THE HEADS ARE ON  
CYLINDER 0  
  
ALL REGISTERS ARE SAVED  
A SEEK IS GIVEN TO CYLINDER 9  
ALL REGISTERS ARE CHECKED FOR IMPROPER CHANGE  
DATA WRITTEN ON CYLINDER 9 IS CHECKED  
  
8533 THIS GETS HEADS TO CYLINDER 0  
8559 FILL REGISTERS FOR A SEEK COMMAND  
8564 SAVE REGISTERS FOR COMPARISON AFTER SEEK COMMAND  
8589 SEEK FOR ONE CYLINDER=7 MILI SEC., FOR TEN=70 MILI SEC  
8598 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
R0 AND R5 IMMEDIATELY AFTER GO  
  
8622 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE  
8646 COMPARE REGISTERS AFTER A SEEK COMMAND  
8653 AT THIS POINT THE CURRENT CYLINDER IS GOOD AND THERE ARE  
NO ERROR BITS  
A READ HEADER AND DATA WILL BE DONE ON CYLINDER 9  
SECTOR 21 TRACK 18, EXPECTED DATA IS 23125  
FOR 20 WORDS  
CLEAR READ INTO BUFFER WITH ALL ONES  
  
8682 FILL WRITE FROM BUFFER WITH EXPECTED DATA  
8703 FILL READ HEADER AND DATA COMMAND FOR 10 WORDS  
8751 CHECK READ WORDS



8770

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 WRITE CHECK DATA & WRITE PROTECT TESTS  
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8773 TEST 45 WRITE CHECK HEADER AND DATA

8775 WRITE CHECK HEADER AND DATA CYLINDER 5 FORMAT 16 BITS PER WORD  
 TRACK 7, SECTOR 4, KEYS 0, NUMBER OF WORDS 266  
 CONSISTING OF  
 10 WORDS OF 12344 (6 BITS FOR CYL, 5 FOR TRACK, 5 FOR SECTOR)  
 10 WORDS OF 17777  
 10 WORDS OF 0  
 10 WORDS OF 052525  
 10 WORDS OF 125252  
 16 WORDS OF LEFT ROTATING ZERO (EG 177776,177775)  
 16 WORDS OF LEFT ROTATING ONE (EG 1,2,4,10)  
 174 WORDS OF 377  
 4 WORDS OF HEADER  
 2 WORDS OF 12345

FIRST THE ABOVE DATA IS WRITTEN BY A WRITE HEADER AND  
 DATA COMMAND  
 CHECK FOR NO ERRORS  
 THEN THE ABOVE DATA IS READ BY A READ HEADER AND DATA  
 CHECK FOR NO ERRORS  
 THEN THE ABOVE WRITE CHECK HEADER AND DATA IS GIVEN

8811 GET HEADS TO CYLINDER 5

8846 FILL WRITE FROM BUFFER WITH HEADER

8856 10 WORDS OF OF THE FOLLOWING DATA  
 12344,17777,0,52525,125252

8885 FILL LEFT ROTATING ZEROS FROM WRFROM+(54.\*2)

8897 FILL LEFT ROTATING ONE INTO WRFROM+(65.\*2)

8907 FILL REST OF DATA

8929 READ INTO BUFFER WILL BE CLEARED

8936 THE WRITE HEADER AND DATA COMMAND WILL BE LOADED

8955 NOW SAVE REGISTERS FOR COMPARISON AFTER WRITE HEADER AND DATA

8993 NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES

9004 NOW COMPARE REGISTERS BEFORE WRITE HEADER AND DATA  
WITH REGISTERS AFTER COMMAND

9024 NOW FILL COMMAND FOR READ

9050 NOW SAVE REGISTERS FOR COMPARISON AFTER READ HEADER AND DATA

9089 CHANGE SAVED REGISTERS TO EXPECTED VALUES

9100 COMPARE REGISTERS BEFORE READ HEADER AND DATA  
WITH REGISTERS AFTER COMMAND

9121 NOW READ INTO BUFFER WILL BE CHECKED TO SEE  
THAT READ WAS GOOD

9140 A WRITE READ HAS BEEN SUCCESSFULLY COMPLETED  
NOW A WRITE CHECK HEADER AND DATA WILL BE GIVEN  
FILL THE WRITE CHECK HEADER AND DATA

9168 SAVE REGISTERS FOR COMPARISON AFTER WRITE CHECK

9209 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
RD AND RS IMMEDIATELY AFTER GO

9233 CHANGE SAVED REGISTERS TO EXPECTED VALUES

9245 COMPARE REGISTERS BEFORE WRITE CHECK HEADER AND DATA  
WITH REGISTERS AFTER COMMAND

9269 TEST 46 WRITE CHECK DATA  
THE DATA FOR THIS TEST IS WRITTEN ON DISK BY PREVIOUS TEST  
WRITE CHECK DATA CYLINDER 5, FORMAT 16 BITS PER WORDS  
TRACK 7, SECTOR 4, KEYS 0, NUMBER OF WORDS 258  
CONSISTING OF  
10 WORDS OF 12344 (6 BITS FOR CYL, 5 FOR TRACK, 5 FOR SECTOR)  
10 WORDS OF 177777  
10 WORDS OF 0  
10 WORDS OF 052525  
10 WORD OF 125252  
16 WORDS OF LEFT ROTATING ZERO (EG 177776, 177775)  
16 WORDS OF LEFT ROTATING ONE (EG 1,2,4,10)  
174 WORDS OF 377  
2 WORDS OF 12345

FIRST THE ABOVE DATA IS FILLED INTO WRITE FROM BUFFER  
THEN THE ABOVE WRITE CHECK DATA IS GIVEN

9302 GET HEADS TO CYLINDER 5

9337 10 WORDS OF EACH 12344,17777,0,52525,125252  
 9365 FILL LEFT ROTATING ZEROS FROM WRFROM+<50.\*2>  
 9377 FILL LEFT ROTATING ONE INTO WRFROM+<65.\*2>  
 9386 FILL REST OF DATA  
 9399 FILL THE WRITE CHECK HEADER AND DATA  
 9417 SAVE REGISTERS FOR COMPARISON AFTER WRITE CHECK  
 9456 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
 RD AND RS IMMEDIATELY AFTER GO  
 9480 CHANGE SAVED REGISTERS TO EXPECTED VALUES  
 9492 COMPARE REGISTERS BEFORE WRITE CHECK HEADER AND DATA  
 WITH REGISTER AFTER COMMAND  
 9516 TEST 47 WRITE CHECK DATA USING UNIBUS B  
 THIS TEST USES UNIBUS B IF CONNECTED TO THE RH  
 IF UNIBUS B IS NOT CONNECTED THEN THIS TEST IS NOT PERFORMED  
 THE DATA FOR THIS TEST IS WRITTEN ON DISK BY PREVIOUS TEST  
 WRITE CHECK DATA CYLINDER 5, FORMAT 16 BITS PER WORDS  
 TRACK 7, SECTOR 4, KEYS 0, NUMBER OF WORDS 258  
 CONSISTING OF  
 10 WORDS OF 12344 (6 BITS FOR CYL, 5 FOR TRACK, 5 FOR SECTOR)  
 10 WORDS OF 177777  
 10 WORDS OF 0  
 10 WORDS OF 052525  
 10 WORD OF 125252  
 16 WORDS OF LEFT ROTATING ZERO (EG 177776,177775)  
 16 WORDS OF LEFT ROTATING ONE (EG 1,2,4,10)  
 174 WORDS OF 377  
 2 WORDS OF 12345

FIRST THE ABOVE DATA IS FILLED INTO WRITE FROM BUFFER  
 THEN THE ABOVE WRITE CHECK DATA IS GIVEN

9546 CHECK TO SEE IF THE PROGRAM IS RUNNING WITH AN RH70  
 9562 GET HEADS TO CYLINDER 5  
 9598 10 WORDS OF EACH 12344,17777,0,52525,125252  
 9626 FILL LEFT ROTATING ZEROS FROM WRFROM+<50.\*2>

9639 FILL LEFT ROTATING ONE INTO WRFROM+<65.\*2>  
9647 FILL REST OF DATA  
9659 FILL THE WRITE CHECK HEADER AND DATA  
9681 SAVE REGISTERS FOR COMPARISON AFTER WRITE CHECK  
9702 SET PORT SELECT  
9721 COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
RO AND RS IMMEDIATELY AFTER GO  
9745 CHANGE SAVED REGISTERS TO EXPECTED VALUES  
9757 COMPARE REGISTERS BEFORE WRITE CHECK HEADER AND DATA  
WITH REGISTER AFTER COMMAND  
9785 TEST 50 WRITE PROTECT OPERATION  
IF STARTING ADDRESS 220 IS USED THIS TEST WILL NOT BE PERFORMED  
  
IF THE PROGRAM WORKS UNDER ACT-11 MONITOR  
THEN THIS TEST IS NOT PERFORMED  
  
IF NO ACT-11 MONITOR IS PRESENT  
THEN THIS TEST IS PERFORMED ONLY ON THE FIRST PASS  
ON SUBSEQUENT PASSES THIS TEST IS NOT DONE  
  
WRITE FROM BUFFER IS FILLED WITH ALL ONES AND  
SECTOR 0, TRACK 0, CYLINDER 0 IS FILLED WITH  
ALL ONES  
ALL REGISTERS ARE SAVED THEN WRITE LOCK BUTTON IS  
PRESSED AND ALL REGISTERS ARE CHECKED.  
WRITE FROM BUFFER IS FILLED WITH 377 AND A WRITE IS  
ATTEMPTED TO SECTOR 0, TRACK 0, CYLINDER 0 70. WORDS  
ALL REGISTERS ARE CHECKED  
THE SAME SECTOR IS READ AND DATA COMPARED TO SEE  
THAT NOTHING GOT DESTROYED (READ DATA SHOULD BE ALL  
ONES AND NOT 377)  
THEN WRITE LOCK BUTTON IS PRESSED TO UNLOCK  
WRITE LOCKS AND ALL REGISTERS ARE COMPARED  
  
9813 THIS CODE CHECKS TO SEE IF MANUAL INTERVENTION TESTS ARE OK  
9834 FILL SECTOR 0, TRACK 0, CYL 0 WITH ONES  
FILL WRITE FROM BUFFER  
9842 FILL WRITE DATA COMMAND

9881 TIME IS NOT CRITICAL  
9890 SAVE REGISTERS FOR COMPARISON AFTER WRITE PROTECT  
9891 BUTTON HAS BEEN HIT  
9911 THE ONLY REGISTER THAT SHOULD CHANGE IS RHDS1 - BIT #11  
-WRL  
9921 COMPARE ALL REGISTERS BEFORE WRITE WAS LOCKED  
OUT WITH REGISTER VALUES AFTER WRITE WAS LOCKED OUT  
9941 NOW A WRITE WILL BE ATTEMPTED WITH WRITE LOCKED  
OUT BY BUTTON  
FILL WRITE FROM BUFFER WITH 377  
9958 TRY A ONE WORD WRITE  
9976 SAVE REGISTERS  
9998 TIME IS NOT CRITICAL  
10007 CHANGE SAVED REGISTERS TO EXPECTED VALUE  
10048 COMPARE REGISTERS BEFORE WRITE WAS ATTEMPTED  
WITH REGISTERS AFTER ATTEMPT  
10069 NOW A READ WILL BE DONE TO DETERMIN THAT  
READS CAN BE DONE WITH WRITE LOCKED OUT AND  
THAT NO DATA ON DISK GOT CHANGED, BUT FIRST CLEAR ERROR  
10080 FILL READ INTO BUFFER WITH 0  
10087 FILL WRITE FROM BUFFER WITH EXPECTED DATA FROM READ  
10094 FILL COMMAND  
10135 COMPARE READ DATA  
10155 SAVE REGISTERS FOR COMPARISON AFTER WRITE LOCK HAS BEEN  
UNLOCKED  
10179 THE ONLY BIT THAT SHOULD CHANGE IS WRL-BIT #11 IN RHDS1  
10188 COMPARE ALL REGISTERS BEFORE WRITE LOCK WAS UNLOCKED  
WITH REGISTERS AFTER WRITE WAS UNLOCKED  
10216 TEST 51 END OF DRIVE

13  
14



10218 THIS IS THE END OF TEST FOR ONE DRIVE  
 10220 IF THERE ARE MORE DRIVES, THEN THE PROGRAM  
 JUMPS TO TEST 4 FOR TESTING THE NEXT DRIVE  
 10223 END PASS IS REACHED ONLY AFTER ALL DRIVES ARE TESTED

10266

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10267

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 \*\*SUBROUTINES\*\*  
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10268

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10271

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 END OF PASS ROUTINE  
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10274 INCREMENT THE PASS NUMBER (\$PASS)  
 TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)  
 IF THERES A MONITOR GO TO IT  
 IF THERE ISN'T JUMP TO TST1

10312

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 JAM CURRENT CYLINDER ROUTINE  
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10314 THIS ROUTINE WILL CHANGE THE CURRENT CYLINDER REGISTER - 'RHCC'  
 BY GIVING A 'SEEK' COMMAND FOLLOWED BY AN INIT WHICH WILL LOAD  
 'RHCC' WITH THE DESIRED CYLINDER VALUE. THE ROUTINE THEN CHECKS  
 THAT THE LOADED VALUE IS CORRECT.

CALL IS:  
           JSR      RD, @#MAKECYL  
           XC                          ; DESIRED VALUE OF CURRENT CYLINDER

10363 THIS FILLS MEMORY WITH GIVEN DATA  
 USED CHIEFLY FOR HEADER INFORMATION

CALL IS  
           JSR      RD, @#FLHEAD      ; FILL HEADER  
           LOC                      ; LOCATION WHERE SAVED  
           XN                       ; NUMBER OF WORDS  
           XD1                      ; DATA REPEATED XN TIMES  
           XD2                      ; DATA REPEATED XN TIMES

10381 NOW FILL DATA

10392 THIS CLEARS ANY BLOCK OF MEMORY.  
FILLING IT WITH ANY DATA  
CALL IS

```

JSR   RO,@#CLAREA
F     ;FROM
N     ;NUMBER OF WORDS
D     ;DATA TO BE FILLED
    
```

R1 WILL HAVE STARTING ADDRESS OF BLOCK TO BE FILLED  
R2 WILL HAVE NUMBER OF WORDS  
R3 WILL HAVE DATA

10424 THIS IS A SUBROUTINE TO FILL SAVED REGISTER LOCATION  
WITH GIVEN VALUE  
CALL IS

```

JSR   RO,@#FILLRE
RHXX  ;REGISTER NAME
D     ;DATA
    
```

10445 THIS SUBROUTINE SETS UP FOR SEARCH  
CALL IS

```

JSR   RO,@#SRCH
C     ;CYLINDER
.BYTE S ;SECTOR
.BYTE T ;TRACK
    
```

10464 THIS SUBROUTINE SETS UP FOR SEEK COMMANDS  
CALL IS

```

JSR   RO,@#SEEKCY
C     ;CYLINDER
    
```

10474 THIS SUBROUTINE SETS UP FOR OFFSET COMMANDS  
CALL IS

```

JSR   RO,@#OFFSET
O     ;MICRO INCHES OFFSET
    
```

10497 THIS CHECKS THAT DEVICE AVAILABLE (DVA) AND READY (RDY) IN RHCS1 = 1  
AND CHECKS MEDIUM ON LINE (MOL). DEVICE PRESENT (DPR). DEVICE READY  
(DRY) IN RHDS1 = 1

10501 IT ALSO CHECKS THAT THERE ARE NO BITS STUCK AT 1 IN RHDS1

10541 THIS IS A SUBROUTINE TO SAVE REGISTERS  
IN THE REGISTER TABLE TO ANY LOCATION  
THE CALL IS

```

JSR   RO,@#SAVER
F     ;FROM
T     ;TO
N     ;NUMBER OF WORDS SAVED
F MUST ALWAYS BE RHCS1
    
```

T MUST ALWAYS BE SAVRE

10590 WHEN AN EVENT IS TO BE TIMED THE RPO4 VECTORS TO "TIME 1"  
PRIORITY OF PROCESS OR IS 4  
PRIORITY OF TRAPS MUST BE 6  
PRIORITY OF RPO4 INTERRUPTS IS 7

10594 THIS IS A WAIT LOOP WHEN AN EVENT IS TO BE TIMED  
THE CALL IS

WAT  
A :ABSOLUTE REGISTER ADDRESS  
B :BIT WAITED FOR  
TA :TIME ALLOWED GIVEN IN 10 MICROSEC  
TO :TOLERANCE PLUS/MINUS IN 10 MICROSEC

R1-WILL HAVE TIME ALLOWED IN 10 MICRO SECONDS  
R2-WILL HAVE TOLERANCE PLUS/MINUS IN 10 MICRO SECONDS  
MINIMUM TIME THAT CAN BE MEASURED IS ABOUT 12 MICRO SECONDS  
FOR THE SLOWEST PROCESSOR

10627 THIS SECTION WAITS FOR BIT, THROUGH TWO COUNT DOWNS

10649 NOW TIME AND TOLERANCE WILL BE CHECKED

10695 THIS IS A WAIT LOOP WHEN NO P-CLOCK IS AVAILABLE  
NO TIMING IS DONE  
CALL IS

WAT  
A :ABSOLUTE REGISTER ADDRESS  
B :BIT WAITE) FOR  
TA :TIME-NOT USED HERE  
TO :TIME-NOT USED HERE

R3-IS A TEMPORARY COUNTER

10707 THIS HAS THE TWO COUNT DOWNS FROM 177777

10730 BIT DID SET SO CHECK IF INTERRUPT OCCURED

10743 THIS CHANGES REGISTER SAVED VALUE  
CALL IS

JSR RD,3#CHREG  
R :REGISTER TO BE CHANGED  
N :NUMBER OF BITS TO BE CHANGED  
NEW :NEW VALUE OF BIT MUST BE 0 OR 1  
P :POSITION OF BIT TO BE CHANGED

NEW AND P WILL BE REPEATED N NUMBER OF TIMES

10773 THIS FILLS A BLOCK WITH INCREMENTAL DATA  
CALL IS

JSR RD,3#FILL  
F :FROM  
N :NUMBER OF WORDS  
S :STARTING VALUE OF DATA

*Handwritten marks*

I

; INCREMENT DATA BY

10790 NOW DATA WILL BE FILLED

10906 THIS IS A SUBROUTINE TO COMPARE REGISTERS  
 GOOD DATA IS ALREADY SAVED IN 'SAVERE'  
 TEST DATA IS IN THE REGISTERS  
 CALL IS

JSR RO, 2#COMREG

SAVERE

RHCSI

N.

RG

; GOOD DATA

; ADDRESS OF ADDRESS TEST DATA

; RETURN FOR ERROR

; RETURN FOR GOOD COMPARISON

ON RETURN WITH ERROR '\$GDDAT' HAS GOOD DATA, '\$BDDAT' HAS BAD DATA  
 'REGADR' HAS REGISTER ADDRESS

10829 NOW SAVE REGISTERS

10933 NOW COMPARES WILL MADE

10961 HERE IS A DETAILED EXPLANATION OF HOW THE LOOP ON ERROR WORKS.  
 ON HITTING AN ERROR IF THE LOOP ON ERROR SWITCH IS SET, THE  
 PROGRAM GOES BACK - USUALLY BACK TO THE BEGINNING OF THE TEST.

10965 WHEN THIS OPERATOR SELECTABLE SCOPE LOOP IS USED THEN THE POINT  
 THE PROGRAM GOES BACK TO CAN BE CHANGED.  
 THE RESTRICTIONS TO THE POINT WHERE THE PROGRAM CAN GO ARE: -  
 1. IT MUST BE WITHIN THE TEST UNDER CONSIDERATION  
 2. LOOP ON ERROR SWITCH MUST BE SET  
 3. THE ERROR MUST OCCUR WITHIN THE TEST UNDER CONSIDERATION  
 IF THE ERROR DOES NOT OCCUR WITHIN THE TEST UNDER CONSIDERATION  
 THE PROGRAM WILL REVERT TO NORMAL OPERATION. HOWEVER, IF LOOP ON  
 TEST SWITCH IS SET AND THIS OPERATOR SELECTABLE SCOPE LOOP IS USED  
 THEN THE PROGRAM WILL LOOP BACK TO THE SELECTED POINT WHEN IT  
 COMES TO THE END OF THE TEST UNDER CONSIDERATION.

AFTER LOOPING FOR SOME TIME IF THE LOOP SWITCH IS PUT DOWN THEN  
 NORMAL OPERATION WILL CONTINUE.

10928 THIS SAVES THE CONTENTS OF ALL HARDWARE REGISTERS  
 IN MEMORY LOCATIONS TAGED FROM "WC" TO "EC2"

10931 THIS IS DONE SO THAT COMPARES ARE DONE WITH SAVED LOCATIONS  
 AND NOT THE REGISTERS THEMSELVES. THIS WILL MAKE  
 ERROR PRINTOUTS FOR GOOD AND BAD DATA ALWAYS DIFFERENT

10955 THIS IS A DATA COMMAND SETUP SUBROUTINE  
 THE CALL IS

JSR RO, 2#RUN

.BYTE

C

; CYLINDER

.BYTE

S

; SECTOR

T

; TRACK

-W

; WORD COUNT

B

; BUS ADDRESS

```

BAI          ;BUS ADDRESS INHIBIT
FMT22!ECI!HCI ;FMT22=1 =16 BIT WORDS
              ;ECI = ECC CORRECTION INHIBIT
              ;HCI = HEADER COMPARE INHIBIT
COM          ;COMMAND ADDRESS

```

10983 THIS IS A SUBROUTINE TO COMPARE TWO BLOCKS IN MEMORY

```

10985 R1 HAS GOOD DATA BUFFER ADDRESS
      R2 HAS TEST DATA BUFFER ADDRESS
      R3 HAS ADDRESS OF RETURN ON ERROR
      R3 HAS NUMBER OF WORDS TO BE COMPARED
      R4 HAS ONE MORE THAN NUMBER OF WORDS TO BE COMPARED

```

```

10991 CALL IS:
          JSR      RD,3#COMPAR
          G
          T          ;ADDRESS OF GOOD DATA
          N          ;ADDRESS OF TEST DATA
          RE         ;NUMBER OF WORDS TO BE COMPARED
          RG         ;RETURN ON ERROR
              ;RETURN ON NO ERROR

```

11039 THIS ROUTINE WILL ALLOW THE CHANGE OF THE BASE ADDRESS FROM 176700 TO ANY TYPED VALUE

```

11114 *****
SCOPE HANDLER ROUTINE
*****

```

```

11117 THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
SW14=1 LOOP ON TEST
SW11=1 INHIBIT ITERATIONS
SW09=1 LOOP ON ERROR
SW08=1 LOOP ON TEST IN SWR<7:0>
CALL SCOPE          ;;SCOPE=IOT

```

11177 \*\*\*\*\*  
 CONVERT BINARY TO DECIMAL AND TYPE ROUTINE  
 \*\*\*\*\*

11193 THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT  
 SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE  
 NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED  
 BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE  
 REPLACED WITH SPACES.  
 CALL:  
       MOV      NUM,-(SP)      ::PUT THE BINARY NUMBER ON THE STACK  
       TYPDS                  ::GO TO THE ROUTINE

:1244 \*\*\*\*\*  
 TYPE ROUTINE  
 \*\*\*\*\*

11247 ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.  
 THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.  
 NOTE1: \$NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.  
 NOTE2: \$FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.  
 NOTE3: \$FILLC CONTAINS THE CHARACTER TO FILL AFTER.  
 CALL:  
 1) USING A TRAP INSTRUCTION  
       TYPE      ,MESADR      ::MESADR IS FIRST ADDRESS OF AN ASCIZ STRING  
 OR  
       TYPE  
       MESADR

:1314 \*\*\*\*\*  
 TTY INPUT ROUTINE  
 \*\*\*\*\*

11325 TK INITIALIZE ROUTINE  
 THIS ROUTINE WILL INITIALIZE THE TTY KEYBOARD INPUT QUEUE  
 SETUP THE INTERRUPT VECTOR AND TURN ON THE KEYBOARD INTERRUPT  
 CALL:  
       JSR      PC,\$TKINT  
       RETURN

11342 TK SERVICE ROUTINE  
 THIS ROUTINE WILL SERVICE THE TTY KEYBOARD INTERRUPT  
 BY READING THE CHARACTER FROM THE INPUT BUFFER AND PUTTING  
 IT IN THE QUEUE.  
 IF THE CHARACTER IS A "CONTROL-C" (^C) \$TKINT IS CALLED AND  
 UPON RETURN EXIT IS MADE TO THE "CONTROL-C" RESTART ADDRESS \$OPERSEL.





11644 :\*\*\*\*\*

11546

\*\*\*\*\*  
 ERROR MESSAGE TIMEOUT ROUTINE  
 \*\*\*\*\*

11648 THIS ROUTINE USES THE "ITEM CONTROL BYTE" (\$ITEMB) TO DETERMINE WHICH  
 ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" (\$ERRTB),  
 AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.  
 IT IS A COPY OF THE \$ERRTYP SUBROUTINE FROM SYSMAC.  
 WITH ONLY MINOR CHANGES  
 FIRST IF SWITCH 6 IS SET AND SWITCH 8 RESET THEN  
 ALL REGISTER CONTENTS WILL BE TYPED BEFORE REPORTING THE ERROR  
 SECOND IF THE CURRENT ERROR HAS THE SAME ITEM NUMBER  
 AS THE PREVIOUS ERROR THEN ONLY THE DATA WILL BE TYPED  
 AND NOT THE ERROR MESSAGE AND HEADER.

11877

\*\*\*\*\*  
 BINARY TO OCTAL (ASCII) AND TYPE  
 \*\*\*\*\*

11890 THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT  
 OCTAL (ASCII) NUMBER AND TYPE IT.  
 \$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE  
 CALL:

```

MOV     NUM, -(SP)      ;; NUMBER TO BE TYPED
TYPOS   ;; CALL FOR TIMEOUT
.BYTE  N                ;; N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
.BYTE  M                ;; M=1 OR 0
                        ;; 1=TYPE LEADING ZEROS
                        ;; 0=SUPPRESS LEADING ZEROS
    
```

\$TYPON----ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST  
 \$TYPOS OR \$TYPOC

CALL:  

```

MOV     NUM, -(SP)      ;; NUMBER TO BE TYPED
TYPON   ;; CALL FOR TIMEOUT
    
```

\$TYPOC---ENTER HERE FOR TIMEOUT OF A 16 BIT NUMBER  
 CALL:

```

MOV     NUM, -(SP)      ;; NUMBER TO BE TYPED
TYPOC   ;; CALL FOR TIMEOUT
    
```

11954 \*\*\*\*\*  
 TRAP DECODER  
 \*\*\*\*\*

11957 THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION  
 AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS  
 OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL  
 GO TO THAT ROUTINE.

11970 \*\*\*\*\*  
 TRAP TABLE  
 \*\*\*\*\*

11972 THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED  
 BY THE "TRAP" INSTRUCTION.

11992 \*\*\*\*\*  
 POWER DOWN AND UP ROUTINES  
 \*\*\*\*\*

12037 \*\*\*\*\*  
 ERROR AND MESSAGE TABLE CONDIMENTS  
 \*\*\*\*\*

\*\*\*\*\*  
 40 57 168 178 189 208 245 303 1322 1505 1536 1507 1513 1525 161  
 \*\*\*\*\*

41  
42  
43  
44  
61  
177  
182  
193  
202  
212  
249  
307  
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350  
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386  
395  
407  
419  
429  
439  
456  
470  
503  
508  
508  
545  
560  
566  
632  
667

\*\*ERROR TABLE, BIT DEFINITIONS & STARTING ADDRESSES\*\*

OPERATIONAL SWITCH SETTINGS  
BASIC DEFINITIONS  
TRAP CATCHER  
ACT11 HOOKS  
STARTING ADDRESSES  
MEMORY MANAGEMENT DEFINITIONS  
COMMON TAGS  
ERROR POINTER TABLE  
REGISTER ADDRESSES

\*\*DIAGNOSTIC CODE\*\*

SETUP TESTS

INITIALIZE THE COMMON TAGS  
GET VALUE FOR SOFTWARE SWITCH REGISTER  
T1 REFERENCE EACH REGISTER  
T2 PARTIAL TEST OF RHAS FOR UNIT NUMBERS PRESENT  
T3 TEST FOR DRIVES PRESENT USING RHAS AND RHCS2  
T4 TYPE SERIAL NUMBER AND DRIVE TYPE  
T5 CHECK MOL TO BE HIGH  
T6 PROGRAM INTERRUPT  
T7 INTERRUPT AT PROCESSOR AND DISK PRIORITY SAME  
T10 PACK ACKNOWLEDGE  
T11 SET VV BIT #6 IN RHDS1  
T12 MAKE CURRENT CYLINDER = 377  
T13 MAKE CURRENT CYLINDER = 777  
T14 ADDRESS PLUG CHANGE ERROR  
T15 CHECK ALL ADDRESS PLUG ADDRESSES

\*\*DRIVE COMMAND TESTS\*\*

T16 NO OPERATION FUNCTION TEST  
T17 DRIVE CLEAR  
T20 READ-IN-PRESET  
T21 READ-IN-PRESET  
T22 MAKE CURRENT CYLINDER = 777  
T23 MAKE CURRENT CYLINDER = 377  
T24 RECALIBRATE COMMAND  
T25 MAKE CURRENT CYLINDER = 777  
T26 MAKE CURRENT CYLINDER = 377  
T27 RECALIBRATE COMMAND  
T30 UNLOAD COMMAND  
T31 OFFSET AND RETURN TO CENTER LINE COMMAND  
T32 OFFSET COMMAND  
READ/WRITE TESTS USING MEDIA  
T33 WRITE/READ HEADER AND DATA (0'S)  
T34 READ DATA (0'S)  
T35 WRITE/READ DATA (1'S & 125252)  
T36 WRITE/READ DATA (125252)  
T37 WRITE/READ DATA (052525)  
T40 WRITE/READ DATA USING JNIBUS B

TABLE OF CONTENTS

7078	T41	IMPLIED SEARCH
7488	T42	IMPLIED SEEK TO CYL 001
7492		SEEK TESTS
7904	T43	SEEK & WRT TEST (CYL = 0-10)
8551	T44	SEEK & READ TEST (CYL = 009)
8810		WRITE CHECK DATA & WRITE PROTECT TESTS
8812	T45	WRITE CHECK HEADER AND DATA
9309	T46	WRITE CHECK DATA
9557	T47	WRITE CHECK DATA USING UNIBUS B
9827	T50	WRITE PROTECT OPERATION
10259	T51	END OF DRIVE
10311		**SUBROUTINES**
10312		END OF PASS ROUTINE
10313		JAM CURRENT CYLINDER ROUTINE
10316		SCOPE HANDLER ROUTINE
10357		CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
11159		TYPE ROUTINE
11223		TTY INPUT ROUTINE
11290		READ AN OCTAL NUMBER FROM THE TTY
11360		ERROR HANDLER ROUTINE
11589		ERROR MESSAGE TIMEOUT ROUTINE
11642		BINARY TO OCTAL (ASCII) AND TYPE
11693		TRAP DECODER
11924		TRAP TABLE
12001		POWER DOWN AND UP ROUTINES
12017		
12039		



L05

MAINDEC-11-DZRJI-A, RPO4/5/6 FUNCT. CONT. TST-PT 1  
DZRJIA.P11 OPERATIONAL SWITCH SETTINGS

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SEQ 0062

53  
54  
55  
56

:\* 7  
:\* 6  
:\* 5

STOP FURTHER COMPARES IF SW08 IS LOW  
TYPE ALL REG. WITH ERROR IF SW8 LOW  
MULT ADDR PLUG TEST IF SW08 IS LOW

```

57          .SBTTL BASIC DEFINITIONS
58
59          ;*INITIAL ADDRESS OF THE STACK POINTER *** 1000 ***
60          001000 STACK= 1000
61          .EQUIV EMT,ERROR      ;;BASIC DEFINITION OF ERROR CALL
62          .EQUIV IOT,SCOPE      ;;BASIC DEFINITION OF SCOPE CALL
63
64          ;*MISCELLANEOUS DEFINITIONS
65          000011 HT= 11          ;;CODE FOR HORIZONTAL TAB
66          000012 LF= 12          ;;CODE FOR LINE FEED
67          000015 CR= 15          ;;CODE FOR CARRIAGE RETURN
68          000200 CRLF= 200       ;;CODE FOR CARRIAGE RETURN-LINE FEED
69          177776 PS= 177776     ;;PROCESSOR STATUS WORD
70          .EQUIV PS,PSW
71          177774 STKLMT= 177774 ;;STACK LIMIT REGISTER
72          177772 PIRQ= 177772   ;;PROGRAM INTERRUPT REQUEST REGISTER
73          177570 DSWR= 177570   ;;HARDWARE SWITCH REGISTER
74          177570 DDISP= 177570  ;;HARDWARE DISPLAY REGISTER
75
76          ;*GENERAL PURPOSE REGISTER DEFINITIONS
77          000000 R0= %0         ;;GENERAL REGISTER
78          000001 R1= %1         ;;GENERAL REGISTER
79          000002 R2= %2         ;;GENERAL REGISTER
80          000003 R3= %3         ;;GENERAL REGISTER
81          000004 R4= %4         ;;GENERAL REGISTER
82          000005 R5= %5         ;;GENERAL REGISTER
83          000006 R6= %6         ;;GENERAL REGISTER
84          000007 R7= %7         ;;GENERAL REGISTER
85          .EQUIV R6,SP          ;;STACK POINTER
86          .EQUIV R7,PC          ;;PROGRAM COUNTER
87
88          ;*PRIORITY LEVEL DEFINITIONS
89          000000 PR0= 0          ;;PRIORITY LEVEL 0
90          000040 PR1= 40         ;;PRIORITY LEVEL 1
91          000100 PR2= 100        ;;PRIORITY LEVEL 2
92          000140 PR3= 140        ;;PRIORITY LEVEL 3
93          000200 PR4= 200        ;;PRIORITY LEVEL 4
94          000240 PR5= 240        ;;PRIORITY LEVEL 5
95          000300 PR6= 300        ;;PRIORITY LEVEL 6
96          000340 PR7= 340        ;;PRIORITY LEVEL 7
97
98          ;*"SWITCH REGISTER" SWITCH DEFINITIONS
99          100000 SW15= 100000
100         040000 SW14= 40000
101         020000 SW13= 20000
102         010000 SW12= 10000
103         004000 SW11= 4000
104         002000 SW10= 2000
105         001000 SW09= 1000
106         000400 SW08= 400
107         000200 SW07= 200
108         000100 SW06= 100
109         000040 SW05= 40
110         000020 SW04= 20
    
```



```

111      000010      SW03= 10
112      000004      SW02= 4
113      000002      SW01= 2
114      000001      SW00= 1
115      .EQUIV      SW09,SW9
116      .EQUIV      SW08,SW8
117      .EQUIV      SW07,SW7
118      .EQUIV      SW06,SW6
119      .EQUIV      SW05,SW5
120      .EQUIV      SW04,SW4
121      .EQUIV      SW03,SW3
122      .EQUIV      SW02,SW2
123      .EQUIV      SW01,SW1
124      .EQUIV      SW00,SW0

```

.\*DATA BIT DEFINITIONS (BIT00 TO BIT15)

```

126      100000      BIT15= 100000
127      040000      BIT14= 40000
128      020000      BIT13= 20000
129      010000      BIT12= 10000
130      004000      BIT11= 4000
131      002000      BIT10= 2000
132      001000      BIT09= 1000
133      000400      BIT08= 400
134      000200      BIT07= 200
135      000100      BIT06= 100
136      000040      BIT05= 40
137      000020      BIT04= 20
138      000010      BIT03= 10
139      000004      BIT02= 4
140      000002      BIT01= 2
141      000001      BIT00= 1
142      .EQUIV      BIT09,BIT9
143      .EQUIV      BIT08,BIT8
144      .EQUIV      BIT07,BIT7
145      .EQUIV      BIT06,BIT6
146      .EQUIV      BIT05,BIT5
147      .EQUIV      BIT04,BIT4
148      .EQUIV      BIT03,BIT3
149      .EQUIV      BIT02,BIT2
150      .EQUIV      BIT01,BIT1
151      .EQUIV      BIT00,BIT0

```

.\*BASIC "CPU" TRAP VECTOR ADDRESSES

```

154      000004      ERRVEC= 4      ;; TIME OUT AND OTHER ERRORS
155      000010      RESVEC= 10     ;; RESERVED AND ILLEGAL INSTRUCTIONS
156      000014      TBITVEC=14    ;; "T" BIT
157      000014      TRTVEC= 14     ;; TRACE TRAP
158      000014      BPTVEC= 14     ;; BREAKPOINT TRAP (BPT)
159      000020      IOTVEC= 20     ;; INPUT/OUTPUT TRAP (IOT) **SCOPE**
160      000024      PWRVEC= 24     ;; POWER FAIL
161      000030      EMTVEC= 30     ;; EMULATOR TRAP (EMT) **ERROR**
162      000034      TRAPVEC=34    ;; "TRAP" TRAP
163      000060      TKVEC= 60     ;; TTY KEYBOARD VECTOR
164

```

000000  
000000  
000000

000064  
000240

TFVEC= 64  
PIRQVEC=240

::TTY PRINTER VECTOR  
::PROGRAM INTERRUPT REQUEST VECTOR

5  
23

000000  
000001  
000002  
000003  
000004  
000005  
000006  
000007  
000008  
000009  
000010  
000011  
000012  
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000088  
000089  
000090  
000091  
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000094  
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000096  
000097  
000098  
000099

.SBTTL TRAP CATCHER

000000

      .=0  
: \*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A " +2.HALT"  
: \*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS  
: \*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

000174 000174  
000175 000000  
000176 000000

DISPREG: .WORD 0           ::SOFTWARE DISPLAY REGISTER  
SWREG: .WORD 0            ::SOFTWARE SWITCH REGISTER

.SBTTL ACT11 HOOKS

:\*\*\*\*\*  
:HOOKS REQUIRED BY ACT11

000200 000200  
000046 000046  
000052 000052  
000200 000200

      \$SVPC=               :SAVE PC  
      .=46                :1)SET LOC.46 TO ADDRESS OF SENDAD IN .SECP  
      SENDAD  
      .=52                :2)SET LOC.52 TO 2000  
      .WORD 2000           ::RESTORE PC  
      .= \$SVPC

.SBTTL STARTING ADDRESSES

000200 000200 005012  
000210 000210 004776  
000220 000220 004762

      .=200  
      JMP 2#BEGIN        :NORMAL START  
      .=210  
      JMP 2#BEGIN2       :JUMP TO SELECT DRIVE START  
      .=220  
      JMP 2#BEGIN1       :JUMP TO NO OPERATOR TESTS START

: \*STARTING ADDRESS 200 FOR NORMAL STARTS  
: \*THIS WILL TEST ALL DRIVES ON THE SYSTEM A SINGLE DRIVE AT A TIME  
: \*  
: \*STARTING ADDRESS 210 WILL TEST ONLY ONE SPECIFIED DRIVE  
: \*  
: \*STARTING ADDRESS 220 WILL JUMP OVER THE TESTS REQUIRING AN OPERATOR  
: \*AT THE DRIVE

000250  
177572  
177574  
177576  
172516  
172300  
172302  
172304  
172306  
172310  
172312  
172314  
172316  
172340  
172342  
172344  
172346  
172350  
172352  
172354  
172356  
001110

.SBTTL MEMORY MANAGEMENT DEFINITIONS

:\*KT11 VECTOR ADDRESS

MMVEC= 250

:\*KT11 STATUS REGISTER ADDRESSES

SRC= 177572

SR1= 177574

SR2= 177576

SR3= 172516

:\*KERNEL "I" PAGE DESCRIPTOR REGISTERS

KIPDR0= 172300

KIPDR1= 172302

KIPDR2= 172304

KIPDR3= 172306

KIPDR4= 172310

KIPDR5= 172312

KIPDR6= 172314

KIPDR7= 172316

:\*KERNEL "I" PAGE ADDRESS REGISTERS

KIPAR0= 172340

KIPAR1= 172342

KIPAR2= 172344

KIPAR3= 172346

KIPAR4= 172350

KIPAR5= 172352

KIPAR6= 172354

KIPAR7= 172356

::\*\*\*\*\*  
.=1110

.SBTTL COMMON TAGS

::\*\*\*\*\*  
:\*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS  
:\*USED IN THE PROGRAM.

001100  
001100  
001102  
001103  
001104  
001106  
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001216

000377

. =1100  
\$CMTAG: .WORD 0  
\$PASS: .WORD 0  
\$TSTNM: .BYTE 00  
\$ERFLG: .BYTE 00  
\$ICNT: .WORD 00  
\$LPADR: .WORD 00  
\$LPERR: .WORD 00  
\$ERTTL: .WORD 00  
\$ITEMB: .BYTE 0  
\$ERMAX: .BYTE 1  
\$ERRPC: .WORD 00  
\$GDADR: .WORD 00  
\$BDADR: .WORD 00  
\$GDADR: .WORD 00  
\$BDADR: .WORD 00  
\$AUTOB: .BYTE 0  
\$INTAG: .BYTE 0  
\$SWR: .WORD DSWR  
\$DISPLAY: .WORD DDISP  
\$TKS: 177560  
\$TKB: 177562  
\$TPS: 177564  
\$TPB: 177566  
\$NULL: .BYTE 0  
\$FILLS: .BYTE 2  
\$FILLC: .BYTE 12  
\$STPFLG: .BYTE 0  
\$REGAD: .WORD 0  
  
\$REG0: .WORD 0  
\$REG1: .WORD 00  
\$REG2: .WORD 00  
\$REG3: .WORD 00  
\$REG4: .WORD 00  
\$REG5: .WORD 00  
\$TMP0: .WORD 00  
\$TMP1: .WORD 00  
\$TMP2: .WORD 00  
\$TMP3: .WORD 00  
\$TMP4: .WORD 00  
\$TMP5: .WORD 0  
\$TIMES: 0  
\$ESCAPE: 0  
\$BELL: .ASCIIZ (207 377 377)

:: START OF COMMON TAGS  
:: CONTAINS PASS COUNT  
:: CONTAINS THE TEST NUMBER  
:: CONTAINS ERROR FLAG  
:: CONTAINS SUBTEST ITERATION COUNT  
:: CONTAINS SCOPE LOOP ADDRESS  
:: CONTAINS SCOPE RETURN FOR ERRORS  
:: CONTAINS TOTAL ERRORS DETECTED  
:: CONTAINS ITEM CONTROL BYTE  
:: CONTAINS MAX. ERRORS PER TEST  
:: CONTAINS PC OF LAST ERROR INSTRUCTION  
:: CONTAINS ADDRESS OF 'GOOD' DATA  
:: CONTAINS ADDRESS OF 'BAD' DATA  
:: CONTAINS 'GOOD' DATA  
:: CONTAINS 'BAD' DATA  
:: RESERVED--NOT TO BE USED  
  
:: AUTOMATIC MODE INDICATOR  
:: INTERRUPT MODE INDICATOR  
  
:: ADDRESS OF SWITCH REGISTER  
:: ADDRESS OF DISPLAY REGISTER  
:: TTY KBD STATUS  
:: TTY KBD BUFFER  
:: TTY PRINTER STATUS REG. ADDRESS  
:: TTY PRINTER BUFFER REG. ADDRESS  
:: CONTAINS NULL CHARACTER FOR FILLS  
:: CONTAINS # OF FILLER CHARACTERS REQUIRED  
:: INSERT FILL CHARS. AFTER A "LINE FEED"  
:: "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)  
:: CONTAINS THE ADDRESS FROM  
:: WHICH (\$REG0) WAS OBTAINED  
:: CONTAINS ((\$REGAD)+0)  
:: CONTAINS ((\$REGAD)+2)  
:: CONTAINS ((\$REGAD)+4)  
:: CONTAINS ((\$REGAD)+6)  
:: CONTAINS ((\$REGAD)+10)  
:: CONTAINS ((\$REGAD)+12)  
:: USER DEFINED  
:: USER DEFINED  
:: USER DEFINED  
:: USER DEFINED  
:: USER DEFINED  
:: USER DEFINED  
:: USER DEFINED  
:: MAX. NUMBER OF ITERATIONS  
:: ESCAPE ON ERROR ADDRESS  
:: CODE FOR BELL



.SBTTL ERROR POINTER TABLE

: \*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.  
: \*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN  
: \*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.  
: \*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).  
: \*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

: \* EM ::POINTS TO THE ERROR MESSAGE  
: \* DH ::POINTS TO THE DATA HEADER  
: \* DT ::POINTS TO THE DATA  
: \* DF ::POINTS TO THE DATA FORMAT

001226

\$ERRTB:

: \*ITEM1

001226 050570

EM1

: RPO4 DID NOT INTERRUPT  
: WAITED ON BIT DID NOT OCCUR  
: PC

001230 066642

DH1

: WAT PC  
: BIT WAITED  
: REG ADDRESS  
: REG CONTENTS  
: RHCSI CONTENTS  
: \$ERRPC, WAITPC, WAITBT, WAITRE, \$BDDAT, CSI  
: 0.0,0.0,0.0

001232 071170

DT1

001234 071476

DF1

: \*ITEM2

001236 050617

EM2

: INTERRUPT ENABLE BIT DOWN BUT  
: WAITED ON BIT DID NOT OCCUR  
: PC

001240 066642

DH1

: WAT PC  
: BIT WAITED  
: REG ADDRESS  
: REG CONTENTS  
: RHCSI CONTENTS  
: \$ERRPC, WAITPC, WAITBT, WAITRE, \$BDDAT, CSI  
: 0.0,0.0,0.0

001242 071170

DT1

001244 071476

DF1

: \*ITEM3

001246 050706

EM3

: RPO4 DID NOT INTERRUPT WHEN  
: WAITED ON BIT DID SET

001250 066642

DH1

: PC  
: WAT PC  
: BIT WAITED  
: REG ADDRESS  
: RHCSI CONTENTS  
: \$ERRPC, WAITPC, WAITBT, WAITRE, \$BDDAT, CSI  
: 0.0,0.0,0.0

001252 071170

DT1

001254 071476

DF1

357					
358			:*ITEM4		
359	001256	050767	EM4		:WAITED ON BIT DID SET BUT
360					:TIME IS IN ERROR
361					:TIME IS GIVEN IN 10 MICRO SEC.
362					: (DECIMAL)
363	001260	067021	DH4		:PC
364					:WAT PC
365					:BIT WAITED
366					:REG ADDRESS
367					:TIME IN 10 MSEC
368	001262	071210	DT4		:\$ERRPC, WAITPC, WAITBT, WAITRE, SBDDAT, WAITIM
369	001264	071505	DF4		:0,0,0,0,0,1
370					
371			:*ITEM5		
372	001266	051100	EM5		:RHAS DOES NOT CLEAR BY
373					:MOVING IN ALL ONES
374	001270	067162	DH5		:PC
375					:REG. ADDR.
376					:GOOD DATA
377					:RECEIVED DATA
378	001272	071230	DT5		:\$ERRPC, REGADR, \$GDDAT, \$BCDAT
379	001274	071514	DF5		:0,0,0,0
380					
381			:*ITEM6		
382	001276	051152	EM6		:LOADING RHER1 FOR ALL
383					:UNITS DID NOT SET ANY BITS
384					:IN RHAS-NO UNITS PRESENT
385	001300	067301	DH6		:PC
386					:REG ADDR
387					:RECEIVED DATA
388	001302	071244	DT6		:\$ERRPC, REGADR, \$BCDAT
389	001304	071521	DF6		:0,0,0
390					
391			:*ITEM7		
392	001306	051240	EM7		:SPECIFIED REGISTER NONEXISTANT
393					:SO ABORT PROGRAM
394	001310	067400	DH7		:PC
395					:ADDR. OF REG.
396	001312	071256	DT7		:\$ERRPC, TEMP1
397	001314	071525	DF7		:0,0
398					
399			:*ITEM10		
400	001316	051322	EM10		:STOPED DRIVE HAS MOL BIT
401					:IN RHDS1 = 1
402	001320	067440	DH10		:PC
403					:TEST NO
404					:FAILING REG ADDR
405					:CONTENTS OF RHCS1
406					:CONTENTS OF RHCS2
407					:CONTENTS OF RHDS1
408					:CONTENTS OF RHER1
409	001322	071266	DT10		:\$ERRPC, \$STNM, \$BDADR, CS1, CS2, DS1, ER1
410	001324	071530	DF10		:0,0,0,0,0,0,0



411					
412			:*ITEM11		
413	001326	051371	EM11		: WITH SPINDLE POWERED DOWN
414					: RHCS2 SHOULD HAVE ONLY
415					: UNIT NUMBER AND IR HIGH
416	001330	067440	DH10		: PC
417					: TEST NO
418					: FAILING REG. ADR
419					: CONTENTS OF RHCS1
420					: CONTENTS OF RHCS2
421					: CONTENTS OF RHDS1
422					: CONTENTS OF RHER1
423	001332	071266	DT10		: \$ERRPC, \$STSTNM, \$BDADR, CS1, CS2, DS1, ER1
424	001334	071530	DF10		: 0,0,0,0,0,0,0
425					
426			:*ITEM12		
427	001336	051476	EM12		: AFTER A POWER UP WITH
428					: NO PACK ACKNOWLEDGE COMMAND
429					: RHDS1 SHOULD HAVE MOL=1, VV=0
430	001340	067440	DH10		: PC
431					: TEST NO
432					: FAILING REGISTER ADDR.
433					: CONTENTS OF RHCS1
434					: CONTENTS OF RHCS2
435					: CONTENTS OF RHDS1
436					: CONTENTS OF RHER1
437	001342	071266	DT10		: \$ERRPC, \$STSTNM, \$BDADR, CS1, CS2, DS1, ER1
438	001344	071530	DF10		: 0,0,0,0,0,0,0
439					
440			:*ITEM13		
441	001346	051604	EM13		: AFTER A POWER UP WITHOUT
442					: ANY INIT RHCS1 SHOULD
443					: HAVE GO=0, DVA=1, RDY=1
444					: IE=0, DISREGARD
445					: ALL OTHER BITS
446	001350	067440	DH10		: PC
447					: TEST NO
448					: FAILING REGISTER ADDR.
449					: CONTENTS OF RHCS1
450					: CONTENTS OF RHCS2
451					: CONTENTS OF RHDS1
452					: CONTENTS OF RHER1
453	001352	071266	DT10		: \$ERRPC, \$STSTNM, \$BDADR, CS1, CS2, DS1, ER1
454	001354	071530	DF10		: 0,0,0,0,0,0,0
455					
456			:*ITEM14		
457	001356	051726	EM14		: AFTER POWER UP RHCC
458					: SHOULD BE=0
459	001360	067162	DHS		: PC
460					: REG. ADDR.
461					: GOOD DATA
462					: RECEIVED DATA
463	001362	071230	DT5		: \$ERRPC, REGADR, \$GDDAT, \$BDDAT
464	001364	071514	DFS		: 0,0,0,0

465					
466			;*ITEM15		
467	001366	052001	EM15		;PACK ACKNOWLEDGE CAUSED
468					;AN ERROR
469					;GOOD DATA IS BEFORE COMMAND
470					;RECEIVED DATA IS AFTER COMMAND
471	001370	067162	DH5		;PC
472					;REG. ADDR.
473					;GOOD DATA
474					;RECEIVED DATA
475	001372	071230	DT5		;SERRPC,REGADR,\$GDDAT,\$BDDAT
476	001374	071514	DF5		;0,0,0,0
477					
478			;*ITEM16		
479	001376	052142	EM16		;GIVING A NO-OP COMMAND CAUSED
480					;AN ERROR
481					;GOOD DATA GIVES REGISTER
482					;CONTENTS BEFORE COMMAND
483					;RECEIVED DATA GIVES REGISTER
484					;CONTENTS AFTER COMMAND
485	001400	067162	DH5		;PC
486					;REG. ADDR.
487					;GOOD DATA
488					;RECEIVED DATA
489	001402	071230	DT5		;SERRPC,REGADR,\$GDDAT,\$BDDAT
490	001404	071514	DF5		;0,0,0,0
491					
492			;*ITEM17		
493	001406	052270	EM17		;DRIVE CLEAR COMMAND
494					;CAUSED AN ERROR
495					;GOOD DATA GIVES WHAT SHOULD
496					;BE THERE
497					;RECEIVED DATA GIVES WHAT WAS
498					;THERE AFTER COMMAND
499	001410	067162	DH5		;PC
500					;REG. ADDR.
501					;GOOD DATA
502					;RECEIVED DATA
503	001412	071230	DT5		;SERRPC,REGADR,\$GDDAT,\$BDDAT
504	001414	071514	DF5		;0,0,0,0
505					
506			;*ITEM20		
507	001416	052425	EM20		;READ-IN COMMAND GAVE AN ERROR
508					;GOOD DATA HAS WHAT SHOULD BE THERE
509					;RECEIVED DATA HAS WHAT WAS
510					;AFTER COMMAND
511	001420	067162	DH5		;PC
512					;REG. ADDR.
513					;GOOD DATA
514					;RECEIVED DATA
515	001422	071230	DT5		;SERRPC,REGADR,\$GDDAT,\$BDDAT
516	001424	071514	DF5		;0,0,0,0
517					
518					

Handwritten signature or initials.

519			:*ITEM 21		
520	001426	052571	EM21		;RHCS1 CONTENTS DURING
521					;COMMAND WAS IN ERROR
522	001430	067162	DH5		
523	001432	071230	DT5		
524	001434	071514	DF5		
525					
526			:*ITEM 22		
527	001436	052644	EM22		;RHDS1 CONTENTS DURING
528					;COMM ANS WAS IN ERROR
529	001440	067162	DH5		
530	001442	071230	DT5		
531	001444	071514	DF5		
532					
533			:*ITEM 23		
534	001446	052717	EM23		;UNLOAD COMMAND GAVE AN ERROR
535					;GOOD DATA GIVES WHAT SHOULD
536					;BE THERE
537					;RECEIVED DATA GIVES WHAT WAS
538					;THERE AFTER COMMAND
539	001450	067162	DH5		
540	001452	071230	DT5		
541	001454	071514	DF5		
542					
543			:*ITEM 24		
544	001456	053062	EM24		;OFFSET COMMAND CAUSED AN ERROR
545					;GOOD DATA IS WHAT SHOULD BE THERE
546					;RECEIVED DATA GIVES WHAT WAS THERE
547					;AFTER AN OFFSET COMMAND
548	001460	067162	DH5		
549	001462	071230	DT5		
550	001464	071514	DF5		
551					
552			:*ITEM 25		
553	001466	053225	EM25		;RETURN TO CENTER LINE COMMAND
554					;CAUSED AN ERROR
555					;GOOD DATA GIVES WHAT SHOULD BE
556					;THERE
557					;RECEIVED DATA GIVES WHAT WAS
558					;THERE AFTER COMMAND
559	001470	067162	DH5		
560	001472	071230	DT5		
561	001474	071514	DF5		
562					
563			:*ITEM 26		
564	001476	053407	EM26		;500 OFFSETS CAUSED AN ERROR
565	001500	067617	DH26		;PC
566					;CONT. OF RHCS1
567					;CONT. OF RHCS2
568					;CONT. OF RHDS1
569					;CONT. OF RHER1
570					;CONT. OF RHER2
571					;CONT. OF RHER3
572	001502	071306	DT26		;\$ERRPC,CS1,CS2,DS1,ER1,ER2,ER3

573	001504	071537	DF26		;0,0,0,0,0,0,0
574					
575				; *ITEM 27	
576	001506	053477	EM27		;WRITE HEADER AND DATA
577					;CAUSED IMPROPER REGISTER CHANGE
578					;GOOD DATA GIVES WHAT
579					;SHOULD BE THERE
580					;RECEIVED DATA GIVES WHAT
581					;WAS THERE AFTER COMMAND
582	001510	067162	DH5		
583	001512	071230	DT5		
584	001514	071514	DF5		
585					
586				; *ITEM 30	
587	001516	053715	EM30		;WRITE HEADER AND DATA
588					;CHANGED WRITE FROM BUFFER
589	001520	070016	DH30		;PC
590					;WORD NO
591					;GOOD DATA
592					;BAD DATA
593	001522	071330	DT30		;\$ERRPC, ERWORD, \$GDDAT, \$BDDAT
594	001524	071547	DF30		;0,0,0,0
595					
596				; *ITEM 31	
597	001526	053775	EM31		;READ HEADER AND DATA CAUSED
598					;IMPROPER REGISTER CHANGE
599					;GOOD DATA HAS WHAT SHOULD
600					;BE THERE
601					;RECEIVED DATA GIVES WHAT
602					;WAS THERE AFTER COMMAND
603	001530	067162	DH5		
604	001532	071230	DT5		
605	001534	071514	DF5		
606					
607				; *ITEM 32	
608	001536	054212	EM32		;WRITE HEADER AND DATA FOLLOWED
609					;BY A READ HEADER AND DATA
610					;CAUSED A READ/WRITE ERROR
611	001540	070016	DH30		
612	001542	071330	DT30		
613	001544	071547	DF30		
614					
615				; *ITEM 33	
616	001546	054321	EM33		;READ DATA CAUSED IMPROPER REGISTER
617					;CHANGE
618					;GOOD DATA GIVES WHAT SHOULD BE THERE
619					;RECEIVED DATA GIVES WHAT WAS THERE AFTER
620					;COMMAND
621	001550	067162	DH5		
622	001552	071230	DT5		
623	001554	071514	DF5		
624					
625				; *ITEM 34	
626	001556	054523	EM34		;READ DATA INCORRECT

627	001560	070016	DH30	
628	001562	071330	DT30	
629	001564	071547	DF30	
630				
631				:*ITEM 35
632	001566	054547	EM35	:WRITE DATA COMMAND CAUSED
633				:IMPROPER REGISTER CHANGE
634				:GOOD DATA GIVES WHAT SHOULD BE THERE
635				:RECEIVED DATA GIVES REGISTER
636				:CONTENTS AFTER WRITE DATA
637	001570	067162	DH5	
638	001572	071230	DT5	
639	001574	071514	DF5	
640				
641				:*ITEM 36
642	001576	054765	EM36	:WRITE DATA COMMAND CHANGED
643				:WRITE FROM BUFFER
644	001600	070016	DH30	
645	001602	071330	DT30	
646	001604	071547	DF30	
647				
648				:*ITEM 37
649	001606	055042	EM37	:SEEK COMMAND CAUSED AN
650				:ERROR
651				:GOOD DATA GIVES WHAT SHOULD
652				:BE THERE
653				:RECEIVED DATA GIVES WHAT
654				:WAS THERE AFTER SEEK COMMAND
655	001610	067162	DH5	
656	001612	071230	DT5	
657	001614	071514	DF5	
658				
659				:*ITEM 40
660	001616	055257	EM40	:WRITE CHECK CAUSED AN
661				:IMPROPER REGISTER CHANGE
662				:GOOD DATA GIVES WHAT SHOULD
663				:BE THERE
664				:RECEIVED DATA GIVES WHAT WAS
665				:THERE AFTER COMMAND
666	001620	067162	DH5	
667	001622	071230	DT5	
668	001624	071514	DF5	
669				
670				:*ITEM 41
671	001626	055466	EM41	:LOCKING OUT WRITES BY WRITE
672				:LOCK BUTTON CAUSED IMPROPER
673				:REGISTER CHANGE
674				:GOOD DATA GIVES WHAT SHOULD
675				:BE THERE
676				:RECEIVED DATA GIVES WHAT
677				:WAS THERE AFTER WRITES
678				:WERE LOCKED OUT BY
679				:BUTTON
680	001630	067162	DH5	

681	001632	071230		DT5	
682	001634	071514		DF5	
683					
684			;*ITEM 42		
685	001636	055747		EM42	
686					; ATTEMPTING TO WRITE WITH WRITE
687					; LOCKED OUT CAUSED IMPROPER
688					; REGISTER CHANGE
689					; GOOD DATA GIVES WHAT SHOULD
690					; BE THERE
691					; RECEIVED DATA GIVES WHAT WAS
692	001640	067162		DH5	; THERE AFTER ATTEMPT
693	001642	071230		DT5	
694	001644	071514		DF5	
695					
696			;*ITEM 43		
697	001646	056225		EM43	
698					; WRITING WITH WRITE LOCKED
699					; OUT CHANGED DISK DATA
700					; GOOD DATA GIVES WHAT WAS
701					; ON DISK BEFORE WRITE WITH
702					; WRITE LOCK WAS ATTEMPTED
703					; RECEIVED DATA GIVES WHAT WAS
704					; READ BACK AFTER WRITE WITH
705	001650	070016		DH30	; WRITE LOCK WAS ATTEMPTED
706	001652	071330		DT30	
707	001654	071547		DF30	
708					
709			;*ITEM 44		
710	001656	056567		EM44	
711					; ENABLING WRITES BY WRITE LOCK
712					; BUTTON CAUSED AN ERROR
713					; GOOD DATA GIVES WHAT SHOULD
714					; BE THERE
715					; RECEIVED DATA GIVES WHAT WAS
716					; THERE AFTER WRITE LOCK
717	001660	067162		DH5	; BUTTON ENABLED WRITES
718	001662	071230		DT5	
719	001664	071514		DF5	
720					
721			;*ITEM 45		
722	001666	057061		EM45	
723					; TRANSFERRING ON LAST BLOCK IE. CYLINDER
724					; 410. SECTOR 21, TRACK 18
725					; CAUSED IMPROPER REGISTER
726					; CHANGE
727					; GOOD DATA GIVES WHAT SHOULD
728					; BE THERE
729					; RECEIVED DATA GIVES WHAT WAS
730	001670	067162		DH5	; THERE AFTER TRANSFER
731	001672	071230		DT5	
732	001674	071514		DF5	
733					
734			;*ITEM 46		



001740  
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001774  
001776  
002000  
002002  
002004  
002006

067162  
071330  
071514  
060713  
070016  
071330  
071547  
061001  
067162  
071330  
071514  
061246  
067162  
071330  
071514  
061534  
070016  
071330  
071547  
061744

DH5  
DT5  
DF5  
:\*ITEM 53  
EM53  
DH30  
DT30  
DF30  
:\*ITEM 54  
EM54  
DH5  
DT5  
DF5  
:\*ITEM 55  
EM55  
DH5  
DT5  
DF5  
:\*ITEM 56  
EM56  
DH30  
DT30  
DF30  
:\*ITEM 57  
EM57

:RECEIVED DATA GIVES WHAT  
:WAS THERE AFTER ATTEMPTED  
:WRITE DATA  
:  
:READ HEADER AND DATA AFTER  
:A SEARCH CAUSED AN ERROR  
:  
:ATTEMPTED OPERATION WITH  
:INVALID ADDRESS CAUSED  
:IMPROPER REGISTER CHANGE  
:GOOD DATA GIVES WHAT SHOULD  
:BE THERE  
:RECEIVED DATA GIVES WHAT WAS  
:THERE AFTER OPERATION  
:  
:WRITING/READING WITH EXPECTED  
:ADDRESS OVERFLOW ERROR CAUSED  
:IMPROPER REGISTER CHANGE  
:GOOD DATA GIVES WHAT SHOULD  
:BE THERE  
:RECEIVED DATA GIVES WHAT  
:WAS THERE AFTER OPERATION  
:  
:DATA READ WITH AN EXPECTED  
:ADDRESS OVERFLOW ERROR IS  
:INCORRECT  
:WORD NO 1 TO 260 SHOULD  
:BE READ  
:WORD NOS 261 TO 266 SHOULD  
:NOT CHANGE DUE TO READ  
:  
:ATTEMPTING DATA COMMAND  
:WITH WRONG FORMAT BIT CAUSED  
:IMPROPER REGISTER CHANGE





997	002056	063065	EM64		: RECALIBRATE COMMAND CAUSED
998					: IMPROPER REGISTER CHANGE.
999					: GOOD DATA GIVES WHAT SHOULD BE
900					: THERE.
901					: RECEIVED DATA GIVES WHAT WAS THERE
902					: AFTER COMMAND
903	002060	067162	DH5		
904	002062	071230	DT5		
905	002064	071514	DF5		
906					
907				: *ITEM65	
908					
910	002066	063304	EM65		: INTERRUPT FAILING
911	002070	070614	DH65		: PC
912					: TEST NO
913					: CONTENTS OF RHCS1
914					: CONTENTS OF RHAS
915					: CONTENTS OF RHDS1
916	002072	071424	DT65		: \$ERRPC, TSTNM, CS1, AS, DS1
917	002074	071600	DF65		: 0,0,0,0,0
918					
919					
920				: *ITEM66	
921	002076	063326	EM66		: HEADER AND DATA COMMAND
922					: FOR HEAD SELECTION TEST
923					: CAUSED AN ERROR
924					: RHDS1 GIVES WHAT TRACK
925					: WAS BEING WRITTEN ON CYLINDER 0
926					: SECTOR 0
927	002100	070737	DH66		: PC
928					: RHDS1
929					: RHER1
930					: RHER2
931					: RHER3
932					: RHCS1
933					: RHCS2
934	002102	071440	DT66		: \$ERRPC, DST, ER1, ER2, ER3, CS1, CS2
935	002104	071605	DF66		: 0,0,0,0,0,0,0
936				: *ITEM67	
937	002106	063520	EM67		: READ HEADER AND DATA ERROR
938					: IN HEAD SELECTION TEST
939					: FIRST FOUR WORDS GIVE HEADER
940					: NEXT WORDS ARE DATA
941					: GOOD DATA WORDS GIVE
942					: THE TRACK NUMBER IN
943					: BITS 4,5,6,7,8
944	002110	070016	DH30		
945	002112	071330	DT30		
946	002114	071547	DF30		
947				: *ITEM70	
948	002116	064010	EM70		: READ HEADER AND DATA ERROR
949					: IN DIFFERENCE LINE TEST
950					: WORD NOS. 1-4 GIVE

951				:HEADER
952				:WORD NOS. 5-260 GIVE DATA
953				:WHICH IS THE CYLINDER
954				:ADDRESS
955	002120	070016	DH30	
956	002122	071330	DT30	
957	002124	071547	DF30	
958				
959				
960	002126	064216	EM71	:*ITEM 71
961				:FORCING OPI CAUSED IMPROPER REGISTER
962				:CHANGE
963				:GOOD DATA GIVES WHAT SHOULD
964				:BE THERE
965				:RECEIVED DATA GIVES WHAT WAS
966				:THERE AFTER 3 INDEX PULSES
967	002130	067162	DHS	:PC
968				:REG. ADDR.
969				:GOOD DATA
970	002132	071230	DT5	:RECEIVED DATA
971	002134	071514	DFS	:SERRPC, REGADR, \$GDDAT, \$BDDAT
972				:0.0,0.0
973	002136	064761	EM74	:*ITEM 74
974				:WHILE USING UNIBUS B
975				:READ DATA CAUSED IMPROPER REGISTER
976				:CHANGE
977				:GOOD DATA GIVES WHAT SHOULD BE THERE
978				:RECEIVED DATA GIVES WHAT WAS THERE AFTER
979	002140	067162	DHS	:COMMAND
980	002142	071230	DT5	
981	002144	071514	DFS	
982				
983				
984	002146	064707	EM73	:*ITEM 73
985				:WHILE USING UNIBUS B
986				:READ DATA INCORRECT
987	002150	070016	DH30	
988	002152	071330	DT30	
989	002154	071547	DF30	
990				
991	002156	064761	EM74	:*ITEM 74
992				:WHILE USING UNIBUS B
993				:WRITE DATA COMMAND CAUSED
994				:IMPROPER REGISTER CHANGE
995				:GOOD DATA GIVES WHAT SHOULD BE THERE
996				:RECEIVED DATA GIVES REGISTER
997	002160	067162	DHS	:CONTENTS AFTER WRITE DATA
998	002162	071230	DT5	
999	002164	071514	DFS	
1000				
1001				
1002	002166	065225	EM75	:*ITEM 75
1003				:WHILE USING UNIBUS B
1004				:WRITE DATA COMMAND CHANGED
				:WRITE FROM BUFFER

1005	002170	070016	DH30		
1006	002172	071330	DT30		
1007	002174	071547	DF30		
1008					
1009					
1010	002176	065330		:*ITEM 76	
1011			EM76		:WHILE USING UNIBUS B
1012					:WRITE CHECK CAUSED AN
1013					:IMPROPER REGISTER CHANGE
1014					:GOOD DATA GIVES WHAT SHOULD
1015					:BE THERE
1016					:RECEIVED DATA GIVES WHAT WAS
1017					:THERE AFTER COMMAND
1018	002200	067162	DH5		
1019	002202	071230	DT5		
1020	002204	071514	DF5		
1021					
1022	002206	065565		:*ITEM 77	
1023	002210	071035	EM77		:CURRENT CYLINDER DOES NOT REFLECT DESIRED 'RHCC'
1024			DH77		:PC
1025					:PC OF JSR
1026					:REGISTER ADDRESS
1027					:GOOD DATA
1028	002212	071460	DT77		:BAD DATA
1029	002214	071515	DF77		:\$ERRPC, PCJSR, REGADR, \$GDDAT, \$BDDAT
1030					:0,0,0,0,0
1031					
1032	002216	066010		:*ITEM 100	
1033	002220	067162	EM100		:ERROR AFTER ADDRESS PLUG CHANGE
1034			DH5		:PC
1035					:REGISTER ADDRESS
1036					:GOOD DATA
1037	002222	071230	DT5		:RECEIVED DATA
1038	002224	071514	DF5		:\$ERRPC, REGADR, \$GDDAT, \$BDDAT
1039					:0,0,0,0
1040					
1041	002226	066072		:*ITEM 101	
1042			EM101		:UNIT DID NOT GO OFFLINE WHEN ADDR
1043	002230	067617	DH26		:PLUG WAS REMOVED
1044					:PC
1045					:CONT OF RHCS1
1046					:CONT OF RHCS2
1047					:CONT OF RHDS1
1048					:CONT OF RHER2
1049	002232	071306	DT26		:CONT OF RHER3
1050	002234	071537	DF26		:\$ERRPC, CS1, CS2, DS1, ER2, ER3
1051					:0,0,0,0,0,0,0
1052					
1053	002236	066154		:*ITEM 102	
1054			EM102		:UNIT DID NOT COME BACK ONLINE WHEN
1055	002240	067617	DH26		:ADDR PLUG WAS REPLACED
1056					:PC
1057					:CONT OF RHCS1
1058					:CONT OF RHCS2
					:CONT OF RHDS1

28 bits

1059				:CONT OF RHER2
1060				:CONT OF RHER3
1061	002242	071306	DT26	:SERRPC,CS1,CS2,DS1,ER2,ER3
1062	002244	071537	DF26	:0,0,0,0,0,0,0
1063				
1064			:*ITEM 103	
1065	002246	066233	EM103	:REGISTER CONTENTS INCORRECT BEFORE A
1066				:DIAGNOSTIC SEEK
1067	002250	067617	DH26	:PC
1068				:CONT OF RHCS1
1069				:CONT OF RHCS2
1070				:CONT OF RHDS1
1071				:CONT OF RHER2
1072				:CONT OF RHER3
1073	002252	071306	DT26	:SERRPC,CS1,CS2,DS1,ER2,ER3
1074	002254	071537	DF26	:0,0,0,0,0,0,0
1075				
1076			:*ITEM 104	
1077	002256	066317	EM104	:REGISTER CONTENTS INCORRECT AFTER A
1078				:DIAGNOSTIC SEEK
1079	002260	067617	DH26	:PC
1080				:CONT OF RHCS1
1081				:CONT OF RHCS2
1082				:CONT OF RHDS1
1083				:CONT OF RHER2
1084				:CONT OF RHER3
1085	002262	071306	DT26	:SERRPC,CS1,CS2,DS1,ER2,ER3
1086	002264	071537	DF26	:0,0,0,0,0,0,0
1087				

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1111  
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1128  
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002266 000254

::\*\*\*\*\*  
:\*RH11 REGISTERS  
:\*\*\*\*\*

RPVEC: 254 ;RPO4 VECTOR ADDRESS

:\*WORD COUNT REGISTER (RHWC)  
:\*EACH BIT IS CALLED BY BIT NUMBER

:\*BUS ADDRESS REGISTER (RHBA)  
:\*EACH BIT IS CALLED BY BIT NUMBER

:\*CONTROL AND STATUS REGISTER 2 (RHCS2)

000001  
000002  
000004  
000010  
000020  
000040  
000100  
000200  
000400  
001000  
002000  
004000  
010000  
020000  
040000  
100000

US1= 1 ;UNIT SELECT (BIT #0)  
US2= 2 ;UNIT SELECT (BIT #1)  
US4= 4 ;UNIT SELECT (BIT #2)  
BAI= 10 ;BUS ADDRESS INCREMENT INHIBIT (BIT #3)  
UNIB= 20 ;UNIBUS B DC LO (BIT #4)  
CLR= 40 ;CLEAR (BIT #5)  
IR= 100 ;INPUT READY (BIT #6)  
OR= 200 ;OUTPUT READY (BIT #7)  
MPE= 400 ;MASS BUS PARITY ERROR (BIT #8)  
MXF= 1000 ;MISSED TRANSFER ERROR (BIT #9)  
PGE= 2000 ;PROGRAM ERROR (BIT #10)  
NEM= 4000 ;NON EXISTANT MEMORY (BIT #11)  
NED= 10000 ;NON EXISTANT DRIVE (BIT #12)  
UPE= 20000 ;UNIBUS PARITY ERROR (BIT #13)  
WCE= 40000 ;WRITE CHECK ERROR (BIT #14)  
DLT= 100000 ;DATA LATE (BIT #15)

:\*DATA BUFFER REGISTER (RHDB)  
:\*EACH BIT IS CALLED BY BIT NUMBER

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000001  
000100  
000200  
000400  
001000  
002000  
004000  
020000  
040000  
100000  
  
000001  
000002  
000004  
000010  
000020  
000040  
000100  
000200  
000400  
001000  
002000  
004000  
010000  
020000  
040000  
100000  
  
000001  
000002  
000004  
000010  
000020  
000040  
000100  
000200  
000400  
001000  
002000  
004000  
010000  
020000  
040000

\*\*\*\*\*  
:RPO4 REGISTERS  
\*\*\*\*\*

:\*CONTROL AND STATUS 1 REGISTER. (#00)

GJ= 1 ;GO (BIT #0)  
IE= 100 ;INTERRUPT ENABLE (BIT #6)  
RDY= 200 ;READY (BIT #7)  
A16= 400 ;HIGH ORDER UNIBUS BITS (BIT #8)  
A17= 1000 ;HIGH ORDER UNIBUS BITS (BIT #9)  
PSEL= 2000 ;PORT SELECT (BIT #10)  
DVA= 4000 ;DEVICE AVAILABLE (BIT #11)  
MCPE= 20000 ;MASSBUS PARITY ERROR (BIT #13)  
TRE= 40000 ;TRANSFER ERROR (BIT #14)  
SC= 100000 ;SPECIAL CONDITION (BIT #15)

:\*STATUS REGISTER (RHDS1) (#01)

DFF5= 1 ;DRIVE FORWARD 5"/SEC. (BIT #0)  
DFF20= 2 ;DRIVE FORWARD 20"/SEC. (BIT #1)  
DIGB= 4 ;DRIVE TO INNER GAVRD BAND (BIT #2)  
GRV= 10 ;GO REVERSE (BIT #3)  
DL64= 20 ;DIFFERENCE LESS THAN 64 (BIT #4)  
DE1= 40 ;DIFFERENCE EQUALS 1 (BIT #5)  
VV= 100 ;VOLUME VALID (BIT #6)  
DRY= 200 ;DRIVE READY (BIT #7)  
DPR= 400 ;DRIVE PRESENT (BIT #8)  
PROG= 1000 ;PROGRAMABLE (BIT #9)  
LBT= 2000 ;LAST SECTOR TRANSFERRED (BIT #10)  
WRL= 4000 ;WRITE LOCK (BIT #11)  
MOL= 10000 ;MEDIUM ON-LINE (BIT #12)  
PIP= 20000 ;POSITIONING OPERATION IN PROGRESS (BIT #13)  
ERR= 40000 ;COMPOSIT ERROR (BIT #14)  
ATA= 100000 ;ATTENTION ACTIVE (BIT #15)

:\*ERROR REGISTER #01 (RHER1) (#02)

ILF= 1 ;ILLEGAL FUNCTION (BIT #0)  
ILR= 2 ;ILLEGAL REGISTER (BIT #1)  
RMR= 4 ;REGISTER MODIFICATION REFUSED (BIT #2)  
PAR= 10 ;PARITY ERROR (BIT #3)  
FER= 20 ;FORMAT ERROR (BIT #4)  
WCF= 40 ;WRITE CLOCK FAIL (BIT #5)  
ECH= 100 ;ECC HARD ERROR (BIT #6)  
HCE= 200 ;HEADER COMPARE ERROR (BIT #7)  
HCRC= 400 ;HEADER CRC ERROR (BIT #8)  
AOE= 1000 ;ADDRESS OVERFLOW ERROR (BIT #9)  
IAE= 2000 ;INVALID ADDRESS ERROR (BIT #10)  
WLE= 4000 ;WRITE LOCK ERROR (BIT #11)  
CTE= 10000 ;DRIVE TIMING ERROR (BIT #12)  
OPI= 20000 ;OPERATION INCOMPLETE (BIT #13)  
UNS= 40000 ;DRIVE UNSAFE (BIT #14)

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1185      100000      DCK=      100000      ;DATA CHECK ERROR (BIT 15)
1186
1187      ;*MAINTAINABILITY REGISTER (RHMR)(#03)
1188
1189      000001      DMD=      1      ;DIAGINOSTIC MODE (BIT #0)
1190      000002      MCLK=     2      ;MAINTAINABILITY CLOCK (BIT #1)
1191      000004      MINX=     4      ;MAINTAINABILITY INDEX (BIT #2)
1192      000010      MSTCK=    10     ;MAINTAINABILITY SECTOR CLOCK (BIT #3)
1193      000020      MRD=     20     ;MAINTAINABILITY READ (BIT #4)
1194      000040      MWR=     40     ;MAINTAINABILITY WRITE (BIT #5)
1195      001000      DTSY=    1000    ;MAINTAINABILITY SYNC DETECTED (BIT #9)
1196
1197      ;*ATTENTION SUMMARY PSEUDO-REGISTER (RHAS) (#04)
1198
1199      000001      AT0=      1      ;DEVICE 0 (BIT #0)
1200      000002      AT1=      2      ;DEVICE 1 (BIT #1)
1201      000004      AT2=      4      ;DEVICE 2 (BIT #2)
1202      000010      AT3=     10     ;DEVICE 3 (BIT #3)
1203      000020      AT4=     20     ;DEVICE 4 (BIT #4)
1204      000040      AT5=     40     ;DEVICE 5 (BIT #5)
1205      000100      AT6=    100     ;DEVICE 6 (BIT #6)
1206      000200      AT7=    200     ;DEVICE 7 (BIT #7)
1207
1208      ;*DESIRED SECTOR/TRACK ADDRESS REGISTER (RHDST) (#1)
1209      ;*EACH BIT IS CALLED BY BIT NUMBER
1210
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1215      ;*DRIVE TYPE REGISTER (RHDT) (#06)
1216      ;*EACH BIT IS CALLED BY BIT NUMBER
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1221
1222      ;*LOCK-AHEAD REGISTER (RHLA) (#07)
1223
1224      000001      EXT1=      1      ;EXTENSION 1 (BIT #0)
1225      000002      EXT2=      2      ;EXTENSION 2 (BIT #1)
1226      000004      EXT4=      4      ;EXTENSION 3 (BIT #2)
1227      000010      EXT10=    10     ;EXTENSION 4 (BIT #3)
1228      000020      EXT20=    20     ;EXTENSION 5 (BIT #4)
1229      000040      EXT40=    40     ;EXTENSION 6 (BIT #5)
1230      000100      SC1=     100     ;SECTOR COUNT FIELD 0 (BIT #6)
1231      000200      SC2=     200     ;SECTOR COUNT FIELD 1 (BIT #7)
1232      000400      SC4=     400     ;SECTOR COUNT FIELD 2 (BIT #8)
1233      001000      SC10=    1000    ;SECTOR COUNT FIELD 3 (BIT #9)
1234      002000      SC20=    2000    ;SECTOR COUNT FIELD 4 (BIT #10)
1235      004000      TRK1=    4000    ;TRACK FIELD 1 (BIT #11)
1236      010000      TRK2=   10000    ;TRACK FIELD 2 (BIT #12)
1237      020000      TRK4=   20000    ;TRACK FIELD 3 (BIT #13)
1238      040000      TRK10=  40000   ;TRACK FIELD 4 (BIT #14)

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1239      100000      TRK20= 100000      ;TRACK FIELD 5 (BIT #15)
1240
1241      ;*ERROR REGISTER #2 (RHER2) (#10)
1242
1243      000001      WCU= 1      ;WRITE CURRENT UNSAFE (BIT #0)
1244      000002      CSF= 2      ;CURRENT SINK FAILURE (BIT #1)
1245      000004      WSU= 4      ;WRITE SELECT UNSAFE (BIT #2)
1246      000010      CSU= 10     ;CURRENT SWITCH UNSAFE (BIT #3)
1247      000020      MSE= 20     ;MOTOR SEQUENCE ERROR (BIT #4)
1248      000040      TDF= 40     ;TRANSITIONS DETECTOR FAILURE (BIT #5)
1249      000100      TUF= 100    ;TRANSITIONS UNSAFE (BIT #6)
1250      000200      FEN= 200    ;FAILSAFE ENABLED (BIT #7)
1251      000400      WRU= 400    ;WRITE READY UNSAFE (BIT #8)
1252      001000      MHS= 1000   ;MULTIPLE HEAD SELECT (BIT #9)
1253      002000      NHS= 2000   ;NO HEAD SELECTION (BIT #10)
1254      004000      IXE= 4000   ;INDEX ERROR (BIT #11)
1255      010000      VU30= 10000  ;30VOLT UNSAFE (BIT #12)
1256      020000      PLU= 20000  ;PLO UNSAFE (BIT #13)
1257      100000      ACU= 100000  ;ACUNSAFE (BIT #15)
1258
1259      ;*OFFSET REGISTER (RHOF) (#11)
1260
1261      000001      OF25= 1      ;OFFSET 25 MICRO INCHES (BIT #0)
1262      000002      OF50= 2      ;OFFSET 50 MICRO INCHES (BIT #1)
1263      000004      OF100= 4     ;OFFSET 100 MICRO INCHES (BIT #2)
1264      000010      OF200= 10    ;OFFSET 200 MICRO INCHES (BIT #3)
1265      000020      OF400= 20    ;OFFSET 400 MICRO INCHES (BIT #4)
1266      000040      OF800= 40    ;OFFSET 800 MICRO INCHES (BIT #5)
1267
1268      000200      OFREV= 200    ;OFFSET NEGATIVE (REVERSE) (BIT #5)
1269      002000      HCI= 2000    ;HEADER COMPARE INHIBIT (BIT #10)
1270      004000      ECI= 4000    ;EPROR CORRECTION CODE INHIBIT (BIT #11)
1271      010000      FMT22= 10000  ;FORMAT BIT (BIT #12)
1272
1273      ;*DESIRED CYLINDER ADDRESS (RHCA) (#12)
1274      ;*EACH BIT IS CALLED BY BIT NUMBER.
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1279      ;*CURRENT CYLINDER ADDRESS (RHCC) (#13)
1280      ;*EACH BIT IS CALLED BY BIT NUMBER
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1285      ;*SERIAL NUMBER REGISTER (RHSN) (#14)
1286      ;*EACH IS CALLED BY BIT NUMBER
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1290
1291      ;*ERROR REGISTER #03 (RHER3) (#15)
1292

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1293	000001	PSU=	1	;PACK SPEED UNSAFE (BIT #0)
1294	000002	VUF=	2	;VELOCITY UNSAFE (BIT #1)
1295	000010	UWR=	10	;ANY UNSAFE EXCEPT READ/WRITE (BIT #3)
1296	000020	PRE=	20	;DISK PACK ROTATION ERROR (BIT #4)
1297	000040	ACL=	40	;AC LOW (BIT #5)
1298	000100	DCL=	100	;DC LOW (BIT #6)
1299	020000	ACE=	20000	;ADDRESS CHANGE ERROR (BIT #13)
1300	040000	SKI=	40000	;SEEK INCOMPLETE (BIT #14)
1301	100000	OCYL=	100000	;OFF CYLINDER (BIT #15)

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;\*ECC POSITION REGISTER (RHEC1) (#16)  
;\*EACH BIT IS CALLED BY BIT NUMBER

;\*ECC PATTERN REGISTER (RHEC2) (#17)  
;\*EACH BIT IS CALLED BY BIT NUMBER

.SBTTL REGISTER ADDRESSES

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1329 002270 176722  
1330 002272 176702  
1331 002274 176704  
1332 002276 176710  
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1336 002300 176700  
1337 002302 176714  
1338 002304 176706  
1339 002306 176740  
1340 002310 176732  
1341 002312 176734  
1342 002314 176742  
1343 002316 176716  
1344 002320 176724  
1345 002322 176712  
1346 002324 176726  
1347 002326 176730  
1348 002330 176744  
1349 002332 176746  
1350 002334 176736  
1351 002336 176720  
1352  
1353  
1354  
1355 002340 176750  
1356 002342 176752  
1357  
1358  
1359  
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1361 002344 172540  
1362 002346 172542  
1363 002350 172544  
1364

;\*RPO4/5/6 DISK I/O REGISTER LOCATED IN THE RH11 CONTROLLER

RHDB: 176722 ;DATA BUFFER  
 RHWC: 176702 ;WORD COUNT  
 RHBA: 176704 ;BUS ADDRESS  
 RHCS2: 176710 ;CONTROL AND STATUS 2

;\*RPO4/5/6 DISK I/O REGISTERS LOCATED IN THE DEVICE CONTROL LOGIC (DCL)

RHCS1: 176700 ;CONTROL AND STATUS 1  
 RHER1: 176714 ;ERROR #1  
 RHDST: 176706 ;DESIRED SECTOR/TRACK ADDRESS  
 RHER2: 176740 ;ERROR #2  
 RHOF: 176732 ;OFFSET  
 RHCA: 176734 ;DESIRED CYLINDER ADDRESS  
 RHER3: 176742 ;ERROR #3  
 RHAS: 176716 ;ATTENTION SUMMARY  
 RHMR: 176724 ;MAINTAINABILITY  
 RHDS1: 176712 ;DRIVE STATUS  
 RHDT: 176726 ;DRIVE TYPE  
 RHSN: 176730 ;SERIAL NUMBER  
 RHEC1: 176744 ;ECC POSITION  
 RHEC2: 176746 ;ECC PATTERN  
 RHCC: 176736 ;CURRENT CYLINDER ADDRESS  
 RHLA: 176720 ;LOOK-AHEAD

;\*ADDITIONAL I/O REGISTERS LOCATED IN THE RH70 CONTROLLER LOGIC

RHBAE: 176750 ;BUS ADDRESS EXTENSION REGISTER  
 RHCS3: 176752 ;CONTROL AND STATUS REGISTER #3

;\*P-CLOCK (KW11-P) I/O REGISTERS

PCLCSR: 172540 ;CONTROL AND STATUS REGISTER  
 PCLBUF: 172542 ;COUNT SET BUFFER  
 PCLCTR: 172544 ;COUNTER







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004676 000010  
004716 000000  
004720 000000  
004722 000000  
004724 000030  
004726 000000  
004730 000030  
004732 000000  
004734 000000  
004736 000000  
004740 000000  
004742 000000  
004744 000000  
004746 000030  
004750 000000  
004752 000000  
004754 000000  
004756 000000  
004760 000000

:\*FLAGS AND INTERNAL PROGRAM CONTROL WORDS

JNITS: .BLKW 9.  
JNIT: .WORD 0  
NOUNIT: .WORD 0  
NUNIT: .WORD 0  
NOPUSH: 0  
SELECT: .WORD 0  
UNITSL: .WORD 0  
UBUSB: 0  
ERFLGS: 0  
FIRST: 0  
ATTENT: 0  
TOTALAT: 0  
RPO6: 0  
RPO5: 0  
PH70: 0  
INUNIT: 0  
TMP0: .WORD 0  
TMP1: .WORD 0  
TMP4: .WORD 0

: THIS IS FILLED WITH -1  
: UNIT UNDER TEST  
: NUMBER OF UNITS PRESENT  
: USED TO KEEP TRACK OF UNIT UNDER TEST  
: USED TO DETERMIN IF THERE ARE MORE  
: THAN ONE UNIT  
: ALL ONES INDICATE NONE OF THE OPERATOR  
: INTERVENTION TESTS WILL BE PERFORMED  
: ALL ONES INDICATE UNIT TO BE SELECTED  
: UNIT NO. SELECTED  
: IF ZERO UNIBUS PRESENT  
: IF ONES NO UNIBUS B  
: ERROR FLAG  
: IF ZERO WILL TYPE HEADER  
: IF ONES WILL NOT TYPE HEADER  
: ATTENTION BIT FOR PRESENT UNIT  
: TOTAL ATTENTION BITS  
: RPO6 DEVICE TYPE FLAG LOCATION  
: MEMOREX RPO4 DEVICE TYPE FLAG  
: IF = 1. PROGRAM IS RUNNING ON RWPO4 SYSTEM  
: IF = 0. PROGRAM IS RUNNING ON RJPO4  
: INITIAL UNIT NO. - USED DURING  
: CHECKING ALL ADDRESS PLUG ADDRESSES  
: TEMP STORAGE  
: TEMP STORAGE



:TEMP STORAGE

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1505 .SBTTL
1506 .SBTTL **DIAGNOSTIC CODE**
1507 .SBTTL
1508
1509
1510 .SBTTL SETUP TESTS
1511
1512
1513 004762 012737 177777 004724 BEGIN1: MOV # -1, @NOPUSH ; JUMP OVER OPERATOR REQUIRED TESTS
1514 004770 005037 004726 CLR @SELECT ; DO NOT SELECT UNIT
1515 004774 000412 BR START
1516 004776 012737 177777 004726 BEGIN2: MOV # -1, @SELECT ; SELECT UNIT
1517 005004 005037 004724 CLR @NOPUSH ; DO NOT JUMP OVER ANY TEST
1518 005010 000404 BR START
1519 005012 005037 004726 BEGIN: CLR @SELECT ; DO NOT SELECT UNIT
1520 005016 005037 004724 CLR @NOPUSH ; DO NOT JUMP OVER ANY OPERATOR
1521 ; INTERVENTION TESTS - NORMAL RUN
1522
1523 START:
1524 005022 000005 RESET
1525 .SBTTL INITIALIZE THE COMMON TAGS
1526 ;; CLEAR THE COMMON TAGS ($CMTAG) AREA
1527 005024 012706 001100 MOV $CMTAG, R6 ;; FIRST LOCATION TO BE CLEARED
1528 005030 005026 CLR (R6)+ ;; CLEAR MEMORY LOCATION
1529 005032 022706 001140 CMP $SWR, R6 ;; DONE?
1530 005036 001374 BNE -6 ;; LOOP BACK IF NO
1531 005040 012706 001000 MOV $STACK, SP ;; SETUP THE STACK POINTER
1532 ;; INITIALIZE A FEW VECTORS
1533 005044 012737 044414 000020 MOV $SCOPE, @IOTVEC ;; IOT VECTOR FOR SCOPE ROUTINE
1534 005052 012737 000340 000022 MOV #340, @IOTVEC+2 ;; LEVEL 7
1535 005060 012737 046612 000030 MOV $ERROR, @EMTVEC ;; EMT VECTOR FOR ERROR ROUTINE
1536 005066 012737 000340 000032 MOV #340, @EMTVEC+2 ;; LEVEL 7
1537 005074 012737 050336 000034 MOV $TRAP, @TRAPVEC ;; TRAP VECTOR FOR TRAP CALLS
1538 005102 012737 000340 000036 MOV #340, @TRAPVEC+2 ;; LEVEL 7
1539 005110 012737 050406 000024 MOV $SPWRDN, @PWRVEC ;; POWER FAILURE VECTOR
1540 005116 012737 000340 000026 MOV #340, @PWRVEC+2 ;; LEVEL 7
1541 005124 005037 001212 CLR $TIMES ;; INITIALIZE NUMBER OF ITERATIONS
1542 005130 005037 001214 CLR $ESCAPE ;; CLEAR THE ESCAPE ON ERROR ADDRESS
1543 005134 112737 000001 001115 MOVB #1, $ERMAX ;; ALLOW ONE ERROR PER TEST
1544 005142 012737 005142 001106 MOV #, $LPADR ;; INITIALIZE THE LOOP ADDRESS FOR SCOPE
1545 005150 012737 005150 001110 MOV #, $LPERR ;; SETUP THE ERROR LOOP ADDRESS
1546 ;; SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
1547 ;; EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
1548 005156 013746 000004 MOV @ERRVEC, -(SP) ;; SAVE ERROR VECTOR
1549 005162 012737 005216 000004 MOV #64$, @ERRVEC ;; SET UP ERROR VECTOR
1550 005170 012737 177570 001140 MOV $DSWR, SWR ;; SETUP FOR A HARDWARE SWITCH REGISTER
1551 005176 012737 177570 001142 MOV $DDISP, DISPLAY ;; AND A HARDWARE DISPLAY REGISTER
1552 005204 022777 177777 173726 CMP # -1, @SWR ;; TRY TO REFERENCE HARDWARE SWR
1553 005212 001012 BNE 65$ ;; BRANCH IF NO TIMEOUT TRAP OCCURRED
1554 ;; AND THE HARDWARE SWR IS NOT = -1
1555 005214 000403 BR 65$ ;; BRANCH IF NO TIMEOUT
1556 005216 012716 005224 64$: MOV #65$, (SP) ;; SET UP FOR TRAP RETURN
1557 005222 000002 RTI
1558 005224 012737 000176 001140 65$: MOV $SWREG, SWR ;; POINT TO SOFTWARE SWR

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1559 005232 012737 000174 001142      MOV      #DISPREG, DISPLAY
1560 005240 012637 000004      66$:    MOV      (SP)+, @#ERRVEC ;;RESTORE ERROR VECTOR
1561
1562
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1564
1565 005244 012737 000C00 177776      MOV      #0, PS ;;SET PROCESSOR STATUS TO 0
1566 005252 012737 000200 000036      MOV      #200, @#TRAPVEC+2 ;;TRAP PRIORITY = 4
1567 005260 013700 002266      MOV      @#RPVEC, R0 ;;GET RP VECTOR ADDRESS
1568 005264 012720 044352      MOV      @#RPVECT, (R0)+ ;;THIS IS FOR UNTIMELY INTERRUPTS
1569 005270 012710 000340      MOV      #340, (R0) ;;DRIVE INTERRUPT SERVICE ROUTINE
1570 ;;PRIORITY = 7
1571
1572 005274 004737 045352      JSR      PC, @#STKINT ;;INITIALIZE THE TTY KEYBOARD
1573 005300 005737 004736      TST      @#FIRST ;;IS THIS FIRST TIME ROUND ?
1574 005304 001001      BNE      1$ ;;DON'T GIVE HEADER IF NOT
1575 005306 000402      BR       2$ ;;HEADER 1ST TIME THROUGH
1576 005310 000137 006110      1$:    JMP      @#SND1 ;;NO HEADER
1577
1578 005314      2$:
1579 005314 104400 005322      TYPE      68$ ;;TYPE ASCIZ STRING
1580 005320 000435      BR       67$ ;;GET OVER THE ASCIZ
1581 ;;68$: .ASCIZ <15><12>'RPO4/5/6 FUNCTIONAL CONTROLLER TEST - PART I - DZRJI-A?'
1582 005414      67$:
1583 005414 104400 005422      TYPE      70$ ;;TYPE ASCIZ STRING
1584 005420 000417      BR       69$ ;;GET OVER THE ASCIZ
1585 ;;70$: .ASCIZ <15><12>'REVISION DATE: 21-MAR-76/<15><12>'
1586 005460      69$:
1587
1588 005460 104400 005466      TYPE      72$ ;;TYPE ASCIZ STRING
1589 005464 000435      BR       71$ ;;GET OVER THE ASCIZ
1590 ;;72$: .ASCIZ <15><12>'ALL DCL'S UNDER TEST MUST BE LOCKED ON THE CORRECT PORT/'
1591 005560      71$:
1592 005560 104400 005566      TYPE      74$ ;;TYPE ASCIZ STRING
1593 005564 000433      BR       73$ ;;GET OVER THE ASCIZ
1594 ;;74$: .ASCIZ <15><12>'IF CHANGE IS REQUIRED ON PORT SWITCH, THEN A CYCLE/'
1595 005654      73$:
1596 005654 104400 005662      TYPE      76$ ;;TYPE ASCIZ STRING
1597 005660 000435      BR       75$ ;;GET OVER THE ASCIZ
1598 ;;76$: .ASCIZ <15><12>'UP SEQUENCE IS REQ FOR STROBING THE PORT SELECT FLOP<15><12>'
1599 005754      75$:
1600
1601 005754 104400 005762      TYPE      78$ ;;TYPE ASCIZ STRING
1602 005760 000426      BR       77$ ;;GET OVER THE ASCIZ
1603 ;;78$: .ASCIZ <15><12>'ALL DCL'S NOT UNDER TEST MUST BE SWITCHED'
1604 006036      77$:
1605 006036 104400 006044      TYPE      80$ ;;TYPE ASCIZ STRING
1606 006042 000422      BR       79$ ;;GET OVER THE ASCIZ
1607 ;;80$: .ASCIZ <15><12>'OFF, OR LOCKED ON THE OTHER PORT'
1608 006110      79$:
1609
1610 006110 012737 177777 004736      SND1:   MOV      #-1, @#FIRST ;;NEXT TIME DO NOT GIVE HEADER
1611
1612      .SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER

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1613	005116	005737	000042			TST	3#42	::ARE WE RUNNING UNDER XXDP/ACT?
1614	006122	001006				BNE	545	::BRANCH IF YES
1615	006124	023727	001140	000176		CMP	SWR, #SWREG	::SOFTWARE SWITCH REG SELECTED?
1616	006132	001005				BNE	655	::BRANCH IF NO
1617	006134	104405				GTSWR		::GET SOFT-SWR SETTINGS
1618	006136	000403				BR	655	
1619	006140	112737	000001	001134	645:	MOVB	#1, SAUTOB	::SET AUTO-MODE INDICATOR
1620	006146				655:			
1621								
1622	006146	032777	010000	172764	RH70CK:	BIT	#SW12, 2SWR	:LOOK TO SEE IF USING RH70
1623	006154	001403				BEJ	33	:IF SW12 = 0, SKIP NEXT
1624	006156	012737	177777	004750		MOV	#-1, 2#RH70	:IF SW12 = 1, CU IS AN RH70
1625	006154				33:			

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\*\*\*\*\*  
: IS THERE A P-CLOCK (KW11-P) ON THE SYSTEM  
: IF SO MAKE 'WAT' TRAPS GO TO 'WAIT.P'  
: IF SO MAKE RPO4 INTERRUPTS GO TO 'TIME 1'  
: IF NOT MAKE 'WAT' TRAPS GO TO 'WAIT.T'  
: IF NOT MAKE RPO4 INTERRUPTS GO TO 'TIME 2'

: \*THE NEXT LINE IS TO BE ADDED LATER  
: \*AND THE JUMP AND NOP REMOVED  
: \*FOR NOW NO CLOCK WILL BE USED

:       MOV     2#15,2#ERRVEC     ;SET TIME-OUT VECTOR  
:  
:       JMP     2#15               ;DO NOT USE CLOCK  
:       NOP  
:       TST     2#PCLCSR           ;REFERENCE P-CLOCK STATUS REGISTER  
:                                 ;ADDRESS = 172540  
:       MOV     #WAIT.P,2#STRPAD+20 ;THERE IS A P-CLOCK  
:       MOV     #TIME1,2#RP4VEC   ;THERE IS A P CLOCK SO  
:                                 ;VECTOR TO TIME1  
:       BR     2\$  
:15:   MOV     #WAIT.T,2#STRPAD+20 ;THERE IS NO P-CLOCK  
: \*\*\*\*\*

006164 012737 041600 004606   2\$: MOV     #TIME2,2#RP4VEC ;RPO4/5/6 INTERRUPTS GO TO 'TIME 2'  
036172 012737 177777 046760   MOV     #-1,2#PRITEM   ;CLEAR PREVIOUS ITEM NUMBER

006200 005737 004726       TST     2#SELECT       ;WAS IT A 200 START  
006204 001442       BEG     TST1       ; BRANCH IF STARTING FROM 200  
006206 104400 006214   TYPE    65\$       ; ;TYPE ASCIZ STRING  
005212 000424       BR     64\$       ; ;GET OVER THE ASCIZ  
:65\$: .ASCIZ <15\<12>\SELECT UNIT NUMBER TO BE TESTED ' <15\<12>  
64\$:       RDOCT  
006264 104411       BIC     #177770,(SP)   ; ONLY KEEP LAST 3 BITS  
006266 042716 177770   MOV     (SP),2#UNIT   ;SAVE UNIT TO BE TESTED  
006272 011637 004716   MOV     (SP)+,2#UNITSL ;SAVE UNIT TO BE TESTED  
006276 012637 004730

005302 001403       BEJ     TST1       ; BRANCH IF STARTING FROM 200  
005304 013737 004730 004716   MOV     2#UNITSL,2#UNIT ;SET UNIT NUMBER

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006312 000004  
006314 012737 000001 001212  
006322 012737 000001 004604  
006330 012706 001000  
006334 012737 046622 000030  
006342 012737 006370 000004  
006350 012700 000024  
006354 012701 002270  
006360 013102  
006362 005300  
006364 001375  
006366 000470  
006370 012737 000006 000004  
006376 022626  
006400 016137 177776 001200  
006406 104007  
006410 032777 020000 172522  
006416 001052  
104400 006426  
006424 000424  
006476  
006476 104400 006504  
006502 000415  
006536  
006536 012746 043522  
006542 104401  
006544 000137 040714  
006550 012737 046612 000030  
006556 012737 000006 000004

```
::*****  
:*TEST 1 REFERENCE EACH REGISTER  
  
:* REFERENCE EACH REGISTER BY A MOVE INSTRUCTION  
  
::*****  
↑TST1: SCOPE  
MOV #1,$TIMES ;:DO 1 ITERATION  
MOV #2-1,$TSTNM ;↑THIS SAVES TEST NJMBER  
MOV #STACK,SP ;SET UP STACK POINTER  
MOV #REGSA1,$EMTVEC ;ERROR VECTOR SO THAT  
;NO REGISTERS ARE SAVED  
MOV #25,$ERRVEC ;SET UP FOR BUS TIMEOUT  
  
MOV #24,R0 ;THERE ARE 24 REG TO TEST  
MOV #RH0B,R1 ;R1 NOW HAS ADDR OF ADDR OF FIRST REG.  
1$: MOV 3(R1)+,R2 ;READ HARDWARE REG.  
DEC R0 ;COUNT DOWN  
BNE 1$ ;BRANCH IF 24 NOT DONE  
BR 3$ ;BRANCH IF 24 DONE  
2$: MOV #ERRVEC+2,$ERRVEC ;RESTORE TRAP CATCHER  
CMP (SP)+,(SP)+ ;CLEAN STACK  
MOV -2(R1),$TMP1 ;STORE FAILING REG ADDR  
ERROR 7 ;REGISTER NON EXISTANT  
BIT #SW13,$SWR ;INHIBIT ERROR PRINTOUT  
BNE 4$ ;BRANCH IF YES  
  
TYPE ,65$ ;:TYPE ASCIZ STRING  
BR 64$ ;:GET OVER THE ASCIZ  
65$: .ASCIZ <15><12>/IF BASE ADDRESS IS TO BE CHANGED HALT  
64$: TYPE ,67$ ;:TYPE ASCIZ STRING  
BR 66$ ;:GET OVER THE ASCIZ  
67$: .ASCIZ <15><12>/PROGRAM AND RESTART AT /  
66$:  
  
MOV #BASECH,-(SP) ;GET READY TO TYPE STARTING ADDRESS  
;OF "CHANGE OF BASE ADDRESS" ROUTINE  
  
4$: JMP 3$EOP ;GO TO END OF PROGRAM -----  
  
3$: MOV #ERROR,$EMTVEC ;RESTORE ERROR VECTOR  
;SO THAT REGISTERS ARE SAVED  
MOV #ERRVEC+2,$ERRVEC ;RESTORE TRAP CATCHER
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:*****
:*TEST 2 PARTIAL TEST OF RHAS FOR UNIT NUMBERS PRESENT
:* CHECK THAT RHAS CAN BE CLEARED BY MOVING ALL ONES INTO IT
:*****
†ST2: SCOPE
MOV #1,STIMES ;:DO 1 ITERATION
MOV #STACK,SP ;:SET STACK POINTER
MOV #3-1,@TSTNM ;:THIS SAVES TEST NUMBER

MOV @RHAS,R1 ;:R1 HAS ADDRESS OF RHAS
MOV #-1,@R1 ;:THIS WRITES ALL ONES INTO RHAS
TSTB @R1 ;:TEST RHAS FOR ALL 0'S
BEQ TST3 ;:BRANCH IF GOOD
MOV @R1,@$BDDAT ;:BAD DATA
CLR @$GDDAT ;:GOOD DATA
MOV R1,@REGADR ;:FAILING REG. (RHAS)

ERROR 5 ;:RHAS DOES NOT CLEAR BY WRITING
:ALL ONES INTO IT

```

```

006564 000004
006566 012737 000001 001212
006574 012706 001000
006600 012737 000002 004604
006606 013701 002316
006612 012711 177777
006616 105711
006620 001407
006622 011137 001126
006626 005037 001124
006632 010137 004600
006636 104005

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006640 000004  
006642 012737 000001 001212  
006650 012737 000003 004604  
006656 000005  
006660 004737 045352  
006664 032777 020000 172246  
006672 001026  
006674 104400 006702  
006700 000423  
006750  
006750 013701 002316  
006754 013702 002276  
006760 005012  
006762 012700 000010  
006766 013704 002302  
006772 012714 177777  
005776 005212  
007000 005300  
007002 001373  
111137 004742  
007010 105037 004743  
007014 105711  
007016 001402  
007020 000137 007372  
007024 032777 020000 172106  
007032 001402  
007034 000137 007730  
007040  
007040 104400 007046  
007044 000420  
007106  
007106 104400 007114

```
*****
*TEST 3 TEST FOR DRIVES PRESENT USING RHAS AND RHCS2
* THE NUMBER OF DRIVES PRESENT IS FOUND
* BY MOVING ALL ONES INTO RHER1 WITH UNIT NUMBER
* IN RHCS2 INCREMENTED FROM ZERO TO SEVEN
* THE BITS SET IN RHAS SHOULD GIVE DRIVES PRESENT
* THE DRIVE TYPE IS CHECKED TO BE AN RPO4/RPO6 AND THEN
* UNITS PRESENT ARE STORED IN A TABLE CALLED 'UNITS'
*****
TST3: SCOPE
MOV #1,STIMES ;DO 1 ITERATION
MOV #4-1,STSTNM ;THIS SAVES TEST NUMBER
RESET ;START WITH AN INIT
JSR PC,STKINT ;INITIALIZE TTY KEYBOARD
BIT #SW13,SWR ;INHIBIT ERROR TYPEOUT?
BNE 4$ ;BRANCH IF YES
TYPE ,65$ ;TYPE ASCIZ STRING
BR ,64$ ;GET OVER THE ASCIZ
;65$: .ASCIZ <15><12><15><12> /LOOKING AT RHAS - DRIVES PRESENT
;64$:
MOV @RHAS,R1 ;R1 HAS ADDR. OF RHAS
MOV @RHCS2,R2 ;R2 HAS ADDR. OF RHCS2
CLR @R2 ;CLEAR RHCS2
MOV #8,R0 ;COUNT
MOV @RHER1,R4 ;R4 HAS ADDR. OF RHER1
;1$: MOV #-1,@R4 ;MOVE ERRORS INTO RHER1
INC @R2 ;INCREMENT UNIT NO.
DEC R0 ;COUNT DOWN DRIVE COUNTER
BNE 1$ ;DO NEXT UNIT IF 8 NOT DONE
MOVB @R1,@TOTALAT ;SAVE TOTAL ATTENTION
;USED IN DRIVE CLEAR TEST
CLR @TOTALAT+1 ;CLEAR UPPER BYTE
TSTB @R1 ;TEST 'RHAS' FOR ANY DRIVES PRESENT
BEQ 2$ ;NONE RESPONDING - TYPE THE MESSAGE
JMP XE2 ;SOME THERE - GO FILL "UNITS" TABLE
;2$: BIT #SW13,SWR ;INHIBIT ERROR TYPE OUT?
BEQ 3$ ;"NO DRIVES" MESSAGE IF NOT
JMP SELTST ;CHECK FOR SELECTED UNIT START AND LOAD
;"UNITS" TABLE WITH SELECTED ONE IF SO
;3$:
TYPE ,67$ ;TYPE ASCIZ STRING
BR ,66$ ;GET OVER THE ASCIZ
;67$: .ASCIZ <15><12> /NO DRIVES PRESENT - RHAS = 0/
;66$:
TYPE ,69$ ;TYPE ASCIZ STRING
```

```

1797 007112 000430          BR      68$          ;;GET OVER THE ASCIZ
1798          ;;69$: .ASCIZ <15><12>/WRITING ONES INTO RHER1 FOR ALL UNIT NUMBERS/
1799 007174          BR      70$          ;;GET OVER THE ASCIZ
1800 007174 104400 007202    TYPE    71$          ;;TYPE ASCIZ STRING
1801 007200 000430          BR      70$          ;;GET OVER THE ASCIZ
1802          ;;71$: .ASCIZ <15><12>/DOES NOT SET ANY BIT IN RHAS SO AEOPT PROGRAM/
1803 007252          BR      72$          ;;GET OVER THE ASCIZ
1804 007252 104400 007270    TYPE    73$          ;;TYPE ASCIZ STRING
1805 007266 000437          BR      72$          ;;GET OVER THE ASCIZ
1806          ;;73$: .ASCIZ <15><12>/TO LOOP ON THIS TEST WO PRINTOUT SET SWITCHES 13, 9, 1, & 0/
1807 007366          BR      72$
1808
1809 007366 000137 040714    JMP     @#SEOP        ;GO OUT----->
1810
1811
1812
1813          ;*SET UP UNITS TABLE
1814
1815 007372          XE2:
1816 007372 012700 000010    2$: MOV     #8, R0      ;COUNTER
1817 007376 012703 004676    MOV     #UNITS, R3    ;POINTER
1818 007402 012723 177777    3$: MOV     #-1, (R3)+ ;PRESET BLOCK TO ALL ONES
1819 007406 005300          DEC     R0            ;COUNT
1820 007410 001374          BNE    3$            ;BRANCH IF 8 NOT DONE
1821 007412 012703 004676    MOV     #UNITS, R3    ;POINTER
1822 007416 005005          CLR    R5            ;INITIALIZE UNIT NO. TO 0
1823 007420 005037 004720    CLR    @#NUNIT        ;NO. OF UNITS PRESENT
1824 007424 012700 000010    MOV     #8, R0      ;COUNTER
1825 007430 011137 001176    MOV     @R1, @#STMP0  ;TEMPORARY STORAGE
1826 007434 006037 001176    4$: ROR    @#STMP0    ;SET CARRY IF ONE IN C BIT
1827 007440 :03120          BCC    5$            ;CHECK NEXT UNIT IF ONE NOT IN BIT 0
1828
1829 007442 010577 172630    MOV     R5, @RHCS2    ;INSERT UNIT NUMBER INTO RHCS2 UA BITS
1830 007446 022777 024020 172650  CMP     #24020, @RHDT ;IS THIS A DUAL PORT RPO4 ?
1831 007454 001503          BEQ    6$            ;TYPE THE UNIT NO. IF YES
1832 007456 022777 020020 172640  CMP     #20020, @RHDT ;IS THIS A SINGLE PORT RPO4 ?
1833 007464 001477          BEQ    6$            ;TYPE UNIT NO. IF YES
1834
1835          ;;*****
1836 007466 022777 024021 172630  CMP     #24021, @RHDT ;DUAL PORT RPO5 ?
1837 007474 001473          BEQ    6$            ;TYPE UNIT NO. IF SO
1838 007476 022777 020021 172620  CMP     #20021, @RHDT ;SINGLE PORT RPO5 ?
1839 007504 001467          BEQ    6$            ;TYPE UNIT NO. IF SO
1840
1841 007506 022777 024022 172610  CMP     #24022, @RHDT ;IS THIS A DUAL PORT RPO6 ?
1842 007514 001463          BEQ    6$            ;TYPE THE UNIT NO. IF SO
1843 007516 022777 020022 172600  CMP     #20022, @RHDT ;IS THIS A SINGLE PORT RPO6 ?
1844 007524 001457          BEQ    6$            ;TYPE UNIT NO. IF SO
1845          ;;*****
1846
1847
1848          ;*NO...IT'S NOT AN RPO4/RPO5/RPO6 DEVICE SO TYPE
1849          ;*OUT THE DEVICE TYPE
1850

```

```

1851 007526 104400 007534      TYPE      65$      ;;TYPE ASCIZ STRING
1852 007532 000410      BR        64$      ;;GET OVER THE ASCIZ
1853      ;;65$: .ASCIZ <15><12>/UNIT NUMBER /
1854 007554      64$:
1855 007554 010546      MOV      R5,-(SP)      ;GET READY TO TYPE UNIT NUMBER
1856 007556 104404      TYPDS
1857 007560 104400 007566      TYPE      67$      ;;TYPE ASCIZ STRING
1858 007564 000406      BR        66$      ;;GET OVER THE ASCIZ
1859      ;;67$: .ASCIZ /, RHDT = /
1860 007602      66$:
1861 007602 017746 172516      MOV      @RHDT,-(SP)      ;GET READY TO TYPE RHDT
1862 007606 104401      TYPDC
1863 007610 104400 007616      TYPE      69$      ;;TYPE ASCIZ STRING
1864 007614 000422      BR        68$      ;;GET OVER THE ASCIZ
1865      ;;69$: .ASCIZ ? - NOT AN RPO4/RPO5/RPO6 DEVICE !!?
1866 007662      68$:
1867 007662 000407      BR        5$      ;NO RPO4/RPO5/RPO6 FOUND SO TEST NEXT UNIT
1868
1869 007664 010523      6$: MOV      R5,(R3)+      ;LOAD TABLE POSITION AND INCR IT
1870 007666 104400 001223      TYPE      $CRLF
1871 007672 010546      MOV      R5,-(SP)      ;PUT DRIVE NO. ON STACK
1872 007674 104404      TYPDS      ;TYPE DRIVE NO.
1873 007676 005237 004720      INC      @#NUNIT      ;INCR THE TOTAL NO. OF UNITS
1874
1875 007702 005205      5$: INC      R5      ;'RHCS2' UNIT ADDRESS
1876 007704 005300      DEC      R0      ;DRIVE COUNTER DOWN ONE
1877 007706 001252      BNE      4$      ;TEST AND DO NEXT UNIT IF 8 NOT DONE
1878
1879 007710 013737 004676 004716      MOV      @#UNITS,@#UNIT      ;SET UNIT NO. TO FIRST ONE FOUND OR 0
1880 007716 013737 004720 004722      MOV      @#NUNIT,@#NUNIT      ;SAVE NO. OF UNITS
1881 007724 005337 004722      DEC      @#NUNIT      ;IF NUNIT = 0 THEN ONLY ONE UNIT
1882      ;IF NUNIT > 0 THEN MORE THAN ONE UNIT
1883
1884 007730 005737 004726      SELTST: TST      @#SELECT      ;STARTING ADDRESS 200
1885 007734 001403      BEQ      TST4      ;SKIP NEXT IF STARTING FROM 200
1886 007736 013737 004730 004716      MOV      @#UNITSL,@#UNIT      ;CHANGE UNIT NUMBER TO SELECTED ONE
    
```





REC-11-DZRII-A, RPO4 5 6 FUNCT. CONT. TST-PT 1  
TYPE SERIAL NUMBER AND DRIVE TYPE

```

1971 010214 000000 004716 665: .ASCIZ (15)(12) PRESS CONTINUE WHEN FINISHED/(15)(12)
1972 010214 000000 004716 665: HALT
1973 010216 000404 BR 95 :CONTINUE, USING SCRATCH PACK ON UNIT #0
1974 010220 011037 004716 115: MOV (R0),2#UNIT :SET UP TO BE THE UNIT UNDER TEST
1975 010224 005337 004720 DEC 2#NOUNITS :DECREMENT BECAUSE UNIT #0 WON'T BE TESTED
1976 010230 013700 004716 95: MOV 2#UNIT,R0 :R0 CONTAINS UNIT UNDER TEST
1977 *****
1978 010234 005037 004744 CLR 2#RPO6 :CLEAR RPO6 DEVICE TYPE FLAG
1979 010240 010077 172032 MOV R0,2#RPO6 :SET UP UNIT ADDRESSING
1980 010244 022777 024022 172052 CMP #24022,2#RPO6 :IS IT A DUAL PORT RPO6 ?
1981 010252 001405 BEQ 25 :YES...SET THE FLAG
1982 010254 022777 020022 172042 CMP #20022,2#RPO6 :IS IT A SINGLE PORT RPO6 ?
1983 010262 001401 BEQ 25 :YES...SET FLAG
1984 010264 000404 BR 35 :DON'T SET FLAG - CHECK FOR RPO4
1985 010266 012737 177777 004744 25: MOV #-1,2#RPO6 :SET THE FLAG
1986 010274 000416 BR 95 :DON'T CHECK FOR RPO4, IT WAS RPO6
1987 *****
1988 010276 005037 004746 35: CLR 2#RPO5 :CLEAR MEMOREX RPO4 DEVICE FLAG
1989 010302 022777 024021 172014 CMP #24021,2#RPO5 :IS IT A DUAL PORT MEMOREX RPO4 ?
1990 010310 001405 BEQ 75 :YES...SET THE FLAG FOR ADDR PLUG TESTS
1991 010312 022777 020021 172004 CMP #20021,2#RPO5 :IS IT A SINGLE PORT MEMOREX RPO4 ?
1992 010320 001401 BEQ 75 :YES...SET THE FLAG FOR ADDR PLUG TESTS
1993 010322 000403 BR 85 :DON'T SET FLAG - NOT MEMOREX DRIVE
1994 010324 012737 177777 004746 75: MOV #-1,2#RPO5 :SET THE FLAG
1995 010332 85: :ASSUME THE NEXT UNIT IS AN RPO4
1996 *****
1997 010332 116037 004656 004740 MOVB ATABLE,R0,3#ATTENT :SET APPROPRIATE ATTENTION BIT
1998 010340 104400 010346 TYPE 695 :TYPE ASCIZ STRING
1999 010344 000414 BR 685 :GET OVER THE ASCIZ
2000 685: .ASCIZ (15)(12) TESTING DRIVE NUMBER/
2001 010376 013746 004716 685: MOV 2#UNIT,-(SP) :UNIT NO. TO STACK
2002 010402 104404 TYPDS :TYPE DRIVE NO.
2003 010404 104400 010412 TYPE 715 :TYPE ASCIZ STRING
2004 010410 000410 BR 705 :GET OVER THE ASCIZ
2005 705: .ASCIZ (15)(12) SERIAL NO. = /
2006 010432 017746 171670 705: MOV 2#RHSN,-(SP) :SAVE 2#RHSN FOR TYPEOUT
2007 010436 104401 TYPDC :GO TYPE--OCTAL ASCII(ALL DIGITS)
2008 010440 104400 010446 TYPE 735 :TYPE ASCIZ STRING
2009 010444 000410 BR 725 :GET OVER THE ASCIZ
2010 725: .ASCIZ (15)(12) DRIVE TYPE = /
2011 010466 017746 171632 725: MOV 2#RMDT,-(SP) :SAVE 2#RMDT FOR TYPEOUT
2012 010472 104401 TYPDC :GO TYPE--OCTAL ASCII(ALL DIGITS)

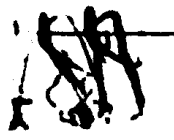
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009

MAINDEC-11-DRII-A. RPO4 5 6 FUNCT. CONT. TST-PT 1 MACY11 27(655) 30-MAR-76 22:59 PAGE 46  
SERIAL.PII TYPE SERIAL NUMBER AND DRIVE TYPE

SEG 0106

:995



:TYPE OUT THE DRIVE TYPE IN ASCII

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199:
199:
199:
0000:
0001: 010474 022777 024020 171622      :*****
0002: 010502 001425      CMP      #24020,DRHDT  :DUAL PORT RPO4 ?
0003: 010504 022777 020020 171612      BEQ      4$      :TYPE ASCII MSG OUT
0004: 010512 001421      CMP      #20020,DRHDT  :SINGLE PORT RPO4 ?
0005: 010514 022777 024021 171602      BEQ      4$      :TYPE THE MESSAGE
0006: 010522 001453      CMP      #24021,DRHDT  :DUAL PORT RPO5 ?
0007: 010524 022777 020021 171572      BEQ      6$      :TYPE THE MESSAGE
0008: 010532 001447      CMP      #20021,DRHDT  :SINGLE PORT RPO5 ?
0009: 010534 022777 024022 171562      BEQ      6$      :TYPE THE MESSAGE
0010: 010542 001424      CMP      #24022,DRHDT  :DUAL PORT RPO6 ?
0011: 010544 022777 020022 171552      BEQ      5$      :TYPE THE MESSAGE
0012: 010552 001420      CMP      #20022,DRHDT  :SINGLE PORT RPO6 ?
0013: 010554 000454      BEQ      5$      :TYPE THE MESSAGE
0014: 010556 000413      BR       1$      :DRIVE IS NOT AN RPO4/RPO5/RPO6
0015: 010556 104400 010564      :DON'T TYPE ASCII MESSAGE OUT
0016: 010556 000413      :
0017: 010556 104400 010564      :
0018: 010556 000413      :
0019: 010556 000413      :
0020: 010556 000413      :
0021: 010556 000413      :
0022: 010556 000413      :
0023: 010556 000413      :
0024: 010556 000413      :
0025: 010612 000435      :
0026: 010612 000435      :
0027: 010614 104400 010622      :
0028: 010614 000413      :
0029: 010620 000413      :
0030: 010650 000416      :
0031: 010650 000416      :
0032: 010652 104400 010660      :
0033: 010652 000413      :
0034: 010656 000413      :
0035: 010656 000413      :
0036: 010706      :
0037: 010706      :
0038: 010706      :
0039: 010706      :
0040: 010706      :
0041: 010706      :
0042: 010706      :
0043: 010706 005777 171414      1$:  TST      DRHSN      :READ SERIAL NO. AND DRIVE TYPE
0044: 010712 005777 171406      TST      DRHDT      :THESE TWO ARE TO HELP SCOPE LOOPS
0045: 010716 017737 171404 002410      MOV      DRHSN,DRSN  :SAVE TO CHECK IF DRIVE CLEAR CLEARS ANY BITS
0046: 010724 017737 171374 002406      MOV      DRHDT,DRDT  :SAVE TO CHECK IF DRIVE CLEAR CLEARS ANY BITS

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010732 000004  
010734 012737 000005 004604  
010742 004737 041366  
010746 032713 010000  
010752 001151  
010754 104400 010762  
010760 000420  
  
011022  
011022 104400 011030  
011026 000424  
  
011100  
011100 104400 011106  
011104 000431  
  
011170  
011170 032713 010000  
011174 001775  
011176 104400 011204  
011202 000435  
  
011272

```
*****  
: *TEST 5 CHECK MOL TO BE HIGH  
:  
: * MAKE SURE THAT DRIVE IS ON LINE BEFORE STARTING PROGRAM  
: * IF DRIVE IS OFFLINE, THEN AFTER TYPE OUT THE PROGRAM WILL  
: * HANG FOR EVER WAITING FOR DRIVE TO GO ON LINE.  
:  
*****  
TST5: SCOPE  
MOV #6-1, J#TSTNM ; THIS SAVES TEST NUMBER  
JSR PC, J#CLDISK ; GIVE INITIALIZE  
BIT #MOL, J#R3 ; CHECK MOL IN RHDS1  
BNE TST6 ; BRANCH IF MOL HIGH  
TYPE ,65$ ; TYPE ASCIZ STRING  
BR ,64$ ; GET OVER THE ASCIZ  
:65$: .ASCIZ '<15><12>DRIVE IS OFFLINE - MOL IS LOW'  
:64$:  
TYPE ,67$ ; TYPE ASCIZ STRING  
BR ,66$ ; GET OVER THE ASCIZ  
:67$: .ASCIZ '<15><12>HIT START ON DRIVE TO GET IT ON LINE'  
:66$:  
TYPE ,69$ ; TYPE ASCIZ STRING  
BR ,69$ ; GET OVER THE ASCIZ  
:69$: .ASCIZ '<15><12>PROGRAM WILL HANG TESTING MOL TILL MOL IS HIGH'  
:68$:  
BIT #MOL, J#R3 ; CHECK MOL IN RHDS1  
BEQ ,15 ; BRANCH IF MOL IS HIGH  
TYPE ,71$ ; TYPE ASCIZ STRING  
BR ,70$ ; GET OVER THE ASCIZ  
:71$: .ASCIZ '<15><12>GOOD - MOL IS NOW HIGH. PROGRAM WILL NOW BE EXECUTED ,15 ,12'  
:70$:
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\*\*\*\*\*  
:TEST 5 PROGRAM INTERRUPT  
:  
: PROGRAM INTERRUPT IS TESTED BY SETTING RDY AND IE  
: IN RHCS1 AT THE SAME TIME  
: THIS SHOULD INTERRUPT THROUGH LOCATION 254  
: THE PROCESSOR PRIORITY IS SET TO 4  
:\*\*\*\*\*

```

TST6: SCOPE
011276 000004
011300 012737 000006 004604      MOV    #7-1, @TSTNM      ; THIS SAVES TEST NUMBER
011306 012706 001000              MOV    #STACK, SP       ; RESET STACK
011312 004737 041366              JSP   PC, @CLDISK      ; SET R1-RHCS1, R2-RHCS2
                                ; R3-RHDS1, R4-RHER1
                                ; GIVE RH-11 INITIALIZE
                                ; SETUP UNIT NUMBER
011316 013700 002266              MOV    @RPVEC, RD       ; GET RP VECTOR ADDRESS
011322 012720 011370              MOV    @RPTRP1, (RD)+   ; THIS IS FOR TIMELY INTERRUPTS
011326 012710 000340              MOV    #340, (RD)       ; RPO4 INTERRUPT SERVICE ROUTINE
                                ; PRIORITY = 7
011332 012737 000200 177776      MOV    #200, PS         ; SET PROCESSOR PRIORITY @ 4 (DISK @ 5)
011340 012711 000300              MOV    #RDY! IE, @R1    ; RDY, IE IN RHCS1 SHOULD CAUSE INTERRUPT
011344 013737 042064 001200      MOV    @TIMCNT, @STMP1 ; COUNTER
011352 005337 001200      IS:   DEC    @STMP1      ; WAIT FOR INTERRUPT
011356 001375              BNE   IS                ; BRANCH IF NOT ZERO
                                ; BEFORE THIS IS ZERO INTERRUPT SHOULD OCCUR
011360 104065              ERROR 65                ; INTERRUPT DID NOT OCCUR
011362 012712 000040              MOV    #40, @R2        ; CLEAR CONTROLLER VIA CS2 CLR BIT
011366 000407              BR    TST7             ; BRANCH TO NEXT TEST -----
011370 022626              RPTRP1: CMP    (SP)+, (SP)+  ; RESTORE STACK
011372 022711 004200              CMP    #DVA! RDY, @R1  ; IE SHOULD BE LOW AS RUPT OCCURRED
011376 001403              BEQ   TST7             ; CONTINUE IF GOOD -----
011400 104065              ERROR 65                ; INTERRUPT OCCURED BUT
                                ; IE FAILED TO RESET
011402 012712 000040              MOV    #40, @R2        ; CLEAR CONTROLLER VIA CS2 CLR BIT

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```

*****
*TEST 7          INTERRUPT AT PROCESSOR AND DISK PRIORITY SAME
*****
*      PROCESSOR PRIORITY IS SET AT 5 (SAME AS THE DISK)
*      IE AND RDY IS SET. THIS SHOULD NOT ALLOW INTERRUPT
*****
TST7:  SCOPE
011406 0000C4
011410 012737 000007 004604      MOV      #10-1, @TSTNM      ;THIS SAVES TEST NUMBER
011416 012706 001000              MOV      #STACK, SP        ;RESET STACK
011422 004737 041366              JSR      PC, @CLDISK       ;SET R1-RHCS1, R2-RHCS2
                                ;R3-RHDS1, R4-RHER1
                                ;GIVE RH-11 INITIALIZE
                                ;SETUP UNIT NUMBER
011426 013700 002266              MOV      @RPVEC, R0        ;GET RP VECTOR ADDRESS
011432 012720 011476              MOV      #RTRP2, (R0)+    ;THIS IS FOR UNTIMELY INTERRUPTS
011436 012710 000340              MOV      #340, (R0)       ;RPO4 INTERRUPT SERVICE ROUTINE
                                ;PRIORITY = 7
011442 012737 000240 177776      MOV      #240, PS         ;SET PROCESSOR PRIORITY @ 5
011450 012711 000300              MOV      #RDY!IE, @R1     ;RDY, IE IN RHCS1 WOULD CAUSE INTERRUPT
011454 013737 042064 001200      MOV      @TIMCNT, @STMP1  ;COUNTER
011462 005337 001200      15:    DEC      @STMP1         ;WAIT FOR INTERRUPT
011466 00137E                    BNE      15               ;BRANCH IF NOT ZERO
                                ;BEFORE THIS IS ZERO INTERRUPT WOULD
                                ;OCCUR
011470 012712 000040              MOV      #40, @R2        ;CLEAR THE CONTROLLER VIA CS2 CLR BIT
011474 000404                    BR       TST10           ;NO INTERRUPT SO CONTINUE -----)
011476 022626                    RTRP2: CMP      (SP)+, (SP)+ ;RESTORE STACK
011500 104065                    ERROR  65               ;INTERRUPT OCCURRED WITH PROCESSOR
                                ;PROCESSOR STATUS SAME AS DISK
011502 012712 000040              MOV      #40, @R2        ;CLEAR THE CONTROLLER VIA CS2 CLR BIT

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011506 000004  
011510 012706 001000  
011514 012737 000010 004604  
011522 004737 041366  
011526 012737 000000 177776  
011534 005737 004724  
011540 001007  
011542 005737 000042  
011546 001004  
011550 005737 001100  
011554 001001  
011556 000402  
011560  
011560 000137 012266  
011564  
011564 104400 011572  
011570 000407

```
*****  
;TEST 10 PACK ACKNOWLEDGE  
  
; IF STARTING ADDRESS 220 IS USED THIS TEST WILL NOT BE PERFORMED  
;  
; IF THE PROGRAM WORKS UNDER ACT-11 MONITOR  
; THEN THIS TEST IS NOT PERFORMED  
;  
; IF NO ACT-11 MONITOR IS PRESENT  
; THEN THIS TEST IS PERFORMED ONLY ON THE FIRST PASS  
; ON SUBSEQUENT PASSES THIS TEST IS NOT DONE  
;  
; THIS TESTS THE ACKNOWLEDGE COMMAND=44  
; VV BIT - RHDS1 BIT #6  
; MOL BIT - RHDS1 BIT #12  
; DVA BIT - RHCS1 BIT #11  
; THE DRIVE IS STOPED MOL IS CHECKED TO BE 0  
; AND DVA SHOULD BE 0  
; THE DRIVE IS POWERED UP  
; VV SHOULD BE 0, MOL SHOULD BE 1, DVA SHOULD BE 1  
; GO SHOULD BE 0  
; ALL REGISTERS EXCEPT RHDB, RHLA AND RHCC ARE STORED  
; PACK ACKNOWLEDGE IS ISSUED  
; ALL STORED REGISTERS SHOULD BE UNCHANGED  
; EXCEPT VV  
  
*****  
TST10: SCOPE  
MOV #STACK,SP ;RESET STACK  
MOV #10,2#TSTNM ;SAVE TEST NUMBER  
JSR PC,2#CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETUP UNIT NUMBER  
MOV #0,PS ;SET PROCESSER STATUS TO 0  
  
;*THIS CODE CHECKS TO SEE IF MANUAL INTERVENTION TESTS ARE OK  
TST 2#NOPUSH ;IS THIS A 220 START ?  
BNE 1$ ;SKIP THIS TEST IF SO  
TST 2#42 ;MONITOR (ACT 11) RETURN ADDRESS  
BNE 1$ ;SKIP THIS TEST  
TST 2#$PASS ;FIRST PASS ?  
BNE 1$ ;SKIP THIS TEST IF NOT  
BR 2$ ;CONTINUE WITH THIS TEST  
  
1$: JMP TST11 ; JUMP TO NEXT TEST -----)  
2$:  
TYPE ,65$ ;:TYPE ASCIZ STRING  
BR ,64$ ;:GET OVER THE ASCIZ  
;:65$: .ASCIZ (<15><12>/STOP DRIVE
```



```

2218 011610          64$:
2219 011610 013746 004716      MOV      Q#UNIT,-(SP)      ;GET UNIT UNDER TEST
2220 011614 104404          TYPDS
2221 011616 104400 011624      TYPE      67$              ;;TYPE ASCIZ STRING
2222 011622 000413          BR        66$              ;;GET OVER THE ASCIZ
2223          ;;67$: .ASCIZ / THEN HIT CONTINUE<<15><12>
2224          66$:
2225 011652 000000          HALT
2226 011654 032713 010000      BIT      #MOL,QR3         ;MOL IN RHDS1 SHOULD BE = 0
2227 011660 001403          BEQ      3$              ;BRANCH IF MOL=0
2228 011662 010337 001122      MOV      R3,Q#SBDADR     ;FAILING REGISTER ADDRESS-RHDS1
2229 011656 104010          ERROR    10              ;ON SPINPLE POWERED DOWN
2230          ;MOL SHOULD BE 0
2231 011670 013746 004716      3$:  MOV      Q#UNIT,-(SP)   ;UNIT NUMBER
2232 011674 052716 000100      BIS      #IR,(SP)        ;INCLUDE IR
2233 011700 022612          CMP      (SP)+,QR2       ;ONLY UNIT NO. AND IR SHOJLD BE
2234          ;HIGH IN RHCS2
2235 011702 001403          BEQ      4$              ;BRANCH IF RHCS2 GOOD
2236 011704 010237 001122      MOV      R2,Q#SBDADR     ;FAILING REGISTER ADDRESS-RHCS2
2237 011710 104011          ERROR    11              ;WITH SPINDLE POWERED DOWN
2238          ;ONLY UNIT NO. AND IR SHOULD BE
2239          ;HIGH
2240
2241 011712          4$:
2242
2243 011712 004737 041366      JSR      PC,Q#CLDISK     ;SET R1-RHCS1, R2-RHCS2
2244          ;R3-RHDS1, R4-RHER1
2245          ;GIVE RH-11 INITIALIZE
2246          ;SETUP UNIT NUMBER
2247 011716 104400 011724      TYPE      69$              ;;TYPE ASCIZ STRING
2248 011722 000407          BR        68$              ;;GET OVER THE ASCIZ
2249          ;;69$: .ASCIZ <15><12>. START DRIVE/
2250          68$:
2251 011742 013746 004716      MOV      Q#UNIT,-(SP)   ;GET UNIT UNDER TEST
2252 011746 104404          TYPDS
2253 011750 104400 011756      TYPE      71$              ;;TYPE ASCIZ STRING
2254 011754 000420          BR        70$              ;;GET OVER THE ASCIZ
2255          ;;71$: .ASCIZ / AFTER HEAD LOAD HIT CONTINUE<<15><12>
2256          70$:
2257 012016 000000          HALT
2258 012020 032713 010000      BIT      #MOL,QR3         ;MOL IN RHDS1 SHOULD BE = 1
2259 012024 001411          BEQ      5$              ;BRANCH IF MOL = 0
2260 012026 032713 000400      BIT      #DPR,QR3        ;DPR IN RHDS1 SHOULD BE = 1
2261 012032 001406          BEQ      5$              ;BRANCH IF DPR = 0
2262 012034 032713 000200      BIT      #DRY,QR3        ;DRY IN RHDS1 SHOULD BE = 1
2263 012040 001403          BEQ      5$              ;BRANCH IF DRY =0
2264 012042 032713 000100      BIT      #VV,QR3         ;VV IN RHDS1 SHOULD BE = 0
2265 012046 001403          BEQ      6$              ;BRANCH IF VV = 0 (GOOD)
2266 012050 010337 001122      5$:  MOV      R3,Q#SBDADR     ;FAILING REGISTER ADDRESS - RHDS1
2267 012054 104012          ERROR    12              ;WITH SPINDLE POWERED UP
2268          ;RHDS1 SHOULD HAVE VV = 0, MOL = :
2269
2270 012056 011100          6$:  MOV      QR1,R0           ;GET RHCS1 CONTENTS
2271 012060 042700 160076      BIC      #SC!TRE!MCPE!76,R0 ;CLEAR SC,TRE,MCPE AND

```



```

2326 ;WITH INTERRUPT DISABLED
2327
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2330 012210 104412 WAT ;WAIT FOR VV BIT TO SET
2331 012212 002322 RHDS1 ;WAIT FOR RHDS1 REGISTER
2332 012214 000100 VV ;WAIT FOR VV BIT IN RHDS1 REGISTER
2333 012216 000001 1. ;ALLOW 10 MICRO SECONDS
2334 012220 000001 1. ;VV MUST SET BETWEEN
2335 ;00 AND 20 MICRO SECONDS
2336
2337
2338 012222 004037 042246 JSR RO,2#CHREG ;CHANGE BITS IN SAVED REGISTER
2339 012226 002322 RHDS1 ;CHANGE RHDS1 REGISTER
2340
2341 012230 000001 1 ;1 BIT/BITS TO BE CHANGED
2342 012232 000001 1 ;NEW VALUE OF VV IS 1
2343 012234 000100 VV ;CHANGE VV BIT
2344
2345 ;*NOW COMPARE REGISTERS SO THAT NO REGISTERS
2346 ;*CHANGED EXCEPT VV IN RHDS1 AND IE IN RHCS1
2347
2348
2349 012236 004037 042354 JSR RO,2#COMREG ;COMPARE SAVED REGISTERS WITH
2350 ;PRESENT VALUE
2351 012242 004612 SAVERE ;GOOD DATA SAVED IN 'SAVERE'
2352 012244 002354 WC ;TEST DATA STARTING FROM 'RHWC'
2353 012246 000022 18. ;18. REGISTERS TO BE COMPARED
2354 012250 012254 11$ ;RETURN TO 11$ ON ERROR
2355 012252 012260 12$ ;RETURN TO 12$ ON NO ERROR
2356
2357 012254 104015 11$: ERROR 15 ;GIVING A PACK ACKNOWLEDGE
2358 012256 000207 RTS PC ;CAUSED AN ERROR
2359 ;PACK ACKNOWLEDGE SHOULD
2360 ;SET VV IN RHDS1
2361 ;INTERRUPT SHOULD MAKE
2362 ;IE = 0
2363 ;NO OTHER REGISTERS SHOULD
2364 ;CHANGE
2365 ;GOOD DATA GIVES
2366 ;CONTENTS OF REGISTER BEFORE
2367 ;PACK ACKNOWLEDGE
2368 ;RECEIVED DATA GIVES
2369 ;CONTENTS OF REGISTER
2370 ;AFTER PACK ACKNOWLEDGE
2371
2372 012260 012737 177777 046760 12$: MOV #-1,2#PRITEM ;CLEAR PREVIOUS ITEM NUMBER
2373

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012256 000004  
  
012270 000005  
012272 004737 045352  
  
012276 012706 001000  
012302 012737 000011 004604  
  
012310 004737 041366  
  
012314 004737 041424  
  
012320 104400 066402  
012324 000000  
012326 013777 002460 167744  
  
  
  
  
012334 004037 041534  
012340 002272  
012342 004612  
  
012344 000022  
  
012346 013777 004606 167712  
  
  
  
  
012354 013746 002460  
012360 052716 000001

```
*****
*TEST 11      SET VV BIT #6 IN RHDS1
*            THIS TEST SETS VV IN RHDS1 INCASE
*            ACT-11 MONITOR IS PRESENT AND THE PREVIOUS TEST
*            IS NOT PERFORMED
*            THERE IS A RESET AT THE BEGINING OF THIS TEST
*            FOR ERROR RECOVERY ONLY.
```

```
*****
†ST11: SCOPE
```

```
;*IN CASE THERE IS ANY DRIVE ERRORS DURING POWER UP
;*OR POWER DOWN OR ANY PARITY ERRORS A RESET IS GIVEN
```

```
RESET
JSR    PC, @#STKINT    ;INITILIZE TK
```

```
MOV    #STACK, SP    ;RESET STACK
MOV    #11, @#STNM    ;SAVE TEST NUMBER
```

```
JSR    PC, @#CLDISK  ;SET R1-RHCS1, R2-RHCS2
                        ;R3-RHDS1, R4-RHER1
                        ;GIVE RH-11 INITIALIZE
                        ;SETUP UNIT NUMBER
JSR    PC, @#CHECK    ;CHECK THAT DVA, RDY, MOL, DPR, DRY = 1
                        ;AND THAT NO STATUS BITS ARE STUCK = 1
                        ;CANNOT CONTINUE TESTS IF THEY AREN'T
                        ;STOP
MOV    @#PKACK, @RHCS1 ;GET READY FOR PKACK
                        ;PACK ACKNOWLEDGE WITH 22 IN RHCS1
```

```
;*NOW SAVE REGISTERS FOR COMPARISON AFTER PACK ACKNOWLEDGE
```

```
JSR    RO, @#SAVER    ;SAVE REGISTERS
RHWG    ;RHWG IS THE FIRST REGISTER SAVED
SAVERE  ;STARTING ADDRESS OF WHERE
        ;THE REGISTERS ARE SAVED
        ;NUMBER OF REGISTERS
        ;SAVED = 18.
```

```
MOV    @#RP4VEC, @RPVEC ;SET RPO4 VECTOR ADDRESS
                        ;TO 'TIME1' IF P-CLOCK IS PRESENT
                        ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
                        ;'TIME' WILL ONLY SAVE
                        ;CURRENT CYLINDER ADDRESS
                        ;AND LOOK AHEAD REGISTERS
```

```
MOV    @#PKACK, -(SP) ;GET READY TO MOVE COMMAND
BIS    #GO, (SP)      ;GET READY TO SET GO
```







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*****
*TEST 14 ADDRESS PLUG CHANGE ERROR
*
* CHECK PROPER ADDRESS PLUG FUNCTIONALITY
* BY PULLING THE ADDRESS PLUG DURING A COMMAND ISSUED
* IN DIAGNOSTIC MODE (TO GUARANTEE COMMAND IS STILL ACTIVE WHEN
* PLUG IS PULLED) AND VERIFYING THAT THE DRIVE GOES OFF LINE
*
* THE ADDRESS PLUG IS THEN REPLACED AND RETURN OF THE PROPER
* RESPONDING DRIVE IS VERIFIED, AS WELL AS THE FACT THAT
* ATTENTION BITS COME UP IN THE PROPER BIT LOCATION(????), AND THE
* ADDRESS CHANGE ERROR (ACE) - RHER3 BIT #13 IS SET, AS WELL AS
* SC, TRE, ERR, AND MCPE; VV IS RESET AND RHCC = 000 (DRIVE RECALIBRATED).
*
* IN ADDITION VERIFICATION IS ALSO MADE THAT NO OTHER DRIVE
* NUMBERS APPEAR ON THE BUSS AFTER THE PLUG IS REPLACED. (?????)

```

```

012644 000004
012646 012737 000001 001212
012654 005737 004744
012660 001005
012662 005737 004746
012666 001002
012670 000137 013656
012674
012674 012706 001000
012700 012737 000014 004604
012706 004737 041366
012712 004737 041446
012716 104400 066402
012722 000000
012724 005737 004724
012730 001007
012732 005737 000042
012736 001004
012740 005737 001100

```

```

*****
TST14: SCOPE
MOV #1,STIMES ;;DO 1 ITERATION
*****
TST @RPO6 ;TEST FOR RPO6 DRIVE
BNE #5 ;IF = 1, DO THIS TEST
TST @RPOS ;TEST FOR MEMOREX RPO4
BNE #5 ;IF = 1, DO THIS TEST
;IF NEITHER FLAG IS UP, ASSUME THE
;DRIVE IS AN ISS RPO4 AND SKIP TEST
JMP TST15 ; JUMP TO NEXT TEST -----)
+5:
*****
MOV #STACK,SP ;RESET STACK
MOV #14,@+STNM ;SAVE TEST NUMBER
JSR PC,@CLDISK ;SET R1-RHCS1, R2-RHCS2
;R3-RHDS1, R4-RHER1
;GIVE RH-11 INITIALIZE
;SETUP UNIT NUMBER
JSR PC,@CHECKT ;CHECK DVA,ROY,MOL,DPR,DRY,VV = 1
;AND THAT NO STATUS BITS ARE STUCK = 1
TYPE ,CPHALT ;CANNOT CONTINUE TESTING IF ANY OF
;THE FIRST SET OF BITS DON'T = 1
HALT ;STOP
;*THIS CODE CHECKS TO SEE IF MANUAL INTERVENTION TESTS ARE OK
TST @NOPUSH ;IS THIS A 220 START ?
BNE #5 ;SKIP THIS TEST IF SO
TST @#42 ;MONITOR (ACT 11) RETURN ADDRESS ?
BNE #5 ;SKIP THIS TEST
TST @SPASS ;FIRST PASS ?

```



```

2604 012744 001001      BNE 15      :SKIP THIS TEST IF NOT
2605 012746 000402      BR 25      :CONTINUE WITH THIS TEST
2606
2607 012750              15:
2608 012750 000137 013656  JMP TST15 ; JUMP TO NEXT TEST -----
2609 012754              25:
2610
2611      :*SET DIAGNOSTIC MODE TO ENABLE A COMMAND ACTIVE WHILE
2612      :*THE PLUG IS PULLED
2613
2614 012754 052777 000001 167336  BIS 0DMD,0RHMR ;SET UP DIAGNOSTIC MODE
2615
2616      :*TAKE AN INITIAL REGISTER SNAPSHOT
2617
2618 012762 004037 041534  JSR RC,0SAVER ;SAVE REGISTERS
2619 012766 002272      RHWC ;RHWC IS THE FIRST REGISTER SAVED
2620 012770 004612      SAVERE ;STARTING ADDRESS OF WHERE
2621      ;THE REGISTERS ARE SAVED
2622 012772 000022      18. ;NUMBER OF REGISTERS
2623      ;SAVED = 18.
2624
2625
2626      :*ISSUE A COMMAND AND THE 'GO' BIT (NOT POSITIONING COMMAND)
2627      :*TO VERIFY COMMAND ABORT IF PLUG IS PULLED
2628
2629
2630
2631
2632      :*ISSUE SOME CLOCKS TO GET THE COMMAND STARTED
2633      :*USE "SEARCH" WITH "DTETST" FLAG UP TO STOP CLOCKING ?
2634
2635
2636 012774 104400 013002      TYPE 655 ;:TYPE ASCIZ STRING
2637 013000 000423      BR 645 ;:GET OVER THE ASCIZ
2638      ;:655: .ASCIZ <15><12>'REMOVE ADDRESS PLUG ON DRIVE/'
2639      ;:645:
2640 013042 013746 004716      MOV 0UNIT,-(SP) ;:GET THE UNIT NO. UNDER TEST
2641 013046 104404      TYPDS ;:TYPE IT OUT
2642 013050 104400 013056      TYPE 675 ;:TYPE ASCIZ STRING
2643 013054 000415      BR 665 ;:GET OVER THE ASCIZ
2644      ;:675: .ASCIZ / THEN PRESS CONTINUE/
2645      ;:665:
2646 013110              HALT ;:WAIT FOR OPERATOR PLUG CHANGE
2647 013110 000000
2648
2649      ;*CHECK THAT THE UNIT NO. UNDER TEST HAS GONE OFFLINE
2650
2651 013112 017700 167166      MOV 0RH0ST,RO ;:ATTEMPT TO ADDRESS THE DRIVE
2652 013116 004737 043300      JSR PC,0PLTREG ;:TAKE REGISTER SNAPSHOTS
2653 013122 032737 010000 002360      BIT 0NED,00CS2 ;:TEST FOR NON EXISTENT DRIVE
2654 013130 001001      BNE 75 ;:CONTINUE IF 'NED' BIT SET (UNIT
2655      ;:IS OFFLINE AS IT SHOULD BE)
2656 013132 104101      ERROR 101 ;:UNIT DID NOT GO OFFLINE WHEN ADDRESS
2657 013134              ;:PLUG WAS REMOVED

```



```

013366 040000 ERR :CHANGE ERR BIT
013370 000000 0 :NEW VALUE OF VV IS 0
013372 000100 VV :CHANGE VV BIT

013374 004037 042246 JSR RC,2#CHREG :CHANGE BITS IN SAVED REGISTER
013400 002276 RHCS2 :CHANGE RHCS2 REGISTER

013402 000001 1 :1 BIT/BITS TO BE CHANGED
013404 000001 1 :NEW VALUE OF NED IS 1
013406 010000 NED :CHANGE NED BIT

013410 004037 042246 JSR RC,2#CHREG :CHANGE BITS IN SAVED REGISTER
013414 002314 RHCR3 :CHANGE RHCR3 REGISTER

013416 000001 1 :1 BIT/BITS TO BE CHANGED
013420 000001 1 :NEW VALUE OF ACE IS 1
013422 020000 ACE :CHANGE ACE BIT

013424 053737 004740 004636 BIS 2#ATTENT,2#SAVERE+24 :SET UNIT UNDER TEST ATTENTION
013432 005037 004654 CLR 2#SAVERE+42 :SET RHCC REGISTER IMAGE TO ALL 0'S

:*TAKE A NEW REGISTER SNAPSHOT AND
:*COMPARE THE REGISTER CONTENTS WITH EXPECTED VALUES

013436 004037 042354 JSR RC,2#COMREG :COMPARE SAVED REGISTERS WITH
:PRESENT VALUE
013442 004612 SAVERE :GOOD DATA SAVED IN 'SAVERE'
013444 002354 WC :TEST DATA STARTING FROM 'RHWC'
013446 000022 18. :18. REGISTERS TO BE COMPARED
013450 013454 58. :RETURN TO 58 ON ERROR
013452 013460 68. :RETURN TO 68 ON NO ERROR

013454 104100 58: ERROR 10J :ADDRESS PLUG CHANGE CAUSED SOME
:REGISTER ERROR
013456 000207 RTS PC :GO BACK AND CHECK THE NEXT REGISTER
:UNTIL ALL 18. HAVE BEEN DONE

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; *NOW CLEAR OUT THE CONTROLLER AND DRIVE
013460          SS:
013460 004737 042366 JSR    PC,2#CLDISK ;SET R1-RHCS1, R2-RHCS2
;R3-RHDS1, R4-RHER1
;GIVE RH-11 INITIALIZE
;SETUP UNIT NUMBER

; *CHANGE THE REGISTER SNAPSHOT TO EXPECTED VALUES AFTER THE CLEAR
013464 004037 042246 JSR    RO,2#CHREG ;CHANGE BITS IN SAVED REGISTER
013470 002300 RHCS1 ;CHANGE RHCS1 REGISTER
3 ;3 BIT/BITS TO BE CHANGED
0 ;NEW VALUE OF SC IS 0
SC ;CHANGE SC BIT
0 ;NEW VALUE OF TRE IS 0
TRE ;CHANGE TRE BIT
0 ;NEW VALUE OF MCPE IS 0
MCPE ;CHANGE MCPE BIT
013510 004037 042246 JSR    RO,2#CHREG ;CHANGE BITS IN SAVED REGISTER
013514 002322 RHDS1 ;CHANGE RHDS1 REGISTER
2 ;2 BIT/BITS TO BE CHANGED
0 ;NEW VALUE OF ATA IS 0
ATA ;CHANGE ATA BIT
0 ;NEW VALUE OF ERR IS 0
ERR ;CHANGE ERR BIT
013530 004037 042246 JSR    RO,2#CHREG ;CHANGE BITS IN SAVED REGISTER
013534 002276 RHCS2 ;CHANGE RHCS2 REGISTER
1 ;1 BIT/BITS TO BE CHANGED
0 ;NEW VALUE OF NED IS 0
NED ;CHANGE NED BIT
013544 004037 042246 JSR    RO,2#CHREG ;CHANGE BITS IN SAVED REGISTER
013550 002314 RHER3 ;CHANGE RHER3 REGISTER
1 ;1 BIT/BITS TO BE CHANGED
0 ;NEW VALUE OF ACE IS 0
ACE ;CHANGE ACE BIT
013560 004037 042246 JSR    RO,2#CHREG ;CHANGE BITS IN SAVED REGISTER
013564 002320 RHMR ;CHANGE RHMR REGISTER
1 ;1 BIT/BITS TO BE CHANGED
0 ;NEW VALUE OF DMD IS 0
DMD ;CHANGE DMD BIT

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2804 013574 043737 004740 004636 BIC @#ATTENT,@#SAVERE+24 ;UNIT UNDER TEST ATTENTION BIT
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2811 013602 004037 042354 JSR RO,@#COMREG ;COMPARE SAVED REGISTERS WITH
2812 ;PRESENT VALUE
2813 013606 004612 SAVERE ;GOOD DATA SAVED IN 'SAVERE'
2814 013610 002354 WC ;TEST DATA STARTING FROM 'RHWC'
2815 013612 000022 18. ;18. REGISTERS TO BE COMPARED
2816 013614 013620 10$ ;RETURN TO 10$ ON ERROR
2817 013616 013624 11$ ;RETURN TO 11$ ON NO ERROR
2818
2819 013620 104100 10$: ERROR 100 ;ADDRESS PLUG CHANGE CAUSED SOME
2820 013622 000207 RTS PC ;INCORRECT REGISTER RESULT
2821
2822 013624 11$:
2823
2824
2825 ;*(USE NED METHOD TO VERIFY
2826 ;*THAT ATTENTION BIT COMES UP IN THE PROPER LOCATION ??)
2827
2828
2829
2830
2831 ;*SET 'VV' IN RHDS1 AFTER RESET FROM THE RECALIBRATE
2832 ;*CAUSED BY PULLING THE PLUG
2833
2834 013624 013746 002460 MOV @#PKACK,-(SP) ;GET READY TO MOVE COMMAND
2835 013630 052716 000001 BIS #GO,(SP) ;GET READY TO SET GO
2836 ;WITHOUT INTERRUPT ENABLE
2837 013634 012677 166440 MOV (SP)+,@RHCS1 ;GO WITH
2838 ;22 IN RHCS1 FOR PACK ACKNOWLEDGE
2839 ;WITH INTERRUPT DISABLED
2840
2841 013640 011100 MOV @R1,RO ;SAVE RHCS1 DURING ABOVE OPERATION
2842 013642 011305 MOV @R3,R5 ;SAVE RHDS1 DURING ABOVE OPERATION
2843
2844 013644 104412 WAT ;WAIT FOR VV BIT TO SET
2845 013646 002322 RHDS1 ;WAIT FOR RHDS1 REGISTER
2846 013650 000100 VV ;WAIT FOR VV BIT IN RHDS1 REGISTER
2847 013652 000001 1. ;ALLOW 10 MICRO SECONDS
2848 013654 000001 1. ;VV MUST SET BETWEEN
2849 ;00 AND 20 MICRO SECONDS
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013656 000004  
013660 012737 000001 001212  
  
013666 005737 004744  
013672 001005  
013674 005737 004746  
013700 001002  
  
013702 000137 014664  
013706  
  
013706 032777 000040 165224  
013714 001002  
013716 000137 014664  
013722  
  
013722 005737 004724  
013726 001007  
013730 005737 000042  
013734 001004  
013736 005737 001100  
013742 001001  
013744 000402  
  
013746  
013746 000137 014664  
013752  
013752 012706 001000  
013756 012737 000015 004604  
  
013764 004737 041366

```
*****  
*TEST 15 CHECK ALL ADDRESS PLUG ADDRESSES  
  
* CHECK THAT ALL ADDRESS PLUG NUMBERS FUNCTION PROPERLY  
  
* THIS TEST IS DONE FOR MEMOREX RPO4'S AND RPO6'S ONLY  
* THIS TEST IS SELECTED BY SW#5 AND THE DEFAULT IS NOT TO DO IT  
  
*****  
TST15: SCOPE  
MOV #1,STIMES ;:DO 1 ITERATION  
  
*****  
TST @#RPO6 ;TEST FOR RPO6 DRIVE  
BNE 4$ ;IF = 1, OK TO DO THIS TEST  
TST @#RPO5 ;TEST FOR MEMOREX RPO4  
BNE 4$ ;IF = 1, OK TO DO THIS TEST  
;NOT AN RPO6 OR MEMOREX RPO4 SO  
;ASSUME AN ISS RPO4 AND SKIP TEST  
JMP TST16 ; JUMP TO NEXT TEST -----)  
4$:  
*****  
;*CHECK TO SEE IF THIS TEST HAS BEEN SELECTED WITH SWS  
BIT #SWS,@SWR ;TEST THE SWITCH  
BNE 5$ ;IF 0, TEST HAS NOT BEEN SELECTED  
JMP TST16 ; JUMP TO NEXT TEST -----)  
5$:  
;TEST SELECTED, CONTINUE IT  
  
;*THIS CODE CHECKS TO SEE IF MANUAL INTERVENTION TESTS ARE OK  
TST @#NOPUSH ;IS THIS A 220 START ?  
BNE 1$ ;SKIP THIS TEST IF SO  
TST @#42 ;MONITOR (ACT 11) RETURN ADDRESS ?  
BNE 1$ ;SKIP THIS TEST  
TST @#$PASS ;FIRST PASS ?  
BNE 1$ ;SKIP THIS TEST IF NOT  
BR 2$ ;CONTINUE WITH THIS TEST  
  
1$:  
JMP TST16 ; JUMP TO NEXT TEST -----)  
2$:  
MOV #STACK,SP ;RESET STACK  
MOV #15,@TSTNM ;SAVE TEST NUMBER  
  
JSR PC,@#CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETUP UNIT NUMBER
```

# K10

```
2905 013770 004737 041446 JSR PC, @CHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1
2906 ;AND THAT NO STATUS BITS ARE STUCK = 1
2907 013774 104400 066402 TYPE .CPHALT ;CANNOT CONTINUE TESTING IF ANY OF
2908 ;THE FIRST SET OF BITS DON'T = 1
2909 014000 000000 HALT ;STOP
2910
2911
2912 014002 104400 014010 TYPE ,65$ ;;TYPE ASCIZ STRING
2913 014006 000426 BR ,64$ ;;GET OVER THE ASCIZ
2914 ;;65$: .ASCIZ <15><12>/ALL DISK DRIVES EXCEPTING THE UNIT UNDER/
2915 64$:
2916 014064 104400 014072 TYPE ,67$ ;;TYPE ASCIZ STRING
2917 014070 000417 BR ,66$ ;;GET OVER THE ASCIZ
2918 ;;67$: .ASCIZ <15><12>/TEST MUST BE POWERED DOWN/<15><12>
2919 66$:
2920
2921
2922
2923 014130 013737 004716 004752 MOV @UNIT, @INUNIT ;MAKE THE INITIAL UNIT NO. = "UNIT"
2924
```

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2930 014136 104400 014144
2931 014142 000420
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2933 014204
2934 014204 013746 004716
2935 014210 104404
2936 014212 104400 014220
2937 014216 000421
2938
2939 014262
2940 014262 104400 014270
2941 014266 000417
2942
2943 014326
2944 014326 000000
2945
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2948 014330 005037 046760
2949 014334 005237 004716
2950 014340 042737 177770 004716
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2952 014346 004737 041366
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2960 014352 017700 165726
2961 014356 004737 043300
2962 014362 032737 010000 002360
2963 014370 001423
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2965 014372 104102
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2967 014374 104400 014402
2968 014400 000407
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2970 014420
2971 014420 013746 004716
2972 014424 104404
2973 014426 104400 014434
2974 014432 000402
2975
2976 014440
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;*CHANGE ADDRESS PLUG ON THE UNIT UNDER TEST
6$:
TYPE 69$ ;;TYPE ASCIZ STRING
BR 68$ ;;GET OVER THE ASCIZ
69$: .ASCIZ <15><12>/REMOVE ADDRESS PLUG ON DRIVE/
68$:
MOV 2#UNIT,-(SP) ;GET THE UNIT UNDER TEST
TYPDS ;TYPE IT OUT
TYPE 71$ ;;TYPE ASCIZ STRING
BR 70$ ;;GET OVER THE ASCIZ
71$: .ASCIZ / REPLACE WITH NEXT HIGHER ADDRESS/
70$:
TYPE 73$ ;;TYPE ASCIZ STRING
BR 72$ ;;GET OVER THE ASCIZ
73$: .ASCIZ <15><12>/PLUG - THEN PRESS CONTINUE/
72$:
HALT
;*HOUSEKEEPING
CLR 2#PRITEM ;CLEAR THE PREVIOUS ERROR NUMBER
INC 2#UNIT ;ADD ONE TO THE UNIT NO.
BIC #1C7,2#UNIT ;TRUNCATE TO LOW ORDER 3 BITS
JSR PC,2#CLDISK ;SET R1-RHCS1, R2-RHCS2
;R3-RHDS1, R4-RHER1
;GIVE RH-11 INITIALIZE
;SETUP UNIT NUMBER
;*ATTEMPT TO ADDRESS THE NEW UNIT NUMBER
MOV 2#RHDS1,RO ;ATTEMPT TO ADDRESS THE NEW DRIVE NO.
JSR PC,2#PUTREG ;TAKE REG. SNAPSHOT IN CASE OF ERROR
BIT #NED,2#CS2 ;TEST FOR NON EXISTENT DRIVE
BEQ 75$ ;CONTINUE IF 'NED' IS NOT SET - DRIVE
;SHOULD BE EXISTENT ON THE BUSS
ERROR 102 ;UNIT NOT AVAILABLE AFTER ADDRESS
;PLUG REPLACED
TYPE 75$ ;;TYPE ASCIZ STRING
BR 74$ ;;GET OVER THE ASCIZ
75$: .ASCIZ <15><12>/UNIT NUMBER/
74$:
MOV 2#UNIT,-(SP) ;GET THE BAD UNIT NUMBER
TYPDS ;TYPE IT OUT
TYPE 77$ ;;TYPE ASCIZ STRING
BR 76$ ;;GET OVER THE ASCIZ
77$: .ASCIZ <15><12>/ /
76$:

```



M10

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2981 014440 023737 004716 004752 7$: CMP @#UNIT,@#INUNIT ;HAVE WE INCREMENTED BACK TO THE
2982 ;ORIGINAL UNIT NO. YET ?
2983 014446 C01233 BNE 6$ ;NO..DO NEXT ADDRESS PLUG
2984 ;YES..CONTINUE WITH TESTS
2985
2986
2987
2988 ;*SET 'VV' IN RHDS1 AFTER RESET FROM THE RECALIBRATE
2989 ;*CAUSED BY PULLING THE ADDRESS PLUGS OUT
2990
2991
2992 014450 013746 002460 MOV @#PKACK,-(SP) ;GET READY TO MOVE COMMAND
2993 014454 052716 000001 BIS #GO,(SP) ;GET READY TO SET GO
2994 ;WITHOUT INTERRUPT ENABLE
2995 014460 012677 165614 MOV (SP)+,@RHCS1 ;GO WITH
2996 ;22 IN RHCS1 FOR PACK ACKNOWLEDGE
2997 ;WITH INTERRUPT DISABLED
2998
2999 014464 011100 MOV @R1,R0 ;SAVE RHCS1 DURING ABOVE OPERATION
3000 014466 011305 MOV @R3,R5 ;SAVE RHDS1 DURING ABOVE OPERATION
3001
3002 014470 104412 WAT ;WAIT FOR VV BIT TO SET
3003 014472 002322 RHDS1 ;WAIT FOR RHDS1 REGISTER
3004 014474 000100 VV ;WAIT FOR VV BIT IN RHDS1 REGISTER
3005 014476 000001 1. ;ALLOW 10 MICRO SECONDS
3006 014500 000001 1. ;VV MUST SET BETWEEN
3007 ;00 AND 20 MICRO SECONDS
3008
3009 014502 104400 014510 TYPE 82$ ;:TYPE ASCIZ STRING
3010 014506 000436 BR 81$ ;:GET OVER THE ASCIZ
3011 ;:82$: .ASCIZ <15><12><15><12> /ALL DISK DRIVES WHICH WERE POWERED UP WHEN THE PROGRAM/
3012 91$:
3013 014604 104400 014612 TYPE 84$ ;:TYPE ASCIZ STRING
3014 014610 000425 BR 83$ ;:GET OVER THE ASCIZ
3015 ;:84$: .ASCIZ <15><12> /WAS STARTED MUST BE POWERED UP AGAIN/<15><12>
3016 93$:

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.SBTTL  
.SBTTL \*\*DRIVE COMMAND TESTS\*\*  
.SBTTL

\*\*\*\*\*  
\*TEST 16 NO OPERATION FUNCTION TEST  
\* ALL POSSIBLE REGISTERS ARE CLEARED THEN A "NOP"=0  
\* IS GIVEN NO CHANGE SHOULD HAPPEN  
\* ALL POSSIBLE REGISTERS ARE FILLED WITH ONES THEN A "NOP"  
\* IS GIVEN NO CHANGE SHOULD HAPPEN  
\*\*\*\*\*

\*\*\*\*\*  
TST16: SCOPE  
MOV #STACK,SP ;RESET STACK  
MOV #16, @#TSTNM ;SAVE TEST NUMBER  
JSR PC, @#CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETUP UNIT NUMBER  
JSR PC, @#CHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1  
;AND THAT NO STATUS BITS ARE STUCK = 1  
TYPE ,CPHALT ;CANNOT CONTINUE TESTING IF ANY OF  
;THE FIRST SET OF BITS DON'T = 1  
HALT ;STOP  
MOV @#NOPERA, @RHCS1 ;GET READY FOR NOPERA  
;NO OPERATION WITH 0 IN RHCS1  
; \*NOW SAVE REGISTERS FOR COMPARISON AFTER NO OPERATION  
JSR RO, @#SAVER ;SAVE REGISTERS  
RHWC ;RHWC IS THE FIRST REGISTER SAVED  
SAVERE ;STARTING ADDRESS OF WHERE  
;THE REGISTERS ARE SAVED  
18. ;NUMBER OF REGISTERS  
;SAVED = 18.  
MOV @#RP4VEC, @RPVEC ;SET RPO4 VECTOR ADDRESS  
;TO 'TIME1' IF P-CLOCK IS PRESENT  
;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT  
;'TIME' WILL ONLY SAVE  
;CURRENT CYLINDER ADDRESS  
;AND LOOK AHEAD REGISTERS

```

014744 013746 002422 MOV 3#NOPEA, -(SP) :SET READY TO MOVE COMMAND
014750 052716 000001 BIS #GO, (SP) :SET READY TO SET GO
:WITHOUT INTERRUPT ENABLE
014754 012677 165320 MOV (SP)+, 2#RHCS1 :GO WITH
:0 IN RHCS1 FOR NO-OPERATION
:WITH INTERRUPT DISABLED

014758 104412 WAIT :WAIT FOR RDY BIT TO SET
014762 002300 RHCS1 :WAIT FOR RHCS1 REGISTER
014766 000000 RDY :WAIT FOR RDY BIT IN RHCS1 REGISTER
014770 000001 :ALLOW 10 MICRO SECONDS
:RDY MUST SET BETWEEN
:00 AND 20 MICRO SECONDS

: *NOW COMPARE REGISTERS BEFORE NO-OP COMMAND
: *WITH AFTER NO-OP COMMAND

014772 004037 042354 JSR RD, 2#COMREG :COMPARE SAVED REGISTERS WITH
:PRESENT VALUE
014776 004612 SAVERE :GOOD DATA SAVED IN 'SAVERE'
015000 002354 NO :TEST DATA STARTING FROM 'RHWC'
015002 000022 18 :18. REGISTERS TO BE COMPARED
015004 015010 15 :RETURN TO 1$ ON ERROR
015006 015014 25 :RETURN TO 2$ ON NO ERROR

015010 104016 1$ :GIVING A NO-OP COMMAND
015012 002307 25 PC :CAUSED AN ERROR
:NO REGISTERS SHOULD CHANGE
:GOOD DATA GIVES REGISTER
:CONTENTS BEFORE COMMAND
:RECEIVED DATA GIVES REGISTER
:CONTENTS AFTER COMMAND

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: \*NOW REPEAT TEST BY MOVING IN ALL POSSIBLE ONES

015014 25:

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015014 004737 041366 JSR PC,2#CLDISK ;SET R1-RHCS1, R2-RHCS2
;R3-RHDS1, R4-RHER1
;GIVE RH-11 INITIALIZE
;SETUP UNIT NUMBER
015020 012700 002272 MOV #RHWC,RO ;ADDR. OF ADDR OF RHWC IN RO
015024 012730 177777 MOV #177777,2(RO)+ ;LOAD 177777 INTO RHWC
015030 012730 177777 MOV #177777,2(RO)+ ;LOAD 177777 INTO RHBA
015034 052730 043010 BIS #43010,2(RO)+ ;LOAD 43010 INTO RHCS2
015040 012730 001400 MOV #1400,2(RO)+ ;LOAD 1400 INTO RHCS1
015044 012730 000000 MOV #0,2(RO)+ ;LOAD 0 INTO RHER1
015050 012730 177777 MOV #177777,2(RO)+ ;LOAD 177777 INTO RHDST
015054 012730 000000 MOV #0,2(RO)+ ;LOAD 0 INTO RHER2
015060 012730 177777 MOV #177777,2(RO)+ ;LOAD 177777 INTO RHOF
015064 012730 177777 MOV #177777,2(RO)+ ;LOAD 177777 INTO RHCA
015070 012730 000000 MOV #0,2(RO)+ ;LOAD 0 INTO RHER3

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: \*NOW SET BITS IN RHAS FOR ALL DRIVES PRESENT

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015074 010046 MOV RO,-(SP) ;: PUSH RO ON STACK
015076 011446 MOV 2R4,-(SP) ;: SAVE RHER1 TO REINSTATE LATER
015100 011246 MOV 2R2,-(SP) ;: SAVE RHCS2 TO BE REINSTATED
;: AFTER ALL ATA BITS HAVE BEEN SET
015102 013700 004742 MOV 2#TOTALAT,RO ;: GET DRIVES PRESENT
015106 005012 CLR 2R2 ;: CLEAR RHCS2 AND CARRY
015110 012705 000010 MOV #8,R5 ;: COUNTER
015114 006000 87$ ROR RO ;: GET BIT INTO CARRY
015116 103002 BCC 88$ ;: BRANCH IF NO UNIT ON THIS BIT
015120 012714 177777 MOV #-1,2R4 ;: MOVE INTO ERROR REGISTER
;: TO SET ATA BIT
015124 005212 88$ INC 2R2 ;: INCREMENT RHCS2 TO NEXT UNIT
015126 005305 DEC R5 ;: COUNT
015130 001371 BNE 87$ ;: BRANCH IF 8 NOT DONE
015132 012612 MOV (SP)+,2R2 ;: REINSTATE RHCS2
015134 012614 MOV (SP)+,2R4 ;: REINSTATE RHER1
015136 012600 MOV (SP)+,RO ;: POP STACK INTO RO
015140 005720 TST (RO)+ ;: GET OVER PHAS IN RO

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015142 012730 177776 MOV #177776,2(RO)+ ;LOAD 177776 INTO RHMR

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0160 015146 004737 041446 JSR PC, @CHECK* :CHECK DVA, RDY, MOL, DPR, DRY, VV = 1
0161 015152 104400 066402 TYPE .CPHALT :AND THAT NO STATUS BITS ARE STUCK = 1
0162 015156 000000 HALT :CANNOT CONTINUE TESTING IF ANY OF
0163 015160 013777 002422 165112 MOV @NOPERA, @RHCS1 :THE FIRST SET OF BITS DON'T = 1
0164 :STOP
0165 :GET READY FOR NOPERA
0166 :NO OPERATION WITH 0 IN RHCS1
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:\*NOW SAVE REGISTERS FOR COMPARISON AFTER A NO-OP

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JSR RO, @SAVER :SAVE REGISTERS
RHWC :RHWC IS THE FIRST REGISTER SAVED
SAVERE :STARTING ADDRESS OF WHERE
18. :THE REGISTERS ARE SAVED
:NUMBER OF REGISTERS
:SAVED = 18.

```

```

MOV @RPH4VEC, @RPVEC :SET RPO4 VECTOR ADDRESS
:TO 'TIME1' IF P-CLOCK IS PRESENT
:OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
:'TIME' WILL ONLY SAVE
:CURRENT CYLINDER ADDRESS
:AND LOOK AHEAD REGISTERS

```

```

MOV @NOPERA, -(SP) :GET READY TO MOVE COMMAND
BIS @GO, (SP) :GET READY TO SET GO
:WITHOUT INTERRUPT ENABLE
MOV (SP)+, @RHCS1 :GO WITH
:0 IN RHCS1 FOR NO-OPERATION
:WITH INTERRUPT DISABLED

```

```

WAT :WAIT FOR RDY BIT TO SET
RHCS1 :WAIT FOR RHCS1 REGISTER
RDY :WAIT FOR RDY BIT IN RHCS1 REGISTER
1. :ALLOW 10 MICRO SECONDS
1. :RDY MUST SET BETWEEN
:00 AND 20 MICRO SECONDS

```

:\*CHANGE REGISTERS TO EXPECTED VALUES

```

TST @RH70 :RUNNING ON AN RH70 ?
BEQ $$ :IF NOT, SKIP NEXT
TST @NUNIT :TESTING MORE THAN ONE DRIVE ?
BNE $$ :SKIP NEXT IF SO
BIC @SC, @SAVERE+6 :CLEAR 'SC' IN RHCS1

```

55:

```

BIC @ATTENT, @SAVERE+24 :CLEAR APPROPRIATE ATA BITS
:FOR WORKING DRIVE IN SAVED RHAS

```

```

3214
3215 015264 004037 042246 JSR RD,3#CHREG :CHANGE BITS IN SAVED REGISTER
3216 015270 002322 RMO51 :CHANGE RMO51 REGISTER
3217
3218 015272 000001 : BIT/BITS TO BE CHANGED
3219 015274 000000 :NEW VALUE OF ATA IS 0
3220 015276 100000 :CHANGE ATA BIT
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3235 015300 004037 042354 JSR RD,3#COMREG :COMPARE SAVED REGISTERS WITH
3236 :PRESENT VALUE
3237 015304 004612 SAVERE :GOOD DATA SAVED IN 'SAVERE'
3238 015306 002354 NO :TEST DATA STARTING FROM 'RHW'
3239 015310 000022 18. :18. REGISTERS TO BE COMPARED
3240 015312 015316 35. :RETURN TO 35 ON ERROR
3241 015314 015322 45 :RETURN TO 45 ON NO ERROR
3242
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3245 015316 104016 35: ERROR 16 :GIVING A NO-OP COMMAND
3246 015320 000207 R15 PC :CAUSED AN ERROR
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\*\*\*\*\*  
\*TEST 17 DRIVE CLEAR  
\* ALL WRITE BITS OF ALL REGISTERS ARE FILLED  
\* WITH ONES EXCEPT GO, CLR, IE, PAT, MCPE, UPE  
\* THEN A DRIVE CLEAR IS GIVEN  
\* THEN ALL REGISTERS ARE CHECKED TO HAVE APPROPRIATE VALUE  
\*\*\*\*\*

```
TST17: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #17, #TSTNM ;SAVE TEST NUMBER

JSR PC, #CLDISK ;SET R1-RHCS1, R2-RHCS2
;R3-RHDS1, R4-RHER1
;GIVE RH-11 INITIALIZE
;SETUP UNIT NUMBER
JSR PC, #CHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1
;AND THAT NO STATUS BITS ARE STUCK = 1
TYPE ,OPHALT ;CANNOT CONTINUE TESTING IF ANY OF
;THE FIRST SET OF BITS DON'T = 1
HALT ;STOP

;*WRITE ALL WRITABLE REGISTER BITS
MOV #RHWC, RO ;ADDR. OF ADDR. OF RHWC IN RO
MOV #177777, #RO+ ;LOAD 177777 INTO RHWC
MOV #177776, #RO+ ;LOAD 177776 INTO RHBA
BIS #43010, #RO+ ;LOAD 43010 INTO RHCS2
MOV #1400, #RO+ ;LOAD 1400 INTO RHCS1
MOV #0, #RO+ ;LOAD 0 INTO RHER1
MOV #177777, #RO+ ;LOAD 177777 INTO RHDST
MOV #0, #RO+ ;LOAD 0 INTO RHER2
MOV #177777, #RO+ ;LOAD 177777 INTO RHOF
MOV #177777, #RO+ ;LOAD 177777 INTO RHCA
MOV #0, #RO+ ;LOAD 0 INTO RHER3

;*NOW SET BITS IN RHAS FOR ALL DRIVES PRESENT
MOV RO, -(SP) ;: PUSH RO ON STACK
MOV #R4, -(SP) ;: SAVE RHER1 TO REINSTATE LATER
MOV #R2, -(SP) ;: SAVE RHCS2 TO BE REINSTATED
;: AFTER ALL ATA BITS HAVE BEEN SET
MOV #TOTALAT, RO ;: GET DRIVES PRESENT
CLR #R2 ;: CLEAR RHCS2 AND CARRY
MOV #8, #R5 ;: COUNTER
ROR RO ;: GET BIT INTO CARRY
BCC #85$ ;: BRANCH IF NO UNIT ON THIS BIT
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94\$:

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0299 015454 012714 177777      MOV      #-1,RA4      ;MOVE INTO ERROR REGISTER
0300                                ;TO SET AT9 BIT
0301 015460 005212      BSS:    INC      JR2      ;INCREMENT RHCS2 TO NEXT UNIT
0302 015462 005305      DEC      R5          ;COUNT
0303 015464 001371      BNE     945         ;BRANCH IF 8 NOT DONE
0304 015466 012612      MOV     (SP)+,JR2    ;REINSTATE RHCS2
0305 015470 012614      MOV     (SP)+,JR4    ;REINSTATE RHER1
0306 015472 012600      MOV     (SP)+,RC     ;POP STACK INTO RC
0307 015474 005720      TST     (R0)+        ;GET OVER PHAS IN RC
0308
0309
0310 015476 012730 177776      MOV     #177776,R0+  ;LOAD 177776 INTO RHMR
0311
0312
0313 015502 017737 164626 004654  MOV     @RHCC,@SAVERE+42 ;SAVE RHCC IN SAVERE TABLE
0314 015510 013777 004606 164550  MOV     @RPHVEC,@RPVEC ;SET RPO4 VECTOR ADDRESS
0315                                ;TO 'TIME1' IF P-CLOCK IS PRESENT
0316                                ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
0317                                ;'TIME' WILL ONLY SAVE
0318                                ;CURRENT CYLINDER ADDRESS
0319                                ;AND LOOK AHEAD REGISTERS
0320
0321
0322 015516 013746 002430      MOV     @DCLEAR,-(SP) ;GET READY TO MOVE COMMAND
0323 015522 052716 000001      BIS     #GO,(SP)     ;GET READY TO SET GO
0324                                ;WITHOUT INTERRUPT ENABLE
0325 015526 012677 164546      MOV     (SP)+,@RHCS1 ;GO WITH
0326                                ;10 IN RHCS1 FOR DRIVE CLEAR
0327                                ;WITH INTERRUPT DISABLED
0328
0329
0330
0331 015532 104412      WAI     ;WAIT FOR RDY BIT TO SET
0332 015534 002300      RHCS1  ;WAIT FOR RHCS1 REGISTER
0333 015536 000200      RDY    ;WAIT FOR RDY BIT IN RHCS1 REGISTER
0334 015540 000001      I.    ;ALLOW 10 MICRO SECONDS
0335 015542 000001      I.    ;RDY MUST SET BETWEEN
0336                                ;00 AND 20 MICRO SECONDS
0337
0338                                ;*NOW LOAD 'SAVERE' REGISTER SNAPSHOT WITH EXPECTED VALUES
0339
0340 015544 004037 041270      JSR     R0,@FILLRE   ;MOV 177777 INTO SAVED RHWC
0341 015550 002272      RHWC   ;SAVED REGISTER TO CHANGE
0342 015552 177777      177777 ;DATA
0343 015554 004037 041270      JSR     R0,@FILLRE   ;MOV 177776 INTO SAVED RHBA
0344 015560 002274      RHBA   ;SAVED REGISTER TO CHANGE
0345 015562 177776      177776 ;DATA
0346 015564 005037 004616      CLR     @SAVERE+4    ;CLEAR LOCATION FOR RHCS2
0347 015570 053737 004716 004616  BIS     @UNIT,@SAVERE+4 ;PUT UNIT # BACK IN THE SAVED RHCS2
0348
0349 015576 005737 004750      TST     @RH70        ;RUNNING ON AN RH70 CONTROLLER ?
0350 015602 001021      BNE     95          ;IF SO SKIP NEXT RH11 CODE
0351
0352

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3353 015604 004037 042246 JSR RO,0#CHREG ;CHANGE BITS IN SAVED REGISTER
3354 015610 002276 RHCS2 ;CHANGE RHCS2 REGISTER
3355
3356 015612 000003 3 ;3 BIT/BITS TO BE CHANGED
3357 015614 000001 1 ;NEW VALUE OF IR IS 1
3358 015616 000100 IR ;CHANGE IR BIT
3359 015620 000001 1 ;NEW VALUE OF BAI IS 1
3360 015622 000010 BAI ;CHANGE BAI BIT
3361 015624 000001 1 ;NEW VALUE OF MXF IS 1
3362 015626 001000 MXF ;CHANGE MXF BIT
3363
3364 015630 004037 042246 JSR RO,0#CHREG ;CHANGE BITS IN SAVED REGISTER
3365 015634 002300 RHCS1 ;CHANGE RHCS1 REGISTER
3366
3367 015636 000001 1 ;1 BIT/BITS TO BE CHANGED
3368 015640 000001 1 ;NEW VALUE OF SC IS 1
3369 015642 100000 SC ;CHANGE SC BIT
3370 015644 000416 BR 9$ ;SKIP NEXT RH70 CODE
3371
3372 015646 9$:
3373
3374 015646 004037 042246 JSR RO,0#CHREG ;CHANGE BITS IN SAVED REGISTER
3375 015652 002276 RHCS2 ;CHANGE RHCS2 REGISTER
3376
3377 015654 000002 2 ;2 BIT/BITS TO BE CHANGED
3378 015656 000001 1 ;NEW VALUE OF IR IS 1
3379 015660 000100 IR ;CHANGE IR BIT
3380 015662 000001 1 ;NEW VALUE OF BAI IS 1
3381 015664 000010 BAI ;CHANGE BAI BIT
3382 015666 005737 004722 TST 0#NUNIT ;TESTING MORE THAN ONE DRIVE ?
3383 015672 001003 BNE 9$ ;SKIP NEXT IF SO
3384 015674 042737 100000 004620 BIC #SC,0#SAVERE+6 ;CLEAR 'SC' IF NOT
3385 015702 9$:
3386 015702 004037 041270 JSR RO,0#FILLRE ;MOV 0 INTO SAVED RHER1
3387 015706 002302 RHER1 ;SAVED REGISTER TO CHANGE
3388 015710 000000 0 ;DATA
3389 015712 004037 041270 JSR RO,0#FILLRE ;MOV 17437 INTO SAVED RHDST
3390 015716 002304 RHDST ;SAVED REGISTER TO CHANGE
3391 015720 017437 17437 ;DATA
3392 015722 004037 041270 JSR RO,0#FILLRE ;MOV 0 INTO SAVED RHER2
3393 015726 002306 RHER2 ;SAVED REGISTER TO CHANGE
3394 015730 000000 0 ;DATA
3395 015732 004037 041270 JSR RO,0#FILLRE ;MOV 116000 INTO SAVED RHOF
3396 015736 002310 RHOF ;SAVED REGISTER TO CHANGE
3397 015740 116000 116000 ;DATA
3398 015742 004037 041270 JSR RO,0#FILLRE ;MOV 1777 INTO SAVED RHCA
3399 015746 002312 RHCA ;SAVED REGISTER TO CHANGE
3400 015750 001777 1777 ;DATA
3401 015752 004037 041270 JSR RO,0#FILLRE ;MOV 0 INTO SAVED RHER3
3402 015756 002314 RHER3 ;SAVED REGISTER TO CHANGE
3403 015760 000000 0 ;DATA
3404 015762 013746 004742 MOV 0#TOTALAT,-(SP) ;GET ALL BITS OF DRIVE & PRESENT
3405 ;IN RHAS
3406 015766 043716 004740 BIC 0#ATTENT,(SP) ;CLEAR WORKING DRIVE BIT

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3407 015772 012637 004636      MOV      (SP)+,2#SAVERE+24 ;MOVE THIS INTO RHAS POSITION
3408 015776 004037 041270      JSR      RO,2#FILLRE      ;MOV 400 INTO SAVED RHMR
3409 016002 002320                RHMR                    ;SAVED REGISTER TO CHANGE
3410 016004 000400                400                    ;DATA
3411
3412 016006                3$:
3413 016006 004037 041270      JSR      RO,2#FILLRE      ;MOV 10700 INTO SAVED RHDS1
3414 016012 002322                RHDS1                  ;SAVED REGISTER TO CHANGE
3415 016014 010700                10700                 ;DATA
3416
3417 016016 013737 002406 004644 4$:  MOV      2#DT,2#SAVERE+32 ;MOVE DRIVE TYPE INTO RHDT
3418                                ;POSITION OF SAVRE TABLE
3419 016024 013737 002410 004646      MOV      2#SN,2#SAVERE+34 ;MOVE SERIAL NUMBER INTO RHSN
3420                                ;POSITION OF SAVERE TABLE
3421
3422 016032 004037 041270      JSR      RO,2#FILLRE      ;MOV 0 INTO SAVED RHEC1
3423 016036 002330                RHEC1                  ;SAVED REGISTER TO CHANGE
3424 016040 000000                0                      ;DATA
3425 016042 004037 041270      JSR      RO,2#FILLRE      ;MOV 0 INTO SAVED RHEC2
3426 016046 002332                RHEC2                  ;SAVED REGISTER TO CHANGE
3427 016050 000000                0                      ;DATA
3428
3429 016052 004037 042246      JSR      RO,2#CHREG       ;CHANGE BITS IN SAVED REGISTER
3430 016056 002300                RHCS1                  ;CHANGE RHCS1 REGISTER
3431
3432 016060 000001                1                      ;1 BIT/BITS TO BE CHANGED
3433 016062 000001                1                      ;NEW VALUE OF PAR IS 1
3434 016064 000010                PAR                    ;CHANGE PAR BIT
3435
3436                                ;*NOW THAT SAVERE TABLE HAS BEEN LOADED WITH
3437                                ;*EXPECTED VALUES, THE REGISTERS WILL BE COMPARED
3438                                ;*WITH SAVERE TABLE
3439
3440
3441 016066 004037 042354      JSR      RO,2#COMREG      ;COMPARE SAVED REGISTERS WITH
3442                                ;PRESENT VALUE
3443 016072 004612                SAVERE                 ;GOOD DATA SAVED IN 'SAVERE'
3444 016074 002354                WC                    ;TEST DATA STARTING FROM 'RHWC'
3445 016076 000022                18.                   ;18. REGISTERS TO BE COMPARED
3446 016100 016104                5$                    ;RETURN TO 5$ ON ERROR
3447 016102 016110                6$                    ;RETURN TO 6$ ON NO ERROR
3448
3449 016104 104017                5$: ERROR 17           ;DRIVE CLEAR COMMAND
3450 016106 000207                RTS PC                ;GAVE AN ERROR
3451                                ;GOOD DATA HAS WHAT SHOULD
3452                                ;BE IN REGISTER AFTER A
3453                                ;DRIVE CLEAR
3454                                ;RECEIVED DATA HAS WHAT
3455                                ;THE REGISTER ACTUALLY
3456                                ;CONTAINED
3457 016110                6$:
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016112 012706 001000  
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016124 004737 041366  
016130 005737 004724  
016134 001007  
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016154 000137 016654  
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016160 104400 016166  
016164 000407

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*****
*TEST 20 READ-IN-PRESET
* IF STARTING ADDRESS 220 IS USED THIS TEST WILL NOT BE PERFORMED
*
* IF THE PROGRAM WORKS UNDER ACT-11 MONITOR
* THEN THIS TEST IS NOT PERFORMED
*
* IF NO ACT-11 MONITOR IS PRESENT THEN
* THIS TEST IS PERFORMED ONLY ON THE FIRST PASS
* ON SUBSEQUENT PASSES THIS TEST IS NOT DONE
*
* THIS TESTS THE READ-IN-PRESET COMMAND
* FIRST THE DRIVE IS POWERED DOWN AND UP IN ORDER TO
* RESET VV-BIT #6 IN RHDS1
* THEN ALL WRITE BITS OF ALL REGISTERS ARE FILLED
* WITH ONES EXCEPT GO, CLR, IE, PAT, MCPE, UPE
* ATA FOR DRIVE UNDER TEST IS MADE = 0
*
* THEN A READ-IN-PRESET COMMAND = 20 IS GIVEN
* THEN ALL REGISTERS ARE TESTED
* THE FOLLOWING SHOULD BE THE REGISTER CONTENTS
* RHCA = 0, RHDST = 0, RHOF SHOULD HAVE
* FMT22 = 0, ECI = 0, HCI = 0, RHDS1 SHOULD HAVE VV = 1
* ALL OTHER REGISTERS SHOULD BE UNCHANGED
*****
†ST20: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #20, #†STNM ;SAVE TEST NUMBER
JSR PC, #CLDISK ;SET R1-RHCS1, R2-RHCS2
;R3-RHDS1, R4-RHER1
;GIVE RH-11 INITIALIZE
;SETUP UNIT NUMBER
; *THIS CODE CHECKS TO SEE IF MANUAL INTERVENTION TESTS ARE OK
TST #NOPUSH ;IS THIS A 220 START ?
BNE !$ ;SKIP THIS TEST IF SO
TST #42 ;MONITOR (ACT 11) RETURN ADDRESS ?
BNE !$ ;SKIP THIS TEST
TST #SPASS ;FIRST PASS ?
BNE !$ ;SKIP THIS TEST IF NOT
BR 2$ ;CONTINUE WITH THIS TEST
1$: JMP TST21 ; JUMP TO NEXT TEST -----)
2$:
TYPE 65$ ;TYPE ASCIZ STRING
BR 64$ ;GET OVER THE ASCIZ
;65$: .ASCIZ <15><12>/STOP DRIVE /

```

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3513 016204          54$:
3514
3515 016204 013746 004716      MOV    2#UNIT,-(SP)    ;GET UNIT UNDER TEST
3516 016210 104404          TYPDS          ;TYPE IT OUT
3517 016212 104400 001223      TYPE    ,SCLRF
3518 016216 032713 010000      3$:    BIT    #MOL,2R3    ;MOL WILL BE HIGH TILL STOF IS HIT
3519 016222 001375          SNE    3$           ;WAIT TILL STOP IS HIT
3520
3521 016224 104400 016232      TYPE    ,67$         ;:TYPE ASCIZ STRING
3522 016230 000406          BR     66$           ;;GET OVER THE ASCIZ
3523
3524 016246          ;;67$: .ASCIZ  /START DRIVE/
3525          66$:
3526 016246 013746 004716      MOV    2#UNIT,-(SP)    ;GET UNIT UNDER TEST
3527 016252 104404          TYPDS          ;TYPE IT
3528 016254 104400 001223      TYPE    ,SCLRF
3529
3530 016260 032713 010000      4$:    BIT    #MOL,2R3    ;MOL WILL BE LOW TILL FILE READY
3531 016264 001775          BEQ    4$           ;WAIT TILL FILE READY
3532
3533 016266 004737 041366      JSR    PC,2#CLDISK    ;SET R1-RHCS1, R2-RHCS2
3534                                ;R3-RHDS1, R4-RHER1
3535                                ;GIVE RH-11 INITIALIZE
3536                                ;SETUP UNIT NUMBER
3537 016272 004737 041424      JSR    PC,2#CHECK     ;CHECK THAT DVA RDY,MOL,DPR,DRY = 1
3538                                ;AND THAT NO STATUS BITS ARE STUCK = 1
3539 016276 104400 066402      TYPE    ,CPHALT      ;CANNOT CONTINUE TESTS IF THEY AREN'T
3540 016302 000000          HALT
3541 016304 012700 002272      MOV    #RHWC,RO       ;STOP
3542                                ;ADDR. OF ADDR. OF RHWC IN RO
3543
3544                                ;*NOW INITIALIZE ALL THE REGISTERS
3545
3546 016310 012730 177777      MOV    #177777,2(RO)+ ;LOAD 177777 INTO RHWC
3547
3548 016314 012730 177777      MOV    #177777,2(RO)+ ;LOAD 177777 INTO RHBA
3549
3550 016320 052730 043010      BIS    #43010,2(RO)+ ;LOAD 43010 INTO RHCS2
3551 016324 012730 001400      MOV    #1400,2(RO)+  ;LOAD 1400 INTO RHCS1
3552
3553 016330 012730 000000      MOV    #0,2(RO)+     ;LOAD 0 INTO RHER1
3554
3555 016334 012730 177777      MOV    #177777,2(RO)+ ;LOAD 177777 INTO RHDST
3556
3557 016340 012730 000000      MOV    #0,2(RO)+     ;LOAD 0 INTO RHER2
3558
3559 016344 012730 177777      MOV    #177777,2(RO)+ ;LOAD 177777 INTO RHOF
3560
3561 016350 012730 177777      MOV    #177777,2(RO)+ ;LOAD 177777 INTO RHCA
3562
3563 016354 012730 000000      MOV    #0,2(RO)+     ;LOAD 0 INTO RHER3
3564
3565                                ;*NOW SET BITS IN RHAS FOR ALL DRIVES PRESENT
3566

```

3567	016360	010046			MOV	RO,-(SP)	;; PUSH RO ON STACK
3568	016362	011446			MOV	DR4,-(SP)	;; SAVE RHER1 TO REINSTATE LATER
3569	016364	011246			MOV	DR2,-(SP)	;; SAVE RHCS2 TO BE REINSTATED
3570							;; AFTER ALL ATA BITS HAVE BEEN SET
3571	016366	013700	004742		MOV	DR#TOTALAT,RO	;; GET DRIVES PRESENT
3572	016372	005012			CLR	DR2	;; CLEAR RHCS2 AND CARRY
3573	016374	012705	000010		MOV	#8,R5	;; COUNTER
3574	016400	006000		88\$:	ROR	RO	;; GET BIT INTO CARRY
3575	016402	103002			BCC	89\$	;; BRANCH IF NO UNIT ON THIS BIT
3576	016404	012714	177777		MOV	#-1,DR4	;; MOVE INTO ERROR REGISTER
3577							;; TO SET ATA BIT
3578	016410	005212		89\$:	INC	DR2	;; INCREMENT RHCS2 TO NEXT UNIT
3579	016412	005305			DEC	R5	;; COUNT
3580	016414	001371			BNE	88\$	;; BRANCH IF 8 NOT DONE
3581	016416	012612			MOV	(SP)+,DR2	;; REINSTATE RHCS2
3582	016420	012614			MOV	(SP)+,DR4	;; REINSTATE RHER1
3583	016422	012600			MOV	(SP)+,RO	;; POP STACK INTO RO
3584	016424	005720			TST	(RO)+	;; GET OVER PHAS IN RO
3585							
3586							
3587	016426	012730	177776		MOV	#177776,DR(RO)+	;; LOAD 177776 INTO RHMR
3588							
3589							
3590	016432	013777	004740	163656	MOV	DR#ATTENT,DRHAS	;; CLEAR WORKING DRIVE 'ATA'
3591	016440	013777	002462	163632	MOV	DR#READIN,DRHCS1	;; GET READY FOR READIN
3592							;; READ IN WITH 20 IN RHCS1
3593							
3594							
3595							;; *NOW SAVE REGISTERS FOR COMPARISON AFTER READ-IN COMMAND
3596							
3597	016446	004037	041534		JSR	RO,DR#SAVER	;; SAVE REGISTERS
3598	016452	002272			RHWC		;; RHWC IS THE FIRST REGISTER SAVED
3599	016454	004612			SAVERE		;; STARTING ADDRESS OF WHERE
3600							;; THE REGISTERS ARE SAVED
3601	016456	000022			18.		;; NUMBER OF REGISTERS
3602							;; SAVED = 18.
3603							
3604	016460	013777	004606	163600	MOV	DR#RP4VEC,DRPVEC	;; SET RPO4 VECTOR ADDRESS
3605							;; TO 'TIME1' IF P-CLOCK IS PRESENT
3606							;; OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
3607							;; 'TIME' WILL ONLY SAVE
3608							;; CURRENT CYLINDER ADDRESS
3609							;; AND LOOK AHEAD REGISTERS
3610							
3611	016466	005737	004750		TST	DR#RH70	;; RUNNING ON AN RH70 CONTROLLER ?
3612	016472	001411			BEQ	7\$	;; SKIP NEXT FOR RH70 IF NOT
3613							
3614	016474	013746	002462		MOV	DR#READIN,-(SP)	;; GET READY TO MOVE COMMAND
3615	016500	052716	000001		BIS	#GO,(SP)	;; GET READY TO SET GO
3616							;; WITHOUT INTERRUPT ENABLE
3617	016504	012677	163570		MOV	(SP)+,DRHCS1	;; GO WITH
3618							;; 20 IN RHCS1 FOR READ IN
3619							;; WITH INTERRUPT DISABLED
3620							





016654 000004  
016656 012736 001000  
016662 012737 000021 004604  
016670 004737 041366  
016674 004737 041424  
016700 104400 066402  
016704 000000  
016706 012700 002272  
016712 012730 177777  
016716 012730 177777  
016722 052730 043010  
016726 012730 001400  
016732 012730 000000  
016736 012730 177777  
016742 012730 000000  
016746 012730 177777

\*\*\*\*\*  
\*TEST 21 READ-IN-PRESET

\* THIS TEST IS THE SAME AS THE PREVIOUS TEST EXCEPT  
\* THAT IT DOES NOT TEST THE SETTING OF VV

\* THIS TEST IS HERE BECAUSE IF ACT-11 MONITOR IS PRESENT  
\* THEN THE PREVIOUS TEST WILL NOT BE PERFORMED  
\* THIS TESTS THE READ-IN-PRESET COMMAND  
\* ALL WRITE BITS OF ALL REGISTERS ARE FILLED  
\* WITH ONES EXCEPT GO, CLR, IE, PAT, MCPE, UPE  
\* ATA FOR DRIVE UNDER TEST IS MADE = 0

\* THEN A READ-IN-PRESET COMMAND = 20 IS GIVEN  
\* THEN ALL REGISTERS ARE TESTED  
\* THE FOLLOWING SHOULD BE THE REGISTER CONTENTS  
\* RHCA = 0, RHDST = 0, RHOF SHOULD HAVE  
\* FMT22 = 0, ECI = 0, HCI = 0, RHDS1 SHOULD HAVE VV = 1  
\* ALL OTHER REGISTERS SHOULD BE UNCHANGED

\*\*\*\*\*

TST21: SCOPE  
MOV #STACK, SP :RESET STACK  
MOV #21, R#1STNM :SAVE TEST NUMBER  
JSR PC, R#CLDISK :SET R1-RHCS1, R2-RHCS2  
:R3-RHDS1, R4-RHER1  
:GIVE RH-11 INITIALIZE  
:SETUP UNIT NUMBER  
JSR PC, R#CHECK :CHECK THAT DVA, RDY, MOL, DPR, DRY = 1  
:AND THAT NO STATUS BITS ARE STUCK = 1  
TYPE .CPHALT :CANNOT CONTINUE TESTS IF THEY AREN'T  
HALT :STOP  
MOV #RHWC, R0 :ADDR. OF ADDR. OF RHWC IN R0

;\*INITIALIZE ALL THE REGISTERS

MOV #177777, R(R0)+ :LOAD 177777 INTO RHWC  
MOV #177777, R(R0)+ :LOAD 177777 INTO RHBA  
BIS #43010, R(R0)+ :LOAD 43010 INTO RHCS2  
MOV #1400, R(R0)+ :LOAD 1400 INTO RHCS1  
MOV #0, R(R0)+ :LOAD 0 INTO RHER1  
MOV #177777, R(R0)+ :LOAD 177777 INTO RHDST  
MOV #0, R(R0)+ :LOAD 0 INTO RHER2  
MOV #177777, R(R0)+ :LOAD 177777 INTO RHOF



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016752 012730 177777  
016756 012730 000000  
016762 012730 177777  
016756 012730 177776  
  
016772 013777 004740 163316  
017000 013777 002462 163272  
  
017006 004037 041534  
017012 002272  
017014 004612  
  
017016 000022  
  
017020 013777 004606 163240  
  
017026 005737 004750  
017032 001411  
  
017034 013746 002462  
017040 052716 000001  
  
017044 012677 163230  
  
017050 011100  
017052 011305  
017054 000406  
  
017056  
  
017056 013746 002462  
017062 052716 000101  
  
017066 012677 163206  
  
017072

MOV #177777,2(RO)+ ;LOAD 177777 INTO RHCA  
MOV #0,2(RO)+ ;LOAD 0 INTO RHER3  
MOV #-1,2(RO)+ ;CLEAR ALL BITS OF RHAS  
MOV #177776,2(RO)+ ;LOAD 177776 INTO RHMR  
  
MOV 2\*ATTENT,2RHAS ;CLEAR WORKING DRIVE 'ATA'  
MOV 2\*READIN,2RHCS1 ;GET READY FOR READIN  
;READ IN WITH 20 IN RHCS1  
  
;\*NOW SAVE REGISTERS FOR COMPARISON AFTER READ-IN COMMAND  
  
JSR RD,2\*SAVER ;SAVE REGISTERS  
RHW ;RHW IS THE FIRST REGISTER SAVED  
SAVERE ;STARTING ADDRESS OF WHERE  
;THE REGISTERS ARE SAVED  
18. ;NUMBER OF REGISTERS  
;SAVED = 18.  
  
MOV 2\*RP4VEC,2PPVEC ;SET RPO4 VECTOR ADDRESS  
;TO 'TIME1' IF P-CLOCK IS PRESENT  
;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT  
;'TIME' WILL ONLY SAVE  
;CURRENT CYLINDER ADDRESS  
;AND LOOK AHEAD REGISTERS  
  
TST 2\*RH70 ;RUNNING ON AN RH70 CONTROLLER ?  
BEQ 9\$ ;SKIP NEXT IF NOT  
  
MOV 2\*READIN,-(SP) ;GET READY TO MOVE COMMAND  
BIS #GO,(SP) ;GET READY TO SET GO  
;WITHOUT INTERRUPT ENABLE  
MOV (SP)+,2RHCS1 ;GO WITH  
;20 IN RHCS1 FOR READ IN  
;WITH INTERRUPT DISABLED  
  
MOV 2R1,RO ;SAVE RHCS1 DURING ABOVE OPERATION  
MOV 2R3,RS ;SAVE RHDS1 DURING ABOVE OPERATION  
BR 10\$ ;SKIP NEXT RH11 CODE  
  
9\$:  
  
MOV 2\*READIN,-(SP) ;GET READY TO MOVE COMMAND  
BIS #GO!IE,(SP) ;GET READY TO SET 'GO' AND  
;ENABLE INTERRUPT  
MOV (SP)+,2RHCS1 ;GO WITH  
;20 IN RHCS1 FOR READ IN  
;WITH INTERRUPT ENABLED  
  
10\$:

```

38001 017072 104412 WAT ;WAIT FOR RDY BIT TO SET
38002 017074 002300 RHCS1 ;WAIT FOR RHCS1 REGISTER
38003 017076 000200 RDY ;WAIT FOR RDY BIT IN RHCS1 REGISTER
38004 017100 000001 ;ALLOW 10 MICRO SECONDS
38005 017102 000001 ;RDY MUST SET BETWEEN
;00 AND 20 MICRO SECONDS
;
; *NOW CHANGE SAVED REGISTERS TO EXPECTED VALLE
38006 017104 004037 041270 JSR RO,3#FILLRE ;MOV 0 INTO SAVED RHCA
38007 017110 002312 RHCA ;SAVED REGISTER TO CHANGE
38008 017112 000000 ;DATA
38009 017114 004037 041270 JSR RO,3#FILLRE ;MOV 0 INTO SAVED RHDST
38010 017120 002304 RHDST ;SAVED REGISTER TO CHANGE
38011 017122 000000 ;DATA
;
38012 017124 004037 042246 JSR RO,3#CHREG ;CHANGE BITS IN SAVED REGISTER
38013 017130 002310 RHCF ;CHANGE RHOF REGISTER
;
38014 017132 000003 ;3 BIT/BITS TO BE CHANGED
38015 017134 000000 ;NEW VALUE OF FMT22 IS 0
38016 017136 010000 FMT22 ;CHANGE FMT22 BIT
38017 017140 000000 ;NEW VALUE OF ECI IS 0
38018 017142 004000 ECI ;CHANGE ECI BIT
38019 017144 000000 ;NEW VALUE OF HCI IS 0
38020 017146 002000 HCI ;CHANGE HCI BIT
;
38021 017150 004037 042246 JSR RO,3#CHREG ;CHANGE BITS IN SAVED REGISTER
38022 017154 002322 RHDS1 ;CHANGE RHCS1 REGISTER
;
38023 017156 000001 ;1 BIT/BITS TO BE CHANGED
38024 017160 000001 ;NEW VALLE OF VV IS 1
38025 017162 000100 VV ;CHANGE VV BIT
;
; *NOW THAT SAVERE TABLE WITH SAVED REGISTERS HAVE
; *THE EXPECTED VALUE AFTER A READ-IN COMMAND
; *COMPARISONS ARE MADE
38026 017164 004037 042354 JSR RO,3#COMREG ;COMPARE SAVED REGISTERS WITH
38027 017170 004612 SAVERE ;PRESENT VALUE
38028 017172 002354 WC ;GOOD DATA SAVED IN 'SAVERE'
38029 017174 000022 ;TEST DATA STARTING FROM 'RHWC'
38030 017176 017202 ;18. REGISTERS TO BE COMPARED
38031 017200 017206 ;RETURN TO $$ ON ERROR
38032 017202 104020 ;RETURN TO 6$ ON NO ERROR
38033 017204 000207 ;READ-IN COMMAND GAVE AN
;ERROR
;GOOD DATA HAS WHAT SHOULD
;BE IN REGISTER AFTER A
;READ-IN COMMAND
;RECEIVED DATA HAS WHAT

```



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3871 017206 005737 004744
3872 017212 001401
3873 017214 000402
3874 017216 000137 017262
3875 017222
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3885 017222 000004
3886 017224 012706 001000
3887 017230 012737 000022 004604
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3889 017236 004737 041366
3890 017242 012777 000001 163050
3891 017250 004037 041036
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3894 017254 000777 777
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3896 017256 000137 017316
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3899 017262
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3904 017262 000004
3905 017264 012706 001000
3906 017270 012737 000023 004604
3907
3908 017276 004737 041366
3909 017302 012777 000001 163010
3910 017310 004037 041036
3911
3912 017314 000377 377
3913
3914
3915

```

```

*****
TST RPO6 ;TEST FOR RPO6 DRIVE
BEQ 75 ;IF = 0, TREAT DRIVE AS AN RPO4
BR 85 ;TREAT AS RPO6 - DO NEXT "MAKECL"
JMP 300G ;DO SECOND FOLLOWING "MAKECL"
*****

*****
*TEST 22 MAKE CURRENT CYLINDER = 777
*****
TST2: SCOPE
MOV #STACK_SP ;RESET STACK
MOV #23-1, #TSTNM ;THIS SAVES TEST NUMBER
JSR PC, #CLDISK ;INIT DRIVE
MOV #DMD, #RHMR ;SET DIAGNOSTIC MODE
JSR RD, #MAKECYL ;SUBROUTINE TO GIVE A SEEK
;COMMAND FOLLOWED BY A INIT
;THIS SHOULD CHANGE RHCC TO 777
JMP #FISH ;DON'T DO NEXT "MAKECL"

DOG:

*****
*TEST 23 MAKE CURRENT CYLINDER = 377
*****
TST23: SCOPE
MOV #STACK_SP ;RESET STACK
MOV #24-1, #TSTNM ;THIS SAVES TEST NUMBER
JSR PC, #CLDISK ;INIT DRIVE
MOV #DMD, #RHMR ;SET DIAGNOSTIC MODE
JSR RD, #MAKECYL ;SUBROUTINE TO GIVE A SEEK
;COMMAND FOLLOWED BY A INIT
;THIS SHOULD CHANGE RHCC TO 377

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017316

FISH:  
:\*\*\*\*\*  
:\*TEST 24 RECALIBRATE COMMAND  
  
:\* ALL POSSIBLE REGISTERS ARE FILLED WITH ONES  
:\* THEN A RECALIBRATE = 6 COMMAND IS GIVEN  
  
:\* NO REGISTERS SHOULD CHANGE EXCEPT RHCC = 0  
  
:\*\*\*\*\*

017316 000004  
017320 012706 001000  
017324 012737 000024 004604  
  
017332 004737 041366  
  
017336 004737 041446  
017342 104400 066402  
017346 000000  
017350 012700 002272  
017354 012730 177777  
  
017360 012730 177776  
017364 052730 000010  
017370 012730 001400  
  
017374 012730 000000  
017400 012730 177777  
017404 012730 000000  
017410 012730 177777  
017414 012730 177777  
017420 012730 000000  
  
017424 010046  
017426 011446  
017430 011246  
  
017432 013700 004742  
017436 005012

TST24: SCOPE  
MOV #STACK,SP ;RESET STACK  
MOV #24,STNM ;SAVE TEST NUMBER  
  
JSR PC,CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETUP UNIT NUMBER  
JSR PC,CHECKT ;CHECK DVA,ROY,MOL,DPR,DRY,VV = 1  
;AND THAT NO STATUS BITS ARE STUCK = 1  
TYPE .CPHALT ;CANNOT CONTINUE TESTING IF ANY OF  
;THE FIRST SET OF BITS DON'T = 1  
HALT ;STOP  
MOV #RHWC,RO ;ADDR. OF ADDR. OF RHWC IN RO  
MOV #177777,RO+ ;LOAD 177777 INTO RHWC  
  
MOV #177776,RO+ ;LOAD 177776 INTO RHBA  
  
BIS #010,RO+ ;LOAD 010 INTO RHCS2  
MOV #1400,RO+ ;LOAD 1400 INTO RHCS1  
  
MOV #0,RO+ ;LOAD 0 INTO RHER1  
MOV #177777,RO+ ;LOAD 177777 INTO RHDST  
MOV #0,RO+ ;LOAD 0 INTO RHER2  
MOV #177777,RO+ ;LOAD 177777 INTO RHOF  
MOV #177777,RO+ ;LOAD 177777 INTO RHCA  
MOV #0,RO+ ;LOAD 0 INTO RHER3  
  
:\*NOW SET BITS IN RHAS FOR ALL DRIVES PRESENT  
  
MOV RO,-(SP) ;:PUSH RO ON STACK  
MOV @R4,-(SP) ;:SAVE RHER1 TO REINSTATE LATER  
MOV @R2,-(SP) ;:SAVE RHCS2 TO BE REINSTATED  
;AFTER ALL ATA BITS HAVE BEEN SET  
MOV @TOTALAT,RO ;:GET DRIVES PRESENT  
CLR @R2 ;:CLEAR RHCS2 AND CARRY



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4024
4025 017556 013746 002426      MOV      Q#RECALI, -(SP)      :SAVE COMMAND
4026 017562 052716 004301      BIS      #DVA!GO!IE!RDY, (SP) :INCLUDE DVA!GO!IE!RDY
4027 017566 005737 004722      TST     Q#NUNIT              :ARE THERE MORE THAN ONE UNIT
4028 017572 001413                BEQ      89$                 :BRANCH IF ONLY ONE UNIT
4029 017574 010037 004760      MOV      RO, Q#TMP4          :GET RHCS1
4030 017600 042737 177677 004760      BIC     *↑CIE, Q#TMP4        :KEEP IE BIT
4031 017606 042716 000100      BIC     #IE, (SP)           :CLEAR IE IN GOOD DATA
4032 017612 053716 004760      BIS     Q#TMP4, (SP)        :GET IE AS IS
4033 017616 052716 100000      BIS     #SC, (SP)          :SET SC IN RHCS1
4034 017622                89$:
4035 017522 011637 001124      MOV     (SP), Q#$GDDAT      :SAVE FOR PRINTOUT
4036 017626 022600                CMP     (SP)+, RO          :DURING ABOVE OPERATION ONLY DVA!GO!IE!RDY
4037                                :AND COMMAND SHOULD BE SET
4038 017630 001405                BEQ     88$                 :BRANCH IF GOOD
4039 017632 010037 001126      MOV     RO, Q#$BDDAT        :BAD DATA
4040 017636 010137 004600      MOV     R1, Q#REGADR        :FAILING REGISTER RHCS1
4041 017642 104021                ERROR   21                 :DURING ABOVE OPERATION ONLY
4042                                :COMMAND AND DVA!GO!IE!RDY SHOULD BE SET
4043 017644 012746 030500      89$: MOV     #MOL!DPR!VV!PIP, -(SP) :SAVE BITS SET DURING OPERATION IN RHDS1
4044 017650 011637 001124      MOV     (SP), Q#$GDDAT      :SAVE FOR PRINTOUT
4045 017654 022605                CMP     (SP)+, R5          :DURING ABOVE OPERATION ONLY MOL!DPR!VV!PIP
4046                                :SHOULD BE SET
4047 017656 001405                BEQ     90$                 :BRANCH IF GOOD
4048 017660 010537 001126      MOV     R5, Q#$BDDAT        :BAD DATA
4049 017664 010337 004600      MOV     R3, Q#REGADR        :FAILING REGISTER RHDS1
4050 017670 104063                ERROR   63                 :DURING ABOVE OPERATION ONLY
4051                                :MOL!DPR!VV!PIP SHOULD BE SET
4052 017672                90$:
4053
4054                                ;*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES
4055
4056 017672 004037 041270      JSR     RO, Q#FILLRE        :MOV 0 INTO SAVED RHCC
4057 017676 002334                RHCC                       :SAVED REGISTER TO CHANGE
4058 017700 000000                0                           :DATA
4059 017702 004037 041270      JSR     RO, Q#FILLRE        :MOV 116377 INTO SAVED RHOF
4060 017706 002310                RHOF                       :SAVED REGISTER TO CHANGE
4061 017710 116377                116377                     :DATA
4062
4063 017712 053737 004740 004636      BIS     Q#ATTENT, Q#SAVERE+24 :SET APPROPRIATE 'ATA' BITS
4064                                :FOR WORKING DRIVE IN
4065                                :SAVED RHAS LOACTION
4066 017720 004037 041270      JSR     RO, Q#FILLRE        :MOV 104206 INTO SAVED RHCS1
4067 017724 002300                RHCS1                       :SAVED REGISTER TO CHANGE
4068 017726 104206                104206                     :DATA
4069 017730 004037 041270      JSR     RO, Q#FILLRE        :MOV 110700 INTO SAVED RHDS1
4070 017734 002322                RHDS1                       :SAVED REGISTER TO CHANGE
4071 017736 110700                110700                     :DATA
4072
4073
4074                                ;*NOW COMPARE REGISTERS AFTER A RECALIBRATE COMMAND
4075
4076
4077 017740 004037 042354      JSR     RO, Q#COMREG        :COMPARE SAVED REGISTERS WITH

```

4078					
4079	017744	004612		SAVERE	
4080	017746	002354		WC	
4081	017750	000022		18.	
4082	017752	017756		1\$	
4083	017754	017762		2\$	
4084					
4085	017756	104064	1\$:	ERROR	64
4086	017760	000207		RTS	PC
4087					
4088					
4089					
4090					
4091					
4092	017762		2\$:		

```

;PRESENT VALUE
;GOOD DATA SAVED IN 'SAVERE'
;TEST DATA STARTING FROM 'RHWC'
;18. REGISTERS TO BE COMPARED
;RETURN TO 1$ ON ERROR
;RETURN TO 2$ ON NO ERROR

;RECALIBRATE COMMAND CAUSED
;AN ERROR
;GOOD DATA GIVES WHAT SHOULD
;BE THERE
;RECEIVED DATA GIVES WHAT WAS
;THERE AFTER COMMAND

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4095 017762 005737 004744
4096 017766 001401
4097 017770 000402
4098 017772 000137 020036
4099 017776
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4107 017776 000004
4108 020000 012706 001000
4109 020004 012737 000025 004604
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4111 020012 004737 0 1366
4112 020016 012777 000001 162274
4113 020024 004037 041036
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4116 020030 000777
4117
4118 020032 000137 020072
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4121 020036
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4126 020036 000004
4127 020040 012706 001000
4128 020044 012737 000026 004604
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4130 020052 004737 041366
4131 020056 012777 000001 162234
4132 020064 004037 041036
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4135 020070 000377
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;:*****
;TST RPO6 ;TEST FOR RPO6 DRIVE
;BEQ 3$ ;IF = 0, TREAT DRIVE AS AN RPO4
;BR 4$ ;TREAT AS RPO6 - DO NEXT "MAKECL"
3$: JMP @CAT ;DO SECOND FOLLOWING "MAKECL"
4$:
;:*****

;:*****
;*TEST 25 MAKE CURRENT CYLINDER = 777
;:*****
TST25: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #26-1, @TSTNM ;THIS SAVES TEST NUMBER
JSR PC, @CLDISK ;INIT DRIVE
MOV #DMD, @RHMR ;SET DIAGNOSTIC MODE
JSR RO, @MAKECYL ;SUBROUTINE TO GIVE A SEEK
;COMMAND FOLLOWED BY A INIT
;THIS SHOULD CHANGE RHCC TO 777
777
JMP @BIRD ;DON'T DO NEXT "MAKECL"

CAT:
;:*****
;*TEST 26 MAKE CURRENT CYLINDER = 377
;:*****
TST26: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #27-1, @TSTNM ;THIS SAVES TEST NUMBER
JSR PC, @CLDISK ;INIT DRIVE
MOV #DMD, @RHMR ;SET DIAGNOSTIC MODE
JSR RO, @MAKECYL ;SUBROUTINE TO GIVE A SEEK
;COMMAND FOLLOWED BY A INIT
;THIS SHOULD CHANGE RHCC TO 377
377

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020072

BIRD:

:\*\*\*\*\*  
:\*TEST 27 RECALIBRATE COMMAND

:\* ALL POSSIBLE REGISTERS ARE FILLED WITH 0  
:\* THEN A RECALIBRATE =6 COMMAND IS GIVEN

:\* NO REGISTERS SHOULD CHANGE EXCEPT RHCC=0

:\*\*\*\*\*

020072 000004  
020074 012706 001000  
020100 012737 000027 004E04  
020106 004737 041366  
020112 004737 041446  
020116 104400 056402  
020122 000000  
020124 012700 002272  
020130 012730 000000  
020134 012730 000000  
020140 052730 000000  
020144 012730 000000  
020150 012730 000000  
020154 012730 000000  
020160 012730 000000  
020164 012730 000000  
020170 012730 000000  
020174 012730 000000  
020200 012730 177777  
020204 012730 000000  
020210 013777 002426 162062

TST27: SCOPE  
MOV #STACK,SP ;RESET STACK  
MOV #27,#TSTNM ;SAVE TEST NUMBER  
JSR PC,#CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETUP UNIT NUMBER  
JSR PC,#CHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1  
;AND THAT NO STATUS BITS ARE STUCK = 1  
TYPE ,CPHALT ;CANNOT CONTINUE TESTING IF ANY OF  
;THE FIRST SET OF BITS DON'T = 1  
HALT ;STOP  
MOV #RHWC,RO ;ADDR. OF ADDR OF RHWC IN RO  
MOV #0,#(RO)+ ;LOAD 0 INTO RHWC  
MOV #0,#(RO)+ ;LOAD 0 INTO RHBA  
BIS #0,#(RO)+ ;LOAD 0 INTO RHCS2  
MOV #0,#(RO)+ ;LOAD 0 INTO RHCS1  
MOV #0,#(RO)+ ;LOAD 0 INTO RHER1  
MOV #0,#(RO)+ ;LOAD 0 INTO RHDST  
MOV #0,#(RO)+ ;LOAD 0 INTO RHER2  
MOV #0,#(RO)+ ;LOAD 0 INTO RHOF  
MOV #0,#(RO)+ ;LOAD 0 INTO RHCA  
MOV #0,#(RO)+ ;LOAD 0 INTO RHER3  
MOV #-1,#(RO)+ ;CLEAR ALL BITS OF RHAS  
MOV #0,#(RO)+ ;LOAD 0 INTO RHMR  
MOV #RECALI,#RHCS1 ;GET READY FOR RECALI  
;RECALIBRATE WITH 6 IN RHCS1

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4191                                     ;*NOW SAVE REGISTERS FOR COMPARISON AFTER RECALIBRATE
4192
4193 020216 004037 041534 JSR    RD,@#SAVER    ;SAVE REGISTERS
4194 020222 002272          RHWC          ;RHWC IS THE FIRST REGISTER SAVED
4195 020224 004612          SAVERE         ;STARTING ADDRESS OF WHERE
4196                                     ;THE REGISTERS ARE SAVED
4197 020226 000022          18.           ;NUMBER OF REGISTERS
4198                                     ;SAVED = 18.
4199 020230 013777 004606 162030 MOV    @#RPO4VEC,@RPVEC ;SET RPO4 VECTOR ADDRESS
4200                                     ;TO 'TIME1' IF P-CLOCK IS PRESENT
4201                                     ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
4202                                     ;'TIME' WILL ONLY SAVE
4203                                     ;CURRENT CYLINDER ADDRESS
4204                                     ;AND LOOK AHEAD REGISTERS
4205
4206 020236 013746 002426 MOV    @#RECALI,-(SP)  ;GET READY TO MOVE COMMAND
4207 020242 052716 000101 BIS    #GO!IE,(SP)    ;GET READY TO SET 'GO' AND
4208                                     ;ENABLE INTERRUPT
4209 020246 012677 162026 MOV    (SP)+,@RHCS1   ;GO WITH
4210                                     ;6 IN RHCS1 FOR RECALIBRATE
4211                                     ;WITH INTERRUPT ENABLED
4212 020252 011100          MOV    @R1,R0        ;SAVE RHCS1 DURING ABOVE OPERATION
4213 020254 011305          MOV    @R3,R5        ;SAVE RHDS1 DURING ABOVE OPERATION
4214
4215 020256 104412          WAT           ;WAIT FOR DRY BIT TO SET
4216 020260 002322          RHDS1        ;WAIT FOR RHDS1 REGISTER
4217 020262 000200          DRY          ;WAIT FOR DRY BIT IN RHDS1 REGISTER
4218 020264 076377          31999.       ;ALLOW 319990 MICRO SECONDS
4219 020266 056701          24001.       ;DRY MUST SET BETWEEN
4220                                     ;79980 AND 560000 MICRO SECONDS
4221
4222                                     ;*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN
4223                                     ;*R0 AND R5 IMMEDIATELY AFTER GO
4224
4225 020270 013746 002426 MOV    @#RECALI,-(SP)  ;SAVE COMMAND
4226 020274 052716 004301 BIS    #DVA!GO!IE!RDY,(SP) ;INCLUDE DVA!GO!IE!RDY
4227 020300 011637 001124 MOV    (SP),@#$GDDAT  ;SAVE FOR PRINTOUT
4228 020304 022600          CMP    (SP)+,R0      ;DURING ABOVE OPERATION ONLY DVA!GO!IE!RDY
4229                                     ;AND COMMAND SHOULD BE SET
4230 020306 001405          BEQ    88$         ;BRANCH IF GOOD
4231 020310 010037 001126 MOV    R0,@#$BDDAT    ;BAD DATA
4232 020314 010137 004600 MOV    R1,@#REGADR    ;FAILING REGISTER RHCS1
4233 020320 104021          ERROR   21        ;DURING ABOVE OPERATION ONLY
4234                                     ;COMMAND AND DVA!GO!IE!RDY SHOULD BE SET
4235 020322 012746 030500 88$: MOV    #MOL!DPR!VV!PIP,-(SP) ;SAVE BITS SET DURING OPERATION IN RHDS1
4236 020326 011637 001124 MOV    (SP),@#$GDDAT  ;SAVE FOR PRINTOUT
4237 020332 022605          CMP    (SP)+,R5     ;DURING ABOVE OPERATION ONLY MOL!DPR!VV!PIP
4238                                     ;SHOULD BE SET
4239 020334 001405          BEQ    90$         ;BRANCH IF GOOD
4240 020336 010537 001126 MOV    R5,@#$BDDAT    ;BAD DATA
4241 020342 010337 004600 MOV    R3,@#REGADR    ;FAILING REGISTER RHDS1
4242 020346 104063          ERROR   63        ;DURING ABOVE OPERATION ONLY
4243                                     ;MOL!DPR!VV!PIP SHOULD BE SET
4244 020350          90$:
    
```

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4245                                     ;*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES
4246
4247 020350 004037 041270 JSR    RD,2#FILLRE ;MOV 0 INTO SAVED RHCC
4248 020354 002334 RHCC  ;SAVED REGISTER TO CHANGE
4249 020356 000000 0 ;DATA
4250
4251 020360 053737 004740 004636 BIS    2#ATTENT,2#SAVERE+24 ;SET APPROPRIATE 'ATA' BITS
4252                                     ;FOR WORKING DRIVE IN
4253                                     ;SAVED RHAS LOACTION
4254
4255 020366 004037 042246 JSR    RD,2#CHREG ;CHANGE BITS IN SAVED REGISTER
4256 020372 002322 RHDS1 ;CHANGE RHDS1 REGISTER
4257
4258 020374 000001 1 ;1 BIT/BITS TO BE CHANGED
4259 020376 000001 1 ;NEW VALUE OF ATA IS 1
4260 020400 100000 ATA ;CHANGE ATA BIT
4261
4262 020402 004037 042246 JSR    RD,2#CHREG ;CHANGE BITS IN SAVED REGISTER
4263 020406 002300 RHCS1 ;CHANGE RHCS1 REGISTER
4264
4265 020410 000001 1 ;1 BIT/BITS TO BE CHANGED
4266 020412 000001 1 ;NEW VALUE OF SC IS 1
4267 020414 100000 SC ;CHANGE SC BIT
4268
4269                                     ;*NOW COMPARE REGISTERS AFTER A RECALIBRATE COMMAND
4270
4271
4272 020416 004037 042354 JSR    RD,2#COMREG ;COMPARE SAVED REGISTERS WITH
4273                                     ;PRESENT VALUE
4274 020422 004612 SAVERE ;GOOD DATA SAVED IN 'SAVERE'
4275 020424 002354 WC ;TEST DATA STARTING FROM 'RHWC'
4276 020426 000022 18. ;18. REGISTERS TO BE COMPARED
4277 020430 020434 1$ ;RETURN TO 1$ ON ERROR
4278 020432 020440 2$ ;RETURN TO 2$ ON NO ERROR
4279
4280 020434 104064 1$: ERROR 64 ;RECALIBRATE COMMAND CAUSED
4281 020436 000207 RTS PC ;AN ERROR
4282                                     ;GOOD DATA GIVES WHAT SHOULD BE
4283                                     ;THERE
4284                                     ;RECEIVED DATA GIVES WHAT WAS
4285 020440 2$: ;THERE AFTER A RECALIBRATE
4286
    
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\*\*\*\*\*

:\*TEST 30 UNLOAD COMMAND

:\* IF STARTING ADDRESS 220 IS USED THIS TEST WILL NOT BE PERFORMED

:\* IF THE PROGRAM WORKS UNDER ACT-11 MONITOR  
THEN THIS TEST IS NOT PERFORMED

:\* IF NO ACT-11 MONITOR IS PRESENT  
THEN THIS TEST IS PERFORMED ONLY ON THE FIRST PASS  
ON SUBSEQUENT PASSES THIS TEST IS NOT DONE

:\* ALL POSSIBLE REGISTERS ARE FILLED WITH ONES  
THEN AN UNLOAD COMMAND =2 IS GIVEN  
NO REGISTERS SHOULD CHANGE EXCEPT MOL SHOULD=0  
THEN THE DRIVE IS POWERED UP BY OPERATOR  
AND A PACK ACKNOWLEDGE COMMAND (ALREADY TESTED)  
SETS VV-IN RHDS1

\*\*\*\*\*

TST30: SCOPE

:\*THIS CODE CHECKS TO SEE IF MANUAL INTERVENTION TESTS ARE OK

020440 000004

020442 005737 004724  
020446 001007  
020450 005737 000042  
020454 001004  
020456 005737 001100  
020462 001001  
020464 000402

13: JMP TST31 : JUMP TO NEXT TEST -----)

23: MOV #STACK,SP :RESET STACK  
MOV #30,#TSTNM :SAVE TEST NUMBER

020504 004737 041366

JSR PC,#CLDISK :SET R1-RHCS1, R2-RHCS2  
:R3-RHDS1, R4-RHER1  
:GIVE RH-11 INITIALIZE  
:SETUP UNIT NUMBER  
JSR PC,#CHECKT :CHECK DVA, RDY, MOL, DPR, DRY, VV = 1  
:AND THAT NO STATUS BITS ARE STUCK = !  
TYPE ,CPHALT :CANNOT CONTINUE TESTING IF ANY OF  
:THE FIRST SET OF BITS DON'T = !  
HALT :STOP

020510 004737 041446

020514 104400 066402

020520 000000

:\*THIS SETTING OF VV IS FOR LOOP ON ERROR ONLY  
:\*WHERE UNLOAD TAKES EFFECT AND CYCLE UP BRINGS VV DOWN

020522 017746 161574

MOV @RHDS1,-(SP) :PUSH RHDS1 ONTO STACK

```

4341 020526 042716 167677      BIC      #167677,(SP)      :CLEAR EVERYTHING EXCEPT VV AND MOL
4342 020532 022726 010100      CMP      #VV!MOL,(SP)    :ARE VV AND MOL SET ?
4343 020536 001504              BEQ      657              :CONTINUE IF YES
4344 020540 044400 020546      TYPE    655              :TYPE ASCIZ STRING
4345 020544 000427              BR       645              :GET OVER THE ASCIZ
4346              :655: .ASCIZ 15<<12>>GET DRIVE HEADS LOADED THEN HIT "CONTINUE"/
4347              645:
4348 020624              TYPE    675              :TYPE ASCIZ STRING
4349 020624 104400 020632      BR       665              :GET OVER THE ASCIZ
4350 020630 000424              :675: .ASCIZ (15<<12>>IF ALREADY LOADED THEN HIT "CONTINUE"/
4351              655:
4352 020702 000000              HALT                    :WAIT FOR CONTINUE
4353
4354
4355 020704 004737 041366      JSR      PC,0#CLDISK    :SET R1-RHCS1, R2-RHCS2
4356              :R3-RHDS1, R4-RHER1
4357              :GIVE RH-11 INITIALIZE
4358              :SETUP UNIT NUMBER
4359 020710 004737 041424      JSR      PC,0#CHECK     :CHECK THAT DVA,RDY,MOL,DPR,DRY = 1
4360              :AND THAT NO STATUS BITS ARE STUCK = 1
4361 020714 104400 056402      TYPE    ,CPHALT        :CANNOT CONTINUE TESTS IF THEY AREN'T
4362 020720 000000              HALT                    :STOP
4363
4364              :*SET VV IN RHDS1 WITH PACK ACKNOWLEDGE
4365
4366
4367 020722 013746 002460      MOV      0#PKACK,-(SP)  :GET READY TO MOVE COMMAND
4368 020726 052716 000001      BIS      #GO,(SP)      :GET READY TO SET GO
4369              :WITHOUT INTERRUPT ENABLE
4370 020732 012577 161342      MOV      (SP)+,0#RHDS1 :GO WITH
4371              :22 IN RHCS1 FOR PACK ACKNOWLEDGE
4372              :WITH INTERRUPT DISABLED
4373
4374
4375
4376 020736 104412              WAT                    :WAIT FOR VV BIT TO SET
4377 020740 002322              RHDS1                  :WAIT FOR RHDS1 REGISTER
4378 020742 000100              VV                      :WAIT FOR VV BIT IN RHDS1 REGISTER
4379 020744 000001              1.                      :ALLOW 10 MICRO SECONDS
4380 020746 000001              1.                      :VV MUST SET BETWEEN
4381              :00 AND 20 MICRO SECONDS
4382
4383
4384 020750              65:
4385 020750 004737 041446      JSR      PC,0#CHECKT   :CHECK DVA,RDY,MOL,DPR & VV = 1
4386 020754 000240              NOP                    :CHECK THAT ALL OTHER BITS = 0
4387 020756 000240              NOP                    :UNLIKE THE OTHER STATUS BIT TESTS,
4388 020760 000240              NOP                    :THERE IS NO HALT IF IT FAILS - IT IS
4389              :USED IN THE MIDDLE OF A SINGLE TEST
4390
4391
4392 020762 012700 002272      MOV      #RHWC,RO      :ADDR. OF ADDR OF RHWC IN RO
4393
4394              :*LOAD ALL POSSIBLE REGISTERS WITH ONES
    
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4439 020766 012730 177777      MOV      #177777,2(RO)+ ;LOAD 177777 INTO RMWC
4440 020772 012730 177777      MOV      #177777,2(RO)+ ;LOAD 177777 INTO RHBA
4441 020776 052730 000010      BIS      #10,2(RO)+ ;LOAD 10 INTO RHCS2
4442 021002 012730 001400      MOV      #1400,2(RO)+ ;LOAD 1400 INTO RHCS1
4443 021006 012730 000000      MOV      #0,2(RO)+ ;LOAD 0 INTO RHER1
4444 021012 012730 177777      MOV      #177777,2(RO)+ ;LOAD 177777 INTO RHDST
4445 021016 012730 000000      MOV      #0,2(RO)+ ;LOAD 0 INTO RHER2
4446 021022 012730 177777      MOV      #177777,2(RO)+ ;LOAD 177777 INTO RHOF
4447 021026 012730 177777      MOV      #177777,2(RO)+ ;LOAD 177777 INTO RHCA
4448 021032 012730 000000      MOV      #0,2(RO)+ ;LOAD 0 INTO RHER3

; *NOW SET BITS IN RHAS FOR ALL DRIVES PRESENT
4449 021036 010046      MOV      RO,-(SP) ;: PUSH RO ON STACK
4450 021040 011446      MOV      2R4,-(SP) ;: SAVE RHER1 TO REINSTATE LATER
4451 021042 011246      MOV      2R2,-(SP) ;: SAVE RHCS2 TO BE REINSTATED
4452 021044 013700 004742      MOV      2#TOTALAT,RC ;: GET DRIVES PRESENT
4453 021050 005012      CLR      2R2 ;: CLEAR RHCS2 AND CARRY
4454 021052 012705 000010      MOV      #8,R5 ;: COUNTER
4455 021056 006000      91$: RCR      RO ;: GET BIT INTO CARRY
4456 021060 103002      BCC     92$ ;: BRANCH IF NO UNIT ON THIS BIT
4457 021062 012714 177777      MOV      #-1,2R4 ;: MOVE INTO ERROR REGISTER
4458 021066 005212      92$: INC      2R2 ;: TO SET ATA BIT
4459 021070 005305      DEC      R5 ;: INCREMENT RHCS2 TO NEXT UNIT
;: COUNT

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4449 021072 001271      BNE      91$           ;BRANCH IF 8 NOT DONE
4450 021074 012612      MOV      (SP)+,R2     ;REINSTATE RHCS2
4451 021076 012614      MOV      (SP)+,R4     ;REINSTATE RHER1
4452 021100 012600      MOV      (SP)+,R0     ;POP STACK INTO R0
4453 021102 005720      TST     (R0)+        ;GET OVER PHAS IN R0

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:\*COMPARE CONTENTS OF RHCS1 AND RHDS1, WHICH WERE SAVED  
:\*DURING THE UNLOAD COMMAND, WITH THE EXPECTED RESULTS

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021170 013746 002424 MOV 3#UNLOAD -(SP) ;PUSH COMMAND ON STACK
021174 052716 004201 BIS #DVA:GO:RDY.(SP) ;INCLUDE THESE BITS SET
021200 005737 004722 TST 2#NUNIT ;IS THERE MORE THAN ONE UNIT ?
021204 001413 BEQ 95 ;SKIP NEXT IF ONLY ONE UNIT
021206 010037 004760 MOV R0,2#TMP4 ;PUT SAVED RHCS1 INTO TMP4
021212 042737 177677 004760 BIC #IE,2#TMP4 ;MASK ALL BUT THE 'IE' BIT IN RHCS1
021220 042716 000100 BIC #IE,(SP) ;CLEAR 'IE' IN EXPECTED DATA
021224 053716 004760 BIS 2#TMP4,(SP) ;SET 'IE' STATE FROM ACTUAL RHCS1 DATA
021230 052716 100000 BIS #SC,(SP) ;SET 'SC' IN RHCS1 SAVED DATA

021234 011637 001124 95: MOV (SP),2#$GDDAT ;SAVE EXPECTED DATA FOR PRINTOUT
021240 022600 CMP (SP)+,R0 ;COMPARE EXPECTED DATA WITH SAVED
;RHCS1 DATA AND RESET THE STACK
021242 001405 BEQ 105 ;CHECK NEXT BITS IF THESE JK
021244 010037 001126 MOV R0,2#$BDDAT ;RHCS1 IS BAD - PRINT IT OUT
021250 010137 004600 MOV R1,2#REGADR ;REGISTER ADDRESS
021254 104021 ERROR 21 ;DURING ABOVE OPERATION ONLY THE
; 'DVA' 'GO' 'RDY' AND COMMAND BITS
; SHOULD BE SET

021256 012746 020400 105: MOV #PIP:DPR, -(SP) ;PUT SOME EXPECTED RHDS1 BITS ON STACK
021262 010537 004760 MOV R5,2#TMP4 ;PUT SAVED RHDS1 INTO TMP4
021266 042737 167677 004760 BIC #C<MOL!VV>,2#TMP4 ;MASK ALL BUT 'MOL' & 'VV' IN RHDS1
021274 042716 010100 BIC #MOL!VV,(SP) ;CLEAR 'MOL' & 'VV' IN EXPECTED RHDS1
021300 053716 004760 BIS 2#TMP4,(SP) ;SET EXPECTED 'MOL' & 'VV' BIT STATES
; FROM THE ACTUAL DATA (DON'T CARE)
021304 011637 001124 MOV (SP),2#$GDDAT ;SAVE EXPECTED DATA FOR PRINTOUT
021310 022605 CMP (SP)+,R5 ;COMPARE EXPECTED DATA WITH SAVED
;RHDS1 DATA AND RESET THE STACK
021312 001405 BEQ 115 ;CONTINUE IF EXPECTED=SAVED
021314 010537 001126 MOV R5,2#$BDDAT ;RHDS1 IS BAD - PRINT IT OUT
021320 010337 004600 MOV R3,2#REGADR ;REGISTER ADDRESS
021324 104063 ERROR 63 ;DURING THE ABOVE OPERATION, ONLY 'PIP'
; AND 'DPR' SHOULD BE SET
; 'MOL' & 'VV' ARE DON'T CARES

021326 113:

021326 104400 021334 TYPE 965 ;:TYPE ASCIZ STRING
021332 000425 BR 955 ;:GET OVER THE ASCIZ
;:965: .ASCIZ '15'<12> IF STANDBY NOT LIT - ERROR AFTER UNLOAD
;:955:
;:THIS PROVIDES A 1 SECOND "STALL"

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;\*NOW CHANGE REGISTERS SAVED BEFORE UNLOAD COMMAND  
;\*TO EXPECTED VALUES AFTER UNLOAD COMMAND

;\* - AGAIN 'MOL' & 'VV' ARE DON'T CARES

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021406 012746 020400  
021412 017737 169704 004760  
021420 042737 167677 004760  
021426 042716 010100  
021432 053716 004760  
  
021436 042716 100200  
021442 012637 004642  
  
021446 004037 042246  
021452 002300  
  
021454 000001  
021456 000001  
021460 000001  
  
021462 005737 004722  
021466 001006  
  
021470 004037 042246  
021474 002300  
  
021476 000001  
021500 000000  
021502 100000  
021504  
  
021504 043737 004740 004636  
  
021512 004037 042354  
021516 004612  
021520 002354  
021522 000021  
021524 021530  
021526 021534  
  
021530 104023  
021532 000207

MOV #PIP:DPR, -(SP) ;SET EXPECTED FINAL RHDS1 BITS  
MOV @RHDS1, @TMP4 ;GET PRESENT ACTUAL RHDS1 CONTENTS  
BIC #1C<MOL!VV>, @TMP4 ;MASK OUT ALL BUT 'MOL' & 'VV'  
BIC #MOL!VV, (SP) ;CLEAR 'MOL' & 'VV' IN EXPECTED RHDS1  
SIS @TMP4, (SP) ;SET EXPECTED 'MOL' & 'VV' STATES  
;FROM THE ACTUAL (DON'T CARE COND.)  
BIC #ATA!DRY, (SP) ;CLEAR THESE ADDITIONAL RHDS1 BITS  
MOV (SP)+, @SAVERE+30 ;CHANGE THE SAVED RHDS1 REGISTER  
;AND ADJUST THE STACK  
  
JSR R0, @CHREG ;CHANGE BITS IN SAVED REGISTER  
RHCS1 ;CHANGE RHCS1 REGISTER  
  
1 ;1 BIT/BITS TO BE CHANGED  
1 ;NEW VALUE OF GO IS 1  
GC ;CHANGE GO BIT  
  
TST @NUNIT ;IS THERE MORE THAN ONE UNIT ?  
BNE 7\$ ;SKIP NEXT IF MORE THAN ONE UNIT  
  
JSR R0, @CHREG ;CHANGE BITS IN SAVED REGISTER  
RHCS1 ;CHANGE RHCS1 REGISTER  
  
1 ;1 BIT/BITS TO BE CHANGED  
0 ;NEW VALUE OF SC IS 0  
SC ;CHANGE SC BIT  
  
BIC @ATTENT, @SAVERE+24 ;CLEAR APPROPRIATE ATA BITS  
;FOR WORKING DRIVE IN SAVED RHAS  
  
;\*NOW COMPARE REGISTERS AFTER THE UNLOAD COMMAND  
;\*WITH EXPECTED VALUES  
  
JSR R0, @COMREG ;COMPARE SAVED REGISTERS WITH  
;PRESENT VALUE  
SAVERE ;GOOD DATA SAVED IN 'SAVERE'  
WC ;TEST DATA STARTING FROM 'RHWC'  
17 ;17 REGISTERS TO BE COMPARED  
3\$ ;RETURN TO 3\$ ON ERROR  
4\$ ;RETURN TO 4\$ ON NO ERROR  
  
3\$: ERROR 23 ;UNLOAD COMMAND GAVE  
RTS PC ;AN ERROR

7\$:

3\$:

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4599                                     :GOOD DATA GIVES WHAT SHOULD
4600                                     :BE THERE
4601                                     :RECEIVED DATA GIVES WHAT WAS
4602                                     :THERE AFTER UNLOAD COMMAND
4603
4604 021534                               45:
4605 021534 104400 021542                TYPE 99$      ::TYPE ASCIZ STRING
4606 021540 000406                        BR 97$       ::GET OVER THE ASCIZ
4607                                     ::99$: .ASCIZ <15><12>/ON DRIVE /
4608                                     97$:
4609 021556                                MOV 2*UNIT,-(SP) ;GET UNIT UNDER TEST
4610 021556 013746 004716                TYPDS
4611 021564 104404                        TYPE 100$    ::TYPE ASCIZ STRING
4612 021570 000444                        BR 99$       ::GET OVER THE ASCIZ
4613                                     ::100$: .ASCIZ <15><12>/IF STANDBY LIT HIT STANDBY OR START - AFTER HEAD LOAD HIT CONT:
4614 021702                                99$:
4615
4616 021702 005037 045760                CLR 2*PRITEM ;CLEAR PREVIOUS ERROR NUMBER
4617 021706 000000                        HALT        ;WAIT FOR CONTINUE
4618 021710 004737 041424                JSR PC,2*CHECK ;CHECKS THAT DVA,RDY,MOL & CPR = 1
4619 021714 000240                        NOP        ;CHECKS THAT ALL OTHER BITS = 0
4620 021716 000240                        NOP        ;THERE IS NO HALT IF IT FAILS - IT
4621 021720 000240                        NOP        ;IS USED IN THE MIDDLE OF A TEST
4622
4623
4624                                     :*SET VV IN RHDS1 AFTER RESET FROM ACTUATING
4625                                     :*THE STANDBY SWITCH AND CYCLING UP (MOL = 1)
4626
4627
4628 021722 013746 002460                MOV 2*PKACK,-(SP) ;GET READY TO MOVE COMMAND
4629 021726 052716 000001                BIS #GO,(SP)    ;GET READY TO SET GO
4630                                     ;WITHOUT INTERRUPT ENABLE
4631 021732 012677 160342                MOV (SP)+,2*RHCS1 ;GO WITH
4632                                     ;22 IN RHCS1 FOR PACK ACKNOWLEDGE
4633                                     ;WITH INTERRUPT DISABLED
4634
4635 021736 011100                        MOV 2*R1,R0    ;SAVE RHCS1 DURING ABOVE OPERATION
4636 021740 011305                        MOV 2*R3,R5    ;SAVE RHDS1 DURING ABOVE OPERATION
4637
4638 021742 104412                        WAT
4639 021744 002322                        RHDS1
4640 021746 000100                        VV
4641 021750 000001                        1.
4642 021752 000001                        1.
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::\*\*\*\*\*  
:\*TEST 31 OFFSET AND RETURN TO CENTER LINE COMMAND

:\* THIS TESTS TWO COMMANDS: (1)OFFSET, (2)RETURN-TO-CENTER-LINE  
:\* ALL POSSIBLE REGISTERS ARE FILLED WITH ONES (EXCEPT RHOF)  
:\* AND AN OFFSET IS GIVEN  
:\* ALL REGISTERS ARE COMPARED, ONLY ATA SHOULD SET  
:\* ALL OTHER REGISTERS SHOULD REMAIN UNCHANGED  
:\* THEN A RETURN-TO-CENTER-LINE IS GIVEN  
:\* ALL REGISTERS ARE COMPARED ONLY ATA SHOULD SET  
:\* AND RHOF SHOULD CLEAR (EXCEPT HCI,ECI,FMT22)  
:\* ALL OTHER REGISTERS SHOULD REMAIN UNCHANGED  
:\* THE ABOVE PROCESS IS REPEATED FOR OFFSET REGISTER  
:\* VALUES OF 1 TO 377 IE. 377 TIMES

::\*\*\*\*\*

021754 000004  
021756 012737 022030 001106  
021764 012706 001000  
021770 012737 000031 004604  
021776 004737 041366  
022002 004737 041424  
022006 104400 066402  
022012 000000  
022014 112737 000001 004610  
022022 112737 000034 004611

TST31: SCOPE  
MOV #15,\$LPADR ;:SET SCOPE LOOP ADDRESS  
MOV #STACK,SP ;:RESET STACK  
MOV #31,\$TSTNM ;:SAVE TEST NUMBER  
JSR PC,\$CLDISK ;:SET R1-RHCS1, R2-RHCS2  
;:R3-RHDS1, R4-RHER1  
;:GIVE RH-11 INITIALIZE  
;:SETUP UNIT NUMBER  
JSR PC,\$CHECK ;:CHECK THAT DVA,RDY,MOL,DPR,DRY = 1  
;:AND THAT NO STATUS BITS ARE STUCK = 1  
TYPE ,CPHALT ;:CANNOT CONTINUE TESTS IF THEY AREN'T  
HALT ;:STOP  
MOVB #1,\$OFSTVL ;:SET OFFSET VALUE TO 1  
MOVB #34,\$OFSTVL+1 ;:SET HCI,ECI,FMT22

18:

022030  
022030 004737 041366  
022034 004737 041446  
022040 000240  
022042 000240  
022044 000240  
022046 012700 002272  
022052 012730 177777  
022056 012730 177777

JSR PC,\$CLDISK ;:SET R1-RHCS1, R2-RHCS2  
;:R3-RHDS1, R4-RHER1  
;:GIVE RH-11 INITIALIZE  
;:SETUP UNIT NUMBER  
JSR PC,\$CHECKT ;:CHECK DVA,RDY,MOL,DPR & VV = 1  
NOP ;:CHECK THAT ALL OTHER BITS = 0  
NOP ;:UNLIKE THE OTHER STATUS BIT TESTS,  
NOP ;:THERE IS NO HALT IF IT FAILS - IT IS  
;:USED IN THE MIDDLE OF A SINGLE TEST  
MOV #RHWC,RO ;:ADDR. OF ADDR OF RHWC IN RO  
MOV #177777,\$(RO)+ ;:LOAD 177777 INTO RHWC  
MOV #177777,\$(RO)+ ;:LOAD 177777 INTO RHBA

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4699
4700 022062 052730 000010      BIS      #10,2(RO)+      ;LOAD 10 INTO RHCS2
4701
4702 022066 012730 001400      MOV      #1400,2(RO)+  ;LOAD 1400 INTO RHCS1
4703
4704
4705 022072 012730 000000      MOV      #0,2(RO)+    ;LOAD 0 INTO RHER1
4706
4707
4708 022076 012730 177777      MOV      #177777,2(RO)+ ;LOAD 177777 INTO RHDST
4709
4710
4711 022102 012730 000000      MOV      #0,2(RO)+    ;LOAD 0 INTO RHER2
4712
4713      ;*THE OFFSET REGISTER WILL BE INCREMENTED FROM 0 TO 377
4714
4715 022106 013730 004610      MOV      2#OFSTVL,2(RO)+ ;SET OFFSET REGISTER
4716
4717 022112 012730 177777      MOV      #177777,2(RO)+ ;LOAD 177777 INTO RHCA
4718
4719 022116 012730 000000      MOV      #0,2(RO)+    ;LOAD 0 INTO RHER3
4720
4721      ;*NOW SET BITS IN RHAS FOR ALL DRIVES PRESENT
4722
4723 022122 010046      MOV      RO,-(SP)      ;: PUSH RO ON STACK
4724 022124 011446      MOV      2R4,-(SP)     ;: SAVE RHER1 TO REINSTATE LATER
4725 022126 011246      MOV      2R2,-(SP)     ;: SAVE RHCS2 TO BE REINSTATED
4726      ;: AFTER ALL ATA BITS HAVE BEEN SET
4727 022130 013700 004742      MOV      2#TOTALAT,RO  ;: GET DRIVES PRESENT
4728 022134 005012      CLR      2R2          ;: CLEAR RHCS2 AND CARRY
4729 022136 012705 000010      MOV      #8,R5        ;: COUNTER
4730 022142 006000      82$: ROR      RO      ;: GET BIT INTO CARRY
4731 022144 103002      BCC      83$         ;: BRANCH IF NO UNIT ON THIS BIT
4732 022146 012714 177777      MOV      #-1,2R4      ;: MOVE INTO ERROR REGISTER
4733      ;: TO SET ATA BIT
4734 022152 005212      83$: INC      2R2      ;: INCREMENT RHCS2 TO NEXT UNIT
4735 022154 005305      DEC      R5          ;: COUNT
4736 022156 001371      BNE      82$         ;: BRANCH IF 8 NOT DONE
4737 022160 012612      MOV      (SP)+,2R2    ;: REINSTATE RHCS2
4738 022162 012614      MOV      (SP)+,2R4    ;: REINSTATE RHER1
4739 022164 012600      MOV      (SP)+,RO     ;: POP STACK INTO RO
4740 022166 005720      TST      (RO)+       ;: GET OVER PHAS IN RO
4741
4742
4743 022170 012730 177776      MOV      #177776,2(RO)+ ;LOAD 177776 INTO RHMR
4744
4745 022174 013777 002454 160076      MOV      2#OFSETC,2RHCS1 ;GET READY FOR OFSETC
4746
4747
4748      ;*NOW SAVE REGISTERS FOR COMPARISON AFTER OFFSET
4749
4750 022202 004037 041534      JSR      RO,2#SAVER   ;: SAVE REGISTERS
4751 022206 002272      RHWC     ;: RHWC IS THE FIRST REGISTER SAVED
4752 022210 004612      SAVERE   ;: STARTING ADDRESS OF WHERE

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4807 022354 001405          BEQ      88$          ;BRANCH IF GOOD
4808 022356 010537 001126    MOV      R5,2#$BDDAT ;BAD DATA
4809 022362 010337 004600    MCV     R3,2#$REGADR ;FAILING REGISTER RHDS!
4810 022366 104063          ERROR   63          ;DURING ABOVE OPERATION ONLY
4811                                     ;PIP!MOL!DPR!VV SHOULD BE SET
4812 022370          88$:
4813
4814                                     ;*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES
4815
4816
4817 022370 004037 042246    JSR     RO,2#CHREG   ;CHANGE BITS IN SAVED REGISTER
4818 022374 002300          RHCS1          ;CHANGE RHCS1 REGISTER
4819
4820 022376 000001          1            ;1 BIT/BITS TO BE CHANGED
4821 022400 000001          1            ;NEW VALUE OF SC IS 1
4822 022402 100000          SC           ;CHANGE SC BIT
4823
4824 022404 004037 042246    JSR     RO,2#CHREG   ;CHANGE BITS IN SAVED REGISTER
4825 022410 002322          RHDS1          ;CHANGE RHDS1 REGISTER
4826
4827 022412 000001          1            ;1 BIT/BITS TO BE CHANGED
4828 022414 000001          1            ;NEW VALUE OF ATA IS 1
4829 022416 100000          ATA          ;CHANGE ATA BIT
4830
4831 022420 053737 004740 004636  BIS     2#ATTENT,2#SAVERE+24 ;SET APPROPRIATE 'ATA' BITS
4832                                     ;FOR WORKING DRIVE IN
4833                                     ;SAVED RHAS LOACTION
4834
4835                                     ;*NOW COMPARE REGISTERS AFTER AN OFFSET COMMAND
4836
4837
4838 022426 004037 042354    JSR     RO,2#COMREG   ;COMPARE SAVED REGISTERS WITH
4839                                     ;PRESENT VALUE
4840 022432 004612          SAVERE        ;GOOD DATA SAVED IN 'SAVERE'
4841 022434 002354          WC           ;TEST DATA STARTING FROM 'RHWC'
4842 022436 000022          18.          ;18. REGISTERS TO BE COMPARED
4843 022440 022444          2$           ;RETURN TO 2$ ON ERROR
4844 022442 022450          3$           ;RETURN TO 3$ ON NO ERROR
4845
4846 022444 104024          2$:          ERROR   24          ;OFFSET COMMAND CAUSED AN ERROR
4847 022446 000207          RTS         PC          ;GOOD DATA IS WHAT SHOULD BE THERE
4848                                     ;RECEIVED DATA GIVES WHAT WAS THERE
4849                                     ;AFTER AN OFFSET COMMAND
4850
4851 022450 013777 004740 157640 3$:    MOV     2#ATTENT,2#RHAS ;CLEAR WORKING DRIVE ATTENTION
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4856                                     ;*NOW A RETURN TO CENTER LINE COMMAND WILL BE GIVEN
4857 ;*****
4858
4859 022456 004737 041446    JSR     PC,2#CHECKT   ;CHECK DVA,RDY,MOL,DPR & VV = 1
4860 022462 000240          NOP           ;CHECK THAT ALL OTHER BITS = 0

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4861 022464 000240      NOP      ;UNLIKE THE OTHER STATUS BIT TESTS,
4862 022466 000240      NOP      ;THERE IS NO HALT IF IT FAILS - IT IS
4863                      ;USED IN THE MIDDLE OF A SINGLE TEST
4864 022470 013777 002456 157602  MOV      @#RETCL,@RHCS1 ;GET READY FOR RETCL
4865                      ;RETURN TO CENTER LINE WITH 16 IN RHCS1
4866
4867
4868                      ;*NOW REGISTERS ARE SAVED FOR COMPARISON AFTER COMMAND
4869
4870 022476 004037 041534  JSR      RO,@#SAVER    ;SAVE REGISTERS
4871 022502 002272      RHWC     ;RHWC IS THE FIRST REGISTER SAVED
4872 022504 004612      SAVERE   ;STARTING ADDRESS OF WHERE
4873                      ;THE REGISTERS ARE SAVED
4874 022506 000022      18.      ;NUMBER OF REGISTERS
4875                      ;SAVED = 18.
4876
4877 022510 013777 004606 157550  MOV      @#RPO4VEC,@RPOVEC ;SET RPO4 VECTOR ADDRESS
4878                      ;TO 'TIME1' IF P-CLOCK IS PRESENT
4879                      ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
4880                      ;'TIME' WILL ONLY SAVE
4881                      ;CURRENT CYLINDER ADDRESS
4882                      ;AND LOOK AHEAD REGISTERS
4883
4884
4885 022516 013746 002456  MOV      @#RETCL,-(SP) ;GET READY TO MOVE COMMAND
4886 022522 052716 0001C1  BIS      #GO!IE,(SP)  ;GET READY TO SET 'GO' AND
4887                      ;ENABLE INTERRUPT
4888 022526 012677 157546  MOV      (SP)+,@RHCS1 ;GO WITH
4889                      ;16 IN RHCS1 FOR RETURN TO CENTER LINE
4890                      ;WITH INTERRUPT ENABLED
4891 022532 011100      MOV      @R1,RO      ;SAVE RHCS1 DURING ABOVE OPERATION
4892 022534 011305      MOV      @R3,R5      ;SAVE RHDS1 DURING ABOVE OPERATION
4893
4894
4895 022536 104412      WAT      ;WAIT FOR DRY BIT TO SET
4896 022540 002322      RHDS1   ;WAIT FOR RHDS1 REGISTER
4897 022542 000200      DRY     ;WAIT FOR DRY BIT IN RHDS1 REGISTER
4898 022544 001750      1000.  ;ALLOW 10000 MICRO SECONDS
4899 022546 001750      1000.  ;DRY MUST SET BETWEEN
4900                      ;00 AND 20000 MICRO SECONDS
4901
4902                      ;*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN
4903                      ;*RO AND R5 IMMEDIATELY AFTER GO
4904
4905 022550 013746 002456  MOV      @#RETCL,-(SP) ;SAVE COMMAND
4906 022554 052716 004301  BIS      #DVA!GO!IE!RDY,(SP) ;INCLUDE DVA!GO!IE!RDY
4907 022560 005737 004722  TST      @#NUNIT      ;ARE THERE MORE THAN ONE UNIT
4908 022564 001413      BEQ     90$          ;BRANCH IF ONLY ONE UNIT
4909 022566 010037 004760  MOV      RO,@#TMP4    ;GET RHCS1
4910 022572 042737 177677 004760  BIC      #!CIE,@#TMP4 ;KEEP IE BIT
4911 022600 042716 000100  BIC      #IE,(SP)     ;CLEAR IE IN GOOD DATA
4912 022604 053716 004760  BIS      @#TMP4,(SP)  ;GET IE AS IS
4913 022610 052716 100000  BIS      #SC,(SP)    ;SET SC IN RHCS1
4914 022614
    
```

90\$:



```

4915 022614 011637 001124      MOV      (SP),@#$GDDAT      ;SAVE FOR PRINTOUT
4916 022620 022600              CMP      (SP)+,R0          ;DURING ABOVE OPERATION ONLY DVA!GO!IE!RDY
4917                                ;AND COMMAND SHOULD BE SET
4918 022622 001405              BEQ      89$              ;BRANCH IF GOOD
4919 022624 010037 001126      MOV      R0,@#$BDDAT      ;BAD DATA
4920 022630 010137 004600      MOV      R1,@#REGADR      ;FAILING REGISTER RHCS1
4921 022634 104021              ERROR    21              ;DURING ABOVE OPERATION ONLY
4922                                ;COMMAND AND DVA!GO!IE!RDY SHOULD BE SET
4923 022636 012746 030500      89$:  MOV      #PIP!MOL!DPR!VV, -(SP) ;SAVE BITS SET DURING OPERATION IN RHDS1
4924 022642 011637 001124      MOV      (SP),@#$GDDAT      ;SAVE FOR PRINTOUT
4925 022646 022605              CMP      (SP)+,R5          ;DURING ABOVE OPERATION ONLY PIP!MOL!DPR!VV
4926                                ;SHOULD BE SET
4927 022650 001405              BEQ      91$              ;BRANCH IF GOOD
4928 022652 010537 001126      MOV      R5,@#$BDDAT      ;BAD DATA
4929 022656 010337 004600      MOV      R3,@#REGADR      ;FAILING REGISTER RHDS1
4930 022662 104063              ERROR    63              ;DURING ABOVE OPERATION ONLY
4931                                ;PIP!MOL!DPR!VV SHOULD BE SET
4932 022664              91$:
4933
4934                                ;*NOW CHANGE SAVED REGISTER TO EXPECTED VALUE
4935
4936
4937 022664 004037 042246      JSR      R0,@#CHREG        ;CHANGE BITS IN SAVED REGISTER
4938 022670 002300              RHCS1                    ;CHANGE RHCS1 REGISTER
4939
4940                                ;1 BIT/BITS TO BE CHANGED
4941 022672 000001              1                          ;NEW VALUE OF SC IS 1
4942 022674 000001              1                          ;CHANGE SC BIT
4943 022676 100000              SC
4944
4945 022700 004037 042246      JSR      R0,@#CHREG        ;CHANGE BITS IN SAVED REGISTER
4946 022704 002322              RHDS1                    ;CHANGE RHDS1 REGISTER
4947
4948                                ;1 BIT/BITS TO BE CHANGED
4949 022706 000001              1                          ;NEW VALUE OF ATA IS 1
4950 022710 000001              1                          ;CHANGE ATA BIT
4951 022712 100000              ATA
4952
4953 022714 053737 004740 004636      BIS      @#ATTENT,@#SAVERE+24 ;SET APPROPRIATE 'ATA' BITS
4954                                ;FOR WORKING DRIVE IN
4955                                ;SAVED RHAS LOACTION
4956 022722 004037 041270      JSR      R0,@#FILLRE      ;MOV BIT15!HCI!ECI!FMT22 INTO SAVED RHOF
4957 022726 002310              RHOF                    ;SAVED REGISTER TO CHANGE
4958 022730 116000              BIT15!HCI!ECI!FMT22      ;DATA
4959
4960                                ;*NOW COMPARE REGISTERS AFTER RETURN-TO-CENTER-LINE
4961
4962 022732 004037 042354      JSR      R0,@#COMREG      ;COMPARE SAVED REGISTERS WITH
4963                                ;PRESENT VALUE
4964 022736 004612              SAVERE                    ;GOOD DATA SAVED IN 'SAVERE'
4965 022740 002354              WC                        ;TEST DATA STARTING FROM 'RHWC'
4966 022742 000022              18.                       ;18. REGISTERS TO BE COMPARED
4967 022744 022750              4$                        ;RETURN TO 4$ ON ERROR
4968 022746 022754              5$                        ;RETURN TO 5$ ON NO ERROR
4969 022750 104025              4$:  ERROR    25          ;RETURN TO CENTER-LINE
4970 022752 000207              RTS      PC                ;COMMAND CAUSED AN ERROR
    
```



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023016 000004  
023020 012737 000764 004756  
023026  
023026 012706 001000  
023032 012737 000032 004604  
023040 004737 041366  
023044 004737 041446  
023050 104400 066402  
023054 000000  
023056 004037 041352  
023062 000260  
023064 013746 002454  
023070 052716 000101  
023074 012677 157200  
023100 104412  
023102 002322  
023104 000200  
023106 000536  
023110 000536  
023112 032777 040000 157202  
023120 001417  
023122 104026  
023124 000004  
023126 004737 041366  
023132 013746 002426  
023136 052716 000101  
023142 012677 157132

```
::*****  
:TEST 32          OFFSET COMMAND  
:  
:  THIS TEST WILL ONLY GIVE REPEATED OFFSETS  
:  
::*****  
TST32: SCOPE  
MOV      #500.,R#TMP1 ;COUNTER  
IS:  
MOV      #STACK,SP    ;RESET STACK  
MOV      #32,R#TSTNM  ;SAVE TEST NUMBER  
JSR      PC,R#CLDISK  ;SET R1-RHCS1, R2-RHCS2  
          ;R3-RHDS1, R4-RHER1  
          ;GIVE RH-11 INITIALIZE  
          ;SETUP UNIT NUMBER  
JSR      PC,R#CHECKT  ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1  
          ;AND THAT NO STATUS BITS ARE STUCK = 1  
          ;CANNOT CONTINUE TESTING IF ANY OF  
          ;THE FIRST SET OF BITS DON'T = 1  
          ;STOP  
JSR      R0,R#OFFSET  ;OFFSET  
          ;260 IN OFFSET REGISTER  
          ;OFFSET -1200 MICRO INCHES  
  
MOV      R#OFFSEC, -(SP) ;GET READY TO MOVE COMMAND  
BIS      #GO!IE, (SP)   ;GET READY TO SET 'GO' AND  
          ;ENABLE INTERRUPT  
MOV      (SP)+,R#RHCS1 ;GO WITH  
          ;14 IN RHCS1 FOR OFFSET  
          ;WITH INTERRUPT ENABLED  
  
WAT  
RHCS1  
DRY  
350.  
350.  
;WAIT FOR DRY BIT TO SET  
;WAIT FOR RHDS1 REGISTER  
;WAIT FOR DRY BIT IN RHDS1 REGISTER  
;ALLOW 3500 MICRO SECONDS  
;DRY MUST SET BETWEEN  
;00 AND 7000 MICRO SECONDS  
BIT      #ERR,R#RHDS1  ;IS ERR SET?  
BEQ      26  
          ;NO  
ERROR    26  
SCOPE  
;REPEATED OFFSETS CAUSED AN ERROR  
  
JSR      PC,R#CLDISK  ;SET R1-RHCS1, R2-RHCS2  
          ;R3-RHDS1, R4-RHER1  
          ;GIVE RH-11 INITIALIZE  
          ;SETUP UNIT NUMBER  
  
MOV      #RECALI, -(SP) ;GET READY TO MOVE COMMAND  
BIS      #GO!IE, (SP)   ;GET READY TO SET 'GO' AND  
          ;ENABLE INTERRUPT  
MOV      (SP)+,R#RHCS1 ;GO WITH  
          ;6 IN RHCS1 FOR RECALIBRATE
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```

.SBTTL READ/WRITE TESTS USING MEDIA
:*****
:*TEST 33 WRITE/READ HEADER AND DATA (0'S)
:
:* WRITE HEADER AND DATA CYLINDER 0, FORMAT 16 BITS PER WORD
:* TRACK 0, SECTOR 0, KEYS=0, NUMBER OF WORDS 256 WORDS
:* OF 0
:* THEN READ HEADER AND DATA FOR ABOVE.
:* WRITE FROM BUFFER AND READ INTO BUFFER ARE FILLED WITH
:* 10000 0 0 0, AND 256 OF 0
:* THE WRITE COMMAND IS THEN LOADED INTO THE REGISTERS EXCEPT
:* THE GO BIT, AND ALL THE REGISTERS ARE SAVED
:* THEN GO IS GIVEN FOR WRITE HEADER AND DATA
:
:* THEN ALL REGISTERS ARE COMPARED TO CHECK FOR IMPROPER CHANGED
:* THEN WRITE FROM BUFFER IS CHECKED TO SEE THAT NOTHING CHANGED
:
:* NOW FOR THE READ COMMAND READ INTO BUFFER IS FILLED
:* WITH ALL ONES, COMMAND IS LOADED INTO REGISTERS EXCEPT
:* GO BIT AND ALL REGISTERS ARE SAVED
:* GO IS GIVEN FOR THE READ COMMAND
:
:* ALL REGISTERS ARE CHECKED FOR IMPROPER CHANGE
:* THEN THE READ DATA IS COMPARED.

```

```

023170 000004
023172 012706 001000
023176 012737 000033 004604
023204 004737 041356
023210 004037 041212
023214 002470
023216 000004
023220 010000
023222 000000
023224 000000
023226 000000
023230 004037 041236
023234 002500
023236 000400
023240 000000

```

```

:*****
*ST33: SCOPE
MOV #STACK, SP :RESET STACK
MOV #33, 2*STNM :SAVE TEST NUMBER
JSR PC, 2*CLDISK :SET R1-RHCS1, R2-RHCS2
:R3-RHDS1, R4-RHER1
:GIVE RH-11 INITIALIZE
:SETUP UNIT NUMBER
:
:*FILL WRITE FROM BUFFER WITH HEADER
JSR RO, 2*FLHEAD :SAVE HEADER DATA IN WRFROM
WRFROM :LOCATION WHERE SAVED
4 :NUMBER OF WORDS SAVED
10000 :FIRST DATA WORD
0 :SECOND DATA WORD
0 :THIRD DATA WORD
0 :FOURTH DATA WORD
:
:*FILL WRITE FROM BUFFER WITH DATA
JSR RO, 2*CLAREA :CLEAR 256. WORDS. FROM WRFROM+10
WRFROM+10 :STARTING FROM WRFROM+10
256. :256. WORDS
0 :FILL WITH 0

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: \*NOW READ INTO BUFFER WILL BE FILLED WITH SAME DATA  
: \*AS WRITE FROM BUFFER SO THAT AFTER A WRITE COMPARISONS  
: \*CAN BE MADE TO MAKE SURE THAT WRITE DID NOT  
: \*CHANGE WRITE FROM BUFFER

023242 004037 041212  
023246 003534  
023250 000004  
023252 010000  
023254 000000  
023256 000000  
023260 000000  
023262 004037 041236  
023266 003544  
023270 000400  
023272 000000

JSR RO,3#FLHEAD ;SAVE HEADER DATA IN REINTO  
REINTO ;LOCATION WHERE SAVED  
4 ;NUMBER OF WORDS SAVED  
10000 ;FIRST DATA WORD  
0 ;SECOND DATA WORD  
0 ;THIRD DATA WORD  
0 ;FOURTH DATA WORD  
JSP RO,3#CLAREA ;CLEAR 256. WORDS. FROM REINTO+10  
REINTO+10 ;STARTING FROM REINTO+10  
256. ;256. WORDS  
J ;FILL WITH 0

: \*NOW THE WRITE HEADER AND DATA COMMAND WILL BE FILLED

023274 004037 043340  
023300 000000  
023302 000  
023303 000  
023304 177374  
023306 002470  
023310 000000  
023312 010000  
023314 002444

.BYTE  
.BYTE

JSR RO,3#RUN ;SETUP TO RUN FOR DATA COMMAND  
0 ;CYLINDER 0  
0 ;SECTOR 0  
0 ;TRACK 0  
-256.-4 ;WORD COUNT (DATA) = 256. +  
4 ;4 HEADER WORDS  
WRFROM ;BUS ADDRESS  
;STARTING ADDRESS OF DATA  
;BUFFER = WRFROM  
0 ;DO NOT INHIBIT BUS ADDRESS INCREMENT  
FMT22 ;16 BITS PER WORD FORMAT  
;DO NOT INHIBIT ECC CORRECTION  
;DO NOT INHIBIT HEADER COMPARE  
WRIFOR ;GET READY TO DO A WRIFOR  
;WRITE HEADER AND DATA WITH 62 IN RHCSI

: \*NOW SAVE REGISTERS FOR COMPARISON AFTER WRITE HEADER AND DATA

023316 004037 041534  
023322 002272  
023324 004612  
023326 000021  
023330 004737 041446  
023334 104400 066402  
023340 000000

JSR RO,3#SAVER ;SAVE REGISTERS  
RHWC ;RHWC IS THE FIRST REGISTER SAVED  
SAVERE ;STARTING ADDRESS OF WHERE  
;THE REGISTERS ARE SAVED  
17. ;NUMBER OF REGISTERS  
;SAVED = 17.  
JSR PC,3#CHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1  
;AND THAT NO STATUS BITS ARE STUCK = 1  
TYPE .CPHALT ;CANNOT CONTINUE TESTING IF ANY OF  
;THE FIRST SET OF BITS DON'T = 1  
HALT ;STOP

```

S164
S165 023342 013777 004606 156716 MOV 2#RPO4VEC,2RPVEC ;SET RPO4 VECTOR ADDRESS
S166 ;TO 'TIME1' IF P-CLOCK IS PRESENT
S167 ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
S168 ;'TIME' WILL ONLY SAVE
S169 ;CURRENT CYLINDER ADDRESS
S170 ;AND LOOK AHEAD REGISTERS
S171
S172
S173 023350 013746 002444 MOV 2#WRIFOR,-(SP) ;GET READY TO MOVE COMMAND
S174 023354 052716 000101 BIS #GO!IE,(SP) ;GET READY TO SET 'GO' AND
S175 ;ENABLE INTERRUPT
S176 023350 012677 156714 MOV (SP)+,2RHCSI ;GO WITH
S177 ;62 IN RHCSI FOR WRITE HEADER AND DATA
S178 ;WITH INTERRUPT ENABLED
S179 023364 011100 MOV 2R1,RO ;SAVE RHCSI DURING ABOVE OPERATION
S180 023366 011305 MOV 2R3,RS ;SAVE RHDSI DURING ABOVE OPERATION
S181
S182 ;*ONE REVOLUTION=16670 MICRO SEC, ONE SECTOR = 760 MICRO SEC
S183
S184
S185 023370 104412 WAIT ;WAIT FOR RDY BIT TO SET
S186 023372 002300 RHCSI ;WAIT FOR RHCSI REGISTER
S187 023374 000200 RDY ;WAIT FOR RDY BIT IN RHCSI REGISTER
S188 023376 001614 908. ;ALLOW 9080 MICRO SECONDS
S189 023400 001507 839. ;RDY MUST SET BETWEEN
S190 ;690 AND 17470 MICRO SECONDS
S191
S192 ;*COMPARE CONTENTS OF RHCSI AND RHDSI ALREADY SAVED IN
S193 ;*RO AND RS IMMEDIATELY AFTER GO
S194
S195 023402 013746 002444 MOV 2#WRIFOR,-(SP) ;SAVE COMMAND
S196 023406 052716 004101 BIS #IE!GO!DVA,(SP) ;INCLUDE IE!GO!DVA
S197 023412 011637 001124 MOV (SP),2#SGDDAT ;SAVE FOR PRINTOUT
S198 023416 022600 CMP (SP)+,RO ;DURING ABOVE OPERATION ONLY IE!GO!DVA
S199 ;AND COMMAND SHOULD BE SET
S200 023420 001405 BEQ 64$ ;BRANCH IF GOOD
S201 023422 010037 001126 MOV RO,2#SBDDAT ;BAD DATA
S202 023426 010137 004600 MOV R1,2#REGADR ;FAILING REGISTER RHCSI
S203 023432 104021 ERROR 21 ;DURING ABOVE OPERATION ONLY
S204 ;COMMAND AND IE!GO!DVA SHOULD BE SET
S205 023434 012746 010500 64$: MOV #MOL!DPR!VV,-(SP) ;SAVE BITS SET DURING OPERATION IN RHDSI
S206 023440 011637 001124 MOV (SP),2#SGDDAT ;SAVE FOR PRINTOUT
S207 023444 022605 CMP (SP)+,RS ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
S208 ;SHOULD BE SET
S209 023446 001405 BEQ 66$ ;BRANCH IF GOOD
S210 023450 010537 001126 MOV RS,2#SBDDAT ;BAD DATA
S211 023454 010337 004600 MOV R3,2#REGADR ;FAILING REGISTER RHDSI
S212 023450 104063 ERROR 63 ;DURING ABOVE OPERATION ONLY
S213 ;MOL!DPR!VV SHOULD BE SET
S214 023462 66$:
S215
S216 ;*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES

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S218 023462 004037 041270 JSR RO,0#FILLRE ;MOV 0 INTO SAVED RHWC
S219 023466 002272 RHWC ;SAVED REGISTER TO CHANGE
S220 023470 000000 0 ;DATA
S221 023472 004037 041270 JSR RO,0#FILLRE ;MOV WRFROM+(260.*2) INTO SAVED RHBA
S222 023476 002274 RHBA ;SAVED REGISTER TO CHANGE
S223 023500 003500 WRFROM+(260.*2) ;DATA
S224 023502 004037 041270 JSR RO,0#FILLRE ;MOV 1 INTO SAVED RHDST
S225 023506 002304 RHDST ;SAVED REGISTER TO CHANGE
S226 023510 000001 1 ;DATA
S227
S228 ;*NOW COMPARE REGISTERS BEFORE WRITE HEADER AND DATA
S229 ;*WITH REGISTERS AFTER COMMAND
S230
S231
S232 023512 004037 042354 JSR RO,0#COMREG ;COMPARE SAVED REGISTERS WITH
S233 ;PRESENT VALUE
S234 023516 004612 SAVERE ;GOOD DATA SAVED IN 'SAVERE'
S235 023520 002354 WC ;TEST DATA STARTING FROM 'RHWC'
S236 023522 000021 17. ;17. REGISTERS TO BE COMPARED
S237 023524 023530 1$ ;RETURN TO 1$ ON ERROR
S238 023526 023534 2$ ;RETURN TO 2$ ON NO ERROR
S239
S240 023530 104027 1$: ERROR 27 ;WRITE HEADER AND DATA
S241 023532 000207 RTS PC ;CAUSED IMPROPER REGISTER
S242 ;CHANGE
S243 ;GOOD DATA GIVES WHAT SHOULD
S244 ;BE THERE
S245 ;RECEIVED DATA GIVES WHAT
S246 ;WAS THERE AFTER COMMAND
S247
S248 ;*NOW WRITE FROM BUFFER WILL BE CHECKED TO SEE THAT
S249 ;*NOTHING GOT CHANGED
S250
S251 023534 2$:
S252
S253 023534 004037 043404 JSR RO,0#COMPAR ;COMPARE TWO BLOCKS OF MEMORY
S254 023540 003534 REINTC ;GOOD DATA STARTS FROM REINTC
S255 023542 002470 WRFROM ;TEST DATA STARTS FROM WRFROM
S256 023544 000404 260. ;260. WORDS TO BE COMPARED
S257 023546 023552 3$ ;RETURN TO 3$ ON ERROR
S258 023550 023556 4$ ;RETURN TO 4$ ON NO ERROR
S259
S260
S261 023552 104030 3$: ERROR 30 ;WRITE HEADER AND DATA
S262 023554 000207 RTS PC ;CHANGED WRITE FROM BUFFER
S263
S264 ;*NOW A READ HEADER AND DATA COMMAND WILL BE GIVEN
S265 ;*READ INTO BUFFER IS FILLED WITH ONES
S266
S267 023556 4$:
S268
S269 023556 004737 041366 JSR PC,0#CLDISK ;SET R1-RHCS1, R2-RHCS2
S270 ;R3-RHDS1, R4-RHER1
S271 ;GIVE RH-11 INITIALIZE

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5272 023562 004037 041236 JSR RO,@CLAREA ; SETUP UNIT NUMBER
5273 023566 003534 REINTO ; CLEAR 260. WORDS FROM REINTO
5274 023570 000404 260. ; STARTING FROM REINTO
5275 023572 177777 -1 ; 260. WORDS
; FILL WITH -1
; *NOW FILL COMMAND
5282 023574 004037 043340 JSR RO,@RUN ; SETUP TO RUN FOR DATA COMMAND
5283 023590 000000 0 ; CYLINDER 0
5284 023602 000 ; SECTOR 0
5285 023603 000 ; TRACK 0
5286 023604 177374 -256.-4 ; WORD COUNT (DATA) = 256. +
; 4 HEADER WORDS
5288 023606 003534 REINTO ; BUS ADDRESS
; STARTING ADDRESS OF DATA
5289 ; BUFFER = REINTO
5290 023610 000000 0 ; DO NOT INHIBIT BUS ADDRESS INCREMENT
5291 023612 014000 ECI!FMT22 ; 16 BITS PER WORD FORMAT
; INHIBIT ECC CORRECTION
5292 ; DO NOT INHIBIT HEADER COMPARE
5293 023614 002450 REFOR ; GET READY TO DO A REFOR
; READ HEADER AND DATA WITH 72 IN RHCS1
; *NOW SAVE REGISTERS FOR COMPARISON AFTER READ HEADER AND DATA
5301 023616 004037 041534 JSR RO,@SAVER ; SAVE REGISTERS
5302 023622 002272 RHWC ; RHWC IS THE FIRST REGISTER SAVED
5303 023624 004612 SAVERE ; STARTING ADDRESS OF WHERE
; THE REGISTERS ARE SAVED
5304 023626 000022 18. ; NUMBER OF REGISTERS
; SAVED = 18.
5305 023630 004737 041446 JSR PC,@CHECKT ; CHECK DVA,RDY,MOL,DPR,DRY,VV = 1
; AND THAT NO STATUS BITS ARE STUCK = 1
5306 023634 104400 066402 TYPE .CPHALT ; CANNOT CONTINUE TESTING IF ANY OF
; THE FIRST SET OF BITS DON'T = 1
5307 023640 000000 HALT ; STOP
5308 023642 013777 004606 156416 MOV @#RP4VEC,@RPVEC ; SET RPO4 VECTOR ADDRESS
; TO 'TIME1' IF P-CLOCK IS PRESENT
; OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
; 'TIME' WILL ONLY SAVE
; CURRENT CYLINDER ADDRESS
; AND LOOK AHEAD REGISTERS
5322 023650 013746 002450 MOV @#REFOR,-(SP) ; GET READY TO MOVE COMMAND
5323 023654 052716 000101 BIS #GO!IE,(SP) ; GET READY TO SET 'GO' AND
; ENABLE INTERRUPT
5324 023660 012677 156414 MOV (SP)+,@RHCS1 ; GO WITH

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5326                                     ;72 IN RHCS1 FOR READ DATA
5327                                     ;WITH INTERRUPT ENABLED
5328 023664 011100      MOV      R1,R0      ;SAVE RHCS1 DURING ABOVE OPERATION
5329 023666 011305      MOV      R3,R5      ;SAVE RHDS1 DURING ABOVE OPERATION
5330
5331
5332 023670 104412      WAT              ;WAIT FOR RDY BIT TO SET
5333 023672 002300      RHCS1          ;WAIT FOR RHCS1 REGISTER
5334 023674 000200      RDY              ;WAIT FOR RDY BIT IN RHCS1 REGISTER
5335 023676 001614      908.          ;ALLOW 9080 MICRO SECONDS
5336 023700 001507      839.          ;RDY MUST SET BETWEEN
5337                                     ;690 AND 17470 MICRO SECONDS
5338
5339                                     ;*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN
5340                                     ;*R0 AND R5 IMMEDIATELY AFTER GO
5341
5342 023702 013746 002450  MOV      Q#REFOR,-(SP) ;SAVE COMMAND
5343 023706 052716 004101  BIS      #IE!GO!DVA,(SP) ;INCLUDE IE!GO!DVA
5344 023712 011637 001124  MOV      (SP),Q#SGDDAT ;SAVE FOR PRINTOUT
5345 023716 022600      CMP      (SP)+,R0      ;DURING ABOVE OPERATION ONLY IE!GO!DVA
5346                                     ;AND COMMAND SHOULD BE SET
5347 023720 001405      BEQ      67$          ;BRANCH IF GOOD
5348 023722 010037 001126  MOV      R0,Q#SBDDAT ;BAD DATA
5349 023726 010137 004600  MOV      R1,Q#REGADR ;FAILING REGISTER RHCS1
5350 023732 104021      ERROR    21          ;DURING ABOVE OPERATION ONLY
5351                                     ;COMMAND AND IE!GO!DVA SHOULD BE SET
5352 023734 012746 010500  67$:    MOV      #MOL!DPR!VV,-(SP) ;SAVE BITS SET DURING OPERATION IN RHDS1
5353 023740 011637 001124  MOV      (SP),Q#SGDDAT ;SAVE FOR PRINTOUT
5354 023744 022605      CMP      (SP)+,R5      ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
5355                                     ;SHOULD BE SET
5356 023746 001405      BEQ      69$          ;BRANCH IF GOOD
5357 023750 010537 001126  MOV      R5,Q#SBDDAT ;BAD DATA
5358 023754 010337 004600  MOV      R3,Q#REGADR ;FAILING REGISTER RHDS1
5359 023760 104063      ERROR    63          ;DURING ABOVE OPERATION ONLY
5360                                     ;MOL!DPR!VV SHOULD BE SET
5361 023762      69$:
5362
5363                                     ;*CHANGE SAVED REGISTERS TO EXPECTED VALUES
5364
5365 023762 004037 041270  JSR      R0,Q#FILLRE ;MOV 0 INTO SAVED RHWC
5366 023766 002272      RHWC          ;SAVED REGISTER TO CHANGE
5367 023770 000000      0              ;DATA
5368 023772 004037 041270  JSR      R0,Q#FILLRE ;MOV REINT0+(260.*2) INTO SAVED RHBA
5369 023776 002274      RHBA          ;SAVED REGISTER TO CHANGE
5370 024000 004544      REINT0+(260.*2) ;DATA
5371 024002 004037 041270  JSR      R0,Q#FILLRE ;MOV 1 INTO SAVED RHDST
5372 024006 002304      RHDST         ;SAVED REGISTER TO CHANGE
5373 024010 000001      1              ;DATA
5374
5375                                     ;*COMPARE REGISTER BEFORE READ HEADER AND DATA
5376                                     ;*WITH REGISTERS AFTER COMMAND
5377
5378
5379 024012 004037 042354  JSR      R0,Q#COMREG ;COMPARE SAVED REGISTERS WITH

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5380 ;PRESENT VALUE
5381 024016 004612 SAVERE ;GOOD DATA SAVED IN 'SAVERE'
5382 024020 002354 WC ;TEST DATA STARTING FROM 'RHWC'
5383 024022 000022 18. ;18. REGISTERS TO BE COMPARED
5384 024024 024030 5$ ;RETURN TO 5$ ON ERROR
5385 024026 024034 6$ ;RETURN TO 6$ ON NO ERROR
5386
5387 024030 104031 5$: ERROR 31 ;READ HEADER AND DATA CAUSED
5388 024032 000207 RTS PC ;IMPROPER REGISTER CHANGE
5389 ;GOOD DATA GIVES WHAT SHOULD
5390 ;BE THERE
5391 ;RECEIVED DATA GIVES WHAT WAS
5392 ;THERE AFTER COMMAND
5393
5394 ;*NOW READ INTO BUFFER WILL BE CHECKED TO SEE
5395 ;*THE READ WAS GOOD
5396
5397 024034 6$:
5398
5399 024034 004037 043404 JSR RD,2#COMPAR ;COMPARE TWO BLOCKS OF MEMORY
5400 024040 002470 WRFROM ;GOOD DATA STARTS FROM WRFROM
5401 024042 003534 REINTC ;TEST DATA STARTS FROM REINTC
5402 024044 000404 260. ;260. WORDS TO BE COMPARED
5403 024046 024052 7$ ;RETURN TO 7$ ON ERROR
5404 024050 024056 10$ ;RETURN TO 10$ ON NO ERROR
5405
5406
5407 024052 104032 7$: ERROR 32 ;WRITE HEADER AND DATA
5408 024054 000207 RTS PC ;FOLLOWED BY A READ HEADER
5409 ;AND DATA GAVE A READ ERROR
5410 ;ERROR MAY BE IN READ OR WRITE
5411 024056 10$:
5412

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::\*\*\*\*\*  
:\*TEST 34 READ DATA (0'S)

:\* THIS TEST READS DATA WRITTEN BY THE PREVIOUS TEST  
:\* THE WRITE FROM BUFFER IS FILLED WITH ALL 0S  
:\* THE COMMAND IS FILLED, THEN ALL REGISTERS SAVED  
:\* THEN READ DATA COMMAND IS GIVEN  
:\* ALL REGISTERS ARE COMPARED FOR PROPER VALUES  
:\* READ DATA INTO 'READ INTO' BUFFER IS CHECKED

::\*\*\*\*\*

†ST34: SCOPE  
MOV #STACK\_SP ;RESET STACK  
MOV #34,‡#†STNM ;SAVE TEST NUMBER  
JSR PC,‡#CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETUP UNIT NUMBER  
;\*FILL WRITE FROM BUFFER WITH EXPECTED DATA  
JSR RO,‡#CLAREA ;CLEAR 256. WORDS, FROM WRFROM  
WRFROM ;STARTING FROM WRFROM  
256. ;256. WORDS  
0 ;FILL WITH 0

;\*NOW THE READ DATA COMMAND WILL BE FILLED

JSR RO,‡#RUN ;SETUP TO RUN FOR DATA COMMAND  
0 ;CYLINDER 0  
0 ;SECTOR 0  
0 ;TRACK 0  
-256. ;WORD COUNT = 256.  
REINTO ;BUS ADDRESS  
;STARTING ADDRESS OF DATA  
;BUFFER = REINTO  
0 ;DO NOT INHIBIT BUS ADDRESS INCREMENT  
ECI!FMT22 ;16 BITS PER WORD FORMAT  
;INHIBIT ECC CORRECTION  
;DO NOT INHIBIT HEADER COMPARE  
READAT ;GET READY TO DO A READAT  
;READ DATA WITH 70 IN RHCS1

;\*NOW SAVE REGISTERS FOR COMPARISON AFTER READ DATA COMMAND

JSR RO,‡#SAVER ;SAVE REGISTERS  
RHWC ;RHWC IS THE FIRST REGISTER SAVED  
SAVERE ;STARTING ADDRESS OF WHERE  
;THE REGISTERS ARE SAVED

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5467 024142 000022          18.          ;NUMBER OF REGISTERS
5468                               ;SAVED = 18.
5469
5470 024144 004737 041446    JSR      PC,@#CHECKT    ;CHECK DVA,RDY,MOL,DPR,DRY,VV = 1
5471                               ;AND THAT NO STATUS BITS ARE STUCK = 1
5472 024150 104400 066402    TYPE     .CPHALT       ;CANNOT CONTINUE TESTING IF ANY OF
5473                               ;THE FIRST SET OF BITS DON'T = 1
5474 024154 000000          HALT
5475                               ;STOP
5476 024156 013777 004606 156102  MOV     @#RPH4VEC,@RPVEC ;SET RPO4 VECTOR ADDRESS
5477                               ;TO 'TIME1' IF P-CLOCK IS PRESENT
5478                               ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
5479                               ;'TIME' WILL ONLY SAVE
5480                               ;CURRENT CYLINDER ADDRESS
5481                               ;AND LOOK AHEAD REGISTERS
5482
5483
5484 024164 013746 002446    MOV     @#READAT,-(SP)  ;GET READY TO MOVE COMMAND
5485 024170 052716 000101    BIS     #GO!IE,(SP)    ;GET READY TO SET 'GO' AND
5486                               ;ENABLE INTERRUPT
5487 024174 012677 156100    MOV     (SP)+,@RHCSI   ;GO WITH
5488                               ;70 IN RHCSI FOR READ DATA
5489                               ;WITH INTERRUPT ENABLED
5490 024200 011100          MOV     @R1,R0         ;SAVE RHCSI DURING ABOVE OPERATION
5491 024202 011305          MOV     @R3,R5         ;SAVE RHDS1 DURING ABOVE OPERATION
5492
5493
5494 024204 104412          WAT
5495 024206 002300          RHCSI                ;WAIT FOR RHCSI REGISTER
5496 024210 000200          RDY                  ;WAIT FOR RDY BIT IN RHCSI REGISTER
5497 024212 001614          908.                ;ALLOW 9080 MICRO SECONDS
5498 024214 001507          839.                ;RDY MUST SET BETWEEN
5499                               ;690 AND 17470 MICRO SECONDS
5500
5501                               ;*COMPARE CONTENTS OF RHCSI AND RHDS1 ALREADY SAVED IN
5502                               ;*R0 AND R5 IMMEDIATELY AFTER GO
5503
5504 024216 013746 002446    MOV     @#READAT,-(SP)  ;SAVE COMMAND
5505 024222 052716 004101    BIS     #IE!GO!DVA,(SP) ;INCLUDE IE!GO!DVA
5506 024226 011637 001124    MOV     (SP),@#$GDDAT  ;SAVE FOR PRINTOUT
5507 024232 022600          CMP     (SP)+,R0       ;DURING ABOVE OPERATION ONLY IE!GO!DVA
5508                               ;AND COMMAND SHOULD BE SET
5509 024234 001405          BEQ     64$           ;BRANCH IF GOOD
5510 024236 010037 001126    MOV     R0,@#$BDDAT    ;BAD DATA
5511 024242 010137 004600    MOV     R1,@#REGADR    ;FAILING REGISTER RHCSI
5512 024246 104021          ERROR 21             ;DURING ABOVE OPERATION ONLY
5513                               ;COMMAND AND IE!GO!DVA SHOULD BE SET
5514 024250 012746 010500    64$: MOV     #MOL!DPR!VV,-(SP) ;SAVE BITS SET DURING OPERATION IN RHDS1
5515 024254 011637 001124    MOV     (SP),@#$GDDAT  ;SAVE FOR PRINTOUT
5516 024260 022605          CMP     (SP)+,R5       ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
5517                               ;SHOULD BE SET
5518 024262 001405          BEQ     66$           ;BRANCH IF GOOD
5519 024264 010537 001126    MOV     R5,@#$BDDAT    ;BAD DATA
5520 024270 010337 004600    MOV     R3,@#REGADR    ;FAILING REGISTER RHDS1

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5521 024274 104063          ERROR 63          ;DURING ABOVE OPERATION ONLY
5522                                     ;MOL!DPR!VV SHOULD BE SET
5523 024276          66$:
5524
5525                                     ;*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES
5526
5527 024276 004037 041270    JSR      RD, @#FILLRE    ;MOV 0 INTO SAVED RHWC
5528 024302 002272          RHWC          ;SAVED REGISTER TO CHANGE
5529 024304 000000          C              ;DATA
5530 024306 004037 041270    JSR      RD, @#FILLRE    ;MOV REINTO+<256.*2> INTO SAVED RHBA
5531 024312 002274          RHBA          ;SAVED REGISTER TO CHANGE
5532 024314 004534          REINTO+<256.*2>      ;DATA
5533 024316 004037 041270    JSR      RD, @#FILLRE    ;MOV 1 INTO SAVED RHDST
5534 024322 002304          RHDST         ;SAVED REGISTER TO CHANGE
5535 024324 000001          1              ;DATA
5536
5537                                     ;*NOW COMPARE REGISTERS BEFORE READ DATA WITH
5538                                     ;*AFTER COMMAND
5539
5540
5541 024326 004037 042354    JSR      RD, @#COMREG    ;COMPARE SAVED REGISTERS WITH
5542                                     ;PRESENT VALUE
5543 024332 004612          SAVERE         ;GOOD DATA SAVED IN 'SAVERE'
5544 024334 002354          WC              ;TEST DATA STARTING FROM 'RHWC'
5545 024336 000022          18.          ;18. REGISTERS TO BE COMPARED
5546 024340 024344          1$           ;RETURN TO 1$ ON ERROR
5547 024342 024350          2$           ;RETURN TO 2$ ON NO ERROR
5548
5549 024344 104033          1$:          ERROR 33          ;READ DATA CAUSED IMPROPER REGISTER
5550 024346 000207          RTS          PC      ;CHANGE
5551                                     ;GOOD DATA GIVES WHAT SHOULD BE THERE
5552                                     ;RECEIVED DATA GIVES WHAT WAS THERE AFTER COMMAND
5553                                     ;*NOW READ INTO BUFFER WILL BE CHECKED TO SEE THAT READ
5554                                     ;*WAS GOOD
5555
5556 024350          2$:
5557
5558 024350 004037 043404    JSR      RD, @#COMPAR    ;COMPARE TWO BLOCKS OF MEMORY
5559 024354 002470          WRFROM        ;GOOD DATA STARTS FROM WRFROM
5560 024356 003534          REINTO        ;TEST DATA STARTS FROM REINTO
5561 024360 000400          256.         ;256. WORDS TO BE COMPARED
5562 024362 024366          3$           ;RETURN TO 3$ ON ERROR
5563 024364 024372          4$           ;RETURN TO 4$ ON NO ERROR
5564
5565
5566 024366 104034          3$:          ERROR 34          ;READ DATA COMMAND
5567 024370 000207          RTS          PC      ;READ INCORRECTLY
5568
5569 024372          4$:

```



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5524 024436 004037 041236 JSR RD,2#CLAREA ;CLEAR 200. WORDS FROM REINT0
5525 024442 003534 REINT0 ;STARTING FROM REINT0
5526 024444 000310 200. ;200. WORDS
5527 024446 177777 -1 ;FILL WITH -1
5528
5529 024450 004037 041236 JSR RD,2#CLAREA ;CLEAR 56. WORDS FROM REINT0+200.*2,
5530 024454 004354 REINT0+200.*2 ;STARTING FROM REINT0+200.*2,
5531 024456 000370 56. ;56. WORDS
5532 024460 125252 125252 ;FILL WITH 125252
5533
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5540 024462 004037 043340 JSR RD,2#RUN ;SETUP TO RUN FOR DATA COMMAND
5541 024466 000000 0 ;CYLINDER 0
5542 024470 000 0 ;SECTOR 0
5543 024472 000 0 ;TRACK 0
5544 024474 177470 -200. ;WORD COUNT = 200.
5545 024476 002470 WRFROM ;BUS ADDRESS
5546
5547 024476 000000 0 ;STARTING ADDRESS OF DATA
5548 024500 010000 0 ;BUFFER = WRFROM
5549
5550
5551 024502 002442 WRIDAT ;DO NOT INHIBIT BUS ADDRESS INCREMENT
5552
5553
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5555
5556
5557 024504 004037 041534 JSR RD,2#SAVER ;SAVE REGISTERS
5558 024510 002272 RHWC ;RHWC IS THE FIRST REGISTER SAVED
5559 024512 004612 SAVERE ;STARTING ADDRESS OF WHERE
5560
5561 024514 000022 18. ;THE REGISTERS ARE SAVED
5562
5563
5564 024516 004737 041446 JSR PC,2#CHECKT ;CHECK DVA,RDY,MOL,DPR,DRY,VV = ;
5565 024522 104400 066402 TYPE ,CPHALT ;AND THAT NO STATUS BITS ARE STUCK = ;
5566
5567 024526 000000 HALT ;CANNOT CONTINUE TESTING IF ANY OF
5568
5569
5570 024530 013777 004606 155530 MOV 2#RP4VEC,2#RPVEC ;THE FIRST SET OF BITS DON'T = 1
5571
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.BYTE  
.BYTE

;\*NOW WRITE DATA COMMAND WILL BE LOADED

;\*WRITE DATA WITH 60 IN RHCS1

;\*NOW SAVE REGISTERS FOR COMPARISON AFTER WRITE DATA

AND LOOK AHEAD REGISTERS



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5678 024536 013746 002442      MOV      2#WRIDAT, -(SP)      :GET READY TO MOVE COMMAND
5679 024542 052716 000101      BIS      #GO!IE, (SP)        :GET READY TO SET 'GO' AND
5680                                     :ENABLE INTERRUPT
5681 024546 012677 155526      MOV      (SP)+, 2#RHCSI      :GO WITH
5682                                     :60 IN RHCSI FOR WRITE DATA
5683                                     :WITH INTERRUPT ENABLED
5684 024552 011100      MOV      2#R1, R0            :SAVE RHCSI DURING ABOVE OPERATION
5685 024554 011305      MOV      2#R3, R5            :SAVE RHDSI DURING ABOVE OPERATION
5686                                     :
5687                                     ;*ONE REVOLUTION = 16670 MICRO SEC, ONE SECTOR=760 MICRO SEC
5688
5689
5690 024556 104412      WAT                                     :WAIT FOR RDY BIT TO SET
5691 024560 002300      RHCSI                            :WAIT FOR RHCSI REGISTER
5692 024562 000200      RDY                               :WAIT FOR RDY BIT IN RHCSI REGISTER
5693 024564 001614      908.                             :ALLOW 9080 MICRO SECONDS
5694 024566 001507      839.                             :RDY MUST SET BETWEEN
5695                                     :690 AND 17470 MICRO SECONDS
5696
5697                                     ;*COMPARE CONTENTS OF RHCSI AND RHDSI ALREADY SAVED IN
5698                                     ;*RC AND RS IMMEDIATELY AFTER GO
5699
5700 024570 013746 002442      MOV      2#WRIDAT, -(SP)      :SAVE COMMAND
5701 024574 052716 004101      BIS      #IE!GO!DVA, (SP)    :INCLUDE IE!GO!DVA
5702 024600 011637 001124      MOV      (SP), 2#$GDDAT      :SAVE FOR PRINTOUT
5703 024604 022500      CMP      (SP)+, R0            :DURING ABOVE OPERATION ONLY IE!GO!DVA
5704                                     :AND COMMAND SHOULD BE SET
5705 024606 001405      BEQ      64$                  :BRANCH IF GOOD
5706 024610 010037 001126      MOV      R0, 2#$BDDAT        :BAD DATA
5707 024614 010137 004600      MOV      R1, 2#REGADR        :FAILING REGISTER RHCSI
5708 024620 104021      ERROR   21                    :DURING ABOVE OPERATION ONLY
5709                                     :COMMAND AND IE!GO!DVA SHOULD BE SET
5710 024622 012746 010500      64$: MOV      #MCL!DPR!VV, -(SP) :SAVE BITS SET DURING OPERATION IN RHCSI
5711 024626 011637 001124      MOV      (SP), 2#$GDDAT      :SAVE FOR PRINTOUT
5712 024632 022605      CMP      (SP)+, R5            :DURING ABOVE OPERATION ONLY MCL!DPR!VV
5713                                     :SHOULD BE SET
5714 024634 001405      BEQ      66$                  :BRANCH IF GOOD
5715 024636 010537 001126      MOV      R5, 2#$BDDAT        :BAD DATA
5716 024642 010337 004600      MOV      R3, 2#REGADR        :FAILING REGISTER RHDSI
5717 024646 104063      ERROR   63                    :DURING ABOVE OPERATION ONLY
5718                                     :MCL!DPR!VV SHOULD BE SET
5719 024650      66$:
5720
5721                                     ;*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE
5722
5723 024650 004037 041270      JSR      R0, 2#FILLRE        :MOV 0 INTO SAVED RHWC
5724 024654 002272      RHWC                            :SAVED REGISTER TO CHANGE
5725 024656 000000      0                                :DATA
5726 024660 004037 041270      JSR      R0, 2#FILLRE        :MOV WRFROM+(200.*2) INTO SAVED RHBA
5727 024664 002274      RHBA                            :SAVED REGISTER TO CHANGE
5728 024666 003310      WRFROM+(200.*2)                :DATA
5729 024670 004037 041270      JSR      R0, 2#FILLRE        :MOV 1 INTO SAVED RHDST
5730 024674 002304      RHDST                            :SAVED REGISTER TO CHANGE
5731 024676 000001      1                                :DATA
    
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: *NOW COMPARE REGISTERS BEFORE WRITE DATA WITH REGISTERS
: *AFTER COMMAND

024700 004037 042354 JSR RO,3#COMREG ;COMPARE SAVED REGISTERS WITH
;PRESENT VALUE
024704 004612 SAVERE ;GOOD DATA SAVED IN 'SAVERE'
024706 002354 NO ;TEST DATA STARTING FROM 'RHWC'
024710 000022 18. ;18. REGISTERS TO BE COMPARED
024712 024715 1$ ;RETURN TO 1$ ON ERROR
024714 024722 2$ ;RETURN TO 2$ ON NO ERROR

024716 104035 1$: ERROR 35 ;WRITE DATA COMMAND CAUSED
024720 000207 RTS PC ;IMPROPER REGISTER CHANGE
;GOOD DATA GIVES WHAT SHOULD
;BE
;RECEIVED DATA GIVES WHAT WAS
;THERE AFTER COMMAND

: *NOW WRITE FROM BUFFER WILL BE CHECKED FOR NO CHANGE

024722 2$:

024722 004037 042404 JSR RO,3#COMPAR ;COMPARE TWO BLOCKS OF MEMORY
024726 003534 REINTO ;GOOD DATA STARTS FROM REINTO
024730 002470 WRFROM ;TEST DATA STARTS FROM WRFROM
024732 000400 256. ;256. WORDS TO BE COMPARED
024734 024740 3$ ;RETURN TO 3$ ON ERROR
024736 024744 4$ ;RETURN TO 4$ ON NO ERROR

024740 104036 3$: ERROR 36 ;WRITE DATA COMMAND CHANGED
024742 000207 RTS PC ;WRITE FROM BUFFER

: *NOW A READ DATA COMMAND WILL BE GIVEN
: *FILL READ INTO BUFFER WITH 200 ZEROS AND 56 OF 377

024744 4$:

024744 004737 041356 JSR PC,3#CLDISK ;SET R1-RHCS1, R2-RHCS2
;R3-RHDS1, R4-RHER1
;GIVE RH-11 INITIALIZE
;SETUP UNIT NUMBER
024750 004037 041236 JSR RO,3#CLAREA ;CLEAR 200. WORDS, FROM REINTO
024754 003534 REINTO ;STARTING FROM REINTO
024756 000310 200. ;200. WORDS
024760 000000 J ;FILL WITH 0

024762 004037 041236 JSR RO,3#CLAREA ;CLEAR 56. WORDS, FROM REINTO+(200.*2)
024766 004254 REINTO+(200.*2) ;STARTING FROM REINTO+(200.*2)
024770 000070 56. ;56. WORDS
024772 000377 377 ;FILL WITH 377

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;*FILL WRITE FROM BUFFER WITH 200 ONES AND 56 OF 377
024774 004037 041236 JSR RD,2*CLAREA ;CLEAR 200. WORDS, FROM WRFROM
025000 002470 WRFROM ;STARTING FROM WRFROM
025002 000310 200. ;200. WORDS
025004 177777 -1 ;FILL WITH -1

025006 004037 041236 JSR RD,2*CLAREA ;CLEAR 56. WORDS, FROM WRFROM+(200.*2)
025012 003310 WRFROM+(200.*2) ;STARTING FROM WRFROM+(200.*2)
025014 000070 56. ;56. WORDS
025016 000377 377 ;FILL WITH 377

;*NOW FILL COMMAND

025020 004037 043340 JSR RD,2*PUN ;SETUP TO RUN FOR DATA COMMAND
025024 000000 0 ;CYLINDER 0
025026 000 .BYTE 0 ;SECTOR 0
025027 000 .BYTE 0 ;TRACK 0
025030 177470 -200. ;WORD COUNT = 200.
025032 003524 REINT0 ;BUS ADDRESS
;STARTING ADDRESS OF DATA
;BUFFER = REINT0
;DO NOT INHIBIT BUS ADDRESS INCREMENT
;16 BITS PER WORD FORMAT
;INHIBIT ECC CORRECTION
;DO NOT INHIBIT HEADER COMPARE
025040 002446 READAT ;GET READY TO DO A READAT
;READ DATA WITH 70 IN RHCS!

;*NOW SAVE REGISTERS FOR COMPARISON AFTER READ DATA COMMAND

025042 004037 041524 JSR RD,2*SAVER ;SAVE REGISTERS
025046 002272 RHWC ;RHWC IS THE FIRST REGISTER SAVED
025050 004612 SAVERE ;STARTING ADDRESS OF WHERE
;THE REGISTERS ARE SAVED
025052 000022 18. ;NUMBER OF REGISTERS
;SAVED = 18.

025054 004737 041446 JSR PC,2*CHECKT ;CHECK DVA,ROY,MOL,DPR,DRY,VV = 1
;AND THAT NO STATUS BITS ARE STUCK = 1
025060 104400 066402 TYPE ,CPHALT ;CANNOT CONTINUE TESTING IF ANY OF
;THE FIRST SET OF BITS DON'T = 1
025064 000000 HALT ;STOP

025066 013777 004606 155172 MOV 2*RP4VEC,2*RPVEC ;SET RPO4 VECTOR ADDRESS
;TO 'TIME1' IF P-CLOCK IS PRESENT
;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
;'TIME' WILL ONLY SAVE
;CURRENT CYLINDER ADDRESS

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155172

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5840                                     :AND LOOK AHEAD REGISTERS
5841
5842
5843 025074 013746 002446      MOV    Q#READAT, -(SP)  :GET READY TO MOVE COMMAND
5844 025100 052716 000101      BIS    #GO!IE, (SP)    :GET READY TO SET 'GO' AND
5845                                     :ENABLE INTERRUPT
5846 025104 012677 155170      MOV    (SP)+, Q#RHCSI  :GO WITH
5847                                     :70 IN RHCSI FOR READ DATA
5848                                     :WITH INTERRUPT ENABLED
5849 025110 011100      MOV    Q#R1, R0        :SAVE RHCSI DURING ABOVE OPERATION
5850 025112 011305      MOV    Q#R3, R5        :SAVE RHDSI DURING ABOVE OPERATION
5851
5852
5853 025114 104412      WAT                                     :WAIT FOR RDY BIT TO SET
5854 025115 002300      RHCSI  :WAIT FOR RHCSI REGISTER
5855 025120 000200      RDY    :WAIT FOR RDY BIT IN RHCSI REGISTER
5856 025122 001514      908.  :ALLOW 9080 MICRO SECONDS
5857 025124 001507      939.  :RDY MUST SET BETWEEN
5858                                     :690 AND 17470 MICRO SECONDS
5859
5860                                     :*COMPARE CONTENTS OF RHCSI AND RHDSI ALREADY SAVED IN
5861                                     :*R0 AND R5 IMMEDIATELY AFTER GO
5862
5863 025126 013746 002446      MOV    Q#READAT, -(SP)  :SAVE COMMAND
5864 025132 052716 004101      BIS    #IE!GO!DVA, (SP) :INCLUDE IE!GO!DVA
5865 025136 011637 001124      MOV    (SP), Q#SGDAT    :SAVE FOR PRINTOUT
5866 025142 022600      CMP    (SP)+, R0        :DURING ABOVE OPERATION ONLY IE!GO!DVA
5867                                     :AND COMMAND SHOULD BE SET
5868 025144 001405      BEQ    67$             :BRANCH IF GOOD
5869 025146 010037 001126      MOV    R0, Q#SBDDAT     :BAD DATA
5870 025152 010137 004600      MOV    R1, Q#REGADR     :FAILING REGISTER RHCSI
5871 025156 104021      ERROR  21             :DURING ABOVE OPERATION ONLY
5872                                     :COMMAND AND IE!GO!DVA SHOULD BE SET
5873 025160 012746 010500      67$: MOV    #MOL!DPR!VV, -(SP) :SAVE BITS SET DURING OPERATION IN RHCSI
5874 025164 011637 001124      MOV    (SP), Q#SGDAT    :SAVE FOR PRINTOUT
5875 025170 022605      CMP    (SP)+, R5        :DURING ABOVE OPERATION ONLY MOL!DPR!VV
5876                                     :SHOULD BE SET
5877 025172 001405      BEQ    69$             :BRANCH IF GOOD
5878 025174 010537 001126      MOV    R5, Q#SBDDAT     :BAD DATA
5879 025200 010337 004600      MOV    R3, Q#REGADR     :FAILING REGISTER RHDSI
5880 025204 104063      ERROR  63             :DURING ABOVE OPERATION ONLY
5881                                     :MOL!DPR!VV SHOULD BE SET
5882 025206      69$:
5883
5884                                     :*CHANGE SAVED REGISTERS TO EXPECTED VALUES
5885
5886 025206 004037 041270      JSR    R0, Q#FILLRE     :MOV 0 INTO SAVED RHWC
5887 025212 002272      RHWC  :SAVED REGISTER TO CHANGE
5888 025214 000000      0    :DATA
5889 025216 004037 041270      JSR    R0, Q#FILLRE     :MOV REINTO+(200.*2) INTO SAVED RHBA
5890 025222 002274      RHBA  :SAVED REGISTER TO CHANGE
5891 025224 004354      REINTO+(200.*2) :DATA
5892 025226 004037 041270      JSR    R0, Q#FILLRE     :MOV 1 INTO SAVED RHDST
5893 025232 002304      RHDST :SAVED REGISTER TO CHANGE

```

# H15

MAINDEC-11-DZRJI-A RPO4 5 6 FUNCT. CONT. TST-PT 1  
DZRJIA.P11 \*35 WRITE READ DATA (1'S 3 125252)

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SEQ 0188

```
5894 025234 000001          1          ;DATA
5895
5896          ;*COMPARE REGISTERS BEFORE READ DATA COMMAND
5897          ;*WITH REGISTERS AFTER COMMAND
5898
5899
5900 025236 004037 042354    JSR      RD,3#COMREG    ;COMPARE SAVED REGISTERS WITH
5901                                ;PRESENT VALUE
5902 025242 004612          SAVERE    ;GOOD DATA SAVED IN 'SAVERE'
5903 025244 002354          WC          ;TEST DATA STARTING FROM 'RHWC'
5904 025246 000022          18.       ;18. REGISTERS TO BE COMPARED
5905 025250 025254          5$        ;RETURN TO 5$ ON ERROR
5906 025252 025260          6$        ;RETURN TO 6$ ON NO ERROR
5907
5908 025254 104033          5$:      ERPOR    33    ;READ DATA CAUSED IMPROPER
5909 025256 000207          RTS      PC      ;REGISTER CHANGE
5910                                ;GOOD DATA GIVES WHAT SHOULD BE THERE
5911                                ;RECEIVED DATA GIVES WHAT WAS THERE
5912                                ;AFTER COMMAND
5913
5914          ;*NOW READ INTO BUFFER IS CHECKED FOR GOOD READ
5915
5916 025260          6$:
5917
5918 025260 004037 043404    JSR      RC,3#COMPAR   ;COMPARE TWO BLOCKS OF MEMORY
5919 025264 002470          WRFROM    ;GOOD DATA STARTS FROM WRFROM
5920 025266 003534          REINTO   ;TEST DATA STARTS FROM REINTO
5921 025270 000400          256.     ;256. WORDS TO BE COMPARED
5922 025272 025276          7$        ;RETURN TO 7$ ON ERROR
5923 025274 025302          10$       ;RETURN TO 10$ ON NO ERROR
5924
5925
5926 025276 104034          7$:      ERROR   34    ;INCORRECT DATA AFTER
5927 025300 000207          RTS      PC      ;WRITE DATA FOLLOWED BY A
5928                                ;READ DATA
5929
5930 025302          10$:
5931
5932
```

5933  
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5986

::\*\*\*\*\*  
:\*TEST 36 WRITE/READ DATA (125252)

:\* THIS TEST GIVES A WRITE DATA COMMAND FOR CYLINDER C  
:\* TRACK 0, SECTOR 0, KEYS 0, 256 WORDS OF 125252  
:\* THIS SECTOR IS FORMATED BY PREVIOUS TEST  
:\* THEN READ DATA COMMAND IS GIVEN FOR 256 WORDS IN  
:\* SAME CYLINDER, TRACK, SECTOR, KEYS  
:\* WRITE FROM BUFFER AND READ INTO BUFFER ARE FILLED WITH  
:\* 256 OF 125252  
:\* THE WRITE DATA COMMAND IS LOADED EXCEPT GO AND IE  
:\* ALL REGISTERS ARE SAVED AND THEN GO IS GIVEN TO WRITE DATI  
:\*  
:\* THEN ALL REGISTERS ARE COMPARED TO CHECK FOR IMPROPER  
:\* CHANGE, THEN WRITE FROM BUFFER IS CHECKED FOR NO CHANGE  
:\*  
:\* THEN READ INTO BUFFER IS FILLED WITH 256 OF ZEROS  
:\* WRITE FROM BUFFER IS FILLED WITH  
:\* 256 OF 125252  
:\* THE COMMAND EXCEPT GO IS LOADED, ALL REGISTERS ARE SAVED  
:\* GO IS GIVEN  
:\*  
:\* ALL REGISTERS ARE CHECKED  
:\* READ DATA IS CHECKED

::\*\*\*\*\*

025302 000004  
025304 012706 001000  
025310 012737 000036 004504  
  
025316 004737 041366  
  
  
  
025322 004037 041236  
025326 002470  
025330 000400  
025332 125252  
  
  
  
025334 004037 041236  
025340 003534  
025342 000400  
025344 125252

†ST36: SCOPE  
MOV #STACK, SP ;RESET STACK  
MOV #36, @†STNM ;SAVE TEST NUMBER  
  
JSR PC, @#CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETUP UNIT NUMBER  
  
;\*NOW FILL WRITE FROM BUFFER - 256 OF 125252  
  
JSR RO, @#CLAREA ;CLEAR 256. WORDS, FROM WRFROM  
WRFROM ;STARTING FROM WRFROM  
256. ;256. WORDS  
125252 ;FILL WITH 125252  
  
:\*NOW READ INTO BUFFER WILL BE FILLED WITH SAME DATA AS  
:\*WRITE FROM BUFFER SO THAT AFTER A WRITE COMPARISONS  
:\*CAN BE MADE TO DETERMINE THAT WRITE DID NOT CHANGE BUFFER  
  
JSR RO, @#CLAREA ;CLEAR 256. WORDS, FROM REINTO  
REINTO ;STARTING FROM REINTO  
256. ;256. WORDS  
125252 ;FILL WITH 125252

```

5987
5988
5989
5990
5991 025246 004037 043340 JSR RO, @#RUN ; SETUP TO RUN FOR DATA COMMAND
5992 025352 000000 0 ; CYLINDER 0
5993 025354 000 .BYTE 0 ; SECTOR 0
5994 025355 000 .BYTE 0 ; TRACK 0
5995 025356 177400 -256. ; WORD COUNT = 256.
5996 025360 002470 WRFROM ; BUS ADDRESS
5997 ; STARTING ADDRESS OF DATA
5998 ; BUFFER = WRFROM
5999 025362 000000 0 ; DO NOT INHIBIT BUS ADDRESS INCREMENT
6000 025364 010000 FMT22 ; 16 BITS PER WORD FORMAT
6001 ; DO NOT INHIBIT ECC CORRECTION
6002 ; DO NOT INHIBIT HEADER COMPARE
6003 025366 002442 WRIDAT ; GET READY TO DO A WRIDAT
6004 ; WRITE DATA WITH 60 IN RHCSI
6005
6006
6007 ; *NOW SAVE REGISTERS FOR COMPARISON AFTER WRITE DATA
6008
6009 025370 004037 041534 JSR RC, @#SAVER ; SAVE REGISTERS
6010 025374 002272 RHWC ; RHWC IS THE FIRST REGISTER SAVED
6011 025376 004612 SAVERE ; STARTING ADDRESS OF WHERE
6012 ; THE REGISTERS ARE SAVED
6013 025400 000022 18. ; NUMBER OF REGISTERS
6014 ; SAVED = 18.
6015
6016 025402 004737 041446 JSR PC, @#CHECKT ; CHECK DVA, RDY, MOL, DPR, DRY, VV = 1
6017 ; AND THAT NO STATUS BITS ARE STUCK = 1
6018 025406 104400 066402 TYPE .CPHALT ; CANNOT CONTINUE TESTING IF ANY OF
6019 ; THE FIRST SET OF BITS DON'T = 1
6020 025412 000000 HALT ; STOP
6021
6022 025414 013777 004606 154644 MOV @#RPHVEC, @RPVEC ; SET RPO4 VECTOR ADDRESS
6023 ; TO 'TIME1' IF P-CLOCK IS PRESENT
6024 ; OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
6025 ; 'TIME' WILL ONLY SAVE
6026 ; CURRENT CYLINDER ADDRESS
6027 ; AND LOOK AHEAD REGISTERS
6028
6029
6030 025422 013746 002442 MOV @#WRIDAT, -(SP) ; GET READY TO MOVE COMMAND
6031 025426 052716 000101 BIS #GO!IE, (SP) ; GET READY TO SET 'GO' AND
6032 ; ENABLE INTERRUPT
6033 025432 012677 154642 MOV (SP)+, @RHCSI ; GO WITH
6034 ; 60 IN RHCSI FOR WRITE DATA
6035 ; WITH INTERRUPT ENABLED
6036 025436 011100 MOV @R1, RO ; SAVE RHCSI DURING ABOVE OPERATION
6037 025440 011305 MOV @R3, R5 ; SAVE RHCSI DURING ABOVE OPERATION
6038
6039
6040 ; *ONE REVOLUTION=16670 MICROSEC. ONE SECTOR=760 MICROSEC

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6041
6042 025442 104412          WAT          ;WAIT FOR RDY BIT TO SET
6043 025444 002300          RHCS1        ;WAIT FOR RHCS1 REGISTER
6044 025446 000200          RDY          ;WAIT FOR RDY BIT IN RHCS1 REGISTER
6045 025450 001614          908.        ;ALLOW 9080 MICRO SECONDS
6046 025452 001507          839.        ;RDY MUST SET BETWEEN
6047                                     ;690 AND 17470 MICRO SECONDS
6048
6049                                     ;*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN
6050                                     ;*RO AND R5 IMMEDIATELY AFTER GO
6051
6052 025454 013746 002442    MOV    2#WRIDAT,-(SP) ;SAVE COMMAND
6053 025450 052716 004101    SIS    #IE!GO!DVA,(SP) ;INCLUDE IE!GO!DVA
6054 025464 011637 001124    MOV    (SP),2#SGDDAT ;SAVE FOR PRINTOUT
6055 025470 022600          CMP    (SP)+,R0      ;DURING ABOVE OPERATION ONLY IE!GO!DVA
6056                                     ;AND COMMAND SHOULD BE SET
6057 025472 001405          BEQ    64$          ;BRANCH IF GOOD
6058 025474 010037 001126    MOV    R0,2#SBDDAT  ;BAD DATA
6059 025500 010137 004600    MOV    R1,2#REGADR  ;FAILING REGISTER RHCS1
6060 025504 104021          ERROR   21         ;DURING ABOVE OPERATION ONLY
6061                                     ;COMMAND AND IE!GO!DVA SHOULD BE SET
6062 025506 012746 010500    64$: MOV    #MCL!DPR!VV,-(SP) ;SAVE BITS SET DURING OPERATION IN RHDS1
6063 025512 011637 001124    MOV    (SP),2#SGDDAT ;SAVE FOR PRINTOUT
6064 025516 022605          CMP    (SP)+,R5     ;DURING ABOVE OPERATION ONLY MCL!DPR!VV
6065                                     ;SHOULD BE SET
6066 025520 001405          BEQ    66$          ;BRANCH IF GOOD
6067 025522 010537 001126    MOV    R5,2#SBDDAT  ;BAD DATA
6068 025526 010337 004600    MOV    R3,2#REGADR  ;FAILING REGISTER RHDS1
6069 025532 104063          ERROR   63         ;DURING ABOVE OPERATION ONLY
6070                                     ;MCL!DPR!VV SHOULD BE SET
6071 025534          66$:
6072
6073                                     ;*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE
6074
6075 025534 004037 041270    JSR    R0,2#FILLRE  ;MOV 0 INTO SAVED RHWC
6076 025540 002272          RHWC        ;SAVED REGISTER TO CHANGE
6077 025542 000000          0          ;DATA
6078 025544 004037 041270    JSR    R0,2#FILLRE  ;MOV WRFROM+(256.*2) INTO SAVED RHBA
6079 025550 002274          RH3A       ;SAVED REGISTER TO CHANGE
6080 025552 003470          WRFROM+(256.*2) ;DATA
6081 025554 004037 041270    JSR    R0,2#FILLRE  ;MOV 1 INTO SAVED RHDST
6082 025560 002304          RHDST      ;SAVED REGISTER TO CHANGE
6083 025562 000001          1          ;DATA
6084
6085                                     ;*NOW COMPARE REGISTERS BEFORE WRITE DATA WITH REGISTERS
6086                                     ;*AFTER COMMAND
6087
6088
6089 025564 004037 042354    JSR    R0,2#COMREG  ;COMPARE SAVED REGISTERS WITH
6090                                     ;PRESENT VALUE
6091 025570 004612          SAVERE     ;GOOD DATA SAVED IN 'SAVERE'
6092 025572 002354          WC        ;TEST DATA STARTING FROM 'RHWC'
6093 025574 000022          18.       ;18. REGISTERS TO BE COMPARED
6094 025576 025602          1$       ;RETURN TO 1$ ON ERROR

```



L15

MAINDEC-11-DZRJI-A, RPO4/5/6 FUNCT. CONT. TST-PT 1  
 DZRJIA.P11 T36 WRITE/READ DATA (125252)

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SEQ 0192

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6095 025600 025606          2$          ;RETURN TO 2$ ON NO ERROR
6096
6097 025602 104035        1$:      ERROR   35          ;WRITE DATA COMMAND CAUSED
6098 025604 000207        RTS      PC          ;IMPROPER REGISTER CHANGE
6099                                     ;GOOD DATA GIVES WHAT SHOULD
6100                                     ;BE
6101                                     ;RECEIVED DATA GIVES WHAT WAS
6102                                     ;THERE AFTER COMMAND
6103
6104                                     ;*NOW WRITE FROM BUFFER WILL CHECKED FOR NO CHANGE
6105
6106 025606          2$:
6107
6108 025606 004037 043404   JSR      RO,3#COMPAR   ;COMPARE TWO BLOCKS OF MEMORY
6109 025612 003534          REINTO    ;GOOD DATA STARTS FROM REINTO
6110 025614 002470          WRFROM    ;TEST DATA STARTS FROM WRFROM
6111 025616 000400          256.      ;256. WORDS TO BE COMPARED
6112 025620 025624          3$          ;RETURN TO 3$ ON ERROR
6113 025622 025630          4$          ;RETURN TO 4$ ON NO ERROR
6114
6115
6116 025624 104036        3$:      ERROR   36          ;WRITE DATA COMMAND CHANGED
6117 025626 000207        RTS      PC          ;WRITE FROM BUFFER
6118
6119                                     ;*NOW A READ DATA COMMAND WILL BE GIVEN
6120                                     ;*FILL READ INTO BUFFER WITH 256 ZEROS
6121
6122 025630          4$:
6123
6124 025630 004737 041366   JSR      PC,3#CLDISK   ;SET R1-RHCS1, R2-RHCS2
6125                                     ;R3-RHDS1, R4-RHER1
6126                                     ;GIVE RH-11 INITIALIZE
6127                                     ;SETUP UNIT NUMBER
6128 025634 004037 041236   JSR      RO,3#CLAREA   ;CLEAR 256. WORDS, FROM REINTO
6129 025640 003534          REINTO    ;STARTING FROM REINTO
6130 025642 000400          256.      ;256. WORDS
6131 025644 000000          0          ;FILL WITH 0
6132
6133
6134                                     ;*FILL WRITE FROM BUFFER WITH 256 OF 125252
6135
6136 025646 004037 041236   JSR      RO,3#CLAREA   ;CLEAR 256. WORDS, FROM WRFROM
6137 025652 002470          WRFROM    ;STARTING FROM WRFROM
6138 025654 000400          256.      ;256. WORDS
6139 025656 125252          125252    ;FILL WITH 125252
6140
6141
6142                                     ;*NOW FILL COMMAND
6143
6144
6145 025660 004037 043340   JSR      RO,3#RUN      ;SETUP TO RUN FOR DATA COMMAND
6146 025664 000000          0          ;CYLINDER 0
6147 025666 000          .BYTE 0    ;SECTOR 0
6148 025667 000          .BYTE 0    ;TRACK 0
  
```



```

6203
6204 025766 013746 002446      MOV    Q#READAT,-(SP)      ;SAVE COMMAND
6205 025772 052716 004101      BIS    #IE!GO!DVA,(SP)    ;INCLUDE IE!GO!DVA
6206 025776 011637 001124      MOV    (SP),Q#$GDDAT      ;SAVE FOR PRINTOUT
6207 026002 022600                CMP    (SP)+,RO           ;DURING ABOVE OPERATION ONLY IE!GO!DVA
6208                                ;AND COMMAND SHOULD BE SET
6209 026004 001405                BEQ    67$                ;BRANCH IF GOOD
6210 026006 010037 001126      MOV    RO,Q#$BDDAT        ;BAD DATA
6211 026012 010137 004600      MOV    R1,Q#REGADR        ;FAILING REGISTER RHCS1
6212 026016 104021                ERROR  21                ;DURING ABOVE OPERATION ONLY
6213                                ;COMMAND AND IE!GO!DVA SHOULD BE SET
6214 026020 012746 010500      67$: MOV    #MOL!DPR!VV,-(SP) ;SAVE BITS SET DURING OPERATION IN RHDS1
6215 026024 011637 001124      MOV    (SP),Q#$GDDAT      ;SAVE FOR PRINTOUT
6216 026030 022605                CMP    (SP)+,R5          ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
6217                                ;SHOULD BE SET
6218 026032 001405                BEQ    69$                ;BRANCH IF GOOD
6219 026034 010537 001126      MOV    R5,Q#$BDDAT        ;BAD DATA
6220 026040 010337 004600      MOV    R3,Q#REGADR        ;FAILING REGISTER RHDS1
6221 026044 104063                ERROR  63                ;DURING ABOVE OPERATION ONLY
6222                                ;MOL!DPR!VV SHOULD BE SET
6223 026046                69$:
6224
6225                                ;*CHANGE SAVED REGISTERS TO EXPECTED VALUES
6226
6227 026046 004037 041270      JSR    RO,Q#FILLRE        ;MOV 0 INTO SAVED RHWC
6228 026052 002272                RHWC                    ;SAVED REGISTER TO CHANGE
6229 026054 000000                0                        ;DATA
6230 026056 004037 041270      JSR    RO,Q#FILLRE        ;MOV REINTO+(256.*2) INTO SAVED RHBA
6231 026062 002274                RHBA                    ;SAVED REGISTER TO CHANGE
6232 026064 004534                REINTO+(256.*2)        ;DATA
6233 026066 004037 041270      JSR    RO,Q#FILLRE        ;MOV 1 INTO SAVED RHDST
6234 026072 002304                RHDST                  ;SAVED REGISTER TO CHANGE
6235 026074 000001                1                        ;DATA
6236
6237                                ;*COMPARE REGISTERS BEFORE READ DATA COMMAND
6238                                ;*WITH REGISTERS AFTER COMMAND
6239
6240
6241 026076 004037 042354      JSR    RO,Q#COMREG        ;COMPARE SAVED REGISTERS WITH
6242                                ;PRESENT VALUE
6243 026102 004612                SAVERE                  ;GOOD DATA SAVED IN 'SAVERE'
6244 026104 002354                WC                        ;TEST DATA STARTING FROM 'RHWC'
6245 026106 000022                18.                     ;18. REGISTERS TO BE COMPARED
6246 026110 026114                5$                       ;RETURN TO 5$ ON ERROR
6247 026112 026120                6$                       ;RETURN TO 6$ ON NO ERROR
6248
6249 026114 104033                5$: ERROR 33              ;READ DATA CAUSED IMPROPER
6250 026116 000207                RTS    PC                ;REGISTER CHANGE
6251                                ;GOOD DATA GIVES WHAT SHOULD BE THE
6252                                ;RECEIVED DATA GIVES WHAT WAS THERE
6253                                ;AFTER COMMAND
6254
6255                                ;*NOW READ INTO BUFFER IS CHECKED FOR GOOD READ
6256
    
```





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6326 026206 004037 041236 JSR RO,0#CLAREA ;CLEAR 200. WORDS FROM REINTO
6327 026212 003534 REINTO ;STARTING FROM REINTO
6328 026214 000310 200. ;200. WORDS
6329 026216 052525 52525 ;FILL WITH 52525
6330
6331 026220 004037 041236 JSR RO,0#CLAREA ;CLEAR 56. WORDS FROM REINTO+(200.*2)
6332 026224 004354 REINTO+(200.*2) ;STARTING FROM REINTO+(200.*2)
6333 026226 030070 56. ;56. WORDS
6334 026230 000377 377 ;FILL WITH 377
6335
6336
6337
6338
6339 ;*NOW WRITE DATA COMMAND WILL BE LOADED
6340 026232 004037 043340 JSR RO,0#RUN ;SETUP TO RUN FOR DATA COMMAND
6341 026236 000000 0 ;CYLINDER 0
6342 026240 000 .BYTE 0 ;SECTOR 0
6343 026241 000 .BYTE 0 ;TRACK 0
6344 026242 177470 -200. ;WORD COUNT = 200.
6345 026244 002470 WRFROM ;BUS ADDRESS
6346 ;STARTING ADDRESS OF DATA
6347 ;BUFFER = WRFROM
6348 026246 000000 0 ;DO NOT INHIBIT BUS ADDRESS INCREMENT
6349 026250 010000 FMT22 ;16 BITS PER WORD FORMAT
6350 ;DO NOT INHIBIT ECC CORRECTION
6351 ;DO NOT INHIBIT HEADER COMPARE
6352 026252 002442 WRIDAT ;GET READY TO DO A WRIDAT
6353 ;WRITE DATA WITH 50 IN RHCSI
6354
6355
6356 ;*NOW SAVE REGISTER FOR COMPARISON AFTER WRITE DATA
6357
6358 026254 004037 041534 JSR RO,0#SAVER ;SAVE REGISTERS
6359 026260 002272 RHWC ;RHWC IS THE FIRST REGISTER SA.ED
6360 026262 004612 SAVERE ;STARTING ADDRESS OF WHERE
6361 ;THE REGISTERS ARE SAVED
6362 026264 000022 18. ;NUMBER OF REGISTERS
6363 ;SAVED = 18.
6364
6365 026266 004737 041446 JSR PC,0#CHECKT ;CHECK DVA,ROY,MOL,CPR,DRY,VV = 1
6366 ;AND THAT NO STATUS BITS ARE STUCK = 1
6367 026272 104400 066402 TYPE .CPHALT ;CANNOT CONTINUE TESTING IF ANY OF
6368 ;THE FIRST SET OF BITS DON'T = 1
6369 026276 000000 HALT ;STOP
6370
6371 026300 013777 004606 153760 MOV 0#RPHVEC,0#RPVEC ;SET RPO4 VECTOR ADDRESS
6372 ;TO 'TIME1' IF P-CLOCK IS PRESENT
6373 ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
6374 ;'TIME' WILL ONLY SAVE
6375 ;CURRENT CYLINDER ADDRESS
6376 ;AND LOOK AHEAD REGISTERS
6377
6378
6379 026306 013746 002442 MOV 0#WRIDAT,-(SP) ;GET READY TO MOVE COMMAND

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6390 026312 052716 000101    BIS    #GO!IE,(SP)    ;GET READY TO SET 'GO' AND
6391                                ;ENABLE INTERRUPT
6392 026316 012677 153756    MOV    (SP)+,RHC$1    ;GO WITH
6393                                ;60 IN RHCS1 FOR WRITE DATA
6394                                ;WITH INTERRUPT ENABLED
6395 026322 011100    MOV    R1,R0        ;SAVE RHCS1 DURING ABOVE OPERATION
6396 026324 011305    MOV    R3,R5        ;SAVE RHDS1 DURING ABOVE OPERATION
6397                                ;*ONE REVOLUTION=16670 MICROSEC, ONE SECTOR=760 MICROSEC
6398                                ;*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN
6399                                ;*R0 AND R5 IMMEDIATELY AFTER GO
6400
6401 026340 013746 002442    MOV    R,WRIDAT,-(SP) ;SAVE COMMAND
6402 026344 052716 004101    BIS    #IE!GO!DVA,(SP) ;INCLUDE IE!GO!DVA
6403 026350 011537 001124    MOV    (SP),R,$GDDAT ;SAVE FOR PRINTOUT
6404 026354 022500    CMP    (SP)+,R0      ;DURING ABOVE OPERATION ONLY IE!GO!DVA
6405                                ;AND COMMAND SHOULD BE SET
6406 026356 001405    BEQ    64$          ;BRANCH IF GOOD
6407 026360 010037 001126    MOV    R0,R,$BDDAT  ;BAD DATA
6408 026364 010137 004600    MOV    R1,R,$REGADR ;FAILING REGISTER RHCS1
6409 026370 104021    ERROR  21          ;DURING ABOVE OPERATION ONLY
6410                                ;COMMAND AND IE!GO!DVA SHOULD BE SET
6411 026372 012746 010500    MOV    #MOL!DPR!VV,-(SP) ;SAVE BITS SET DURING OPERATION IN RHDS1
6412 026376 011637 001124    MOV    (SP),R,$GDDAT ;SAVE FOR PRINTOUT
6413 026402 022605    CMP    (SP)+,R5     ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
6414                                ;SHOULD BE SET
6415 026404 001405    BEQ    66$          ;BRANCH IF GOOD
6416 026406 010537 001126    MOV    R5,R,$BDDAT  ;BAD DATA
6417 026412 010337 004600    MOV    R3,R,$REGADR ;FAILING REGISTER RHDS1
6418 026416 104063    ERROR  63          ;DURING ABOVE OPERATION ONLY
6419                                ;MOL!DPR!VV SHOULD BE SET
6420 026420                                66$:
6421
6422                                ;*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE
6423
6424 026420 004037 041270    JSR    RC,R,$FILLRE ;MOV 0 INTO SAVED RHC$
6425 026424 002272    RHWC                                ;SAVED REGISTER TO CHANGE
6426 026426 000000    C                                ;DATA
6427 026430 004037 041270    JSR    R0,R,$FILLRE ;MOV WRFROM+(200.*2) INTO SAVED RH$A
6428 026434 002274    RHBA                                ;SAVED REGISTER TO CHANGE
6429 026436 003310    WRFROM+(200.*2) ;DATA
6430 026440 004037 041270    JSR    R0,R,$FILLRE ;MOV 1 INTO SAVED RHD$T
6431 026444 002304    RHD$T                                ;SAVED REGISTER TO CHANGE
6432 026446 000001    I                                ;DATA
6433

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6434                                     ;*NOW COMPARE REGISTERS BEFORE WRITE DATA WITH REGISTERS
6435                                     ;*AFTER COMMAND
6436
6437
6438 026450 004037 042354                JSR      RO,2#COMREG      ;COMPARE SAVED REGISTERS WITH
6439                                     ;PRESENT VALUE
6440 026454 004612                        SAVERE                    ;GOOD DATA SAVED IN 'SAVERE'
6441 026456 002354                        WC                        ;TEST DATA STARTING FROM 'RHWC'
6442 026460 000022                        18.                      ;18. REGISTERS TO BE COMPARED
6443 026462 026466                        1$                       ;RETURN TO 1$ ON ERROR
6444 026464 026472                        2$                       ;RETURN TO 2$ ON NO ERROR
6445
6446 026466 104035                        1$: ERROR 35             ;WRITE DATA COMMAND CAUSED
6447 026470 000207                        RTS      PC              ;IMPROPER REGISTER CHANGE
6448                                     ;GOOD DATA GIVES WHAT SHOULD
6449                                     ;BE
6450                                     ;RECEIVED DATA GIVES WHAT WAS
6451                                     ;THERE AFTER COMMAND
6452
6453                                     ;*NOW WRITE FROM BUFFER WILL BE CHECKED FOR NO CHANGE
6454
6455 026472                        2$:
6456
6457 026472 004037 043404                JSR      RO,3#COMPAR     ;COMPARE TWO BLOCKS OF MEMORY
6458 026476 003534                        REINTO                    ;GOOD DATA STARTS FROM REINTO
6459 026500 002470                        WRFROM                    ;TEST DATA STARTS FROM WRFROM
6460 026502 000400                        256.                     ;256. WORDS TO BE COMPARED
6461 026504 026510                        3$                       ;RETURN TO 3$ ON ERROR
6462 026506 026514                        4$                       ;RETURN TO 4$ ON NO ERROR
6463
6464
6465 026510 104036                        3$: ERROR 36             ;WRITE DATA COMMAND CHANGED
6466 026512 000207                        RTS      PC              ;WRITE FROM BUFFER
6467
6468                                     ;*NOW A READ DATA COMMAND WILL BE GIVEN
6469
6470                                     ;*FILL READ INTO BUFFER WITH 200 ZEROS AND 56 OF ALL ONES
6471
6472 026514                        4$:
6473
6474 026514 004737 041366                JSR      PC,3#CLDISK     ;SET R1-RHCS1, R2-RHCS2
6475                                     ;R3-RHDS1, R4-RHER1
6476                                     ;GIVE RH-11 INITIALIZE
6477                                     ;SETUP UNIT NUMBER
6478 026520 004037 041236                JSR      RO,3#CLAREA     ;CLEAR 200. WORDS, FROM REINTO
6479 026524 003534                        REINTO                    ;STARTING FROM REINTO
6480 026526 000310                        200.                     ;200. WORDS
6481 026530 000000                        0                         ;FILL WITH 0
6482
6483 026532 004037 041236                JSR      RO,3#CLAREA     ;CLEAR 56. WORDS, FROM REINTO+(200.*2)
6484 026536 004354                        REINTO+(200.*2)          ;STARTING FROM REINTO+(200.*2)
6485 026540 000070                        56.                     ;56. WORDS
6486 026542 000377                        377                     ;FILL WITH 377
6487

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6498
6499           ;*FILL WRITE FROM BUFFER WITH 200 OF 52525 AND 56 OF 0
6490
6491 026544 004037 041236 JSR   RO,2*CLAREA ;CLEAR 200. WORDS FROM WRFROM
6492 026550 002470 WRFROM ;STARTING FROM WRFROM
6493 026552 000310 200. ;200. WORDS
6494 026554 052525 52525 ;FILL WITH 52525
6495
6496 026556 004037 041236 JSR   RO,2*CLAREA ;CLEAR 56. WORDS FROM WRFROM+(200.*2)
6497 026552 003310 WRFROM+(200.*2) ;STARTING FROM WRFROM+(200.*2)
6498 026554 000070 56. ;56. WORDS
6499 026556 000377 377 ;FILL WITH 377
6500
6501
6502           ;*NOW FILL COMMAND
6503
6504
6505 026570 004037 043340 JSR   RO,2*RJN ;SETUP TO RUN FOR DATA COMMAND
6506 026574 000000 0 ;CYLINDER 0
6507 026576 000 .BYTE 0 ;SECTOR 0
6508 026577 000 .BYTE 0 ;TRACK 0
6509 026600 177470 -200. ;WORD COUNT = 200.
6510 026602 003534 REINTO ;BUS ADDRESS
6511 ;STARTING ADDRESS OF DATA
6512 ;BUFFER = REINTO
6513 026604 000000 0 ;DO NOT INHIBIT BUS ADDRESS INCREMENT
6514 026606 014000 ECI!FM*22 ;16 BITS PER WORD FORMAT
6515 ;INHIBIT ECC CORRECTION
6516 ;DO NOT INHIBIT HEADER COMPARE
6517 026610 002446 READAT ;GET READY TO DO A READAT
6518 ;READ DATA WITH 70 IN RHCSI
6519
6520
6521           ;*NOW SAVE REGISTERS FOR COMPARISON AFTER READ DATA COMMAND
6522
6523
6524 026612 004037 041534 JSR   RO,3*SAVER ;SAVE REGISTERS
6525 026616 002272 RHWC ;RHWC IS THE FIRST REGISTER SAVED
6526 026620 004612 SAVERE ;STARTING ADDRESS OF WHERE
6527 026622 000022 18. ;THE REGISTERS ARE SAVED
6528 ;NUMBER OF REGISTERS
6529 ;SAVED = 18.
6530
6531 026624 004737 041446 JSR   PC,3*CHECKT ;CHECK DVA, RDY, MQL, DPR, DRY, VV = 1
6532 026630 104400 066402 TYPE ,CPHALT ;AND THAT NO STATUS BITS ARE STUCK = 1
6533 ;CANNOT CONTINUE TESTING IF ANY OF
6534 026634 000000 HALT ;THE FIRST SET OF BITS DON'T = 1
6535 ;STOP
6536 026636 013777 004606 153422 MOV   3*RP4VEC,3RPVEC ;SET RPO4 VECTOR ADDRESS
6537 ;TO 'TIME1' IF P-CLOCK IS PRESENT
6538 ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
6539 ;'TIME' WILL ONLY SAVE
6540 ;CURRENT CYLINDER ADDRESS
6541 ;AND LOOK AHEAD REGISTERS

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6542
6543
6544 026644 013746 002446      MOV      0#READAT, -(SP)      ;GET READY TO MOVE COMMAND
6545 026650 052716 000101      BIS      #GO!IE, (SP)        ;GET READY TO SET 'GO' AND
6546                                     ;ENABLE INTERRUPT
6547 026654 012677 153420      MOV      (SP)+, 0RHCS1       ;GO WITH
6548                                     ;70 IN RHCS1 FOR READ DATA
6549                                     ;WITH INTERRUPT ENABLED
6550 026660 011100      MOV      0R1, R0             ;SAVE RHCS1 DURING ABOVE OPERATION
6551 026662 011305      MOV      0R3, R5             ;SAVE RHDS1 DURING ABOVE OPERATION
6552
6553
6554 026664 104412      WAT                                     ;WAIT FOR RDY BIT TO SET
6555 026666 002300      RHCS1                             ;WAIT FOR RHCS1 REGISTER
6556 026670 000200      RDY                                 ;WAIT FOR RDY BIT IN RHCS1 REGISTER
6557 026672 001614      908.                               ;ALLOW 9080 MICRO SECONDS
6558 026674 001507      839.                               ;RDY MUST SET BETWEEN
6559                                     ;690 AND 17470 MICRO SECONDS
6560
6561                                     ;*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN
6562                                     ;*R0 AND R5 IMMEDIATELY AFTER GO
6563
6564 026676 013746 002446      MOV      0#READAT, -(SP)      ;SAVE COMMAND
6565 026702 052716 004101      BIS      #IE!GO!DVA, (SP)    ;INCLUDE IE!GO!DVA
6566 026706 011637 001124      MOV      (SP), 0#$GDDAT      ;SAVE FOR PRINTOUT
6567 026712 022600      CMP      (SP)+, R0           ;DURING ABOVE OPERATION ONLY IE!GO!DVA
6568                                     ;AND COMMAND SHOULD BE SET
6569 026714 001405      BEQ      67$                 ;BRANCH IF GOOD
6570 026716 010037 001126      MOV      R0, 0#$BDDAT        ;BAD DATA
6571 026722 010137 004600      MOV      R1, 0#REGADR        ;FAILING REGISTER RHCS1
6572 026726 104021      ERROR    21                  ;DURING ABOVE OPERATION ONLY
6573                                     ;COMMAND AND IE!GO!DVA SHOULD BE SET
6574 026730 012746 010500      67$: MOV      #MOL!DPR!VV, -(SP) ;SAVE BITS SET DURING OPERATION IN RHDS1
6575 026734 011637 001124      MOV      (SP), 0#$GDDAT      ;SAVE FOR PRINTOUT
6576 026740 022605      CMP      (SP)+, R5           ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
6577                                     ;SHOULD BE SET
6578 026742 001405      BEQ      69$                 ;BRANCH IF GOOD
6579 026744 010537 001126      MOV      R5, 0#$BDDAT        ;BAD DATA
6580 026750 010337 004600      MOV      R3, 0#REGADR        ;FAILING REGISTER RHDS1
6581 026754 104063      ERROR    63                  ;DURING ABOVE OPERATION ONLY
6582                                     ;MOL!DPR!VV SHOULD BE SET
6583 026756      69$:
6584
6585                                     ;*CHANGE SAVED REGISTERS TO EXPECTED VALUES
6586
6587 026756 004037 041270      JSR      R0, 0#FILLRE         ;MOV 0 INTO SAVED RHWC
6588 026762 002272      RHWC                             ;SAVED REGISTER TO CHANGE
6589 026764 000000      0                                 ;DATA
6590 026766 004037 041270      JSR      R0, 0#FILLRE         ;MOV REINT0+(200.*2) INTO SAVED RHBA
6591 026772 002274      RHBA                             ;SAVED REGISTER TO CHANGE
6592 026774 004354      REINT0+(200.*2) ;DATA
6593 026776 004037 041270      JSR      R0, 0#FILLRE         ;MOV 1 INTO SAVED RHDST
6594 027002 002304      RHDST                             ;SAVED REGISTER TO CHANGE
6595 027004 000001      1                                 ;DATA

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6596
6597
6598
6599
6600
6601 027006 004037 042354 JSR RO.3#COMREG ;COMPARE SAVED REGISTERS WITH
6602 ;PRESENT VALUE
6603 027012 004612 SAVERE ;GOOD DATA SAVED IN 'SAVERE'
6604 027014 002354 WC ;TEST DATA STARTING FROM 'RHWC'
6605 027016 000022 19. ;19. REGISTERS TO BE COMPARED
6606 027020 027024 5$ ;RETURN TO 5$ ON ERROR
6607 027022 027030 6$ ;RETURN TO 6$ ON NO ERROR
6608
6609 027024 104033 5$: ERROR 33 ;READ DATA CAUSED IMPROPER
6610 027026 000207 RTS PC ;REGISTER CHANGE
6611 ;GOOD DATA GIVES WHAT SHOULD BE THE
6612 ;RECEIVED DATA GIVES WHAT WAS THERE
6613 ;AFTER COMMAND
6614
6615 ;*NOW READ INTO BUFFER IS CHECKED FOR GOOD READ
6616
6617 027030 6$:
6618
6619 027030 004037 043404 JSR RO.3#COMPAR ;COMPARE TWO BLOCKS OF MEMORY
6620 027034 002470 WRFROM ;GOOD DATA STARTS FROM WRFROM
6621 027036 003534 REINTO ;TEST DATA STARTS FROM REINTO
6622 027040 000400 256. ;256. WORDS TO BE COMPARED
6623 027042 027046 7$ ;RETURN TO 7$ ON ERROR
6624 027044 027052 10$ ;RETURN TO 10$ ON NO ERROR
6625
6626
6627 027046 104034 7$: ERROR 34 ;INCORRECT DATA AFTER
6628 027050 000207 RTS PC ;WRITE DATA FOLLOWED BY A
6629 ;READ DATA
6630 027052 10$:
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::\*\*\*\*\*  
:\*TEST 40 WRITE/READ DATA USING UNIBUS B

::\* THIS TEST USES UNIBUS B IF CONNECTED TO THE RH  
:\* IF UNIBUS B IS NOT CONNECTED THEN THIS TEST IS NOT PERFORMED  
:\* THIS TEST GIVES A WRITE DATA COMMAND FOR CYLINDER C  
:\* TRACK 0, SECTOR 0, KEYS 0, 200 WORDS OF 052525  
:\* THIS SECTOR IS FORMATED BY PREVIOUS TEST  
:\* THEN READ DATA COMMAND IS GIVEN FOR 256 WORDS IN  
:\* SAME CYLINDER, TRACK, SECTOR, KEYS  
:\* THESE COMMANDS USE UNIBUS B FOR DATA  
:\* WRITE FROM BUFFER AND READ INTO BUFFER ARE FILLED WITH  
:\* 200 OF 052525 AND 56 OF 377  
:\* THE WRITE DATA COMMAND IS LOADED EXCEPT GO AND IE  
:\* ALL REGISTERS ARE SAVED AND THEN GO IS GIVEN TO WRITE DATA

::\* THEN ALL REGISTERS ARE COMPARED TO CHECK FOR IMPROPER  
:\* CHANGE, THEN WRITE FROM BUFFER IS CHECKED FOR NO CHANGE

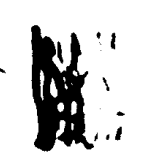
::\* THEN READ INTO BUFFER IS FILLED WITH 200 IF ZEROS  
:\* AND 56 ALL ONES, WRITE FROM BUFFER IS FILLED WITH 200  
:\* WORDS OF 52525 AND 56 WORDS OF 0  
:\* THE COMMAND EXCEPT GO IS LOADED, ALL REGISTERS ARE SAVED  
:\* GO IS GIVEN

::\* ALL REGISTER ARE CHECKED  
:\* READ DATA IS CHECKED

::\*\*\*\*\*

027052 000004  
027054 012737 000001 001212  
027062 012706 001000  
027066 012737 00004C 004604  
027074 004737 041366  
027100 005737 004750  
027104 001402  
027106 000137 030222  
027112  
027112 005037 004732  
027116 004037 041236  
027122 002470

TST40: SCOPE  
MOV #1,STIMES ;DO 1 ITERATION  
MOV #STACK,SP ;RESET STACK  
MOV #40,J#TSTNM ;SAVE TEST NUMBER  
JSR PC,J#CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETUP UNIT NUMBER  
;\*CHECK TO SEE IF THE PROGRAM IS RUNNING WITH AN RH70  
TST J#RH70 ;TEST FOR RH70 CONTROLLER  
BEQ 30\$ ;IF FLAG = 1, THIS TEST IS SKIPPED  
JMP TST41 ; JUMP TO NEXT TEST -----  
30\$: ;IF FLAG = 1, DO THIS TEST  
CLR J#UBUSB ;CLEAR UNIBUS INDICATOR  
;\*NOW FILL WRITE FROM BUFFER-200 OF 52525 AND 56 OF 377  
JSR R0,J#CLAREA ;CLEAR 200. WORDS FROM WRFROM  
WRFROM ;STARTING FROM WRFROM



K16

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6689 027124 000310          200.          ;200. WORDS
6690 027126 052525          52525         ;FILL WITH 52525
6691
6692 027130 004037 041236   JSR    RO,2#CLAREA ;CLEAR 56. WORDS, FROM WRFROM+<200.*2>
6693 027134 003310          WRFROM+<200.*2> ;STARTING FROM WRFROM+<200.*2>
6694 027136 000070          56.           ;56. WORDS
6695 027140 000377          377           ;FILL WITH 377
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6702 027142 004037 041236   JSR    RO,2#CLAREA ;CLEAR 200. WORDS, FROM REINTO
6703 027146 003534          REINTO         ;STARTING FROM REINTO
6704 027150 000310          200.          ;200. WORDS
6705 027152 052525          52525         ;FILL WITH 52525
6706
6707 027154 004037 041236   JSR    RO,2#CLAREA ;CLEAR 56. WORDS, FROM REINTO+<200.*2>
6708 027160 004354          REINTO+<200.*2> ;STARTING FROM REINTO+<200.*2>
6709 027162 000070          56.           ;56. WORDS
6710 027164 000377          377           ;FILL WITH 377
6711
6712
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6716 027166 004037 043340   JSR    RO,2#RUN   ;SETUP TO RUN FOR DATA COMMAND
6717 027172 000000          0             ;CYLINDER 0
6718 027174 000          .BYTE        ;SECTOR 0
6719 027175 000          .BYTE        ;TRACK 0
6720 027176 177470          -200.         ;WORD COUNT = 200.
6721 027200 002470          WRFROM        ;BUS ADDRESS
6722
6723
6724 027202 000000          0             ;STARTING ADDRESS OF DATA
6725 027204 010000          FMT22        ;BUFFER = WRFROM
6726
6727
6728 027206 002442          WRIDAT       ;DO NOT INHIBIT BUS ADDRESS INCREMENT
6729
6730
6731 027210 052777 002000 153062 BIS    #PSEL,2RHCS1 ;WRITE DATA WITH 60 IN RHCS1
6732
6733
6734
6735
6736 027216 004037 041534   JSR    RO,2#SAVER ;SET PORT B
6737 027222 002272          RHWC         ;THAT IS UNIBUS B
6738 027224 004612          SAVERE
6739
6740 027226 000022          18.          ;*NOW SAVE REGISTER FOR COMPARISON AFTER WRITE DATA
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6743 027230 004737 041446 JSR PC, @#CHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1
6744 ;AND THAT NO STATUS BITS ARE STUCK = 1
6745 027234 104400 066402 TYPE ,CPHALT ;CANNOT CONTINUE TESTING IF ANY OF
6746 ;THE FIRST SET OF BITS DON'T = 1
6747 027240 000000 HALT ;STOP
6749 027242 013777 004606 153016 MOV @#RPHVEC, @RPVEC ;SET RPO4 VECTOR ADDRESS
6750 ;TO 'TIME1' IF P-CLOCK IS PRESENT
6751 ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
6752 ;'TIME' WILL ONLY SAVE
6753 ;CURRENT CYLINDER ADDRESS
6754 ;AND LOOK AHEAD REGISTERS
6755
6756 027250 013746 002442 MOV @#WRIDAT, -(SP) ;GET READY TO MOVE COMMAND
6757 027254 052716 002101 BIS #GO!IE!PSEL, (SP) ;GET READY TO SET 'GO' AND
6758 ;ENABLE INTERRUPT
6759 027260 012677 153014 MOV (SP)+, @RHCSI ;GO WITH
6760 ;60 IN RHCSI FOR WRITE DATA
6761 ;WITH INTERRUPT ENABLED
6762 027264 011100 MOV @R1, R0 ;SAVE RHCSI DURING ABOVE OPERATION
6763 027266 011305 MOV @R3, R5 ;SAVE RHDS1 DURING ABOVE OPERATION
6764
6765 ;*ONE REVOLUTION=16670 MICROSEC, ONE SECTOR=760 MICROSEC
6766
6767
6768 027270 104412 WAT ;WAIT FOR RDY BIT TO SET
6769 027272 002300 RHCSI ;WAIT FOR RHCSI REGISTER
6770 027274 030200 RDY ;WAIT FOR RDY BIT IN RHCSI REGISTER
6771 027276 001614 908. ;ALLOW 9080 MICRO SECONDS
6772 027300 001507 839. ;RDY MUST SET BETWEEN
6773 ;690 AND 17470 MICRO SECONDS
6774
6775 ;*COMPARE CONTENTS OF RHCSI AND RHDS1 ALREADY SAVED IN
6776 ;*R0 AND R5 IMMEDIATELY AFTER GO
6777
6778 027302 013746 002442 MOV @#WRIDAT, -(SP) ;SAVE COMMAND
6779 027306 052716 006101 BIS #IE!GO!DVA!PSEL, (SP) ;INCLUDE IE!GO!DVA!PSEL
6780 027312 011637 001124 MOV (SP), @#SGDDAT ;SAVE FOR PRINTOUT
6781 027316 022600 CMP (SP)+, R0 ;DURING ABOVE OPERATION ONLY IE!GO!DVA!PSEL
6782 ;AND COMMAND SHOULD BE SET
6783 027320 001405 BEQ 64$ ;BRANCH IF GOOD
6784 027322 010037 001126 MOV R0, @#SBDDAT ;BAD DATA
6785 027326 010137 004600 MOV R1, @#REGADR ;FAILING REGISTER RHCSI
6786 027332 104021 ERROR 21 ;DURING ABOVE OPERATION ONLY
6787 ;COMMAND AND IE!GO!DVA!PSEL SHOULD BE SET
6788 027334 012746 010500 64$: MOV #MOL!DPR!VV, -(SP) ;SAVE BITS SET DURING OPERATION IN R-CSI
6789 027340 011637 001124 MOV (SP), @#SGDDAT ;SAVE FOR PRINTOUT
6790 027344 022605 CMP (SP)+, R5 ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
6791 ;SHOULD BE SET
6792 027346 001405 BEQ 66$ ;BRANCH IF GOOD
6793 027350 010537 001126 MOV R5, @#SBDDAT ;BAD DATA
6794 027354 010337 004600 MOV R3, @#REGADR ;FAILING REGISTER RHDS1
6795 027360 104063 ERROR 63 ;DURING ABOVE OPERATION ONLY
6796 ;MOL!DPR!VV SHOULD BE SET

```

M16

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6797 027362          66$:
6798
6799
6800          ;*CHECK IF NEM NON EXISTANT MEMORY IS SET
6801          ;*IF SET IT MEANS UNIBUS B IS NOT CONNECTED
6802          ;*SO THIS TEST IS NOT PERFORMED
6903
6804 027362 032777 004000 152706      BIT      #NEM,2RHCS2      ;TEST NEM
6805 027370 001441          BEQ      11$          ;BRANCH IF UNIBUS B THERE
6806 027372 012737 177777 004732      MOV      #-1,2#UBUSB    ;UNIBUS B NOT THERE
6807 027400 104400 027406          TYPE     68$          ;:TYPE ASCIZ STRING
6808 027404 000425          BR       67$          ;:GET OVER THE ASCIZ
6809          ;:68$: .ASCIZ <15><12>/THE RH DOES NOT HAVE UNIBUS B CONNECTED
6810          ;:67$:
6811 027460 104400 001223          TYPE     , $CRLF
6812 027464 104400 001223          TYPE     , $CRLF
6813 027470 000137 030222          JMP      2#10$         ;JUMP TO NEXT TEST - NO UNIBUS B
6814 027474
6815 027474 104400 027502          TYPE     70$         ;:TYPE ASCIZ STRING
6816 027500 000424          BR       69$         ;:GET OVER THE ASCIZ
6817          ;:70$: .ASCIZ <15><12>/THE RH DOES HAVE UNIBUS B CONNECTED
6818          ;:69$:
6819 027552 104400 001223          TYPE     , $CRLF
6820 027556 104400 001223          TYPE     , $CRLF
6821
6822          ;*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE
6823
6824 027562 004037 041270          JSR      RO,2#FILLRE    ;MOV 0 INTO SAVED RHCW
6825 027566 002272          RHWC          ;SAVED REGISTER TO CHANGE
6826 027570 000000          0            ;DATA
6827 027572 004037 041270          JSR      RO,2#FILLRE    ;MOV WRFROM+(200.*2) INTO SAVED RH9A
6828 027576 002274          RH9A          ;SAVED REGISTER TO CHANGE
6829 027600 003310          WRFROM+(200.*2) ;DATA
6830 027602 004037 041270          JSR      RO,2#FILLRE    ;MOV 1 INTO SAVED RHDST
6831 027606 002304          RHDST          ;SAVED REGISTER TO CHANGE
6832 027610 000001          1            ;DATA
6833
6834          ;*NOW COMPARE REGISTERS BEFORE WRITE DATA WITH REGISTERS
6835          ;*AFTER COMMAND
6836
6837
6838 027612 004037 042354          JSR      RO,2#COMREG    ;COMPARE SAVED REGISTERS WITH
6839          ;PRESENT VALUE
6840 027616 004612          SAVERE          ;GOOD DATA SAVED IN 'SAVERE'
6841 027620 002354          WC            ;TEST DATA STARTING FROM 'RHWC'
6842 027622 000022          18.           ;18. REGISTERS TO BE COMPARED
6843 027624 027630          1$           ;RETURN TO 1$ ON ERROR
6844 027626 027634          2$           ;RETURN TO 2$ ON NO ERROR
6845
6846 027630 104074          1$:          ERROR 74      ;WHILE USING UNIBUS B
6847          ;WRITE DATA COMMAND CAUSED
6848 027632 000207          RTS          PC      ;IMPROPER REGISTER CHANGE
6849          ;GOOD DATA GIVES WHAT SHOULD
6850          ;BE

```

:RECEIVED DATA GIVES WHAT WAS  
:THERE AFTER COMMAND

:\*NOW WRITE FROM BUFFER WILL BE CHECKED FOR NO CHANGE

35:

0042424	JSR	RD, 2#COMPAR	:COMPARE TWO BLOCKS OF MEMORY
			:2000 DATA STARTS FROM REINTC
			:1551 DATA STARTS FROM WRFROM
			:256. WORDS TO BE COMPARED
			:RETURN TO 35 ON ERROR
			:RETURN TO 45 ON NO ERROR

35:

ERRDR	75	:WHILE USING UNIBUS B
RTS	PC	:WRITE DATA COMMAND CHANGED
		:WRITE FROM BUFFER

:\*NOW A READ DATA COMMAND WILL BE GIVEN  
:\*FILL READ INTO BUFFER WITH 200 ZEROS AND 56 OF ALL ONES

45:

004737	041355	JSR	PC, 2#CLDISK	:SET R1-RHCS1, R2-RHCS2
				:R3-RHCS1, R4-RHER1
				:GIVE RH-11 INITIALIZE
				:SETUP UNIT NUMBER
004037	041236	JSR	RD, 2#CLAREA	:CLEAR 200. WORDS, FROM REINTC
003310		REINTC		:STARTING FROM REINTC
000000		200.		:200. WORDS
		0		:FILL WITH 0
004037	041236	JSR	RD, 2#CLAREA	:CLEAR 56. WORDS, FROM REINTC+(200.*2)
004037		REINTC+(200.*2)		:STARTING FROM REINTC+(200.*2)
000070		56.		:56. WORDS
000377		377		:FILL WITH 377

:\*FILL WRITE FROM BUFFER WITH 200 OF 52525 AND 56 OF 3

004037	041236	JSR	RD, 2#CLAREA	:CLEAR 200. WORDS, FROM WRFROM
002470		WRFROM		:STARTING FROM WRFROM
000310		200.		:200. WORDS
052525		52525		:FILL WITH 52525

004037	041236	JSR	RD, 2#CLAREA	:CLEAR 56. WORDS, FROM WRFROM+(200.*2)
003310		WRFROM+(200.*2)		:STARTING FROM WRFROM+(200.*2)
000070		56.		:56. WORDS
000377		377		:FILL WITH 377

:\*NOW FILL COMMAND







E01

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030176 000207          RTS    PC          ;REGISTER CHANGE
;GOOD DATA GIVES WHAT SHOULD BE THE
;RECEIVED DATA GIVES WHAT WAS THERE
;AFTER COMMAND
; *NOW READ INTO BUFFER IS CHECKED FOR GOOD READ
030200          6$:
030200 004037 043404    JSR     RD, @*COMPAR ;COMPARE TWO BLOCKS OF MEMORY
030204 002470          WRFROM ;GOOD DATA STARTS FROM WRFROM
030206 003534          REINTC ;TEST DATA STARTS FROM REINTC
030210 000400          256. ;256. WORDS TO BE COMPARED
030212 030216          7$ ;RETURN TO 7$ ON ERROR
030214 030222          10$ ;RETURN TO 10$ ON NO ERROR
030216 104073          7$:    ERROR  73    ;WHILE USING UNIBUS B
;INCORRECT DATA AFTER
030220 000207          RTS     PC          ;WRITE DATA FOLLOWED BY A
;READ DATA
030222          10$:

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7094

\*\*\*\*\*  
\*TEST 41 IMPLIED SEARCH

\* ONLY NEW ADDITION IN THIS TEST IS AN IMPLIED SEARCH  
\* A WRITE HEADER AND DATA IS GIVEN FOR MORE THAN ONE SECTOR  
\* OF WORDS  
\* WRITE HEADER AND DATA CYLINDER 0, FORMAT 16 BITS PER WORD  
\* TRACK 0, SECTOR 0, KEYS=0, NUMBER OF WORDS  
\* 266 (4 HEADER 256 DATA=0 4 HEADER 2 DATA=1  
\* THEN READ HEADER AND DATA FOR ABOVE SECTOR 1 ONLY  
\* WRITE FROM BUFFER AND READ INTO BUFFER ARE FILLED WITH  
\* 10000 0 0 0 AND 256 OF 0, 10000 1 0 0, AND 2 OF 1  
\* THE WRITE COMMAND IS THEN LOADED INTO THE REGISTERS EXCEPT  
\* THE GO BIT, AND ALL THE REGISTERS ARE SAVED  
\* THEN GO IS GIVEN FOR WRITE HEADER AND DATA

\* THEN ALL REGISTERS ARE COMPARED TO CHECK FOR IMPROPER CHANGE  
\* THEN WRITE FROM BUFFER IS CHECKED TO SEE THAT NOTHING CHANGED

\* NOW FOR THE READ COMMAND READ INTO BUFFER IS FILLED  
\* WITH 377, WRITE FROM BUFFER IS FILLED WITH 10000 1 0 0 1 1  
\* AND 254 OF ZEROS COMMAND IS LOADED INTO REGISTERS EXCEPT  
\* GO AND IE THEN ALL REGISTERS ARE SAVED  
\* GO IS GIVEN FOR THE READ COMMAND, 256 WORDS

\* ALL REGISTERS ARE CHECKED FOR IMPROPER CHANGE  
\* THEN THE READ DATA IS COMPARED

\*\*\*\*\*

\*ST41: SCOPE  
MOV #STACK, SP ;RESET STACK  
MOV #41, #TSTNM ;SAVE TEST NUMBER  
  
JSR PC, #CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETUP UNIT NUMBER  
  
:\*FILL WRITE FROM BUFFER WITH HEADER  
  
JSR R0, #FLHEAD ;SAVE HEADER DATA IN WRFROM  
WRFROM ;LOCATION WHERE SAVED  
+ ;NUMBER OF WORDS SAVED  
10000 ;FIRST DATA WORD  
0 ;SECOND DATA WORD  
0 ;THIRD DATA WORD  
0 ;FOURTH DATA WORD  
  
:\*FILL WRITE FROM BUFFER WITH DATA  
JSR R0, #CLAREA ;CLEAR 256. WORDS. FROM WRFROM+10  
WRFROM+10 ;STARTING FROM WRFROM+10  
256. ;256. WORDS

030222 000004  
030224 012706 001000  
030230 012737 000041 0046C4  
  
030236 004737 041366  
  
030242 004937 041212  
030246 002470  
030250 000004  
030252 010000  
030254 000000  
030256 000000  
030260 000000  
  
030262 004037 041236  
030266 002500  
030270 000400

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7095 030272 000000 0 ;FILL WITH 0
7096
7097
7098 ;*FILL WRITE FROM BUFFER WITH NEXT SECTOR HEADER
7099
7100 030274 004037 041212 JSR RO,0#FLHEAD ;SAVE HEADER DATA IN WRFROM+(260.*2)
7101 030300 003500 WRFROM+(260.*2) ;LOCATION WHERE SAVED
7102 030302 000004 4 ;NUMBER OF WORDS SAVED
7103 030304 010000 10000 ;FIRST DATA WORD
7104 030306 000001 1 ;SECOND DATA WORD
7105 030310 000000 0 ;THIRD DATA WORD
7106 030312 000000 0 ;FOURTH DATA WORD
7107
7108 ;*FILL WRITE FROM BUFFER WITH NEXT SECTOR DATA
7109 030314 004037 041236 JSR RO,0#CLAREA ;CLEAR 2 WORDS FROM WRFROM+(264.*2)
7110 030320 003510 WRFROM+(264.*2) ;STARTING FROM WRFROM+(264.*2)
7111 030322 000002 2 ;2 WORDS
7112 030324 000001 1 ;FILL WITH 1
7113
7114
7115 ;*NOW READ INTO BUFFER WILL BE FILLED WITH SAME DATA
7116 ;*AS WRITE FROM BUFFER SO THAT AFTER A WRITE COMPARISONS
7117 ;*CAN BE MADE TO MAKE SURE THAT WRITE DID NOT
7118 ;*CHANGE WRITE FROM BUFFER.
7119
7120
7121 030326 004037 041212 JSR RO,0#FLHEAD ;SAVE HEADER DATA IN REINTC
7122 030332 003534 REINTC ;LOCATION WHERE SAVED
7123 030334 000004 4 ;NUMBER OF WORDS SAVED
7124 030336 010000 10000 ;FIRST DATA WORD
7125 030340 000000 0 ;SECOND DATA WORD
7126 030342 000000 0 ;THIRD DATA WORD
7127 030344 000000 0 ;FOURTH DATA WORD
7128 030346 004037 041236 JSR RO,0#CLAREA ;CLEAR 256 WORDS FROM REINTC+10
7129 030352 003544 REINTC+10 ;STARTING FROM REINTC+10
7130 030354 000400 256 ;256 WORDS
7131 030356 000000 0 ;FILL WITH 0
7132
7133
7134 030360 004037 041212 JSR RO,0#FLHEAD ;SAVE HEADER DATA IN REINTC+(260.*2)
7135 030364 004544 REINTC+(260.*2) ;LOCATION WHERE SAVED
7136 030366 000004 4 ;NUMBER OF WORDS SAVED
7137 030370 010000 10000 ;FIRST DATA WORD
7138 030372 000001 1 ;SECOND DATA WORD
7139 030374 000000 0 ;THIRD DATA WORD
7140 030376 000000 0 ;FOURTH DATA WORD
7141 030400 004037 041236 JSR RO,0#CLAREA ;CLEAR 2 WORDS FROM REINTC+(264.*2)
7142 030404 004554 REINTC+(264.*2) ;STARTING FROM REINTC+(264.*2)
7143 030406 000002 2 ;2 WORDS
7144 030410 000001 1 ;FILL WITH 1
7145
7146
7147 ;*NOW THE WRITE HEADER AND DATA COMMAND WILL BE FILLED
7148

```

# H01

7149									
7150	030412	004037	343340		JSR	RD, @#RUN		: SETUP TO RUN FOR DATA COMMAND	
7151	030416	000000			0			: CYLINDER 0	
7152	030420	000		.BYTE	0			: SECTOR 0	
7153	030421	000		.BYTE	0			: TRACK 0	
7154	030422	177366			-262.-4			: WORD COUNT (DATA) = 262. +	
7155								: 4 HEADER WORDS	
7156	030424	002470			WRFROM			: BUS ADDRESS	
7157								: STARTING ADDRESS OF DATA	
7158								: BUFFER = WRFROM	
7159	030426	000000			0			: DO NOT INHIBIT BUS ADDRESS INCREMENT	
7160	030430	010000			FMT22			: 16 BITS PER WORD FORMAT	
7161								: DO NOT INHIBIT ECC CORRECTION	
7162								: DO NOT INHIBIT HEADER COMPARE	
7163	030432	002444			WRIFOR			: GET READY TO DO A WRIFOR	
7164								: WRITE HEADER AND DATA WITH 62 IN RHCS1	
7165									
7166									
7167								: *NOW SAVE REGISTERS FOR COMPARISON AFTER WRITE HEADER AND DATA	
7168	030434	004037	041534		JSR	RD, @#SAVER		: SAVE REGISTERS	
7169	030440	002272			RHWC			: RHWC IS THE FIRST REGISTER SAVED	
7170	030442	004612			SAVERE			: STARTING ADDRESS OF WHERE	
7171								: THE REGISTERS ARE SAVED	
7172	030444	000022			18.			: NUMBER OF REGISTERS	
7173								: SAVED = 18.	
7174									
7175	030446	004737	041446		JSR	PC, @#CHECKT		: CHECK DVA, RDY, MOL, DPR, DRY, VV = 1	
7176								: AND THAT NO STATUS BITS ARE STUCK = 1	
7177	030452	104400	066402		TYPE	.CPHALT		: CANNOT CONTINUE TESTING IF ANY OF	
7178								: THE FIRST SET OF BITS DON'T = 1	
7179	030456	000000			HALT			: STOP	
7180									
7181	030460	013777	004606 151600		MOV	@#RP4VEC, @RPVEC		: SET RPO4 VECTOR ADDRESS	
7182								: TO 'TIME1' IF P-CLOCK IS PRESENT	
7183								: OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT	
7184								: 'TIME' WILL ONLY SAVE	
7185								: CURRENT CYLINDER ADDRESS	
7186								: AND LOOK AHEAD REGISTERS	
7187									
7188									
7189									
7190	030466	013746	002444		MOV	@#WRIFOR, -(SP)		: GET READY TO MOVE COMMAND	
7191	030472	052716	000101		BIS	#GO! IE, (SP)		: GET READY TO SET 'GO' AND	
7192								: ENABLE INTERRUPT	
7193	030476	012677	151576		MOV	(SP)+, @RHCS1		: GO WITH	
7194								: 62 IN RHCS1 FOR WRITE HEADER AND DATA	
7195	030502	011100			MOV	@R1, RD		: WITH INTERRUPT ENABLED	
7196	030504	011305			MOV	@R3, R5		: SAVE RHCS1 DURING ABOVE OPERATION	
7197								: SAVE RHCS1 DURING ABOVE OPERATION	
7198									
7199									
7200								: *ONE REVOLUTION = 16670 MICRO SEC. ONE SECTOR = 760 MICRO SEC	
7201	030506	104412			WAT			: WAIT FOR RDY BIT TO SET	
7202	030510	002300			RHCS1			: WAIT FOR RHCS1 REGISTER	

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7203 030512 000200          RDY          ;WAIT FOR RDY BIT IN RHCS1 REGISTER
7204 030514 002001          1025.        ;ALLOW 10250 MICRO SECONDS
7205 030516 001553          8.5.         ;RDY MUST SET BETWEEN
7206                                     ;1500 AND 19000 MICRO SECONDS
7207
7208                                     ;*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN
7209                                     ;*R0 AND R5 IMMEDIATELY AFTER GO
7210
7211 030520 013746 002444     MOV      Q#WRIFOR, -(SP) ;SAVE COMMAND
7212 030524 052716 004101     BIS      #IE!GO!DVA, (SP) ;INCLUDE IE!GO!DVA
7213 030530 011637 001124     MOV      (SP), Q#$GDDAT ;SAVE FOR PRINTOUT
7214 030534 022600          CMP      (SP)+, R0      ;DURING ABOVE OPERATION ONLY IE!GO!DVA
7215                                     ;AND COMMAND SHOULD BE SET
7216 030536 001405          BQA      64$          ;BRANCH IF GOOD
7217 030540 010037 001126     MOV      R0, Q#$BDDAT ;BAD DATA
7218 030544 010137 004600     MOV      R1, Q#REGADR ;FAILING REGISTER RHCS1
7219 030550 104021          ERRC.    21          ;DURING ABOVE OPERATION ONLY
7220                                     ;COMMAND AND IE!GO!DVA SHOULD BE SET
7221 030552 012746 010500     54$: MOV      #MOL!DPR!VV, -(SP) ;SAVE BITS SET DURING OPERATION IN RHDS:
7222 030556 011637 001124     MOV      (SP), Q#$GDDAT ;SAVE FOR PRINTOUT
7223 030562 022605          CMP      (SP)+, R5      ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
7224                                     ;SHOULD BE SET
7225 030564 001405          BEQ      66$          ;BRANCH IF GOOD
7226 030566 010537 001126     MOV      R5, Q#$BDDAT ;BAD DATA
7227 030572 010337 004600     MOV      R3, Q#REGADR ;FAILING REGISTER RHDS1
7228 030576 104063          ERROR    63          ;DURING ABOVE OPERATION ONLY
7229                                     ;MOL!DPR!VV SHOULD BE SET
7230 030600          66$:
7231
7232                                     ;*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES
7233
7234 030600 004037 041270     JSR      R0, Q#FILLRE ;MOV 0 INTO SAVED RHWC
7235 030604 002272          RHWC          ;SAVED REGISTER TO CHANGE
7236 030606 000000          0            ;DATA
7237 030610 004037 041270     JSR      R0, Q#FILLRE ;MOV WRFROM+<266.*2> INTO SAVED RHBA
7238 030614 002274          RHBA          ;SAVED REGISTER TO CHANGE
7239 030616 003514          WRFROM+<266.*2> ;DATA
7240 030620 004037 041270     JSR      R0, Q#FILLRE ;MOV 2 INTO SAVED RHDST
7241 030624 002304          RHDST          ;SAVED REGISTER TO CHANGE
7242 030626 000002          2            ;DATA
7243
7244                                     ;*NOW COMPARE REGISTERS BEFORE WRITE HEADER AND DATA
7245                                     ;*WITH REGISTERS AFTER COMMAND
7246
7247
7248 030630 004037 042354     JSR      R0, Q#COMREG ;COMPARE SAVED REGISTERS WITH
7249                                     ;PRESENT VALUE
7250 030634 004612          SAVERE        ;GOOD DATA SAVED IN 'SAVERE'
7251 030636 002354          WC           ;TEST DATA STARTING FROM 'RHWC'
7252 030640 000022          18.          ;18. REGISTERS TO BE COMPARED
7253 030642 030646          1$          ;RETURN TO 1$ ON ERROR
7254 030644 030652          2$          ;RETURN TO 2$ ON NO ERROR
7255
7256 030646 104027          1$: ERROR    27          ;WRITE HEADER AND DATA

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7257 030650 000207 R/S PC ;CAUSED IMPROPER REGISTER
7258 ;CHANGE
7259 ;GOOD DATA GIVES WHAT SHOULD
7250 ;BE THERE
7261 ;RECEIVED DATA GIVES WHAT
7262 ;WAS THERE AFTER COMMANT
7263
7264 ;*NOW WRITE FROM BUFFER WILL BE CHECKED TO SEE THAT
7265 ;*NOTHING GOT CHANGED
7266
7267 030652 2$:
7268
7269 030652 004037 043404 JSR RO,2#COMPAR ;COMPARE TWO BLOCKS OF MEMORY
7270 030656 003534 REINTO ;GOOD DATA STARTS FROM REINTO
7271 030660 002470 WRFROM ;TEST DATA STARTS FROM WRFROM
7272 030662 000412 266. ;266. WORDS TO BE COMPARED
7273 030664 030670 3$ ;RETURN TO 3$ ON ERROR
7274 030666 030674 4$ ;RETURN TO 4$ ON NO ERROR
7275
7276
7277 030670 104030 3$: ERROR 30 ;WRITE HEADER AND DATA
7278 030672 000207 RTS PC ;CHANGED WRITE FROM BUFFER
7279
7280 ;*NOW A READ HEADER AND DATA COMMAND WILL BE GIVEN
7281 ;*FOR SECTOR 1, 256 WORDS
7282 ;*READ INTO BUFFER IS FILLED WITH ONES
7283 030674 4$:
7284
7285 030674 004737 041366 JSR PC,2#CLDISK ;SET R1-RHCS1, R2-RHCS2
7286 ;R3-RHDS1, R4-RHER1
7287 ;GIVE RH-!1 INITIALIZE
7288 ;SETUP UNIT NUMBER
7289 030700 004037 041236 JSR RO,2#CLAREA ;CLEAR 260. WORDS, FROM REINTO
7290 030704 003534 REINTO ;STARTING FROM REINTO
7291 030706 000404 260. ;260. WORDS
7292 030710 000377 377 ;FILL WITH 377
7293
7294
7295 ;*WRITE FROM BUFFER IS FILLED WITH EXPECTED DATA
7296
7297
7298 030712 004037 041212 JSR RO,2#FLHEAD ;SAVE HEADER DATA IN WRFROM
7299 030716 002470 WRFROM ;LOCATION WHERE SAVED
7300 030720 000006 6 ;NUMBER OF WORDS SAVED
7301 030722 010000 10000 ;FIRST DATA WORD
7302 030724 000001 1 ;SECOND DATA WORD
7303 030726 000000 0 ;THIRD DATA WORD
7304 030730 000000 0 ;FOURTH DATA WORD
7305 030732 000001 1 ;FIFTH DATA WORD
7306 030734 000001 1 ;SIXTH DATA WORD
7307 030736 004037 041236 JSR RO,2#CLAREA ;CLEAR 254 WORDS, FROM WRFROM+(6.*2)
7308 030742 002504 WRFROM+(6.*2) ;STARTING FROM WRFROM+(6.*2)
7309 030744 000254 254 ;254 WORDS
7310 030746 000000 0 ;FILL WITH 0
    
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7311
7312
7313
7314
7315
7316 030750 004037 043340 JSR RO, @#RUN ; SETUP TO RUN FOR DATA COMMAND
7317 030754 000000 0 ; CYLINDER 0
7318 030756 001 .BYTE 1 ; SECTOR 1
7319 030757 000 .BYTE 0 ; TRACK 0
7320 030760 177374 -256.-4 ; WORD COUNT (DATA) = 256. +
7321 ; 4 HEADER WORDS
7322 030762 003534 REINTO ; BUS ADDRESS
7323 ; STARTING ADDRESS OF DATA
7324 ; BUFFER = REINTO
7325 030764 000000 0 ; DO NOT INHIBIT BUS ADDRESS INCREMENT
7326 030766 014000 ECI!FMT22 ; 16 BITS PER WORD FORMAT
7327 ; INHIBIT ECC CORRECTION
7328 ; DO NOT INHIBIT HEADER COMPARE
7329 030770 002450 REFOR ; GET READY TO DO A REFOR
7330 ; READ HEADER AND DATA WITH 72 IN RHCS1
7331
7332
7333 ; *NOW SAVE REGISTERS FOR COMPARISON AFTER READ HEADER AND DATA
7334
7335 030772 004037 041534 JSR RO, @#SAVER ; SAVE REGISTERS
7336 030776 002272 RHWC ; RHWC IS THE FIRST REGISTER SAVED
7337 031000 004612 SAVERE ; STARTING ADDRESS OF WHERE
7338 ; THE REGISTERS ARE SAVED
7339 031002 000022 18. ; NUMBER OF REGISTERS
7340 ; SAVED = 18.
7341
7342 031004 004737 041446 JSR PC, @#CHECKT ; CHECK DVA, RDY, MOL, DPR, DRY, VV = 1
7343 ; AND THAT NO STATUS BITS ARE STUCK = !
7344 031010 104400 066402 TYPE .CPHALT ; CANNOT CONTINUE TESTING IF ANY OF
7345 ; THE FIRST SET OF BITS DON'T = 1
7346 031014 000000 HALT ; STOP
7347
7348 031016 013777 004606 151242 MOV @#RP4VEC, @RPVEC ; SET RPO4 VECTOR ADDRESS
7349 ; TO 'TIME1' IF P-CLOCK IS PRESENT
7350 ; OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
7351 ; 'TIME' WILL ONLY SAVE
7352 ; CURRENT CYLINDER ADDRESS
7353 ; AND LOOK AHEAD REGISTERS
7354
7355
7356 031024 013746 002450 MOV @#REFOR, -(SP) ; GET READY TO MOVE COMMAND
7357 031030 052716 000101 BIS #GO!IE, (SP) ; GET READY TO SET 'GO' AND
7358 ; ENABLE INTERRUPT
7359 031034 012677 151240 MOV (SP)+, @RHCS1 ; GO WITH
7360 ; 72 IN RHCS1 FOR READ DATA
7361 ; WITH INTERRUPT ENABLED
7362 031040 011100 MOV @R1, RO ; SAVE RHCS1 DURING ABOVE OPERATION
7363 031042 011305 MOV @R3, R5 ; SAVE RHDS1 DURING ABOVE OPERATION
7364

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7365                                     ;*ONE REVOLUTION = 16670 MICRO SEC, ONE SECTOR 760 MICRO SECONDS
7366
7367
7368 031044 104412                       WAT                               ;WAIT FOR RDY BIT TO SET
7369 031046 002300                       RHCS1                             ;WAIT FOR RHCS1 REGISTER
7370 031050 000200                       RDY                               ;WAIT FOR RDY BIT IN RHCS1 REGISTER
7371 031052 001614                       908.                             ;ALLOW 9080 MICRO SECONDS
7372 031054 001507                       839.                             ;RDY MUST SET BETWEEN
7373                                     ;690 AND 17470 MICRO SECONDS
7374
7375                                     ;*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN
7376                                     ;*R0 AND R5 IMMEDIATELY AFTER GO
7377
7378 031056 013746 002450                 MOV    Q#REFOR, -(SP)             ;SAVE COMMAND
7379 031062 052716 004101                 BIS    #IE!GO!DVA, (SP)         ;INCLUDE IE!GO!DVA
7380 031066 011637 001124                 MOV    (SP), Q#$GDDAT           ;SAVE FOR PRINTOUT
7381 031072 022600                       CMP    (SP)+, R0                ;DURING ABOVE OPERATION ONLY IE!GO!DVA
7382                                     ;AND COMMAND SHOULD BE SET
7383 031074 001405                       BEQ    67$                      ;BRANCH IF GOOD
7384 031076 010037 001126                 MOV    R0, Q#$BDDAT            ;BAD DATA
7385 031102 010137 004600                 MOV    R1, Q#REGADR           ;FAILING REGISTER RHCS1
7386 031106 104021                       ERROR  21                      ;DURING ABOVE OPERATION ONLY
7387                                     ;COMMAND AND IE!GO!DVA SHOULD BE SET
7388 031110 012746 010500                 67$: MOV    #MOL!DPR!VV, -(SP)   ;SAVE BITS SET DURING OPERATION IN RHDS1
7389 031114 011637 001124                 MOV    (SP), Q#$GDDAT         ;SAVE FOR PRINTOUT
7390 031120 022605                       CMP    (SP)+, R5              ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
7391                                     ;SHOULD BE SET
7392 031122 001405                       BEQ    69$                      ;BRANCH IF GOOD
7393 031124 010537 001126                 MOV    R5, Q#$BDDAT            ;BAD DATA
7394 031130 010337 004600                 MOV    R3, Q#REGADR           ;FAILING REGISTER RHDS1
7395 031134 104063                       ERROR  63                      ;DURING ABOVE OPERATION ONLY
7396                                     ;MOL!DPR!VV SHOULD BE SET
7397                                     69$:
7398
7399                                     ;*CHANGE SAVED REGISTERS TO EXPECTED VALUES
7400
7401 031136 004037 041270                 JSR    R0, Q#FILLRE           ;MOV 0 INTO SAVED RHWC
7402 031142 002272                       RHWC                             ;SAVED REGISTER TO CHANGE
7403 031144 000000                       0                                ;DATA
7404 031146 004037 041270                 JSR    R0, Q#FILLRE           ;MOV REINT0+(260.*2) INTO SAVED RHBA
7405 031152 002274                       RHBA                             ;SAVED REGISTER TO CHANGE
7406 031154 004544                       REINT0+(260.*2)                 ;DATA
7407 031156 004037 041270                 JSR    R0, Q#FILLRE           ;MOV 2 INTO SAVED RHDST
7408 031162 002304                       RHDST                           ;SAVED REGISTER TO CHANGE
7409 031164 000002                       2                                ;DATA
7410
7411                                     ;*COMPARE REGISTERS BEFORE READ HEADER AND DATA
7412                                     ;*WITH REGISTERS AFTER COMMAND
7413
7414
7415 031166 004037 042354                 JSR    R0, Q#COMREG           ;COMPARE SAVED REGISTERS WITH
7416                                     ;PRESENT VALUE
7417 031172 004612                       SAVERE                           ;GOOD DATA SAVED IN 'SAVERE'
7418 031174 002354                       WC                               ;TEST DATA STARTING FROM 'RHWC'

```



7419	031176	000022		18.						
7420	031200	031204		5\$						;18. REGISTERS TO BE COMPARED
7421	031202	031210		6\$						;RETURN TO 5\$ ON ERROR
7422										;RETURN TO 6\$ ON NO ERROR
7423										
7424	031204	104031		5\$:	ERROR	31				;READ HEADER AND DATA CAUSED
7425	031206	000207			RTS	PC				;IMPROPER REGISTER CHANGE
7426										;GOOD DATA GIVES WHAT SHOULD
7427										;BE THERE
7428										;RECEIVED DATA GIVES WHAT WAS
7429										;THERE AFTER COMMAND
7430										
7431										;*NOW READ INTO BUFFER WILL BE CHECKED TO SEE
7432										;*THAT READ WAS GOOD
7433										
7434	031210			6\$:						
7435										
7436	031210	004037	043404		JSP	RD, 2#COMPAR				;COMPARE TWO BLOCKS OF MEMORY
7437	031214	002470			WRFROM					;GOOD DATA STARTS FROM WRFROM
7438	031216	003534			REINTO					;TEST DATA STARTS FROM REINTO
7439	031220	000404			260.					;260. WORDS TO BE COMPARED
7440	031222	031226			7\$					;RETURN TO 7\$ ON ERROR
7441	031224	031232			10\$					;RETURN TO 10\$ ON NO ERROR
7442										
7443										
7444										
7445	031226	104032		7\$:	ERROR	32				;WRITE HEADER AND DATA
7446	031230	000207			RTS	PC				;FOLLOWED BY A READ HEADER
7447										;AND DATA GAVE A READ ERROR
7448										;ERROR MAY BE IN READ OR WRITE
7449	031232			10\$:						

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\*\*\*\*\*  
\*TEST 42 IMPLIED SEEK TO CYL 001

.SBTTL SEEK TESTS

\* ONLY NEW ADDITION IN THIS TEST IS AN IMPLIED SEEK FROM CYLINDER 0  
\* TO CYLINDER 1, A WRITE HEADER AND DATA IS GIVEN FOR MORE THAN  
\* ONE SECTOR OF WORDS

\* WRITE HEADER AND DATA CYLINDER 0, FORMAT 16 BITS PER WORD  
\* TRACK 18, SECTOR 21, KEYS=0, NUMBER OF WORDS 266 WORDS  
\* (4 HEADER, 256 DATA=125, 4 HEADER 2 DATA = 2000  
\* THEN READ HEADER AND DATA FOR ABOVE, TRACK 0, SECTOR 0, CYL=1  
\* WRITE FROM BUFFER AND READ INTO BUFFER ARE FILLED WITH  
\* 10000 0 0 0 AND 256 OF 125, 10001 0 0 0, AND 2 OF 2000  
\* THE WRITE COMMAND IS THEN LOADED INTO THE REGISTERS EXCEPT  
\* THE GO BIT, AND ALL THE REGISTERS ARE SAVED  
\* THEN GO IS GIVEN FOR WRITE HEADER AND DATA

\* THEN ALL REGISTERS ARE COMPARED TO CHECK FOR IMPROPER CHANGE  
\* THEN WRITE FROM BUFFER IS CHECKED TO SEE THAT NOTHING CHANGED

\* NOW FOR READ COMMAND READ INTO BUFFER IS FILLED  
\* WITH ALL ONES, WRITE FROM BUFFER IS LOADED WITH  
\* 10001 0 0 0 AND 2 OF 2000, 254 OF ZEROS COMMAND IS LOADED  
\* INTO REGISTERS EXCEPT GO AND IE ALL REGISTERS ARE SAVED  
\* GO IS GIVEN FOR THE READ COMMAND

\* ALL REGISTERS ARE CHECKED FOR IMPROPER CHANGE  
\* THEN THE READ DATA IS COMPARED.

\*\*\*\*\*

ST42: SCOPE  
MOV #STACK, SP ;RESET STACK  
MOV #42, @#TSTNM ;SAVE TEST NUMBER  
JSR PC, @#CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETUP UNIT NUMBER  
;\*FILL WRITE FROM BUFFER WITH HEADER  
JSR R0, @#FLHEAD ;SAVE HEADER DATA IN WRFROM  
WRFROM ;LOCATION WHERE SAVED  
4 ;NUMBER OF WORDS SAVED  
10000 ;FIRST DATA WORD  
<18.\*400!<21.> ;SECOND DATA WORD  
0 ;THIRD DATA WORD  
0 ;FOURTH DATA WORD

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031303 004037 041236  
031304 003500  
031305 004037  
031306 001125  
  
031304 004037 041212  
031310 003500  
031312 000004  
031314 010001  
031316 000000  
031320 000000  
031322 000000  
  
031324 004037 041236  
031330 003510  
031332 000002  
031334 002000  
  
031336 004037 041212  
031342 003534  
031344 000004  
031346 010000  
031350 011025  
031352 000000  
031354 000000  
031356 004037 041236  
031362 003544  
031364 000400  
031366 001125  
  
031370 004037 041212  
031374 004544  
031376 000004  
031400 010001  
031402 000000  
031404 000000  
031406 000000  
031410 004037 041236  
031414 004554  
031416 000002  
031420 002000

: \*FILL WRITE FROM BUFFER WITH DATA  
JSR R0,0#CLAREA ;CLEAR 256. WORDS, FROM WRFROM+10  
WRFROM+10 ;STARTING FROM WRFROM+10  
256. ;256. WORDS  
<18.\*40>!21. ;FILL WITH <18.\*40>!21.  
  
: \*FILL WRITE FROM BUFFER WITH NEXT TRACK HEADER  
JSR R0,0#FLHEAD ;SAVE HEADER DATA IN WRFROM+<260.\*2>  
WRFROM+<260.\*2> ;LOCATION WHERE SAVED  
4 ;NUMBER OF WORDS SAVED  
10001 ;FIRST DATA WORD  
0 ;SECOND DATA WORD  
0 ;THIRD DATA WORD  
0 ;FOURTH DATA WORD  
  
: \*FILL WRITE FROM BUFFER WITH NEXT TRACK DATA  
JSR R0,0#CLAREA ;CLEAR 2 WORDS, FROM WRFROM+<264.\*2>  
WRFROM+<264.\*2> ;STARTING FROM WRFROM+<264.\*2>  
2 ;2 WORDS  
2000 ;FILL WITH 2000  
  
: \*NOW READ INTO BUFFER WILL BE FILLED WITH SAME DATA  
: \*AS WRITE FROM BUFFER SO THAT AFTER A WRITE COMPARISONS  
: \*CAN BE MADE TO MAKE SURE THAT WRITE DID NOT  
: \*CHANGE WRITE FROM BUFFER.  
  
JSR R0,0#FLHEAD ;SAVE HEADER DATA IN REINT0  
REINT0 ;LOCATION WHERE SAVED  
4 ;NUMBER OF WORDS SAVED  
10000 ;FIRST DATA WORD  
<18.\*400 !>21. ;SECOND DATA WORD  
0 ;THIRD DATA WORD  
0 ;FOURTH DATA WORD  
JSR R0,0#CLAREA ;CLEAR 256. WORDS, FROM REINT0+10  
REINT0+10 ;STARTING FROM REINT0+10  
256. ;256. WORDS  
<18.\*40>!21. ;FILL WITH <18.\*40>!21.  
  
JSR R0,0#FLHEAD ;SAVE HEADER DATA IN REINT0+<260.\*2>  
REINT0+<260.\*2> ;LOCATION WHERE SAVED  
4 ;NUMBER OF WORDS SAVED  
10001 ;FIRST DATA WORD  
0 ;SECOND DATA WORD  
0 ;THIRD DATA WORD  
0 ;FOURTH DATA WORD  
JSR R0,0#CLAREA ;CLEAR 2 WORDS, FROM REINT0+<264.\*2>  
REINT0+<264.\*2> ;STARTING FROM REINT0+<264.\*2>  
2 ;2 WORDS  
2000 ;FILL WITH 2000



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7515 031516 104412
7516 031520 002300
7517 031522 000200
7518 031524 003237
7519 031526 001515
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7525 031530 013746 002444
7526 031534 052716 004101
7527 031540 011637 001124
7528 031544 022600
7529
7530 031546 001405
7531 031550 010037 001126
7532 031554 010137 004600
7533 031560 104021
7534
7535 031562 012746 010500 643:
7536 031566 011637 001124
7537 031572 022605
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7539 031574 001405
7540 031576 010537 001126
7541 031602 010337 004600
7542 031606 104063
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7544 031610 665:
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7548 031610 004037 041270
7549 031614 002272
7550 031616 000000
7551 031620 004037 041270
7552 031624 002274
7553 031626 003514
7554 031630 004037 041270
7555 031634 002312
7556 031636 000001
7557 031640 004037 041270
7558 031644 002334
7559 031646 000001
7560 031650 004037 041270
7561 031654 002304
7562 031656 000001
7563
7564
7565

```

: \*ONE REVOLUTION = 16670 MICRO1 SEC, ONE SECTOR = 760 MICRO1 SEC

```

WAT          :WAIT FOR RDY BIT TO SET
RHCSI1      :WAIT FOR RHCSI REGISTER
RDY         :WAIT FOR RDY BIT IN RHCSI REGISTER
1695.       :ALLOW 16950 MICRO SECONDS
845.        :RDY MUST SET BETWEEN
            :8500 AND 25400 MICRO SECONDS

```

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: *COMPARE CONTENTS OF RHCSI AND RHDS1 ALREADY SAVED IN
: *RO AND RS IMMEDIATELY AFTER GO

```

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MOV 2#WRIFOR, -(SP) :SAVE COMMAND
BIS 2#IE!GO!DVA, (SP) :INCLUDE IE!GO!DVA
MOV (SP), 2#SGDDAT :SAVE FOR PRINTOUT
CMP (SP)+, RO :DURING ABOVE OPERATION ONLY IE!GO!DVA
                :AND COMMAND SHOULD BE SET
BEQ 645 :BRANCH IF GOOD
MOV RO, 2#SBDDAT :BAD DATA
MOV R1, 2#REGADR :FAILING REGISTER RHCSI
ERROR 21 :DURING ABOVE OPERATION ONLY
                :COMMAND AND IE!GO!DVA SHOULD BE SET
MOV 2#MOL!DPR!VV, -(SP) :SAVE BITS SET DURING OPERATION IN RHCSI
MOV (SP), 2#SGDDAT :SAVE FOR PRINTOUT
CMP (SP)+, RS :DURING ABOVE OPERATION ONLY MOL!DPR!VV
                :SHOULD BE SET
BEQ 665 :BRANCH IF GOOD
MOV RS, 2#SBDDAT :BAD DATA
MOV R3, 2#REGADR :FAILING REGISTER RHDS1
ERROR 63 :DURING ABOVE OPERATION ONLY
                :MOL!DPR!VV SHOULD BE SET

```

: \*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES

```

JSR RO, 2#FILLRE :MOV 3 INTO SAVED RHWC
RHWC :SAVED REGISTER TO CHANGE
0 :DATA
JSR RO, 2#FILLRE :MOV WRFROM+(266.*2) INTO SAVED RHBA
RHBA :SAVED REGISTER TO CHANGE
WRFROM+(266.*2) :DATA
JSR RO, 2#FILLRE :MOV 1 INTO SAVED RHCA
RHCA :SAVED REGISTER TO CHANGE
1 :DATA
JSR RO, 2#FILLRE :MOV 1 INTO SAVED RHCC
RHCC :SAVED REGISTER TO CHANGE
1 :DATA
JSR RO, 2#FILLRE :MOV 1 INTO SAVED RHDST
RHDST :SAVED REGISTER TO CHANGE
1 :DATA

```

```

: *NOW COMARE REGISTERS BEFORE WRITE HEADER AND DATA
: *WITH REGISTERS AFTER COMMAND

```

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7666
7667
7668 031660 004037 042354 JSR RO.3#COM'REG ;COMPARE SAVED REGISTERS WITH
7669 ;PRESENT VALUE
7670 031664 004612 SAVERE ;GOOD DATA SAVED IN 'SAVERE'
7671 031666 002354 WC ;TEST DATA STARTING FROM 'F4WC'
7672 031670 000022 18. ;18. REGISTERS TO BE COMPARED
7673 031672 031676 19. ;RETURN TO 19 ON ERROR
7674 031674 031702 23 ;RETURN TO 23 ON NO ERROR
7675
7676 031676 104037 19: ERROR 27 ;WRITE HEADER AND DATA
7677 031700 000207 RTS PC ;CAUSED IMPROPER REGISTER
7678 ;CHANGE
7679 ;GOOD DATA GIVES WHAT SHOULD
7680 ;BE THERE
7681 ;RECEIVED DATA GIVES WHAT
7682 ;WAS THERE AFTER COMMANT
7683
7684 ;*NOW WRITE FROM BUFFER WILL BE CHECKED TO SEE THAT
7685 ;*NOTHING GOT CHANGED
7686
7687 031702 29:
7688
7689 031702 004037 043404 JSR RO.3#COMPAR ;COMPARE TWO BLOCKS OF MEMORY
7690 031706 003534 REINTO ;GOOD DATA STARTS FROM REINTO
7691 031710 002470 WRFROM ;TEST DATA STARTS FROM WRFROM
7692 031712 000412 266. ;266. WORDS TO BE COMPARED
7693 031714 031720 3$ ;RETURN TO 3$ ON ERROR
7694 031716 031724 4$ ;RETURN TO 4$ ON NO ERROR
7695
7696
7697 031720 104030 3$: ERROR 30 ;WRITE HEADER AND DATA
7698 031722 000207 RTS PC ;CHANGED WRITE FROM BUFFER
7699
7700 ;*NOW A READ HEADER AND DATA COMMAND WILL BE GIVEN
7701 ;*READ INTO BUFFER IS FILLED WITH ONES
7702
7703 031724 4$:
7704
7705 031724 004737 041356 JSR PC.2#CLDISK ;SET R1-RHCS1, R2-RHCS2
7706 ;R3-RHDS1, R4-RHER1
7707 ;GIVE RH-11 INITIALIZE
7708 ;SETUP UNIT NUMBER
7709 031730 004037 041236 JSR RO.3#CLAREA ;CLEAR 260. WORDS, FROM REINTO
7710 031734 003534 REINTO ;STARTING FROM REINTO
7711 031736 000404 260. ;260. WORDS
7712 031740 177777 -1 ;FILL WITH -1
7713
7714
7715 ;*WRITE FROM BUFFER IS FILLED WITH 10001,0,0,0,2000,2000, AND 354 OF 0
7716
7717 031742 004037 041212 JSR RO.3#FLHEAD ;SAVE HEADER DATA IN WRFROM
7718 031746 002470 WRFROM ;LOCATION WHERE SAVED
7719 031750 000006 6 ;NUMBER OF WORDS SAVED

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7720 031752 010001      10001      ;FIRST DATA WORD
7721 031754 000000      0          ;SECOND DATA WORD
7722 031756 000000      0          ;THIRD DATA WORD
7723 031760 000000      0          ;FOURTH DATA WORD
7724 031762 002000      2000       ;FIFTH DATA WORD
7725 031764 002000      2000       ;SIXTH DATA WORD
7726 031766 004037 041236 JSR      RD, @#CLAREA ;CLEAR 254. WORDS, FROM WRFROM+(6*2)
7727 031772 002504      WRFROM+(6*2) ;STARTING FROM WRFROM+(6*2)
7728 031774 000376      254.       ;254. WORDS
7729 031776 000000      0          ;FILL WITH 0
7730
7731
7732      ;*NOW FILL COMMAND
7733
7734 032000 004037 043340 JSR      RD, @#RUN    ;SETUP TO RUN FOR DATA COMMAND
7735 032004 000001      1          ;CYLINDER 1
7736 032006      000      .BYTE      0          ;SECTOR 0
7737 032007      000      .BYTE      0          ;TRACK 0
7738 032010 177374      -256.-4    ;WORD COUNT (DATA) = 256. +
7739      ;4 HEADER WORDS
7740 032012 003534      REINTC     ;BUS ADDRESS
7741      ;STARTING ADDRESS OF DATA
7742      ;BUFFER = REINTC
7743 032014 000000      0          ;DO NOT INHIBIT BUS ADDRESS INCREMENT
7744 032016 014000      ECI!FMT22 ;16 BITS PER WORD FORMAT
7745      ;INHIBIT ECC CORRECTION
7746      ;DO NOT INHIBIT HEADER COMPARE
7747 032020 002450      REFOR     ;GET READY TO DO A REFOR
7748      ;READ HEADER AND DATA WITH 72 IN RHCS!
7749
7750
7751      ;*NOW SAVE REGISTERS FOR COMPARISON AFTER READ HEADER AND DATA
7752
7753 032022 004037 041534 JSR      RD, @#SAVER ;SAVE REGISTERS
7754 032026 002272      RHWC     ;RHWC IS THE FIRST REGISTER SAVED
7755 032030 004612      SAVERE    ;STARTING ADDRESS OF WHERE
7756      ;THE REGISTERS ARE SAVED
7757 032032 000022      18.       ;NUMBER OF REGISTERS
7758      ;SAVED = 18.
7759
7760 032034 004737 041446 JSR      PC, @#CHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = ?
7761      ;AND THAT NO STATUS BITS ARE STUCK = ?
7762 032040 104400 066402 TYPE      ,CPHALT   ;CANNOT CONTINUE TESTING IF ANY OF
7763      ;THE FIRST SET OF BITS DON'T = ?
7764 032044 000000      HALT     ;STOP
7765
7766 032046 013777 004606 150212 MOV      @#RP4VEC, @RPVEC ;SET RPO4 VECTOR ADDRESS
7767      ;TO 'TIME1' IF P-CLOCK IS PRESENT
7768      ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
7769      ;'TIME' WILL ONLY SAVE
7770      ;CURRENT CYLINDER ADDRESS
7771      ;AND LOOK AHEAD REGISTERS
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7828
7829 032216 004037 042354 JSR RD,3#COMREG ;COMPARE SAVED REGISTERS WITH
7830 ;PRESENT VALUE
7831 032222 004512 SAVERE ;GOOD DATA SAVED IN 'SAVERE'
7832 032224 002354 WC ;TEST DATA STARTING FROM 'RHWC'
7833 032226 000022 18. ;18. REGISTERS TO BE COMPARED
7834 032230 032234 5$ ;RETURN TO 5$ ON ERROR
7835 032232 032240 6$ ;RETURN TO 6$ ON NO ERROR
7836
7837
7838 032234 104031 5$: ERROR 31 ;READ HEADER AND DATA CAUSED
7839 032236 000207 RTS PC ;IMPROPER REGISTER CHANGE
7840 ;GOOD DATA GIVES WHAT SHOULD
7841 ;BE THERE
7842 ;RECEIVED DATA GIVES WHAT WAS
7843 ;THERE AFTER COMMAND
7844
7845 ;*NOW READ INTO BUFFER WILL BE CHECKED TO SEE
7846 ;*THAT READ WAS GOOD
7847
7848
7849 032240 6$:
7850 032240 004037 043404 JSR RD,3#COMPAR ;COMPARE TWO BLOCKS OF MEMORY
7851 032244 002470 WRFROM ;GOOD DATA STARTS FROM WRFROM
7852 032246 003534 REINTO ;TEST DATA STARTS FROM REINTO
7853 032250 000404 260. ;260. WORDS TO BE COMPARED
7854 032252 032256 7$ ;RETURN TO 7$ ON ERROR
7855 032254 032262 10$ ;RETURN TO 10$ ON NO ERROR
7856
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7858
7859 032256 104032 7$: ERROR 32 ;WRITE HEADER AND DATA
7860 032260 000207 RTS PC ;FOLLOWED BY A READ HEADER
7861 ;AND DATA GAVE A READ ERROR
7862 ;ERROR MAY BE IN READ OR WRITE
7863 032262 10$:
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7984 032262 000004
7985 032264 012706 001000
7986 032270 012737 000043 004604
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7988 032276 004737 041366
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7998 032302 012737 010000 032460
7999 032310 012737 001125 032500
7900 032316 012737 010001 032512
7901 032324 012737 002000 032532
7902 032332 012737 000000 032540
7903 032340 012737 000001 032750
7904 032346 012737 000001 032760
7905 032354 012737 010001 033042
7906 032362 012737 002000 033062
7907 032370 012737 000001 033102
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7911 032376 013777 004606 147662
7912
7913
7914
7915
7916
7917
7918 032404 013746 002426

```

```

*****
*TEST 43      SEEK & WRT TEST (CYL = 0-10)

```

```

*      THIS TEST GETS THE HEADS OUT TO CYLINDER 10 NOT BY ONE
*      SEEK BUT BY TEN IMPLIED SEEKS ONE CYLINDER AT A TIME
*
*      THIS TEST STARTS WITH A (ALREADY TESTED) RECALIBRATE
*      THAT IS CYLINDER ZERO.  THEN ON CYLINDER 0 SECTOR
*      #21 TRACK #18 IT WRITES 266 WORDS THERE BY GETTING
*      THE HEAD TO CYLINDER 1 THEN IT WRITES 266 WORDS ON
*      CYLINDER 1 SECTOR #21 TRACK #18 THERE BY GETTING
*      THE HEADS TO CYLINDER 2
*      THIS IS REPEATED 10 TIMES GETTING THE
*      HEADS TO CYLINDER 10
*      THEN A SEEK COMMAND IS GIVEN TO CYLINDER 0
*      AND DATA ALREADY WRITTEN IS CHECKED

```

```

*****

```

```

†ST43:  SCOPE
        MOV      #STACK,SP      ;RESET STACK
        MOV      #43, @†STNM    ;SAVE TEST NUMBER
        JSR      PC, @#CLDISK   ;SET R1-RHCS1, R2-RHCS2
                                   ;R3-RHDS1, R4-RHER1
                                   ;GIVE RH-11 INITIALIZE
                                   ;SETUP UNIT NUMBER

```

```

*THE FOLLOWING MOVES ARE TO INITIALIZE TEST FROM
*CYLINDER 0
*THESE LOCATIONS ARE CHANGED DURING TEST TO ENABLE
*GOING TO NEXT CYLINDER

```

```

MOV      #10000, @#ST1+10
MOV      #<<18.#40>>#21., @#ST2+10
MOV      #10001, @#ST3+10
MOV      #2000, @#ST4+10
MOV      #0, @#ST5+4
MOV      #1, @#ST6+6
MOV      #1, @#ST6+16
MOV      #10001, @#ST9+10
MOV      #2000, @#ST10+10
MOV      #1, @#ST11+4

```

```

*THIS IS TO GET THE HEADS TO CYLINDER 0

```

```

MOV      @#RP4VEC, @RPVEC      ;SET RPO4 VECTOR ADDRESS
                                   ;TO 'TIME1' IF P-CLOCK IS PRESENT
                                   ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
                                   ;'TIME' WILL ONLY SAVE
                                   ;CURRENT CYLINDER ADDRESS
                                   ;AND LOOK AHEAD REGISTERS
MOV      @#RECALI., -(SP)     ;GET READY TO MOVE COMMAND

```

```

7919 032410 052716 000101      BIS      #GO!IE,(SP)      ;GET READY TO SET 'GO' AND
7920                                ;ENABLE INTERRUPT
7921 032414 012677 147660      MOV      (SP)+,DRHCS1  ;GO WITH
7922                                ;6 IN RHCS1 FOR RECALIBRATE
7923                                ;WITH INTERRUPT ENABLED
7924 032420 011100      MOV      DR1,RO        ;SAVE RHCS1 DURING ABOVE OPERATION
7925 032422 011305      MOV      DR3,R5        ;SAVE RHDS1 DURING ABOVE OPERATION
7926
7927 032424 104412      WAT                                ;WAIT FOR DRY BIT TO SET
7928 032426 002322      RHDS1                          ;WAIT FOR RHDS1 REGISTER
7929 032430 000200      DRY                              ;WAIT FOR DRY BIT IN RHDS1 REGISTER
7930 032432 076377      31999.                          ;ALLOW 319990 MICRO SECONDS
7931 032434 056701      24001.                          ;DRY MUST SET BETWEEN
7932                                ;79980 AND 560000 MICRO SECONDS
7933 032436 012737 000012 001200  MOV      #10.,2#STMP1  ;TEN COUNT TO GET TO CYLINDER 10
7934
7935 032444 004737 041366      JSR      PC,DRCLDISK  ;SET R1-RHCS1, R2-RHCS2
7936                                ;R3-RHDS1, R4-RHER1
7937                                ;GIVE RH-11 INITIALIZE
7938                                ;SETUP UNIT NUMBER
7939
7940                                ;*FILL WRITE FROM BUFFER WITH HEADER
7941 032450      ST1:
7942
7943 032450 004037 041212      JSR      RO,DRFLHEAD  ;SAVE HEADER DATA IN WRFROM
7944                                ;LOCATION WHERE SAVED
7945 032454 002470      WRFROM                          ;NUMBER OF WORDS SAVED
7946 032456 000004      4                                ;FIRST DATA WORD
7947 032460 010000      10000                          ;SECOND DATA WORD
7948 032462 011025      <18.*400>!21.                  ;THIRD DATA WORD
7949 032464 000000      0                                ;FOURTH DATA WORD
7950 032466 000000      0
7951                                ;*FILL WRITE FROM BUFFER WITH DATA
7952 032470      ST2:
7953 032470 004037 041236      JSR      RO,DRCLAREA  ;CLEAR 256. WORDS, FROM WRFROM+10
7954 032474 002500      WRFROM+10                      ;STARTING FROM WRFROM+10
7955 032476 000400      256.                          ;256. WORDS
7956 032500 001125      <0.*2000>!<18.*40>!21.        ;FILL WITH <0.*2000>!<18.*40>!21.
7957
7958                                ;*FILL WRITE FROM BUFFER WITH NEXT TRACK HEADER
7959 032502      ST3:
7960
7961 032502 004037 041212      JSR      RO,DRFLHEAD  ;SAVE HEADER DATA IN WRFROM+<260.*2>
7962 032506 003500      WRFROM+<260.*2>                ;LOCATION WHERE SAVED
7963 032510 000004      4                                ;NUMBER OF WORDS SAVED
7964 032512 010001      10001                          ;FIRST DATA WORD
7965 032514 000000      0                                ;SECOND DATA WORD
7966 032516 000000      0                                ;THIRD DATA WORD
7967 032520 000000      0                                ;FOURTH DATA WORD
7968
7969                                ;*FILL WRITE FROM BUFFER WITH NEXT TRACK DATA
7970 032522      ST4:
7971 032522 004037 041236      JSR      RO,DRCLAREA  ;CLEAR 2 WORDS, FROM WRFROM+<264.*2>
7972

```



```

8027 032624 011100      MOV    R1,R0      ;SAVE RHCS1 DURING ABOVE OPERATION
8028 032626 011305      MOV    R3,R5      ;SAVE RHDS1 DURING ABOVE OPERATION
8029
8030                      ;*ONE REVOLUTION = 16670 MICRO SECONDS, ONE SECTOR = 760 MICRO SEC.
8031                      ;*MAX TIME ALLOWED = ONE REVOLUTION + SEEK + 2 SECTORS
8032                      ;*MIN TIME ALLOWED = 2 SECTORS + SEEK
8033
8034
8035 032630 104412      WAT                    ;WAIT FOR RDY BIT TO SET
8036 032632 002300      RHCS1                ;WAIT FOR RHCS1 REGISTER
8037 032634 000200      RDY                  ;WAIT FOR RDY BIT IN RHCS1 REGISTER
8038 032636 003237      1695.                ;ALLOW 16950 MICRO SECONDS
8039 032640 001515      845.                  ;RDY MUST SET BETWEEN
                        ;8500 AND 25400 MICRO SECONDS
8040
8041
8042                      ;*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN
8043                      ;*R0 AND R5 IMMEDIATELY AFTER GO
8044
8045 032642 013746 002444  MOV    R2,WRIFOR,-(SP) ;SAVE COMMAND
8046 032646 052716 004101  BIS    IE!GO!DVA,(SP) ;INCLUDE IE!GO!DVA
8047 032652 011637 001124  MOV    (SP),R2$GDDAT  ;SAVE FOR PRINTOUT
8048 032656 022600      CMP    (SP)+,R0      ;DURING ABOVE OPERATION ONLY IE!GO!DVA
8049                      ;AND COMMAND SHOULD BE SET
8050 032660 001405      BEQ    64$           ;BRANCH IF GOOD
8051 032662 010037 001126  MOV    R0,R2$BDDAT    ;BAD DATA
8052 032666 010137 004600  MOV    R1,R2$REGADR   ;FAILING REGISTER RHCS1
8053 032672 104021      ERROR  21           ;DURING ABOVE OPERATION ONLY
8054                      ;COMMAND AND IE!GO!DVA SHOULD BE SET
8055 032674 012746 010500  64$:  MOV    #MOL!DPR!VV,-(SP) ;SAVE BITS SET DURING OPERATION IN RHDS1
8056 032700 011637 001124  MOV    (SP),R2$GDDAT  ;SAVE FOR PRINTOUT
8057 032704 022605      CMP    (SP)+,R5      ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
8058                      ;SHOULD BE SET
8059 032706 001405      BEQ    66$           ;BRANCH IF GOOD
8060 032710 010537 001126  MOV    R5,R2$BDDAT    ;BAD DATA
8061 032714 010337 004600  MOV    R3,R2$REGADR   ;FAILING REGISTER RHDS1
8062 032720 104063      ERROR  63           ;DURING ABOVE OPERATION ONLY
8063                      ;MOL!DPR!VV SHOULD BE SET
8064 032722      66$:
8065
8066                      ;*NOW CHANGES SAVED REGISTERS TO EXPECTED VALUES
8067
8068 032722 004037 041270  JSR    R0,R2$FILLRE   ;MOV 0 INTO SAVED RHWC
8069 032726 002272      RHWC                ;SAVED REGISTER TO CHANGE
8070 032730 000000      0                    ;DATA
8071 032732 004037 041270  JSR    R0,R2$FILLRE   ;MOV WRFROM+(266.*2) INTO SAVED RH3A
8072 032736 002274      RH3A                ;SAVED REGISTER TO CHANGE
8073 032740 003514      WRFROM+(266.*2)     ;DATA
8074 032742      ST6:
8075 032742 004037 041270  JSR    R0,R2$FILLRE   ;MOV 1 INTO SAVED RHCC
8076 032746 002334      RHCC                ;SAVED REGISTER TO CHANGE
8077 032750 000001      1                    ;DATA
8078 032752 004037 041270  JSR    R0,R2$FILLRE   ;MOV 1 INTO SAVED RHCA
8079 032756 002312      RHCA                ;SAVED REGISTER TO CHANGE
8080 032760 000001      1                    ;DATA

```

```

8081 032762 004037 041270      JSR      RO, @#FILLRE      ;MOV 1 INTO SAVED RHDST
8082 032766 002304              RHDST                      ;SAVED REGISTER TO CHANGE
8083 032770 000001              1                          ;DATA
8094
8085                                ;*COMPARE REGISTERS BEFORE WRITE HEADER AND DATA
9086                                ;*WITH REGISTERS AFTER COMMAND
8087
8088
8089 032772 004037 042354      JSR      RO, @#COMREG      ;COMPARE SAVED REGISTERS WITH
8090                                ;PRESENT VALUE
8091 032776 004612              SAVERE                      ;GOOD DATA SAVED IN 'SAVERE'
8092 033000 002354              WC                          ;TEST DATA STARTING FROM 'RHWC'
8093 033002 000022              18.                         ;18. REGISTERS TO BE COMPARED
8094 033004 033010              ST7                          ;RETURN TO ST7 ON ERROR
8095 033006 033014              ST8                          ;RETURN TO ST8 ON NO ERROR
8096 033010 104027      ST7:  ERROR 27              ;WRITE HEADER AND DATA CAUSED
8097 033012 000207              RTS      PC                  ;IMPROPER REGISTER CHANGE
8098                                ;GOOD DATA GIVES WHAT SHOULD BE
8099                                ;THERE
8100                                ;RECEIVED DATA GIVES WHAT WAS BE
8101                                ;THERE AFTER COMMAND
8102
8103                                ;*SETUP TO READ HEADER AND DATA FOR NEXT TRACK
8104                                ;*FILL READ INTO BUFFER WITH ALL ONES
8105
8106 033014      ST8:
8107
8108 033014 004737 041366      JSR      PC, @#CLDISK      ;SET R1-RHCS1, R2-RHCS2
8109                                ;R3-RHDS1, R4-RHER1
8110                                ;GIVE RH-11 INITIALIZE
8111                                ;SETUP UNIT NUMBER
8112 033020 004037 041236      JSR      RO, @#CLAREA      ;CLEAR 260. WORDS, FROM REINTO
8113 033024 003534              REINTO                      ;STARTING FROM REINTO
8114 033026 000404              260.                         ;260. WORDS
8115 033030 177777              -1                          ;FILL WITH -1
8116
8117
8118                                ;*FILL WRITE FROM BUFFER WITH EXPECTED DATA
8119
8120 033032      ST9:
8121
8122 033032 004037 041212      JSR      RO, @#FLHEAD      ;SAVE HEADER DATA IN WRFROM
8123 033036 002470              WRFROM                      ;LOCATION WHERE SAVED
8124 033040 000004              4                            ;NUMBER OF WORDS SAVED
8125 033042 010001              10001                        ;FIRST DATA WORD
8126 033044 000000              0                            ;SECOND DATA WORD
8127 033046 000000              0                            ;THIRD DATA WORD
8128 033050 000000              0                            ;FOURTH DATA WORD
8129
8130 033052 004037 041236      ST10: JSR      RO, @#CLAREA      ;CLEAR 2 WORDS, FROM WRFROM+(4*2)
8131 033056 002500              WRFROM+(4*2)                ;STARTING FROM WRFROM+(4*2)
8132 033060 000002              2                            ;2 WORDS
8133 033062 002000              1*2000                      ;FILL WITH 1*2000
8134

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8135	033064	004037	041236	JSR	RD, @#CLAREA	; CLEAR 254. WORDS, FROM WRFROM+(6*2)
8136	033070	002504			WRFROM+(6*2)	; STARTING FROM WRFROM+(6*2)
8137	033072	000376			254.	; 254. WORDS
8138	033074	000000			0	; FILL WITH 0
8139						
8140						
8141						
8142	033076			ST11:		; *FILL COMMAND INTO REGISTERS
8143						
8144	033076	004037	043340	JSR	RD, @#RUN	; SETUP TO RUN FOR DATA COMMAND
8145	033102	000001			1	; CYLINDER 1
8146	033104	000		.BYTE	0	; SECTOR 0
8147	033105	000		.BYTE	0	; TRACK 0
8148	033106	177374			-256.-4	; WORD COUNT (DATA) = 256. +
8149						; 4 HEADER WORDS
8150	033110	003534		REINTO		; BUS ADDRESS
8151						; STARTING ADDRESS OF DATA
8152						; BUFFER = REINTO
8153	033112	000000			0	; DO NOT INHIBIT BUS ADDRESS INCREMENT
8154	033114	014000		ECI!FMT22		; 16 BITS PER WORD FORMAT
8155						; INHIBIT ECC CORRECTION
8156						; DO NOT INHIBIT HEADER COMPARE
8157	033116	002450		REFOR		; GET READY TO DO A REFOR
8158						; READ HEADER AND DATA WITH 72 IN RHCS1
8159						
8160						
8161						; *SAVE REGISTERS FOR COMPARISON AFTER READ HEADER
8162						; *AND DATA
8163						
8164	033120	004037	041534	JSR	RD, @#SAVER	; SAVE REGISTERS
8165	033124	002272		RHWC		; RHWC IS THE FIRST REGISTER SAVED
8166	033126	004612		SAVERE		; STARTING ADDRESS OF WHERE
8167						; THE REGISTERS ARE SAVED
8168	033130	000020			16.	; NUMBER OF REGISTERS
8169						; SAVED = 16.
8170						
8171	033132	004737	041446	JSR	PC, @#CHECKT	; CHECK DVA, RDY, MOL, DPR, DRY, JV = 1
8172						; AND THAT NO STATUS BITS ARE STUCK = 1
8173	033136	104400	066402	TYPE	.CPHALT	; CANNOT CONTINUE TESTING IF ANY OF
8174						; THE FIRST SET OF BITS DON'T = 1
8175	033142	000000		HALT		; STOP
8176						
8177	033144	013777	004606 147114	MOV	@#RP4VEC, @RPVEC	; SET RPO4 VECTOR ADDRESS
8178						; TO 'TIME1' IF P-CLOCK IS PRESENT
8179						; OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
8180						; 'TIME' WILL ONLY SAVE
8181						; CURRENT CYLINDER ADDRESS
8182						; AND LOOK AHEAD REGISTERS
8183						
8184						
8185	033152	013746	002450	MOV	@#REFOR, -(SP)	; GET READY TO MOVE COMMAND
8186	033156	052716	000101	RIS	#GO!IE, (SP)	; GET READY TO SET 'GO' AND
8187						; ENABLE INTERRUPT
8188	033162	012677	147112	MOV	(SP)+, @RHCS1	; GO WITH

APC4 5 6 FUNCT. CONT. TST-PT 1  
SEEK & WAIT TEST CYL = 0-10

:72 IN RHCS1 FOR READ DATA  
:WITH INTERRUPT ENABLED  
:SAVE RHCS1 DURING ABOVE OPERATION  
:SAVE RHDS1 DURING ABOVE OPERATION

033166 011100  
033170 011305

MOV JRI,RO  
MOV JI3,R5

033172 104412  
033174 002300  
033176 000200  
033200 001514  
033202 001507

WAIT :WAIT FOR RDY BIT TO SET  
RHCS1 :WAIT FOR RHCS1 REGISTER  
RDY :WAIT FOR RDY BIT IN RHCS1 REGISTER  
909. :ALLOW 9080 MICRO SECONDS  
839. :RDY MUST SET BETWEEN  
:690 AND 17470 MICRO SECONDS

:\*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
:\*RO AND R5 IMMEDIATELY AFTER GO

033204 013746 002450  
033210 052716 004101  
033214 011637 001124  
033220 022600

MOV J#REFOR -(SP) :SAVE COMMAND  
BIS #IE!GO!DVA,(SP) :INCLUDE IE!GO!DVA  
MOV (SP),J#SGDDAT :SAVE FOR PRINTOUT  
CMP (SP)+,RO :DURING ABOVE OPERATION ONLY IE!GO!DVA  
:AND COMMAND SHOULD BE SET  
BEQ 645 :BRANCH IF GOOD  
MOV RO,J#SBDDAT :BAD DATA  
MOV R1,J#REGADR :FAILING REGISTER RHCS1  
ERROR 21 :DURING ABOVE OPERATION ONLY  
:COMMAND AND IE!GO!DVA SHOULD BE SET

033222 001405  
033224 010037 001126  
033230 010137 004600  
033234 104021

MOV #MOL!DPR!VV -(SP) :SAVE BITS SET DURING OPERATION IN RHDS1  
MOV (SP),J#SGDDAT :SAVE FOR PRINTOUT  
CMP (SP)+,R5 :DURING ABOVE OPERATION ONLY MOL!DPR!VV  
:SHOULD BE SET

033236 012746 010500  
033242 011637 001124  
033246 022605

645:

BEQ 665 :BRANCH IF GOOD  
MOV R5,J#SBDDAT :BAD DATA  
MOV R3,J#REGADR :FAILING REGISTER RHDS1  
ERROR 63 :DURING ABOVE OPERATION ONLY  
:MOL!DPR!VV SHOULD BE SET

033250 001405  
033252 010537 001126  
033256 010337 004600  
033262 104063

665:

:\*CHANGE SAVED REGISTERS TO EXPECTED VALUES

033264 004037 041270  
033270 002272  
033272 000000  
033274 004037 041270  
033300 002274  
033302 004544  
033304 004037 041270  
033310 002304  
033312 000001

JSR RO,J#FILLRE :MOV 0 INTO SAVED RHWC  
RHWC :SAVED REGISTER TO CHANGE  
0 :DATA  
JSR RO,J#FILLRE :MOV REINTO+(260.\*2) INTO SAVED RHBA  
RHBA :SAVED REGISTER TO CHANGE  
REINTO+(260.\*2) :DATA  
JSR RO,J#FILLRE :MOV 1 INTO SAVED RHDS1  
RHDS1 :SAVED REGISTER TO CHANGE  
1 :DATA

:\*COMPARE REGISTERS BEFORE READ HEADER AND DATA WITH  
:\*REGISTERS AFTER COMMAND

033314 004037 042354

JSR RO,J#COMREG :COMPARE SAVED REGISTERS WITH



;\*THE HEADS ARE NOW AT CYLINDER 10  
;\*ALL REGISTERS WILL BE SAVED AND A SEEK WILL BE GIVEN  
;\*TO CYLINDER 0  
;\*FILL REGISTERS FOR A SEEK COMMAND

000000  
000001  
000002  
000003  
000004  
000005  
000006  
000007  
000008  
000009  
000010  
000011  
000012  
000013  
000014  
000015  
000016  
000017  
000018  
000019  
000020  
000021  
000022  
000023  
000024  
000025  
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000027  
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000046  
000047  
000048  
000049

033452 ST17:

033452 004737 041366

JSR R0,2#CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETLP JNIT NUMBER

033456 004037 041336

JSR R0,2#SEEKCY ;SEEK FOR  
0 ;CYLINDER 0

033464 004037 041534

;\*SAVE REGISTERS FOR COMPARISON AFTER SEEK COMMAND  
JSR R0,2#SAVER ;SAVE REGISTERS  
RHWC ;RHWC IS THE FIRST REGISTER SAVED  
SAVERE ;STARTING ADDRESS OF WHERE  
;THE REGISTERS ARE SAVED  
18. ;NUMBER OF REGISTERS  
;SAVED = 18.

033476 013777 004606 146562

MOV 2#RPF4VEC,3#RPFVEC ;SET RPO4 VECTOR ADDRESS  
;TO 'TIME1' IF P-CLOCK IS PRESENT  
;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT  
;'TIME' WILL ONLY SAVE  
;CURRENT CYLINDER ADDRESS  
;AND LOOK AHEAD REGISTERS

033504 013746 002452

MOV 2#SEECOM, -(SP) ;GET READY TO MOVE COMMAND  
BIS #GO!IE, (SP) ;GET READY TO SET 'GO' AND  
;ENABLE INTERRUPT

033514 012677 146560

MOV (SP)+, 2#RHCS1 ;GO WITH  
;4 IN RHCS1 FOR SEEK  
;WITH INTERRUPT ENABLED  
MOV 2#R1, R0 ;SAVE RHCS1 DURING ABOVE OPERATION  
MOV 2#R3, R5 ;SAVE RHDS1 DURING ABOVE OPERATION

;\*SEEK FOR ONE CYLINDER=7MILI SEC., FOR TEN=70 MILI SEC

033524 104412

WAIT ;WAIT FOR DRY BIT TO SET  
RHDS1 ;WAIT FOR RHDS1 REGISTER  
DRY ;WAIT FOR DRY BIT IN RHDS1 REGISTER  
7000. ;ALLOW 70000 MICRO SECONDS  
35. ;DRY MUST SET BETWEEN  
;69650 AND 70350 MICRO SECONDS

033530 000200

033532 015530

033534 000043

;\*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN  
;\*R0 AND R5 IMMEDIATELY AFTER GO

033536 013746 002452

MOV 2#SEECOM, -(SP) ;SAVE COMMAND  
BIS #DVA!GO!IE!RDY, (SP) ;INCLUDE DVA!GO!IE!RDY

033542 052716 004301

E03

```

0351 033546 011637 001124      MOV      (SP),2#SGDDAT      :SAVE FOR PRINTOUT
0352 033552 022600              CMP      (SP)+,R0          :DURING ABOVE OPERATION ONLY D/A!GO!IE!RDY
0353                          :AND COMMAND SHOULD BE SET
0354 033554 001405      BEQ      64$              :BRANCH IF GOOD
0355 033555 010037 001126      MOV      R0,2#SBDDAT      :BAD DATA
0356 033556 010137 004600      MOV      R1,2#REGADR      :FAILING REGISTER RHCS1
0357 033556 104021      ERROR    21              :DURING ABOVE OPERATION ONLY
0358                          :COMMAND AND DVA!GO!IE!RDY SHOULD BE SET
0359 033570 012746 030500      MOV      #PIP!MOL!DPR!VV, 64$:  (SP) :SAVE BITS SET DURING OPERATION IN RHCS1
0360 033574 011637 001124      MOV      (SP),2#SGDDAT      :SAVE FOR PRINTOUT
0361 033600 022600      CMP      (SP)+,R5          :DURING ABOVE OPERATION ONLY PIP!MOL!DPR!VV
0362                          :SHOULD BE SET
0363 033602 001405      BEQ      66$              :BRANCH IF GOOD
0364 033604 010537 001126      MOV      R5,2#SBDDAT      :BAD DATA
0365 033610 010337 004600      MOV      R3,2#REGADR      :FAILING REGISTER RHDS1
0366 033614 104063      ERROR    63              :DURING ABOVE OPERATION ONLY
0367                          :PIP!MOL!DPR!VV SHOULD BE SET
0368 033616                          66$:
0369
0370                          :*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUE
0371
0372 033616 004037 041270      JSR      R0,2#FILLRE      :MOV 0 INTO SAVED RHCC
0373 033622 002334      RHCC    0                :SAVED REGISTER TO CHANGE
0374 033624 000000      0                :DATA
0375
0376 033626 053737 004740 004636      BIS      2#ATTENT,2#SAVERE+24 :SET APPROPRIATE 'ATA' BITS
0377                          :FOR WORKING DRIVE IN
0378                          :SAVED RHAS LOACTION
0379
0380 033634 004037 042246      JSR      R0,2#CHREG      :CHANGE BITS IN SAVED REGISTER
0381 033640 002322      RHDS1   0                :CHANGE RHDS1 REGISTER
0382
0383 033642 000001      1                :1 BIT/BITS TO BE CHANGED
0384 033644 000001      1                :NEW VALUE OF ATA IS 1
0385 033646 100000      ATA     0                :CHANGE ATA BIT
0386
0387 033650 004037 042246      JSR      R0,2#CHREG      :CHANGE BITS IN SAVED REGISTER
0388 033654 002300      RHCS1   0                :CHANGE RHCS1 REGISTER
0389
0390 033656 000001      1                :1 BIT/BITS TO BE CHANGED
0391 033650 000001      1                :NEW VALUE OF SC IS 1
0392 033652 100000      SC     0                :CHANGE SC BIT
0393
0394                          :*COMPARE REGISTERS AFTER A SEEK COMMAND
0395
0396 033664 004037 042354      JSR      R0,2#COMREG      :COMPARE SAVED REGISTERS WITH
0397                          :PRESENT VALUE
0398 033670 004612      SAVERE   0                :GOOD DATA SAVED IN 'SAVERE'
0399 033672 002354      WC     0                :TEST DATA STARTING FROM 'RHWC'
0400 033674 000022      18.     0                :18. REGISTERS TO BE COMPARED
0401 033676 033702      ST18    0                :RETURN TO ST19 ON ERROR
0402 033700 033706      ST19    0                :RETURN TO ST19 ON NO ERROR
0403
0404 033702 104037      ST18:  ERROR    37        :SEEK COMMAND CAUSED AN

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0459                                     ;DO NOT INHIBIT HEADER COMPARE
0460 034004 002450 REFOR                   ;GET READY TO DO A REFOR
0461                                     ;READ HEADER AND DATA WITH 72 IN RHCS1
0462
0463
0464 034006 004737 041446 JSR PC,2#CHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1
0465                                     ;AND THAT NO STATUS BITS ARE STUCK = 1
0466 034012 104400 066402 TYPE .CPHALT   ;CANNOT CONTINUE TESTING IF ANY OF
0467                                     ;THE FIRST SET OF BITS DON'T = 1
0468 034016 000000 HALT                   ;STOP
0469
0470 034020 013777 004606 146240 MOV 2#RPO4VEC,2#RPO4VEC ;SET RPO4 VECTOR ADDRESS
0471                                     ;TO 'TIME1' IF P-CLOCK IS PRESENT
0472                                     ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
0473                                     ;'TIME' WILL ONLY SAVE
0474                                     ;CURRENT CYLINDER ADDRESS
0475                                     ;AND LOOK AHEAD REGISTERS
0476
0477
0478 034026 013746 002450 MOV 2#REFOR, -(SP) ;GET READY TO MOVE COMMAND
0479 034032 052716 000101 BIS #GO!IE, (SP) ;GET READY TO SET 'GO' AND
0480                                     ;ENABLE INTERRUPT
0481 034036 012677 146236 MOV (SP)+,2#RHCS1 ;GO WITH
0482                                     ;72 IN RHCS1 FOR READ DATA
0483                                     ;WITH INTERRUPT ENABLED
0484
0485
0486 034042 104412 WAT                       ;WAIT FOR RDY BIT TO SET
0487 034044 002300 RHCS1                   ;WAIT FOR RHCS1 REGISTER
0488 034046 000200 RDY                       ;WAIT FOR RDY BIT IN RHCS1 REGISTER
0489 034050 001614 908.                     ;ALLOW 9080 MICRO SECONDS
0490 034052 001507 839.                     ;RDY MUST SET BETWEEN
0491                                     ;690 AND 17470 MICRO SECONDS
0492
0493                                     ;*CHECK READ WORDS
0494
0495 034054 004037 043404 JSR RC,2#COMPAR ;COMPARE TWO BLOCKS OF MEMORY
0496 034060 002470 WRFROM                   ;GOOD DATA STARTS FROM WRFROM
0497 034062 003534 REINTO                   ;TEST DATA STARTS FROM REINTO
0498 034064 000404 260.                     ;260. WORDS TO BE COMPARED
0499 034066 034072 ST26                       ;RETURN TO ST26 ON ERROR
0500 034070 034076 ST27                       ;RETURN TO ST27 ON NO ERROR
0501
0502
0503 034072 104032 ST26: ERROR 32             ;READ HEADER AND DATA
0504 034074 000207 RTS PC                   ;FOLLOWING A SEEK TO CYLINDER 0
0505                                     ;FROM CYLINDER 10 GAVE AN
0506                                     ;ERROR
0507
0508 034076 ST27:
0509

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\*\*\*\*\*  
: \*TEST 44 SEEK & READ TEST (CYL = 009)

: \* THIS TEST DEPENDS ON HEADER AND DATA WRITTEN BY THE  
: \* PREVIOUS TEST. AT THIS POINT THE HEADS ARE ON  
: \* CYLINDER 0

: \* ALL REGISTERS ARE SAVED  
: \* A SEEK IS GIVEN TO CYLINDER 9  
: \* ALL REGISTERS ARE CHECKED FOR IMPROPER CHANGE  
: \* DATA WRITTEN ON CYLINDER 9 IS CHECKED

\*\*\*\*\*  
TST44: SCOPE

034076 000004  
034100 012706 001000  
034104 012737 000044 004604

MOV #STACK, SP ;RESET STACK  
MOV #44, #TSTNM ;SAVE TEST NUMBER

034112 004737 041366

JSR PC, #CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETUP UNIT NUMBER

034116 013777 004606 146142

: \*THIS GETS HEADS TO CYLINDER 0  
MOV #RPO4VEC, #RPVEC ;SET RPO4 VECTOR ADDRESS  
;TO 'TIME1' IF P-CLOCK IS PRESENT  
;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT  
; 'TIME' WILL ONLY SAVE  
;CURRENT CYLINDER ADDRESS  
;AND LOOK AHEAD REGISTERS

034124 013746 002426  
034130 052716 000101

MOV #RECALI, -(SP) ;GET READY TO MOVE COMMAND  
BIS #GO!IE, (SP) ;GET READY TO SET 'GO' AND  
;ENABLE INTERRUPT

034134 012677 146140

MOV (SP)+, #RHCS1 ;GO WITH  
;5 IN RHCS1 FOR RECALIBRATE  
;WITH INTERRUPT ENABLED

034140 011100  
034142 011305

MOV #R1, R0 ;SAVE RHCS1 DURING ABOVE OPERATION  
MOV #R3, R5 ;SAVE RHDS1 DURING ABOVE OPERATION

034144 104412  
034146 002322  
034150 000200  
034152 076377  
034154 056701

WAT ;WAIT FOR DRY BIT TO SET  
RHDS1 ;WAIT FOR RHDS1 REGISTER  
DRY ;WAIT FOR DRY BIT IN RHDS1 REGISTER  
31999. ;ALLOW 319990 MICRO SECONDS  
24001. ;DRY MUST SET BETWEEN  
;79980 AND 560000 MICRO SECONDS

: \*FILL REGISTERS FOR A SEEK COMMAND

034156 004037 041336  
034162 000012

JSR R0, #SEEKCY ;SEEK FOR  
10. ;CYLINDER 10.

2

ref



```

8564      : *SAVE REGISTERS FOR COMPARISON AFTER SEEK COMMAND
8565 034164 004037 041534 JSR   RO,0#SAVER   ;SAVE REGISTERS
8566 034170 002272      RHWC   ;RHWC IS THE FIRST REGISTER SAVED
8567 034172 004612      SAVERE  ;STARTING ADDRESS OF WHERE
8568      : THE REGISTERS ARE SAVED
8569 034174 000022      18.      ;NUMBER OF REGISTERS
8570      : SAVED = 18.
8571
8572 034176 013777 004606 146062 MOV   0#RPO4VEC,0#RPVEC ;SET RPO4 VECTOR ADDRESS
8573      : TO 'TIME1' IF P-CLOCK IS PRESENT
8574      : OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
8575      : 'TIME' WILL ONLY SAVE
8576      : CURRENT CYLINDER ADDRESS
8577      : AND LOOK AHEAD REGISTERS
8578
8579
8580 034204 013746 002452 MOV   0#SEECOM,-(SP) ;GET READY TO MOVE COMMAND
8581 034210 052716 000101 BIS   #GO!IE,(SP)    ;GET READY TO SET 'GO' AND
8582      : ENABLE INTERRUPT
8583 034214 012677 146060 MOV   (SP)+,0#RHCSI  ;GO WITH
8584      : 4 IN RHCSI FOR SEEK
8585      : WITH INTERRUPT ENABLED
8586 034220 011100 MOV   0#R1,RO        ;SAVE RHCSI DURING ABOVE OPERATION
8587 034222 011305 MOV   0#R3,R5        ;SAVE RHDSI DURING ABOVE OPERATION
8588
8589      : *SEEK FOR ONE CYLINDER=7 MILI SEC.. FOR TEN=70 MILI SEC
8590
8591 034224 104412 WAT           ;WAIT FOR DRY BIT TO SET
8592 034226 002322 RHDSI        ;WAIT FOR RHDSI REGISTER
8593 034230 000200 DRY          ;WAIT FOR DRY BIT IN RHDSI REGISTER
8594 034232 015530 7000. ;ALLOW 70000 MICRO SECONDS
8595 034234 000043 35.   ;DRY MUST SET BETWEEN
8596      : 69650 AND 70350 MICRO SECONDS
8597
8598      : *COMPARE CONTENTS OF RHCSI AND RHDSI ALREADY SAVED IN
8599      : *RO AND R5 IMMEDIATELY AFTER GO
8600
8601 034236 013746 002452 MOV   0#SEECOM,-(SP) ;SAVE COMMAND
8602 034242 052716 004301 BIS   #DVA!GO!IE!RDY,(SP) ;INCLUDE DVA!GO!IE!RDY
8603 034246 011637 001124 MOV   (SP),0#$GDDAT ;SAVE FOR PRINTOUT
8604 034252 022600 CMP   (SP)+,RO      ;DURING ABOVE OPERATION ONLY DVA!GO!IE!RDY
8605      : AND COMMAND SHOULD BE SET
8606 034254 001405 BEQ   67$         ;BRANCH IF GOOD
8607 034256 010037 001126 MOV   RO,0#$BDDAT ;BAD DATA
8608 034262 010137 004600 MOV   R1,0#REGADR ;FAILING REGISTER RHCSI
8609 034266 104021 ERROR  21 ;DURING ABOVE OPERATION ONLY
8610      : COMMAND AND DVA!GO!IE!RDY SHOULD BE SET
8611 034270 012746 030500 67$: MOV   #PIP!MOL!DPR!VV,-(SP) ;SAVE BITS SET DURING OPERATION IN RHDSI
8612 034274 011637 001124 MOV   (SP),0#$GDDAT ;SAVE FOR PRINTOUT
8613 034300 022605 CMP   (SP)+,R5     ;DURING ABOVE OPERATION ONLY PIP!MOL!DPR!VV
8614      : SHOULD BE SET
8615 034302 001405 BEQ   69$         ;BRANCH IF GOOD
8616 034304 010537 001126 MOV   R5,0#$BDDAT ;BAD DATA
8617 034310 010337 004600 MOV   R3,0#REGADR ;FAILING REGISTER RHDSI

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8672 034406 004737 041366 JSR PC, @#CLDISK ;SET R1-RHCS1, R2-RHCS2
8673 ;R3-RHDS1, R4-RHER1
8674 ;GIVE RH-1; INITIALIZE
8675 ;SETUP UNIT NUMBER
8676 034412 004037 041236 JSR RO, @#CLAREA ;CLEAR 260. WORDS, FROM REINTO
8677 034416 003534 REINTO ;STARTING FROM REINTO
8678 034420 000404 260. ;260. WORDS
8679 034422 177777 -1 ;FILL WITH -1
8680
8681
8682 ;*FILL WRITE FROM BUFFER WITH EXPECTED DATA
8683
8684
8685 034424 004037 041212 JSR RO, @#FLHEAD ;SAVE HEADER DATA IN WRFROM
8686 034430 002470 WRFROM ;LOCATION WHERE SAVED
8687 034432 000004 4 ;NUMBER OF WORDS SAVED
8688 034434 010011 10011 ;FIRST DATA WORD
8689 034436 011025 <18.*400>!<21.> ;SECOND DATA WORD
8690 034440 000000 0 ;THIRD DATA WORD
8691 034442 000000 0 ;FOURTH DATA WORD
8692 034444 004037 041236 JSR RO, @#CLAREA ;CLEAR 20. WORDS, FROM WRFROM+<4.*2>
8693 034450 002500 WRFROM+<4.*2> ;STARTING FROM WRFROM+<4.*2>
8694 034452 000024 20. ;20. WORDS
8695 034454 023125 <9.*2000>!<18.*40>!<21.> ;FILL WITH <9.*2000>!<18.*40>!<2
8696
8697 034456 004037 041236 JSR RO, @#CLAREA ;CLEAR 250. WORDS, FROM WRFROM+<24.*2>
8698 034462 002550 WRFROM+<24.*2> ;STARTING FROM WRFROM+<24.*2>
8699 034464 000372 250. ;250. WORDS
8700 034466 177777 -1 ;FILL WITH -1
8701
8702
8703 ;*FILL READ HEADER AND DATA COMMAND FOR 10 WORDS
8704
8705 034470 004037 043340 JSR RO, @#RUN ;SETUP TO RUN FOR DATA COMMAND
8706 034474 000011 9. ;CYLINDER 9.
8707 034476 025 .BYTE 21. ;SECTOR 21.
8708 034477 022 .BYTE 18. ;TRACK 18.
8709 034500 177750 -20.-4 ;WORD COUNT (DATA) = 20. +
8710 ;4 HEADER WORDS
8711 034502 003534 REINTO ;BUS ADDRESS
8712 ;STARTING ADDRESS OF DATA
8713 ;BUFFER = REINTO
8714 034504 000000 0 ;DO NOT INHIBIT BUS ADDRESS INCREMENT
8715 034506 014000 ECI!FMT22 ;16 BITS PER WORD FORMAT
8716 ;INHIBIT ECC CORRECTION
8717 ;DO NOT INHIBIT HEADER COMPARE
8718 034510 002450 REFOR ;GET READY TO DO A REFOR
8719 ;READ HEADER AND DATA WITH 72 IN RHCS1
8720
8721
8722 034512 004737 041446 JSR PC, @#CHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1
8723 ;AND THAT NO STATUS BITS ARE STUCK = 1
8724 034516 104400 066402 TYPE ,CPHALT ;CANNOT CONTINUE TESTING IF ANY OF
8725 ;THE FIRST SET OF BITS DON'T = 1

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8726 034522 000000 HALT ;STOP
8727
8728 034524 013777 004606 145534 MOV @#RP4VEC,@RPVEC ;SET RPO4 VECTOR ADDRESS
8729 ;TO 'TIME1' IF P-CLOCK IS PRESENT
8730 ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
8731 ;'TIME' WILL ONLY SAVE
8732 ;CURRENT CYLINDER ADDRESS
8733 ;AND LOOK AHEAD REGISTERS
8734
8735
8736 034532 013746 002450 MOV @#REFOR,-(SP) ;GET READY TO MOVE COMMAND
8737 034536 052716 000101 BIS #GO!IE,(SP) ;GET READY TO SET 'GO' AND
8738 ;ENABLE INTERRUPT
8739 034542 012677 145532 MOV (SP)+,@RHCS1 ;GO WITH
8740 ;72 IN RHCS1 FOR READ DATA
8741 ;WITH INTERRUPT ENABLED
8742
8743
8744 034546 104412 WAT ;WAIT FOR RDY BIT TO SET
8745 034550 002300 RHCS1 ;WAIT FOR RHCS1 REGISTER
8746 034552 000200 RDY ;WAIT FOR RDY BIT IN RHCS1 REGISTER
8747 034554 001614 908. ;ALLOW 9080 MICRO SECONDS
8748 034556 001507 839. ;RDY MUST SET BETWEEN
8749 ;690 AND 17470 MICRO SECONDS
8750
8751 ;*CHECK READ WORDS
8752
8753 034560 004037 043404 JSR RD,@#COMPAR ;COMPARE TWO BLOCKS OF MEMORY
8754 034564 002470 WRFROM ;GOOD DATA STARTS FROM WRFROM
8755 034566 003534 REINTO ;TEST DATA STARTS FROM REINTO
8756 034570 000404 250. ;250. WORDS TO BE COMPARED
8757 034572 034576 3$ ;RETURN TO 3$ ON ERROR
8758 034574 034602 4$ ;RETURN TO 4$ ON NO ERROR
8759
8760
8761 034576 104032 3$: ERROR 32 ;READ HEADER AND DATA
8762 034600 000207 RTS PC ;FOLLOWING A SEEK TO CYLINDER 9
8763 ;FROM CYLINDER 0 GAVE AN
8764 ;ERROR
8765 034602 4$:
8766
8767
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8769

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M03

MAINDEC-11-DZRJI-A, RPO4/5/6 FUNCT. CONT. TST-PT 1  
DZRJIA.P11 WRITE CHECK DATA & WRITE PROTECT TESTS

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SEQ 0244

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8771

.SBTTL WRITE CHECK DATA & WRITE PROTECT TESTS

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8797 034602 000004
8798 034604 012705 001000
8799 034610 012737 000045 004604
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8801 034616 004737 041366
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8805 034622 004737 041446
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8807 034626 104400 066402
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8809 034632 000000
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8813 034634 004037 041336
8814 034640 000005
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8816 034642 013777 004606 145416
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8824 034650 013746 002452
8825 034654 052716 000101

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*****
;TEST 45 WRITE CHECK HEADER AND DATA

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; * WRITE CHECK HEADER AND DATA CYLINDER 5 FORMAT 16 BITS PER WORD
; * TRACK 7, SECTOR 4, KEYS 0, NUMBER OF WORDS 266
; * CONSISTING OF
; * 10 WORDS OF 12344 (6 BITS FOR CYL, 5 FOR TRACK, 5 FOR SECTOR)
; * 10 WORDS OF 177777
; * 10 WORDS OF 0
; * 10 WORDS OF 052525
; * 10 WORDS OF 125252
; * 16 WORDS OF LEFT ROTATING ZERO (EG 177776, 177775)
; * 16 WORDS OF LEFT ROTATING ONE (EG 1,2,4,10)
; * 174 WORDS OF 377
; * 4 WORDS OF HEADER
; * 2 WORDS OF 12345

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; * FIRST THE ABOVE DATA IS WRITTEN BY A WRITE HEADER AND
; * DATA COMMAND
; * CHECK FOR NO ERRORS
; * THEN THE ABOVE DATA IS READ BY A READ HEADER AND DATA
; * CHECK FOR NO ERRORS
; * THEN THE ABOVE WRITE CHECK HEADER AND DATA IS GIVEN

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;ST45: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #45, @#TSTNM ;SAVE TEST NUMBER

JSR PC, @#CLDISK ;SET R1-RHCS1, R2-RHCS2
;R3-RHDS1, R4-RHER1
;GIVE RH-11 INITIALIZE
;SETUP UNIT NUMBER
JSR PC, @#CHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1
;AND THAT NO STATUS BITS ARE STUCK = 1
TYPE ,CPHALT ;CANNOT CONTINUE TESTING IF ANY OF
;THE FIRST SET OF BITS DON'T = 1
HALT ;STOP

; *GET HEADS TO CYLINDER 5

JSR RO, @#SEEKCY ;SEEK FOR
5 ;CYLINDER 5

MOV @#RP4VEC, @#RVEC ;SET RPO4 VECTOR ADDRESS
;TO 'TIME1' IF P-CLOCK IS PRESENT
;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
;'TIME' WILL ONLY SAVE
;CURRENT CYLINDER ADDRESS
;AND LOOK AHEAD REGISTERS

MOV @#SEECOM, -(SP) ;GET READY TO MOVE COMMAND
BIS @#GO!IE, (SP) ;GET READY TO SET 'GO' AND

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034650 012677 145414 MOV (SP)+,RHC1 :ENABLE INTERRUPT
:GO WITH
:4 IN RHC1 FOR SEEK
034664 011100 MOV R1,R0 :WITH INTERRUPT ENABLED
034666 011305 MOV R3,R5 :SAVE RHC1 DURING ABOVE OPERATION
:SAVE RHC1 DURING ABOVE OPERATION

034670 104412 WAIT :WAIT FOR DRY BIT TO SET
034672 003333 RHDS1 :WAIT FOR RHDS1 REGISTER
034674 000300 DRY :WAIT FOR DRY BIT IN RHDS1 REGISTER
034676 015530 7000. :ALLOW 7000 MICRO SECONDS
034700 000043 35. :DRY MUST SET BETWEEN
:69650 AND 70350 MICRO SECONDS

034702 004737 041366 JSR R0,R#CLDISK :SET R1-RHC1, R2-RHC2
:R3-RHDS1, R4-RHER1
:GIVE RH-11 INITIALIZE
:SETUP UNIT NUMBER

:*FILL WRITE FROM BUFFER WITH HEADER

034706 004037 041212 JSR R0,R#FLHEAD :SAVE HEADER DATA IN WRFROM
WRFROM :LOCATION WHERE SAVED
4 :NUMBER OF WORDS SAVED
10005 :FIRST DATA WORD
(7*40)!4 :SECOND DATA WORD
0 :THIRD DATA WORD
0 :FOURTH DATA WORD

:*10 WORDS OF OF THE FOLLOWING DATA
:* 12344,17777,0,52525,125252

034726 004037 041236 JSR R0,R#CLAREA :CLEAR 10. WORDS, FROM WRFROM+(4.*2)
034732 002500 WRFROM+(4.*2) :STARTING FROM WRFROM+(4.*2)
034734 000012 10. :10. WORDS
034736 012344 (5*2000)!(7*40)!4 :FILL WITH (5*2000)!(7*40)!4

034740 004037 041236 JSR R0,R#CLAREA :CLEAR 10. WORDS, FROM WRFROM+(14.*2)
034744 002524 WRFROM+(14.*2) :STARTING FROM WRFROM+(14.*2)
034746 000012 10. :10. WORDS
034750 177777 -1 :FILL WITH -1

034752 004037 041236 JSR R0,R#CLAREA :CLEAR 10. WORDS, FROM WRFROM+(24.*2)
034756 002550 WRFROM+(24.*2) :STARTING FROM WRFROM+(24.*2)
034760 000012 10. :10. WORDS
034762 000000 0 :FILL WITH 0

034764 004037 041236 JSR R0,R#CLAREA :CLEAR 10. WORDS, FROM WRFROM+(34.*2)
034770 002574 WRFROM+(34.*2) :STARTING FROM WRFROM+(34.*2)
034772 000012 10. :10. WORDS
034774 052525 52525 :FILL WITH 52525

034776 004037 041236 JSR R0,R#CLAREA :CLEAR 10. WORDS, FROM WRFROM+(44.*2)

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; *THE WRITE HEADER AND DATA COMMAND WILL BE LOADED
035134 004037 043340 JSR RO.2#RUN ; SETUP TO RUN FOR DATA COMMAND
035140 000005 ; CYLINDER 5
035142 004 ; SECTOR 4
035143 007 ; TRACK 7
035144 177366 -262.-4 ; WORD COUNT (DATA) = 262. +
; 4 HEADER WORDS
035146 002470 WRFROM ; BUS ADDRESS
; STARTING ADDRESS OF DATA
; BUFFER = WRFROM
035150 000000 0 ; DO NOT INHIBIT BUS ADDRESS INCREMENT
035152 010000 FMT22 ; 16 BITS PER WORD FORMAT
; DO NOT INHIBIT ECC CORRECTION
; DO NOT INHIBIT HEADER COMPARE
035154 002444 WRIFOR ; GET READY TO DO A WRIFOR
; *WRITE HEADER AND DATA WITH 62 IN RHCS1

; *NOW SAVE REGISTERS FOR COMPARISON AFTER WRITE HEADER AND DATA
035156 004037 041534 JSR RO.2#SAVER ; SAVE REGISTERS
035162 002272 RHWC ; RHWC IS THE FIRST REGISTER SAVED
035164 004612 SAVERE ; STARTING ADDRESS OF WHERE
; THE REGISTERS ARE SAVED
035166 000022 18. ; NUMBER OF REGISTERS
; SAVED = 18.

035170 004737 041446 JSR PC.2#CHECKT ; CHECK DVA, RDY, MCL, DPR, DRY, VV = 1
; AND THAT NO STATUS BITS ARE STUCK = 1
035174 104400 066402 TYPE .CPHALT ; CANNOT CONTINUE TESTING IF ANY OF
; THE FIRST SET OF BITS DON'T = 1
035200 000000 HALT ; STOP
035202 013777 004606 145056 MOV 2#RPAVEC, 3#RVEC ; SET RPO4 VECTOR ADDRESS
; TO 'TIME1' IF P-CLOCK IS PRESENT
; OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
; 'TIME' WILL ONLY SAVE
; CURRENT CYLINDER ADDRESS
; AND LOCK AHEAD REGISTERS

035210 013746 002444 MOV 2#WRIFOR, -(SP) ; GET READY TO MOVE COMMAND
035214 052716 000101 BIS #GO!IE, (SP) ; GET READY TO SET 'GO' AND
; ENABLE INTERRUPT
035220 012677 145054 MOV (SP)+, 3#RHCS1 ; GO WITH
; 62 IN RHCS1 FOR WRITE HEADER AND DATA
; WITH INTERRUPT ENABLED

035224 104412 WAT ; WAIT FOR RDY BIT TO SET
035226 002300 RHCS1 ; WAIT FOR RHCS1 REGISTER

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00000 035230 000200          RDY          ;WAIT FOR RDY BIT IN RHCS1 REGISTER
00001 035232 001732          996.        ;ALLOW 9860 MICRO SECONDS
00002 035234 001502          934.        ;RDY MUST SET BETWEEN
                                ;1520 AND 18200 MICRO SECONDS

00003                                ;*NOW CHANGE SAVED REGISTERS TO EXPECTED VALUES
00004 035236 004037 041270    JSR          RC,0#FILLRE ;MOV 0 INTO SAVED RHWC
00005 035242 002272          RHWC        ;SAVED REGISTER TO CHANGE
00006 035244 000000          C          ;DATA
00007 035246 004037 041270    JSR          RD,0#FILLRE ;MOV WRFROM+(266.*2) INTO SAVED RHBA
00008 035252 002274          RHBA        ;SAVED REGISTER TO CHANGE
00009 035254 003514          WRFROM+(266.*2) ;DATA
00010 035256 004037 041270    JSR          RD,0#FILLRE ;MOV 3406 INTO SAVED RHDST
00011 035262 002304          RHDST      ;SAVED REGISTER TO CHANGE
00012 035264 003406          3406       ;DATA

00013                                ;*NOW COMPARE REGISTERS BEFORE WRITE HEADER AND DATA
00014                                ;*WITH REGISTERS AFTER COMMAND

00015
00016 035266 004037 042354    JSR          RD,0#COMREG ;COMPARE SAVED REGISTERS WITH
00017                                ;PRESENT VALUE
00018 035272 004612          SAVERE     ;GOOD DATA SAVED IN 'SAVERE'
00019 035274 002254          WC        ;TEST DATA STARTING FROM 'RHWC'
00020 035276 000022          18.       ;18. REGISTERS TO BE COMPARED
00021 035300 035304          3$        ;RETURN TO 3$ ON ERROR
00022 035302 035310          4$        ;RETURN TO 4$ ON NO ERROR

00023 035304 104027          3$:        ;WRITE HEADER AND DATA
00024 035306 000207          R'S       27 ;CAUSED IMPROPER REGISTER
                                PC
                                ;CHANGE
                                ;GOOD DATA GIVES WHAT SHOULD
                                ;BE THERE
                                ;RECEIVED DATA GIVES WHAT
                                ;WAS THERE AFTER COMMENT

00025                                ;*NOW FILL COMMAND FOR READ

00026
00027 035310          4$:
00028 035312 004737 041366    JSR          PC,0#CLDISK ;SET R1-RHCS1, R2-RHCS2
00029                                ;R3-RHDS1, R4-RHER1
00030                                ;GIVE RH-11 INITIALIZE
00031                                ;SETUP UNIT NUMBER

00032 035314 004037 043340    JSR          RC,0#RUN    ;SETUP TO RUN FOR DATA COMMAND
00033 035320 000005          S        ;CYLINDER 5
00034 035322          004        ;SECTOR 4
00035 035323          007        ;TRACK 7
00036 035324 177366          -262.-4   ;WORD COUNT (DATA) = 262. +
00037 035326 003534          REINTC    ;4 HEADER WORDS
                                ;BUS ADDRESS
                                ;STARTING ADDRESS OF DATA
                                ;BUFFER = REINTC

```

15  
78

```

9042 035330 000000 0 ;DO NOT INHIBIT BUS ADDRESS INCREMENT
9043 035332 014000 ECI!FMT22 ;16 BITS PER WORD FORMAT
9044 ;INHIBIT ECC CORRECTION
9045 ;DO NOT INHIBIT HEADER COMPARE
9046 035334 002450 REFOR ;GET READY TO DO A REFOR
9047 ;READ HEADER AND DATA WITH 72 IN RHCS1
9048
9049 ;*NOW SAVE REGISTERS FOR COMPARISON AFTER READ HEADER AND DATA
9050
9051 035336 004037 041534 JSR RO,0#SAVER ;SAVE REGISTERS
9052 035342 002272 RHWC ;RHWC IS THE FIRST REGISTER SAVED
9053 035344 004612 SAVERE ;STARTING ADDRESS OF WHERE
9054 ;THE REGISTERS ARE SAVED
9055 035346 000022 18. ;NUMBER OF REGISTERS
9056 ;SAVED = 18.
9057
9058 035350 004737 041446 JSR PC,0#CHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1
9059 ;AND THAT NO STATUS BITS ARE STUCK = 1
9060 035354 104400 066402 TYPE .OPHALT ;CANNOT CONTINUE TESTING IF ANY OF
9061 ;THE FIRST SET OF BITS DON'T = 1
9062 035360 000000 HALT ;STOP
9063
9064 035362 013777 004606 144676 MOV 0#RPF4VEC,0#RPFVEC ;SET RPO4 VECTOR ADDRESS
9065 ;TO 'TIME1' IF P-CLOCK IS PRESENT
9066 ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
9067 ;'TIME' WILL ONLY SAVE
9068 ;CURRENT CYLINDER ADDRESS
9069 ;AND LOOK AHEAD REGISTERS
9070
9071
9072
9073 035370 013746 002450 MOV 0#REFOR,-(SP) ;GET READY TO MOVE COMMAND
9074 035374 052716 000101 BIS #GO!IE,(SP) ;GET READY TO SET 'GO' AND
9075 ;ENABLE INTERRUPT
9076 035400 012677 144674 MOV (SP)+,0#RHCS1 ;GO WITH
9077 ;72 IN RHCS1 FOR READ DATA
9078 ;WITH INTERRUPT ENABLED
9079
9080
9081 035404 104412 WAT ;WAIT FOR RDY BIT TO SET
9082 035406 002300 RHCS1 ;WAIT FOR RHCS1 REGISTER
9083 035410 000200 RDY ;WAIT FOR RDY BIT IN RHCS1 REGISTER
9084 035412 001732 986. ;ALLOW 9860 MICRO SECONDS
9085 035414 001502 834. ;RDY MUST SET BETWEEN
9086 ;1520 AND 18200 MICRO SECONDS
9087
9088 ;*CHANGE SAVED REGISTERS TO EXPECTED VALUES
9089
9090 035416 004037 041270 JSR RO,0#FILLRE ;MOV 0 INTO SAVED RHWC
9091 035422 002272 RHWC ;SAVED REGISTER TO CHANGE
9092 035424 000000 0 ;DATA
9093 035426 004037 041270 JSR RC,0#FILLRE ;MOV REINT0+266.*2 INTO SAVED RHSA
9094 035432 002274 RHSA ;SAVED REGISTER TO CHANGE
9095 035434 004560 REINT0+266.*2 ;DATA

```

```

9096 035436 004037 041270 JSR RO,0#FILLRE :MOV 3406 INTO SAVED RHDST
9097 035442 002304 RHDST :SAVED REGISTER TO CHANGE
9098 035444 003406 3406 :DATA
9099
9100 :*COMPARE REGISTERS BEFORE READ HEADER AND DATA
9101 :*WITH REGISTERS AFTER COMMAND
9102
9103
9104
9105 035446 004037 042354 JSR RO,0#COMREG :COMPARE SAVED REGISTERS WITH
9106 :PRESENT VALUE
9107 035452 004612 SAVERE :GOOD DATA SAVED IN 'SAVERE'
9108 035454 002354 WC :TEST DATA STARTING FROM 'RHWC'
9109 035456 000022 19. :19. REGISTERS TO BE COMPARED
9110 035460 035464 5$ :RETURN TO 5$ ON ERROR
9111 035462 035470 6$ :RETURN TO 6$ ON NO ERROR
9112
9113
9114 035464 104031 5$: ERROR 31 :READ HEADER AND DATA CAUSED
9115 035466 000207 RTS PC :IMPROPER REGISTER CHANGE
9116 :GOOD DATA GIVES WHAT SHOULD
9117 :BE THERE RECEIVED DATA GIVES WHAT WAS
9118 :RECEIVED DATA GIVES WHAT WAS
9119 :THERE AFTER COMMAND
9120
9121 :*NOW READ INTO BUFFER WILL BE CHECKED TO SEE
9122 :*THAT READ WAS GOOD
9123
9124
9125 035470 6$:
9126
9127 035470 004037 043404 JSR RO,0#COMPAR :COMPARE TWO BLOCKS OF MEMORY
9128 035474 002470 WRFROM :GOOD DATA STARTS FROM WRFROM
9129 035476 003534 REINTO :TEST DATA STARTS FROM REINTO
9130 035500 000412 266. :266. WORDS TO BE COMPARED
9131 035502 035506 7$ :RETURN TO 7$ ON ERROR
9132 035504 035512 10$ :RETURN TO 10$ ON NO ERROR
9133
9134
9135 035506 104032 7$: ERROR 32 :WRITE HEADER AND DATA
9136 035510 000207 RTS PC :FOLLOWED BY A READ HEADER
9137 :AND DATA GAVE A READ ERROR
9138 :ERROR MAY BE IN READ OR WRITE
9139
9140 :*A WRITE, READ HAS BEEN SUCCESSFULLY COMPLETED
9141 :*NOW A WRITE CHECK HEADER AND DATA WILL BE GIVEN
9142 :*FILL THE WRITE CHECK HEADER AND DATA
9143
9144 035512 10$:
9145
9146 035512 004737 041366 JSR PC,0#CLDISK :SET R1-RHCS1, R2-RHCS2
9147 :R3-RHDS1, R4-RHER1
9148 :GIVE RH-11 INITIALIZE
9149 :SETUP UNIT NUMBER

```

```

9150
9151 035516 004037 043340 JSR R0,0#RUN ;SETUP TO RUN FOR DATA COMMAND
9152 035522 000005 ;CYLINDER 5
9153 035524 004 ;SECTOR 4
9154 035525 007 ;TRACK 7
9155 035526 177366 ;WORD COUNT (DATA) = 262. *
9156 ;4 HEADER WORDS
9157 035530 002470 WRFROM ;BUS ADDRESS
9158 ;STARTING ADDRESS OF DATA
9159 ;BUFFER = WRFROM
9160 035532 000000 0 ;DO NOT INHIBIT BUS ADDRESS INCREMENT
9161 035534 014000 ECI!FMT2 ;16 BITS PER WORD FORMAT
9162 ;INHIBIT ECC CORRECTION
9163 ;DO NOT INHIBIT HEADER COMPARE
9164 035536 002440 WRCHDT ;GET READY TO DO A WRCHDT
9165 ;WRITE CHECK HEADER AND DATA WITH 52 IN RHCS1
9166
9167
9168 ;*SAVE REGISTERS FOR COMPARISON AFTER WRITE CHECK
9169
9170 035540 ST25:
9171 035540 004037 041534 JSR R0,0#SAVER ;SAVE REGISTERS
9172 035544 002272 RHWC ;RHWC IS THE FIRST REGISTER SAVED
9173 035546 004512 SAVERE ;STARTING ADDRESS OF WHERE
9174 ;THE REGISTERS ARE SAVED
9175 035550 000022 18. ;NUMBER OF REGISTERS
9176 ;SAVED = 18.
9177
9178 035552 004737 041446 JSR PC,0#CHECKT ;CHECK DVA,RDY,MOL,DPR,DRY,VV = 1
9179 ;AND THAT NO STATUS BITS ARE STUCK = 1
9180 035556 104400 066402 TYPE ,CPHALT ;CANNOT CONTINUE TESTING IF ANY OF
9181 ;THE FIRST SET OF BITS DON'T = 1
9182 035562 000000 HALT ;STOP
9183
9184 035564 013777 004606 144474 MOV 0#RPHVEC,0RPVEC ;SET RPO4 VECTOR ADDRESS
9185 ;TO 'TIME1' IF P-CLOCK IS PRESENT
9186 ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
9187 ;'TIME' WILL ONLY SAVE
9188 ;CURRENT CYLINDER ADDRESS
9189 ;AND LOOK AHEAD REGISTERS
9190
9191
9192 035572 013746 002440 MOV 0#WRCHDT,-(SP) ;GET READY TO MOVE COMMAND
9193 035576 052716 000101 BIS 00!IE,(SP) ;GET READY TO SET 'GO' AND
9194 ;ENABLE INTERRUPT
9195 035602 012677 144472 MOV ,(SP)+,0RHCS1 ;GO WITH
9196 ;52 IN RHCS1 FOR WRITE CHECK HEADER AND DATA
9197 ;WITH INTERRUPT ENABLED
9198 035606 011100 MOV 0R1,R0 ;SAVE RHCS1 DURING ABOVE OPERATION
9199 035610 011305 MOV 0R3,R5 ;SAVE RHDS1 DURING ABOVE OPERATION
9200
9201
9202 035612 104412 WAT ;WAIT FOR RDY BIT TO SET
9203 035614 002300 RHCS1 ;WAIT FOR RHCS1 REGISTER

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9204 035616 000200 RDY ;WAIT FOR RDY BIT IN RHCS1 REGISTER
9205 035620 001732 986. ;ALLOW 9860 MICRO SECONDS
9206 035622 001502 834. ;RDY MUST SET BETWEEN
9207 ; 1520 AND 18200 MICRO SECONDS
9208
9209 ;*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN
9210 ;*RD AND R5 IMMEDIATELY AFTER GO
9211
9212 035624 013746 002440 MOV @#WRCHDT, -(SP) ;SAVE COMMAND
9213 035630 052716 004101 BIS #IE!DVA!GO, (SP) ;INCLUDE IE!DVA!GO
9214 035634 011637 001124 MOV (SP), @#SGDDAT ;SAVE FOR PRINTOUT
9215 035640 022600 CMP (SP)+, R0 ;DURING ABOVE OPERATION ONLY IE!DVA!GO
9216 ; AND COMMAND SHOULD BE SET
9217 035642 001405 BEQ 64$ ;BRANCH IF GOOD
9218 035644 010037 001126 MOV R0, @#SBDDAT ;BAD DATA
9219 035650 010137 004600 MOV R1, @#REGADR ;FAILING REGISTER RHCS1
9220 035654 104021 ERROR 21 ;DURING ABOVE OPERATION ONLY
9221 ; COMMAND AND IE!DVA!GO SHOULD BE SET
9222 035656 012746 010500 64$: MOV #MOL!DPR!VV, -(SP) ;SAVE BITS SET DURING OPERATION IN RHDS1
9223 035662 011637 001124 MOV (SP), @#SGDDAT ;SAVE FOR PRINTOUT
9224 035666 022605 CMP (SP)+, R5 ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
9225 ; SHOULD BE SET
9226 035670 001405 BEQ 66$ ;BRANCH IF GOOD
9227 035672 010537 001126 MOV R5, @#SBDDAT ;BAD DATA
9228 035676 010337 004600 MOV R3, @#REGADR ;FAILING REGISTER RHDS1
9229 035702 104063 ERROR 63 ;DURING ABOVE OPERATION ONLY
9230 ; MOL!DPR!VV SHOULD BE SET
9231 035704 66$:
9232
9233 ;*CHANGE SAVED REGISTERS TO EXPECTED VALUES
9234
9235 035704 004037 041270 JSR R0, @#FILLRE ;MOV 0 INTO SAVED RHWC
9236 035710 002272 RHWC ;SAVED REGISTER TO CHANGE
9237 035712 000000 0 ;DATA
9238 035714 004037 041270 JSR R0, @#FILLRE ;MOV WRFROM+(266.*2) INTO SAVED RHBA
9239 035720 002274 RHBA ;SAVED REGISTER TO CHANGE
9240 035722 003514 WRFROM+(266.*2) ;DATA
9241 035724 004037 041270 JSR R0, @#FILLRE ;MOV 3406 INTO SAVED RHDST
9242 035730 002304 RHDST ;SAVED REGISTER TO CHANGE
9243 035732 003406 3406 ;DATA
9244
9245 ;*COMPARE REGISTERS BEFORE WRITE CHECK HEADER AND DATA
9246 ;*WITH REGISTERS AFTER COMMAND
9247
9248
9249 035734 004037 042354 JSR R0, @#COMREG ;COMPARE SAVED REGISTERS WITH
9250 ;PRESENT VALUE
9251 035740 004612 SAVERE ;GOOD DATA SAVED IN 'SAVERE'
9252 035742 002354 WC ;TEST DATA STARTING FROM 'RHWC'
9253 035744 000022 18. ;18. REGISTERS TO BE COMPARED
9254 035746 035752 8$ ;RETURN TO 8$ ON ERROR
9255 035750 035756 9$ ;RETURN TO 9$ ON NO ERROR
9256
9257 035752 104040 8$: ERROR 40 ;WRITE CHECK CAUSED

```

9258 035754 000207  
9259  
9260  
9261  
9262  
9263  
9264  
9265  
9266 035756

RIS PC

:AN IMPROPER REGISTER  
:CHANGE  
:GOOD DATA GIVES WHAT  
:SHOULD BE THERE  
:RECEIVED DATA GIVES WHAT  
:WAS THERE AFTER COMMAND

93:

9267  
9268  
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9317  
9318  
9319  
9320

035756 000004  
035760 012706 001000  
035764 012737 000046 004604  
035772 004737 041366  
035776 004737 041446  
036002 104400 066402  
036006 000000  
036010 004037 041336  
036014 000005  
036016 013777 004606 144242  
036024 013746 002452  
036030 052716 000101  
036034 012677 144240

```
*****  
*TEST 46 WRITE CHECK DATA  
* THE DATA FOR THIS TEST IS WRITTEN ON DISK BY PREVIOUS TEST  
* WRITE CHECK DATA CYLINDER 5, FORMAT 16 BITS PER WORDS  
* TRACK 7, SECTOR 4, KEYS 0, NUMBER OF WORDS 258  
* CONSISTING OF  
* 10 WORDS OF 12344 (6 BITS FOR CYL, 5 FOR TRACK, 5 FOR SECTOR,  
* 10 WORDS OF 177777  
* 10 WORDS OF 0  
* 10 WORDS OF 052525  
* 10 WORD OF 125252  
* 16 WORDS OF LEFT ROTATING ZERO (EG177776,177775)  
* 16 WORDS OF LEFT ROTATING ONE (EG 1.2.4.10)  
* 174 WORDS OF 377  
* 2 WORDS OF 12345
```

```
* FIRST THE ABOVE DATA IS FILLED INTO WRITE FROM BUFFER  
* THEN THE ABOVE WRITE CHECK DATA IS GIVEN
```

```
*****
```

```
TST46: SCOPE  
MOV #STACK,SP ;RESET STACK  
MOV #46,@#TSTNM ;SAVE TEST NUMBER  
JSR PC,@#CLDISK ;SET R1-RHCS1, R2-RHCS2  
;R3-RHDS1, R4-RHER1  
;GIVE RH-11 INITIALIZE  
;SETUP UNIT NUMBER  
JSR PC,@#CHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1  
;AND THAT NO STATUS BITS ARE STUCK = 1  
TYPE ,CPHALT ;CANNOT CONTINUE TESTING IF ANY OF  
;THE FIRST SET OF BITS DON'T = 1  
HALT ;STOP  
;*GET HEADS TO CYLINDER 5  
JSR RD,@#SEEKCY ;SEEK FOR  
5 ;CYLINDER 5  
MOV @#RP4VEC,@RPVEC ;SET RPO4 VECTOR ADDRESS  
;TO 'TIME1' IF P-CLOCK IS PRESENT  
;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT  
;'TIME' WILL ONLY SAVE  
;CURRENT CYLINDER ADDRESS  
;AND LOOK AHEAD REGISTERS  
MOV @#SEECOM,-(SP) ;GET READY TO MOVE COMMAND  
BIS #GO!IE,(SP) ;GET READY TO SET 'GO' AND  
;ENABLE INTERRUPT  
MOV (SP)+,@RHCS1 ;GO WITH  
;4 IN RHCS1 FOR SEEK
```



```

9321                                     ;WITH INTERRUPT ENABLED
9322
9323
9324 036040 104412 WAT ;WAIT FOR DRY BIT TO SET
9325 036042 002322 RHDS1 ;WAIT FOR RHDS1 REGISTER
9326 036044 000200 DRY ;WAIT FOR DRY BIT IN RHDS1 REGISTER
9327 036046 004704 2500. ;ALLOW 25000 MICRO SECONDS
9328 036050 004704 2500. ;DRY MUST SET BETWEEN
9329                                     ;CO AND 50000 MICRO SECONDS
9330
9331
9332 036052 004737 041366 JSR PC,0#CLDISK ;SET R1-RHCS1, R2-RHCS2
9333                                     ;R3-RHDS1, R4-RHER1
9334                                     ;GIVE RH-11 INITIALIZE
9335                                     ;SETUP UNIT NUMBER
9336
9337                                     ;*10 WORDS OF EACH 12344,17777,0.52525,125252
9338
9339 036056 004037 041236 JSR RO,0#CLAREA ;CLEAR 10. WORDS, FROM WRFROM
9340 036062 002470 WRFROM ;STARTING FROM WRFROM
9341 036064 000012 10. ;10. WORDS
9342 036066 012344 <5*2000>!<7*40>!4 ;FILL WITH <5*2000>!<7*40>!4
9343
9344 036070 004037 041236 JSR RO,0#CLAREA ;CLEAR 10. WORDS, FROM WRFROM+<10.*2>
9345 036074 002514 WRFROM+<10.*2> ;STARTING FROM WRFROM+<10.*2>
9346 036076 000012 10. ;10. WORDS
9347 036100 177777 -1 ;FILL WITH -1
9348
9349 036102 004037 041236 JSR RO,0#CLAREA ;CLEAR 10. WORDS, FROM WRFROM+<20.*2>
9350 036106 002540 WRFROM+<20.*2> ;STARTING FROM WRFROM+<20.*2>
9351 036110 000012 10. ;10. WORDS
9352 036112 000000 0 ;FILL WITH 0
9353
9354 036114 004037 041236 JSR RO,0#CLAREA ;CLEAR 10. WORDS, FROM WRFROM+<30.*2>
9355 036120 002564 WRFROM+<30.*2> ;STARTING FROM WRFROM+<30.*2>
9356 036122 000012 10. ;10. WORDS
9357 036124 052525 52525 ;FILL WITH 52525
9358
9359 036126 004037 041236 JSR RO,0#CLAREA ;CLEAR 10. WORDS, FROM WRFROM+<40.*2>
9360 036132 002610 WRFROM+<40.*2> ;STARTING FROM WRFROM+<40.*2>
9361 036134 000012 10. ;10. WORDS
9362 036136 125252 125252 ;FILL WITH 125252
9363
9364
9365                                     ;*FILL LEFT ROTATING ZEROS FROM WRFROM+<50.*2>
9366
9367 036140 010146 MOV R1,-(SP) ;;PUSH R1 ON STACK
9368 036142 012700 177776 MOV #177776,R0 ;DATA
9369 036146 012705 000020 MOV #16,R5 ;COUNT
9370 036152 012701 002634 MOV #WRFROM+<50.*2>,R1 ;WHERE DATA GOES
9371 036156 000261 SEC
9372 036160 010021 15: MOV RO,(R1)+ ;STORE DATA
9373 036162 006100 ROL RO ;GET ZERO ONE BIT LEFT
9374 036164 005305 DEC R5 ;COUNT 16

```



```

9429 036276 000000 HALT ,STOP
9430
9431 035300 013777 004606 143760 MOV @#RP4VEC,@RPVEC ;SET RPO4 VECTOR ADDRESS
9432 ;TO 'TIME1' IF P-CLOCK IS PRESENT
9433 ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
9434 ;'TIME' WILL ONLY SAVE
9435 ;CURRENT CYLINDER ADDRESS
9436 ;AND LOOK AHEAD REGISTERS
9437
9438
9439 035306 013746 002436 MOV @#WRCHK,-(SP) ;GET READY TO MOVE COMMAND
9440 036312 052716 000101 BIS #GO!IE,(SP) ;GET READY TO SET 'GO' AND
9441 ;ENABLE INTERRUPT
9442 036316 012677 143756 MOV (SP)+,@RHCS1 ;GO WITH
9443 ;50 IN RHCS1 FOR WRITE CHECK DATA
9444 ;WITH INTERRUPT ENABLED
9445 036322 011100 MOV @R1,R0 ;SAVE RHCS1 DURING ABOVE OPERATION
9446 036324 011305 MOV @R3,R5 ;SAVE RHDS1 DURING ABOVE OPERATION
9447
9448
9449 036326 104412 WAT ;WAIT FOR RDY BIT TO SET
9450 036330 002300 RHCS1 ;WAIT FOR RHCS1 REGISTER
9451 036332 000200 RDY ;WAIT FOR RDY BIT IN RHCS1 REGISTER
9452 036334 001732 986. ;ALLOW 9860 MICRO SECONDS
9453 035336 001502 834. ;RDY MUST SET BETWEEN
9454 ;1520 AND 18200 MICRO SECONDS
9455
9456 ;*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN
9457 ;*R0 AND R5 IMMEDIATELY AFTER GO
9458
9459 036340 013746 002436 MOV @#WRCHK,-(SP) ;SAVE COMMAND
9460 036344 052716 004101 BIS #IE!DVA!GO,(SP) ;INCLUDE IE!DVA!GO
9461 036350 011637 001124 MOV (SP),@#$GDDAT ;SAVE FOR PRINTOUT
9462 036354 022600 CMP (SP)+,R0 ;DURING ABOVE OPERATION ONLY IE!DVA!GO
9463 ;AND COMMAND SHOULD BE SET
9464 036356 001405 BEQ 67$ ;BRANCH IF GOOD
9465 036360 010037 001126 MOV R0,@#$BDDAT ;BAD DATA
9466 036364 010137 004600 MOV R1,@#REGADR ;FAILING REGISTER RHCS1
9467 036370 104021 ERROR 21 ;DURING ABOVE OPERATION ONLY
9468 ;COMMAND AND IE!DVA!GO SHOULD BE SET
9469 036372 012746 010500 67$: MOV #MOL!DPR!VV,-(SP) ;SAVE BITS SET DURING OPERATION IN RHDS1
9470 036376 011637 001124 MOV (SP),@#$GDDAT ;SAVE FOR PRINTOUT
9471 036402 022605 CMP (SP)+,R5 ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
9472 ;SHOULD BE SET
9473 036404 001405 BEQ 69$ ;BRANCH IF GOOD
9474 036406 010537 001126 MOV R5,@#$BDDAT ;BAD DATA
9475 036412 010337 004600 MOV R3,@#REGADR ;FAILING REGISTER RHDS1
9476 036416 104063 ERROR 63 ;DURING ABOVE OPERATION ONLY
9477 ;MOL!DPR!VV SHOULD BE SET
9478 036420 69$:
9479
9480 ;*CHANGE SAVED REGISTERS TO EXPECTED VALUES
9481
9482 036420 004037 041270 JSR R0,@#FILLRE ;MOV 0 INTO SAVED RHWC

```







```

9622 036674 000016      10.      :10. WORDS
9623 036676 125252      125252   :FILL WITH 125252
9624
9625      :*FILL LEFT ROTATING ZEROS FROM WRFROM+(50.*2)
9626
9627
9628 036700 010146      MOV      R1, -(SP)      ;; PUSH R1 ON STACK
9629 036702 012700      MOV      #177776, R0    ;; DATA
9630 036706 012705      MOV      #16, R5        ;; COUNT
9631 036712 012701      MOV      #WRFROM+(50.*2), R1 ;; WHERE DATA GOES
9632 036716 000261      SEC
9633 036720 010021      18:     MOV      R0, (R1)+      ;; STORE DATA
9634 036722 006100      ROL      R0              ;; GET ZERO ONE BIT LEFT
9635 036724 005305      DEC      R5              ;; COUNT 16
9636 036726 001374      BNE      18              ;; BRANCH IF 16 NOT DONE
9637
9638      :*FILL LEFT ROTATING ONE INTO WRFROM+(65.*2)
9639
9640
9641 036730 000241      CLC
9642 036732 012700      28:     MOV      #1, R0
9643 036736 010021      MOV      R0, (R1)+
9644 036740 006300      RSL      R0
9645 036742 103375      BCC      28
9646 036744 012501      MOV      (SP)+, R1      ;; POP STACK INTO R1
9647
9648      :*FILL REST OF DATA
9649 036746 004037      JSR      R0, @CLAREA    ;; CLEAR 174. WORDS, FROM WRFROM+(92.*2)
9650 036752 002734      WRFROM+(92.*2)        ;; STARTING FROM WRFROM+(92.*2)
9651 036754 000256      174.
9652 036756 000377      377.
9653      :174. WORDS
9654      :FILL WITH 377
9655
9656 036760 004037      JSR      R0, @CLAREA    ;; CLEAR 2 WORDS, FROM WRFROM+(256.*2)
9657 036764 003470      WRFROM+(256.*2)      ;; STARTING FROM WRFROM+(256.*2)
9658 036766 000002      2
9659 036770 012345      <5*2000>!<7*40>!5   ;; FILL WITH <5*2000>!<7*40>!5
9660
9661      :*FILL THE WRITE CHECK HEADER AND DATA
9662
9663
9664 036772 004037      JSR      R0, @R1N      ;; SETUP TO RUN FOR DATA COMMAND
9665 036776 000005      5
9666 037000 004      ;; CYLINDER 5
9667 037001 007      ;; SECTOR 4
9668 037002 177400      7
9669 037004 002470      -256.
9670      WRFROM
9671      ;; TRACK 7
9672      ;; WORD COUNT = 256.
9673      ;; BUS ADDRESS
9674      ;; STARTING ADDRESS OF DATA
9675      ;; BUFFER = WRFROM
9676 037006 000000      0
9677 037010 014000      ;; DO NOT INHIBIT BUS ADDRESS INCREMENT
9678      ECI!FMT22
9679      ;; INHIBIT ECC CORRECTION
9680      ;; DO NOT INHIBIT HEADER COMPARE
9681 037012 002436      WRCHK
9682      ;; GET READY TO DO A WRCHK
9683      :WRITE CHECK DATA WITH 50 IN RHOS1

```

```

9678
9679 037014 052777 002000 143256 BIS #PSEL,DRHCS1 ;SET PORT B
9680 ;THAT IS UNIBUS B
9681
9682 ;*SAVE REGISTERS FOR COMPARISON AFTER WRITE CHECK
9683 JSR RO,DRSAVER ;SAVE REGISTERS
9684 037022 004037 041534 ;RHWC IS THE FIRST REGISTER SAVED
9685 037026 002272 ;STARTING ADDRESS OF WHERE
9686 037030 004512 SAVERE ;THE REGISTERS ARE SAVED
9687 ;NUMBER OF REGISTERS
9688 037032 000022 18. ;SAVED = 18.
9689
9690 037034 004737 041446 JSR PC,DRCHECKT ;CHECK DVA, RDY, MOL, DPR, DRY, VV = 1
9691 037040 104400 066402 TYPE ,CPHALT ;AND THAT NO STATUS BITS ARE STUCK = :
9692 ;CANNOT CONTINUE TESTING IF ANY OF
9693 037044 000000 HALT ;THE FIRST SET OF BITS DON'T = 1
9694 ;STOP
9695 037046 013777 004606 143212 MOV DRP4VEC,DRPVEC ;SET RPO4 VECTOR ADDRESS
9696 ;TO 'TIME1' IF P-CLOCK IS PRESENT
9697 ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
9698 ;'TIME' WILL ONLY SAVE
9699 ;CURRENT CYLINDER ADDRESS
9700 ;AND LOOK AHEAD REGISTERS
9701
9702 ;*SET PORT SELECT
9703
9704 037054 013746 002436 MOV DRWRCHEK,-(SP) ;GET READY TO MOVE COMMAND
9705 037050 052716 002101 BIS #GO!IE!PSEL,(SP) ;GET READY TO SET 'GO' AND
9706 ;ENABLE INTERRUPT
9707 037064 012677 143210 MOV (SP)+,DRHCS1 ;GO WITH
9708 ;50 IN RHCS1 FOR WRITE CHECK DATA
9709 ;WITH INTERRUPT ENABLED
9710 037070 011100 MOV DR1,RO ;SAVE RHCS1 DURING ABOVE OPERATION
9711 037072 011305 MOV DR3,RS ;SAVE RHDS1 DURING ABOVE OPERATION
9712
9713
9714 037074 104412 WAIT ;WAIT FOR RDY BIT TO SET
9715 037076 002300 RHCS1 ;WAIT FOR RHCS1 REGISTER
9716 037100 000200 RDY ;WAIT FOR RDY BIT IN RHCS1 REGISTER
9717 037102 001732 986. ;ALLOW 9860 MICRO SECONDS
9718 037104 001502 934. ;RDY MUST SET BETWEEN
9719 ;1520 AND 18200 MICRO SECONDS
9720
9721 ;*COMPARE CONTENTS OF RHCS1 AND RHDS1 ALREADY SAVED IN
9722 ;*RO AND RS IMMEDIATELY AFTER GO
9723
9724 037106 013746 002436 MOV DRWRCHEK,-(SP) ;SAVE COMMAND
9725 037112 052716 006101 BIS #IE!DVA!PSEL!GO,(SP) ;INCLUDE IE!DVA!PSEL!GO
9726 037116 011637 001124 MOV (SP),DRSGDDAT ;SAVE FOR PRINTOUT
9727 037122 022600 CMP (SP)+,RO ;DURING ABOVE OPERATION ONLY IE!DVA!PSEL!GO
9728 ;AND COMMAND SHOULD BE SET
9729 037124 001406 BEQ 675 ;BRANCH IF GOOD
    
```



DJRJI.P11 T47 WRITE CHECK DATA USING UNIBUS B

```

9730 037126 010037 001125      MOV      RO,0#SBDDAT      ;BAD DATA
9731 037132 010137 004600      MOV      R1,0#REGADR     ;FAILING REGISTER RHCS1
9732 037136 104021                ERROR 21                 ;DURING ABOVE OPERATION ONLY
9733                                ;COMMAND AND IE!DVA!PSEL!GC SHOULD BE SET
9734 037140 012746 010500      67$: MOV      #MOL!DPR!VV, -(SP) ;SAVE BITS SET DURING OPERATION IN RHDS1
9735 037144 011637 001124      MOV      (SP),0#SGDDAT   ;SAVE FOR PRINTOUT
9736 037150 022605      CMP      (SP)+,R5        ;DURING ABOVE OPERATION ONLY MOL!DPR!VV
9737                                ;SHOULD BE SET
9738 037152 001405      BEQ      69$            ;BRANCH IF GOOD
9739 037154 010537 001126      MOV      R5,0#SBDDAT     ;BAD DATA
9740 037160 010337 004600      MOV      R3,0#REGADR     ;FAILING REGISTER RHDS1
9741 037164 104053                ERROR 63                 ;DURING ABOVE OPERATION ONLY
9742                                ;MOL!DPR!VV SHOULD BE SET
9743 037166                                69$:
9744
9745                                ;*CHANGE SAVED REGISTERS TO EXPECTED VALUES
9746
9747 037166 004037 041270      JSR      RO,0#FILLRE     ;MOV 0 INTO SAVED RHWC
9748 037172 002272      RHWC                                ;SAVED REGISTER TO CHANGE
9749 037174 000000      0                                ;DATA
9750 037176 004037 041270      JSR      RO,0#FILLRE     ;MOV WRFROM+(256.*2) INTO SAVED RHBA
9751 037202 002274      RHBA                                ;SAVED REGISTER TO CHANGE
9752 037204 003470      WRFROM+(256.*2) ;DATA
9753 037206 004037 041270      JSR      RO,0#FILLRE     ;MOV 3405 INTO SAVED RHDST
9754 037212 002304      RHDST                                ;SAVED REGISTER TO CHANGE
9755 037214 003405      3405                                ;DATA
9756
9757                                ;*COMPARE REGISTERS BEFORE WRITE CHECK HEADER AND DATA
9758                                ;*WITH REGISTER AFTER COMMAND
9759
9760 037216 004037 042354      JSR      RO,0#COMREG     ;COMPARE SAVED REGISTERS WITH
9761                                ;PRESENT VALUE
9762 037222 004612      SAVERE                                ;GOOD DATA SAVED IN 'SAVERE'
9763 037224 002354      WC                                ;TEST DATA STARTING FROM 'RHWC'
9764 037226 000022      18.                                ;18. REGISTERS TO BE COMPARED
9765 037230 037234      8$                                ;RETURN TO 8$ ON ERROR
9766 037232 037240      9$                                ;RETURN TO 9$ ON NO ERROR
9767
9768 037234 104076      8$: ERROR 76             ;WHILE USING UNIBUS B
9769                                ;WRITE CHECK CAUSED
9770 037236 000207      RTS      PC              ;AN IMPROPER REGISTER
9771                                ;CHANGE
9772                                ;GOOD DATA GIVES WHAT
9773                                ;SHOULD BE THERE
9774                                ;RECEIVED DATA GIVES WHAT
9775                                ;WAS THERE AFTER COMMANDS
9776
9777 037240      9$:
9778
9779
9780
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9782

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037240 000004  
  
037242 005737 004724  
037246 001007  
037250 005737 000042  
037254 001004  
037256 005737 001100  
037262 001001  
037264 000402  
  
037266  
037266 000137 040464  
037272  
037272 012706 001000  
037276 012737 000050 004604  
  
037304 004737 041366  
  
  
  
037310 004037 041236

```
*****  
*TEST 50 WRITE PROTECT OPERATION  
* IF STARTING ADDRESS 220 IS USED THIS TEST WILL NOT BE PERFORMED  
*  
* IF THE PROGRAM WORKS UNDER ACT-11 MONITOR  
* THEN THIS TEST IS NOT PERFORMED  
*  
* IF NO ACT-11 MONITOR IS PRESENT  
* THEN THIS TEST IS PERFORMED ONLY ON THE FIRST PASS  
* ON SUBSEQUENT PASSES THIS TEST IS NOT DONE  
*  
* WRITE FROM BUFFER IS FILLED WITH ALL ONES AND  
* SECTOR 0, TRACK 0, CYLINDER 0 IS FILLED WITH  
* ALL ONES  
* ALL REGISTERS ARE SAVED THEN WRITE LOCK BUTTON IS  
* PRESSED AND ALL REGISTERS ARE CHECKED.  
* WRITE FROM BUFFER IS FILLED WITH 377 AND A WRITE IS  
* ATTEMPTED TO SECTOR 0, TRACK 0, CYLINDER 0 70. WORDS  
* ALL REGISTERS ARE CHECKED  
* THE SAME SECTOR IS READ AND DATA COMPARED TO SEE  
* THAT NOTHING GOT DESTROYED (READ DATA SHOULD BE ALL  
* ONES AND NOT 377)  
* THEN WRITE LOCK BUTTON IS PRESSED TO UNLOCK  
* WRITE LOCKS AND ALL REGISTERS ARE COMPARED
```

```
*****  
TST50: SCOPE
```

:\*THIS CODE CHECKS TO SEE IF MANUAL INTERVENTION TESTS ARE OK

```
TST 2#NOPUSH :IS THIS A 220 START ^  
BNE 1$ :SKIP THIS TEST IF 50  
TST 2#42 :MONITOR (ACT 11) RETURN ADDRESS ?  
BNE 1$ :SKIP THIS TEST  
TST 2#SPASS :FIRST PASS ?  
BNE 1$ :SKIP THIS TEST IF NOT  
BR 2$ :CONTINUE WITH THIS TEST
```

```
1$: JMP TST51 ; JUMP TO NEXT TEST -----  
2$:
```

```
MOV #STACK, SP :RESET STACK  
MOV #50, 2#TSTNM :SAVE TEST NUMBER  
JSR PC, 2#CLDISK :SET R1-RHCS1, R2-RHCS2  
:R3-RHDS1, R4-RHER1  
:GIVE RH-11 INITIALIZE  
:SETUP UNIT NUMBER
```

```
:*FILL SECTOR 0, TRACK 0, CYL 0 WITH ONES  
:*FILL WRITE FROM BUFFER  
JSR R0, 2#CLAREA :CLEAR 256. WORDS. FROM WRFROM
```

```

9837 037314 002470 WRFROM :STARTING FROM WRFROM
9838 037316 000400 256. :256. WORDS
9839 037320 177777 -1 :FILL WITH -1
9840
9841
9842 :*FILL WRITE DATA COMMAND
9843
9844 037322 004037 043343 JSR RC,0*RUN :SETUP TO RUN FOR DATA COMMAND
9845 037326 000000 C :CYLINDER 0
9846 037330 000 .BYTE 0 :SECTOR 0
9847 037331 000 .BYTE 0 :TRACK 0
9848 037332 177400 -256. :WORD COUNT = 256.
9849 037334 002470 WRFROM :BUS ADDRESS
9850 :STARTING ADDRESS OF DATA
9851 :BUFFER = WRFROM
9852 037336 000000 0 :DO NOT INHIBIT BUS ADDRESS INCREMENT
9853 037340 010000 FMT22 :16 BITS PER WORD FORMAT
9854 :DO NOT INHIBIT ECC CORRECTION
9855 :DO NOT INHIBIT HEADER COMPARE
9856 037342 002442 WRIDAT :GET READY TO DO A WRIDAT
9857 :WRITE DATA WITH 60 IN RHCS1
9858
9859
9860 037344 004727 041446 JSR PC,0*CHECKT :CHECK DVA, RDY, MOL, DPR, DRY, VV = 1
9861 :AND THAT NO STATUS BITS ARE STUCK = 1
9862 037350 104400 066402 TYPE ,CPHALT :CANNOT CONTINUE TESTING IF ANY OF
9863 :THE FIRST SET OF BITS DON'T = 1
9864 037354 000000 HALT :STOP
9865
9866 037356 013777 004606 142702 MOV 0*RP4VEC,0RPVEC :SET RPO4 VECTOR ADDRESS
9867 :TO 'TIME1' IF P-CLOCK IS PRESENT
9868 :OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
9869 :'TIME' WILL ONLY SAVE
9870 :CURRENT CYLINDER ADDRESS
9871 :AND LOOK AHEAD REGISTERS
9872
9873
9874 037364 013746 002442 MOV 0*WRIDAT,-(SP) :GET READY TO MOVE COMMAND
9875 037370 052716 000101 BIS *GO!IE,(SP) :GET READY TO SET 'GO' AND
9876 :ENABLE INTERRUPT
9877 037374 012677 142700 MOV (SP)+,0RHCS1 :GO WITH
9878 :60 IN RHCS1 FOR WRITE DATA
9879 :WITH INTERRUPT ENABLED
9880
9881 :*TIME IS NOT CRITICAL
9882
9883 037400 104412 WAT :WAIT FOR RDY BIT TO SET
9884 037402 002300 RHCS1 :WAIT FOR RHCS1 REGISTER
9885 037404 000200 RDY :WAIT FOR RDY BIT IN RHCS1 REGISTER
9886 037406 004704 2500. :ALLOW 25000 MICRO SECONDS
9887 037410 004704 2500. :RDY MUST SET BETWEEN
9888 :00 AND 50000 MICRO SECONDS
9889
9890 :*SAVE REGISTERS FOR COMPARISON AFTER WRITE PROTECT
9891
9892

```

```

9991                                     ;*BUTTON HAS BEEN HIT
9992
9993 037412 004037 041534      JSR      RO,0#SAVER      ;SAVE REGISTERS
9994 037416 002272              RHWC              ;RHWC IS THE FIRST REGISTER SAVED
9995 037420 004612              SAVERE            ;STARTING ADDRESS OF WHERE
9996                                     ;THE REGISTERS ARE SAVED
9997 037422 000022              18.              ;NUMBER OF REGISTERS
9998                                     ;SAVED = 18.
9999
9900 037424 104400 037432      TYPE      69$      ;;TYPE ASCIZ STRING
9901 037430 000407              BR      67$      ;;GET OVER THE ASCIZ
9902                                     ;:69$:
9903                                     ;67$:
9903 037450                      .ASCIZ <15><12>/ON DRIVE /
9904 037450 013746 004716      MOV      0#UNIT,-(SP) ;GET UNIT UNDER TEST
9905 037454 104404              TYPDS
9906 037456 104400 037464      TYPE      70$      ;;TYPE ASCIZ STRING
9907 037462 000442              BR      69$      ;;GET OVER THE ASCIZ
9908                                     ;:70$:
9909                                     ;69$:
9909 037570                      .ASCIZ <15><12>/PUSH WRITE PROTECT BUTTON TO LOCK OUT WRITES THEN HIT CONTINUE/
9910 037570 000000              HALT
9911                                     ;*THE ONLY REGISTER THAT SHOULD CHANGE IS RHDS1 - BIT #11
9912                                     ;*-WRL
9913
9914 037572 004037 042246      JSR      RO,0#CHREG   ;CHANGE BITS IN SAVED REGISTER
9915 037576 002322              RHDS1           ;CHANGE RHDS1 REGISTER
9916
9917 037600 000001              1               ;1 BIT/BITS TO BE CHANGED
9918 037602 000001              1               ;NEW VALUE OF WRL IS 1
9919 037604 004000              WRL            ;CHANGE WRL BIT
9920
9921                                     ;*COMPARE ALL REGISTERS BEFORE WRITE WAS LOCKED
9922                                     ;*OUT WITH REGISTER VALUES AFTER WRITE WAS LOCKED OUT
9923
9924 037606 004037 042354      JSR      RO,0#COMREG  ;COMPARE SAVED REGISTERS WITH
9925                                     ;PRESENT VALUE
9926 037612 004612              SAVERE            ;GOOD DATA SAVED IN 'SAVERE'
9927 037614 002354              WC              ;TEST DATA STARTING FROM 'RHWC'
9928 037616 000022              18.              ;18. REGISTERS TO BE COMPARED
9929 037620 037624              3$              ;RETURN TO 3$ ON ERROR
9930 037622 037630              4$              ;RETURN TO 4$ ON NO ERROR
9931
9932 037624 104041 3$:          ERROR  41          ;LOCKING OUT WRITE BY
9933 037626 000207              RTS      PC       ;WRITE LOCK BUTTON CAUSED
9934                                     ;IMPROPER REGISTER CHANGE
9935                                     ;GOOD DATA GIVES WHAT SHOULD
9936                                     ;BE THERE
9937                                     ;RECEIVED DATA GIVES WHAT
9938                                     ;WAS THERE AFTER WRITE
9939                                     ;WAS LOCKED OUT BY BUTTON
9940
9941                                     ;*NOW A WRITE WILL BE ATTEMPTED WITH WRITE LOCKED
9942                                     ;*OUT BY BUTTON
9943                                     ;*FILL WRITE FROM BUFFER WITH 377
9944
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9999
10000 037732 104412 WAT ;WAIT FOR RDY BIT TO SET
10001 037734 002300 RHCS1 ;WAIT FOR RHCS1 REGISTER
10002 037736 000200 RDY ;WAIT FOR RDY BIT IN RHCS1 REGISTER
10003 037740 004704 2500. ;ALLOW 25000 MICRO SECONDS
10004 037742 004704 2500. ;RDY MUST SET BETWEEN
;00 AND 50000 MICRO SECONDS
10005
10006
10007 ;*CHANGE SAVED REGISTERS TO EXPECTED VALUE
10008
10009 037744 017737 142322 004612 MOV @RHWC,@#SAVERE ;RHWC IS UNPREDICTABLE
10010 037752 017737 142316 004614 MOV @RHBA,@#SAVERE+2 ;RHBA IS UNPREDICTABLE
10011 037750 017746 142312 MOV @RHCS2,-(SP) ;GET RHCS2
10012 037764 042716 177477 BIC #1<IR!OR>,(SP) ;KEEP IR AND OR
10013 037770 042737 000300 004616 BIC #IR!OR,@#SAVERE+4 ;CLEAR SAVED IR OR
10014 037776 052637 004616 BIS (SP)+,@#SAVERE+4 ;SET OR IR AS REQUIRED
10015
10016 040002 004037 042246 JSR RO,@#CHREG ;CHANGE BITS IN SAVED REGISTER
10017 040006 002300 RHCS1 ;CHANGE RHCS1 REGISTER
10018
10019 040010 000002 2 ;2 BIT/BITS TO BE CHANGED
10020 040012 000001 1 ;NEW VALUE OF SC IS 1
10021 040014 100000 SC ;CHANGE SC BIT
10022 040016 000001 1 ;NEW VALUE OF TRE IS 1
10023 040020 040000 TRE ;CHANGE TRE BIT
10024 040022 004037 041270 JSR RO,@#FILLRE ;MOV 1 INTO SAVED RHDST
10025 040026 002304 RHDST ;SAVED REGISTER TO CHANGE
10026 040030 000001 1 ;DATA
10027
10028 040032 004037 042246 JSR RO,@#CHREG ;CHANGE BITS IN SAVED REGISTER
10029 040036 002302 RHER1 ;CHANGE RHER1 REGISTER
10030
10031 040040 000001 1 ;1 BIT/BITS TO BE CHANGED
10032 040042 000001 1 ;NEW VALUE OF WLE IS 1
10033 040044 004000 WLE ;CHANGE WLE BIT
10034
10035 040046 004037 042246 JSR RO,@#CHREG ;CHANGE BITS IN SAVED REGISTER
10036 040052 002322 RHDS1 ;CHANGE RHDS1 REGISTER
10037
10038 040054 000002 2 ;2 BIT/BITS TO BE CHANGED
10039 040056 000001 1 ;NEW VALUE OF ATA IS 1
10040 040060 100000 ATA ;CHANGE ATA BIT
10041 040062 000001 1 ;NEW VALUE OF ERR IS 1
10042 040064 040000 ERR ;CHANGE ERR BIT
10043
10044 040066 053737 004740 004636 BIS @#ATTENT,@#SAVERE+24 ;SET APPROPRIATE 'ATA' BITS
10045 ;FOR WORKING DRIVE IN
10046 ;SAVED RHAS LOACTION
10047
10048 ;*COMPARE REGISTERS BEFORE WRITE WAS ATTEMPTED
10049 ;*WITH REGISTERS AFTER ATTEMPT
10050
10051
10052 040074 004037 042354 JSR RO,@#COMREG ;COMPARE SAVED REGISTERS WITH

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10053                                     ;PRESENT VALUE
10054 040100 004612                     ;GOOD DATA SAVED IN 'SAVERE'
10055 040102 002354                     ;TEST DATA STARTING FROM 'RHWC'
10056 040104 000022                     ;18. REGISTERS TO BE COMPARED
10057 040106 040112                     ;RETURN TO 5$ ON ERROR
10058 040110 040116                     ;RETURN TO 6$ ON NO ERROR
10059
10060 040112 104042                     5$: ERROR 42
10061 040114 000207                     RTS PC
10062
10063                                     ;ATTEMPTING TO WRITE WITH
10064                                     ;WRITE LOCKED OUT
10065                                     ;CAUSED IMPROPER REGISTER
10066                                     ;CHANGE
10067                                     ;GOOD DATA GIVES WHAT SHOULD
10068                                     ;BE THERE
10069                                     ;RECEIVED DATA GIVES WHAT WAS
10070                                     ;THERE AFTER ATTEMPT
10071
10072                                     ;*NOW A READ WILL BE DONE TO DETERMIN THAT
10073                                     ;*READS CAN BE DONE WITH WRITE LOCKED OUT AND
10074                                     ;*THAT NO DATA ON DISK GOT CHANGED, BUT FIRST CLEAR ERROR
10075 040116                     6$:
10076
10077 040116 004737 041366               JSR PC, @#CLDISK ;SET R1-RHCS1, R2-RHCS2
10078                                     ;R3-RHDS1, R4-RHER1
10079                                     ;GIVE RH-11 INITIALIZE
10080                                     ;SETUP UNIT NUMBER
10081
10082 040122 004037 041236               ;*FILL READ INTO BUFFER WITH 0
10083 040126 003534               JSR RO, @#CLAREA ;CLEAR 256. WORDS, FROM REINTO
10084 040130 000400               REINTO ;STARTING FROM REINTO
10085 040132 000000               256. ;256. WORDS
10086                                     0 ;FILL WITH 0
10087
10088 040134 004037 041236               ;*FILL WRITE FROM BUFFER WITH EXPECTED DATA FROM READ
10089 040140 002470               JSR RO, @#CLAREA ;CLEAR 256. WORDS, FROM WRFROM
10090 040142 000400               WRFROM ;STARTING FROM WRFROM
10091 040144 177777               256. ;256. WORDS
10092                                     -1 ;FILL WITH -1
10093
10094                                     ;*FILL COMMAND
10095
10096 040146 004037 043340               JSR RO, @#RUN ;SETUP TO RUN FOR DATA COMMAND
10097 040152 000000               0 ;CYLINDER 0
10098 040154 000 ;SECTOR 0
10099 040155 000 ;TRACK 0
10100 040156 177400               -256. ;WORD COUNT = 256.
10101 040160 003534               REINTO ;BUS ADDRESS
10102                                     ;STARTING ADDRESS OF DATA
10103                                     ;BUFFER = REINTO
10104 040162 000000               0 ;DO NOT INHIBIT BUS ADDRESS INCREMENT
10105 040164 014000               ECI!FMT22 ;16 BITS PER WORD FORMAT
10106                                     ;INHIBIT ECC CORRECTION
    
```

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10107 ;DO NOT INHIBIT HEADER COMPARE
10108 040166 002446 READAT ;GET READY TO DO A READAT
10109 ;READ DATA WITH 70 IN RHCS1
10110
10111
10112 040170 013777 004606 142070 MOV @#RPO4VEC,@RPOVEC ;SET RPO4 VECTOR ADDRESS
10113 ;TO 'TIME1' IF P-CLOCK IS PRESENT
10114 ;OR TO 'TIME2' IF P-CLOCK IS NOT PRESENT
10115 ;'TIME' WILL ONLY SAVE
10116 ;CURRENT CYLINDER ADDRESS
10117 ;AND LOOK AHEAD REGISTERS
10118
10119
10120 040176 013746 002446 MOV @#READAT,-(SP) ;GET READY TO MOVE COMMAND
10121 040202 052716 000101 BIS @GO!IE,(SP) ;GET READY TO SET 'GO' AND
10122 ;ENABLE INTERRUPT
10123 040206 012677 142066 MOV (SP)+,@RHCS1 ;GO WITH
10124 ;70 IN RHCS1 FOR READ DATA
10125 ;WITH INTERRUPT ENABLED
10126
10127
10128 040212 104412 WAT ;WAIT FOR RDY BIT TO SET
10129 040214 002300 RHCS1 ;WAIT FOR RHCS1 REGISTER
10130 040216 000200 RDY ;WAIT FOR RDY BIT IN RHCS1 REGISTER
10131 040220 001614 908. ;ALLOW 9080 MICRO SECONDS
10132 040222 001507 839. ;RDY MUST SET BETWEEN
10133 ;690 AND 17470 MICRO SECONDS
10134
10135 ;*COMPARE READ DATA
10136
10137 040224 004037 043404 JSR RO,@#COMPAR ;COMPARE TWO BLOCKS OF MEMORY
10138 040230 002470 WRFROM ;GOOD DATA STARTS FROM WRFROM
10139 040232 003534 REINTO ;TEST DATA STARTS FROM REINTO
10140 040234 000400 256. ;256. WORDS TO BE COMPARED
10141 040236 040242 7$ ;RETURN TO 7$ ON ERROR
10142 040240 040244 8$ ;RETURN TO 8$ ON NO ERROR
10143
10144
10145 040242 104043 7$: ERROR 43 ;WRITING WITH WRITE
10146 ;LOCKED CHANGED DISK
10147 ;GOOD DATA GIVES WHAT WAS
10148 ;ON DISK BEFORE WRITE WITH
10149 ;WRITE LOCK WAS ATTEMPTED
10150
10151 ;RECEIVED DATA GIVES WHAT
10152 ;WAS READ BACK AFTER WRITE
10153 ;WITH WRITE LOCKED WAS ATTEMPTED
10154
10155 ;*SAVE REGISTERS FOR COMPARISON AFTER WRITE LOCK HAS BEEN
10156 ;*UNLOCKED
10157
10158 040244 8$: JSR RO,@#SAVER ;SAVE REGISTERS
10159 040244 004037 041534 RHWC ;RHWC IS THE FIRST REGISTER SAVED
10160 040250 002272

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00161 040252 004612 SAVERE ;STARTING ADDRESS OF WHERE
00162 040254 000022 18. ;THE REGISTERS ARE SAVED
00163 040256 ;NUMBER OF REGISTERS
00164 040258 ;SAVED = 18.
00165 040256 ST20:
00166 040256 104400 040264 TYPE 65$ ;:TYPE ASCIZ STRING
00167 040262 000407 BR 64$ ;:GET OVER THE ASCIZ
00168 040262 ;:55$: .ASCIZ <15><12>>ON DRIVE /
00169 040302 64$:
00170 040302 013746 004716 MOV 0#UNIT,-(SP) ;GET UNIT UNDER TEST
00171 040302 104404 TYPDS
00172 040306 104400 040316 TYPE 67$ ;:TYPE ASCIZ STRING
00173 040314 000440 BR 66$ ;:GET OVER THE ASCIZ
00174 040314 ;:67$: .ASCIZ <15><12>>PUSH WRITE PROTCT BUTTON TO UNLOCK WRITES THEN HIT CONTINUE/15
00175 040416 66$:
00176 040416 000000 HALT
00177
00178 ;*THE ONLY BIT THAT SHOULD CHANGE IS WRL-BIT #11 IN RHDS!
00179
00180 040420 004037 042246 JSR R0,0#CHREG ;CHANGE BITS IN SAVED REGISTER
00181 040424 002222 RHDS! ;CHANGE RHDS! REGISTER
00182
00183 040426 000001 1 ;1 BIT/BITS TO BE CHANGED
00184 040430 000000 0 ;NEW VALUE OF WRL IS 0
00185 040432 004000 WRL ;CHANGE WRL BIT
00186
00187 ;*COMPARE ALL REGISTERS BEFORE WRITE LOCK WAS UNLOCKED
00188 ;*WITH REGISTERS AFTER WRITE WAS UNLOCKED
00189
00190 040434 004037 042354 JSR R0,0#COMREG ;COMPARE SAVED REGISTERS WITH
00191 040440 004612 SAVERE ;PRESENT VALUE
00192 040442 002354 NO ;GOOD DATA SAVED IN 'SAVERE'
00193 040444 000022 18. ;TEST DATA STARTING FROM 'RHWC'
00194 040446 040452 99$ ;18. REGISTERS TO BE COMPARED
00195 040450 040456 10$ ;RETURN TO 99$ ON ERROR
00196 ;RETURN TO 10$ ON NO ERROR
00197
00198 040458 104044 99$: ERROR 44 ;UNLOCKING WRITES BY WRITE
00199 040454 000207 RTS PC ;LOCK BUTTON CAUSED AN ERROR
00200 ;GOOD DATA GIVES WHAT SHOULD
00201 ;BE THERE
00202 ;RECEIVED DATA GIVES WHAT WAS
00203 ;THERE AFTER WRITES WERE
00204 ;UNLOCKED
00205 ;ON THIS ERROR NO LOOPING IS RECOMMENDED
00206 ;JUST A HALT ON ERROR WILL DO THE SAME
00207 ;THING AS ONLY THE REGISTERS ARE READ
00208 ;CLEAR PREVIOUS ITEM NUMBER
00209 040456 012737 177777 046760 10$: MOV #1,0#PRITEM

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040464 000004  
040466 012737 000001 001272  
040474 012737 000000 177776  
  
040502 104400 040510  
040506 000424  
  
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040560 013746 004716  
040564 104404  
040566 104400 040574  
040572 000402  
  
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040600 013746 001112  
040604 104404  
040606 005037 001112  
040612 005037 001102  
040616 005737 004726  
040622 001413  
  
040624 005237 001100  
040630 104400 041021  
040634 013746 001100  
040640 104404  
040642 104400 041016  
040646 000137 007744  
  
040652 012737 177777 046760 38:  
040660 005337 004720  
040664 001413  
040666 013700 004716  
040672 012701 00467E  
040676 022100 18:  
040700 001401  
040702 00077E  
040704 011137 004716 28:  
040710 000137 007744

```
::*****  
::*****  
::*****  
:*TEST 51      END OF DRIVE  
  
:*      THIS IS THE END OF TEST FOR ONE DRIVE  
  
:*      IF THERE ARE MORE DRIVES, THEN THE PROGRAM  
:*      JUMPS TO TEST 4 FOR TESTING THE NEXT DRIVE  
  
:*      END PASS IS REACHED ONLY AFTER ALL DRIVES ARE TESTED  
  
::*****  
15751: SCOPE  
MOV      #1,STIMES      ;;DO 1 ITERATION  
MOV      #0,PS          ;;REINSTATE PS TO 0  
  
        TYPE      655      ;;TYPE ASCIZ STRING  
BR       648          ;;GET OVER THE ASCIZ  
655:    .ASCIZ  (<15><12>/TOTAL ERRORS ON THIS PASS ON UNIT NO.)  
648:    MOV       @UNIT,-(SP)  ;GET READY TO TYPE UNIT NUMBER  
        TYPDS  
        TYPE      673      ;;TYPE ASCIZ STRING  
BR       658          ;;GET OVER THE ASCIZ  
658:    .ASCIZ  '='  
  
658:    MOV       @SERCTL,-(SP) ;GET READY TO TYPE NUMBER OF ERRORS  
        TYPDS  
CLR      @SERCTL      ;CLEAR TOTAL NUMBER OF ERRORS  
CLR      @STSTNM      ;CLEAR TEST NUMBER  
TST     @SELECT       ;STARTING FROM 200 ?  
BEQ     35            ;CHECK NEXT DRIVE IF SO  
        ;CONTINUE WITH THIS ONE IF NOT  
  
INC      @SPASS       ;INCREASE PASS COUNT  
TYPE    @SENDMG      ;TYPE "END PASS #"  
MOV     @SPASS,-(SP)  
TYPDS  
TYPE    @SENULL  
JMP     @TST4        ;JUMP TEST 4 -----  
  
38:    MOV      @-1,@PRITEM  ;CLEAR PREVIOUS ITEM NUMBER  
DEC     @NOUNITS     ;NO. OF UNITS PRESENT  
BEQ     $EOP        ;BRANCH IF ALL DRIVES COMPLETE  
MOV     @UNIT,R0     ;UNIT UNDER TEST  
MOV     @UNITS,R1    ;TABLE POINTER  
18:    CMP     (R1)+,R0    ;IS THIS UNIT JUST TESTED ?  
BEQ     28          ;BRANCH IF YES  
BR     18           ;BRANCH IF NO  
28:    MOV     (R1),@UNIT  ;MAKE THIS NEXT UNIT  
JMP     @TST4        ;TEST THE NEXT DRIVE -----
```

D06

MINCEC-11-DRII-A, RPO4 S 6 FUNCT. CONT. TST-PT 1  
DRII.PI! :S! END OF DRIVE

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:0265



.SBTTL JAM CURRENT CYLINDER ROUTINE

.\*THIS ROUTINE WILL CHANGE THE CURRENT CYLINDER REGISTER - 'RHCC'  
 .\*BY GIVING A 'SEEK' COMMAND FOLLOWED BY AN INIT WHICH WILL LOAD  
 .\*'RHCC' WITH THE DESIRED CYLINDER VALUE. THE ROUTINE THEN CHECKS  
 .\*THAT THE LOADED VALUE IS CORRECT.

.\*CALL IS:  
 .\* JSR RD,2#MAKECYL ;DESIRED VALUE OF CURRENT CYLINDER  
 .\* XC

MAKECYL:  
 041036 010546 MOV R5, -(SP) ;: PUSH R5 ON STACK  
 041037 010037 041422 MOV RD, 2#PCJSR ;: PC OF JSR+4  
 041038 162737 000004 041422 SUB #4, 2#PCJSR ;: SAVE PC OF JSR  
 041039 012005 MOV (RD)+, R5 ;: GETTING READY TO FILL DESIRED CYLINDER  
 041040 010577 141232 MOV R5, 2#RHCA ;: FILL DESIRED CYLINDER REGISTER  
 041041 005077 141220 CLR 2#RHST ;: MAKE SURE DESIRED SECTOR TRACK IS NOT ILLEGAL  
 041042 013777 002452 141206 MOV 2#SEECOM, 2#RHCSI ;: FILL SEEK COMMAND  
 041043 012777 000001 141220 MOV #DMD, 2#RHMR ;: SET DIAGNOSTIC MODE  
 041100 004737 043300 JSR PC, 2#PUTREG ;: TAKE A REGISTER SNAPSHOT  
 041101 032737 040000 002404 BIT #ERR, 2#DS1 ;: CHECK FOR COMPOSITE ERROR  
 041102 001401 BEQ 2\$ ;: NOT = 1, A-OK  
 041103 104103 ERROR 103 ;: REGISTER CONTENTS INCORRECT BEFORE A  
 ;: DIAGNOSTIC SEEK  
 041116 052777 000001 141154 2\$: BIS #GO, 2#RHCSI ;: ISSUE 'GO' TO SEEK COMMAND  
 041117 000240 NOP ;: ALLOW TIME FOR SEEK TO HANG UP  
 041118 000240 NOP ;: ALLOW TIME FOR SEEK TO HANG UP  
 041119 000240 NOP ;: ALLOW TIME FOR SEEK TO HANG UP  
 041120 000240 NOP ;: ALLOW TIME FOR SEEK TO HANG UP  
 041134 004737 043300 JSR PC, 2#PUTREG ;: TAKE A 2ND REGISTER SNAPSHOT  
 041135 032737 040000 002404 BIT #ERR, 2#DS1 ;: CHECK FOR ERRORS  
 041136 001401 BEQ 3\$ ;: NOT = 1, A-OK  
 041137 104104 ERROR 104 ;: REGISTER CONTENTS INCORRECT AFTER  
 ;: A DIAGNOSTIC SEEK  
 041152 004737 041366 3\$: JSR PC, 2#CLDISK ;: GIVE INIT TO FORCE THE TRANSFER  
 041153 017737 141152 001126 MOV 2#RHCC, 2#\$BDDAT ;: TEST DATA  
 041154 020537 001126 CMP R5, 2#\$BDDAT ;: COMPARE CURRENT CYLINDER  
 041155 001406 BEQ 1\$ ;: BRANCH IF GOOD  
 041156 010537 001124 MOV R5, 2#\$GDDAT ;: GOOD VALUE OF RHCC  
 041157 013737 002334 004500 MOV 2#RHCC, 2#REGADR ;: FAILING REGISTER ADDRESS  
 041158 104077 ERROR 77 ;: CURRENT CYLINDER DOES NOT MATCH DESIRED CYLINDER  
 ;: REGISTER AFTER A SEEK AND AN INIT  
 041206 012605 1\$: MOV (SP)+, R5 ;: POP STACK INTO R5  
 041207 000200 RTS RD

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;\*THIS SUBROUTINE SETS UP FOR SEARCH  
;\*CALL IS  
;\* JSR RO,3\*SRCH  
;\* C ;CYLINDER  
;\* .BYTE S ;SECTOR  
;\* .BYTE T ;TRACK

SRCH: MOV (RO)+,3RHCA ;SET DESIRED CYLINDER ADDRESS  
MOV (RO)+,3RHST ;SET DESIRED SECTOR/TRACK ADDRESS  
MOV 3\*SERCH,3RHCS1 ;GET READY FOR SEARCH  
;WITH 30 IN RHCS1  
RTS RO

;\*THIS SUBROUTINE SETS UP FOR SEEK COMMANDS  
;\*CALL IS  
;\* JSR RO,3\*SEEKCY  
;\* C ;CYLINDER  
;\* .

SEEKCY: MOV (RO)+,3RHCA ;SET DESIRED CYLINDER ADDRESS  
MOV 3\*SEEKCOM,3RHCS1 ;MOV 4 INTO RHCS1  
RTS RO ;RETURN TO MAIN PROGRAM





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041422 000000  
041424 011637 041422  
041430 162737 000004 041422  
041436 011346  
041440 052716 000100  
041444 000406  
041446 011637 041422  
041452 162737 000004 041422  
041460 011346  
041462 011146  
041464 042716 173577  
041470 022726 004200  
041474 001403  
041476 011137 001122  
041502 104061  
041504 042716 102000  
041510 022726 010700  
041514 001404  
041516 011337 001122  
041522 104062  
041524 000207  
041526 052716 000006  
041532 000207

: \*THIS CHECKS THAT DEVICE AVAILABLE (DVA) AND READY (RDY) IN RHCSI = 1  
: \*AND CHECKS MEDIUM ON LINE (MOL), DEVICE PRESENT (DPR), DEVICE READY  
: \* (DRY) IN RHCSI = 1  
  
: \*IT ALSO CHECKS THAT THERE ARE NO BITS STUCK AT 1 IN RHCSI  
  
PCJSR: 0 ;PC OF JSR  
  
CHECK: MOV (SP), @#PCJSR ;SAVE PC OF JSR+4  
SUB #4, @#PCJSR ;GET PC OF JSR  
MOV @R3, -(SP) ;GET RHCSI  
BIS #VV, (SP) ;DONT CHECK VV BIT  
BR CHECKC ;GOTO COMMON CHECK ROUTINE  
  
CHECKT: MOV (SP), @#PCJSR ;SAVE PC OF JSR+4  
SUB #4, @#PCJSR ;GET PC OF JSR  
MOV @R3, -(SP) ;GET RHCSI & DO VV CHECK AT 3\$  
  
CHECKC: MOV @R1, -(SP) ;GET CS1  
BIC #173577, (SP) ;CLEAR UNWANTED BITS  
CMP #DVA!RDY, (SP)+ ;RHCSI SHOULD HAVE DEVICE AVAILABLE  
AND BE READY  
BEQ 3\$ ;BRANCH IF IT DOES  
MOV @R1, @#\$BDADR ;BAD DATA REGISTER (RHCSI)  
ERROR 61 ;RHCSI DID NOT HAVE DEVICE  
AVAILABLE RIGHT AT THE START  
;ALL OTHER BITS SHOULD BE 0  
  
3\$: BIC #ATA!LBT, (SP) ;CLEAR UNWANTED BITS  
CMP #MOL!DPR!DRY!VV, (SP)+ ;RHCSI SHOULD HAVE THESE SET  
BEQ 7\$ ;BRANCH IF GOOD  
MOV @R3, @#\$BDADR ;BAD DATA IN REGISTER (RHCSI)  
ERROR 62 ;RHCSI HAS SOME BITS OTHER  
THAN MOL, DRY, DPR, VV SET  
;ALL OTHER BITS SHOULD BE 0  
RTS PC ;RETURN TO TEST AND HALT/CONTINUE  
;DEPENDING ON WHETHER THIS IS A  
;"FATAL" ERROR  
  
7\$: ADD #6, (SP) ;ADJUST STACK TO JUMP OVER HALT IN TEST  
RTS PC ;RETURN TO THE TEST AND CONTINUE

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041534 010146  
041536 010246  
041540 010346  
041542 012001  
041544 012002  
041546 012003  
041550 013122  
041552 005303  
041554 001375  
041556 012603  
041560 012502  
041562 012501  
041564 000200

```

; *THIS IS A SUBROUTINE TO SAVE REGISTERS
; *IN THE REGISTER TABLE TO ANY LOCATION
; *THE CALL IS
; *JSR   RD, @SAVER
; *      F   :FROM
; *      T   :TO
; *      N   :NUMBER OF WORDS SAVED
; *F MUST ALWAYS BE RHCSI
; *T MUST ALWAYS BE SAVRE

```

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SAVER:
      MOV   R1, -(SP)      ;; PUSH R1 ON STACK
      MOV   R2, -(SP)      ;; PUSH R2 ON STACK
      MOV   R3, -(SP)      ;; PUSH R3 ON STACK
      MOV   (R0)+, R1      ;; FROM
      MOV   (R0)+, R2      ;; TO
      MOV   (R0)+, R3      ;; NUMBER
1$:   MOV   @R1+, (R2)+    ;; SAVE REGISTER CONTENTS
      DEC   R3             ;; COUNT
      BNE  1$             ;; BRANCH IF NOT DONE
      MOV   (SP)+, R3      ;; POP STACK INTO R3
      MOV   (SP)+, R2      ;; POP STACK INTO R2
      MOV   (SP)+, R1      ;; POP STACK INTO R1
      RTS   R0

```

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; *WHEN AN EVENT IS TO BE TIMED THE RPO4 VECTORS TO "TIME 1"
; *PRIORITY OF PROCESS OR IS 4
; *PRIORITY OF TRAPS MUST BE 6
; *PRIORITY OF RPO4 INTERRUPTS IS 7
; *

```

041566 005077 140552  
041572 017737 140552 041624  
041600 017737 140530 004664  
041606 017737 140524 004662  
041614 000002

```

TIME1: CLR   @PCLCSR      ; STOP THE CLOCK
      MOV   @PCLCTR, @WAITTM ; GET TIME ON CLOCK
TIME2: MOV   @RHCC, @FINACC ; GET CURRENT CYLINDER
      MOV   @RHLA, @FINALA  ; GET LOOK AHEAD
      RTI                      ; RETURN TO WAIT P OR WAIT.T

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10594      ;*THIS IS A WAIT LOOP WHEN AN EVENT IS TO BE TIMED
10595      ;*THE CALL IS
10596      ;*   WAT
10597      ;*   A   ;ABSOLUTE REGISTER ADDRESS
10598      ;*   B   ;BIT WAITED FOR
10599      ;*   TA  ;TIME ALLOWED GIVEN IN 10 MICROSEC
10600      ;*   TO  ;TOLERANCE PLUS/MINUS IN 10 MICROSEC
10601      ;*
10602      ;*R1-WILL HAVE TIME ALLOWED IN 10 MICRO SECONDS
10603      ;*R2-WILL HAVE TOLERANCE PLUS/MINUS IN 10 MICRO SECONDS
10604      ;*MINIMUM TIME THAT CAN BE MEASURED IS ABOUT 12 MICRO SECONDS
10605      ;*FOR THE SLOWEST PROCESSOR
10606
10607      041616 000000      WAITPC: 0      ;WAT PC
10608      041620 000000      WAITRE: 0      ;WAIT ON REGISTER ADDRESS
10609      041622 000000      WAITBT: 0      ;WAIT ON BIT
10610      041624 000000      WAITTM: 0      ;WAITED TIME
10611      041626 005037 041624      WAIT.P: CLR @#WAITTM      ;CLEAR WAITED TIME
10612      041632 005077 140510      CLR @PCLBUF ;CLEAR COUNT SET BUFFER
10613      041636 012777 000021 140500      MOV #GO!BIT4,@PCLCSR ;COUNT UP, 100 KHZ, START CLOCK
10614      041644 010046      MOV R0,-(SP) ;PUSH R0 ON STACK
10615      041646 010146      MOV R1,-(SP) ;PUSH R1 ON STACK
10616      041650 010246      MOV R2,-(SP) ;PUSH R2 ON STACK
10617      041652 010346      MOV R3,-(SP) ;PUSH R3 ON STACK
10618      041654 016600 000010      MOV 10(SP),R0 ;R0 HAS ADDRESS OF NEXT LOCATION
10619      041660 010037 041616      MOV R0,@#WAITPC ;NOW WAITPC HAS WAT PC + 2
10620      041664 162737 000002 041616      SUB #2,@#WAITPC ;WAT PC IS IN WAITPC
10621      041672 013037 041620      MOV @ (R0)+,@#WAITRE ;WAIT ON REGISTER ADDRESS
10622      041676 012037 041622      MOV (R0)+,@#WAITBT ;WAIT ON BIT
10623      041702 012001      MOV (R0)+,R1 ;R1 HAS TIME IN 10 MSEC
10624      041704 012002      MOV (R0)+,R2 ;R2 HAS TOLERANCE IN 10 MSEC
10625      041706 010066 000010      MOV R0,10(SP) ;RESTORE RETURN ON STACK
10626
10627      ;*THIS SECTION WAITS FOR BIT, THROUGH TWO COUNT DOWNS
10628      041712 013703 042064      MOV @#TIMCNT,R3 ;R3 IS A TEMPORARY COUNTER
10629      041716 033777 041622 177674 1$: BIT @#WAITBT,@#WAITRE ;IS REQUIRED BIT THERE
10630      041724 001025      BNE 4$ ;BRANCH IF YES
10631      041726 005303      DEC R3 ;COUNT IF REQUIRED BIT NOT THERE
10632      041730 001372      BNE 1$
10633      041732 013703 042064      MOV @#TIMCNT,R3 ;TEMPORARY COUNTER
10634      041736 033777 041622 177654 2$: BIT @#WAITBT,@#WAITRE ;IS REQUIRED BIT THERE
10635      041744 001015      BNE 4$ ;BRANCH IF YES
10636      041746 005303      DEC R3 ;COUNT IF REQUIRED BIT NOT THERE
10637      041750 001372      BNE 2$
10638      041752 017737 177642 001126      MOV @#WAITRE,@#SBDDAT ;REGISTER CONTENTS FOR TYPEOUT
10639      041760 032777 000100 140312      BIT #IE,@RHCS1 ;DID ANY INTERRUPT OCCUR
10640      041766 001402      BEQ 3$ ;BRANCH IF YES
10641      041770 104001      ERROR 1 ;RPO4 DID NOT INTERRUPT
10642      041772 000427      BR 7$ ;OUT
10643      041774 104002      BR 3$ ;RPO4 INTERRUPTED BUT WAITED
10644      ;ON BIT DID NOT OCCUR
10645      ;EVEN AFTER TWO COUNT DOWNS
10646      ;FROM 177777 TO 0
10647      041776 000425      BR 7$ ;OUT

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10650 042000 017737 177514 001126 4S:
10651 042006 032777 000100 140254
10652 042014 001402
10653 042016 104003
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10656 042020 000414
10657 042022 160201
10658 042024 023701 041624
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10661 042030 103002
10662 042032 104004
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10664 042034 000406
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10666 042036 060202
10667 042040 060201
10668 042042 020137 041624
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10670 042046 103001
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10673 042052
10674 042052 012603
10675 042054 012602
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10694 042064 177777
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10696 042066
10697 042066 010046
10698 042070 010346
10699 042072 016600 000004
10700 042076 010037 041616
10701 042102 162737 000002 041616

; *NOW TIME AND TOLERANCE WILL BE CHECKED
MOV 3WAITR, 3#SBDAT ; REGISTER CONTENTS FOR TYPEOUT
BIT #IE, 3RHCS1 ; DID ANY INTERRUPT OCCUR
BEQ 5S ; BRANCH IF YES
ERROR 3 ; INTERRUPT DID NOT OCCUR EVEN
; AFTER ONE BNE AND ONE MOV
; OF THE WAITED ON BIT SETTING
; OUT
BR 7S ; RI NOW HAS LOWER LIMIT OF TIME
SUB R2, R1 ; FOR GOOD RESULTS, WAITTM
CMP 2#WAITTM, R1 ; MUST BE GREATER OR EQUAL
; TORI
BHS 6S ; BRANCH IF GOOD
ERROR 4 ; BIT DID OCCUR BUT TIME
; TAKEN IS BELOW LOWER LIMIT
; OUT
BR 7S
6S: ADD R2, R2 ; DOUBLE TOLERANCE
ADD R2, R1 ; RI NOW HAS UPPER LIMIT OF TIME
CMP R1, 2#WAITTM ; FOR GOOD RESULTS, WAITTM
; MUST BE LESS OR EQUAL TO R1
BHS 7S ; BRANCH IF GOOD
ERROR 4 ; BIT DID OCCUR BUT TIME TAKEN
; IS ABOVE UPPER LIMIT
7S: MOV (SP)+, R3 ; POP STACK INTO R3
MOV (SP)+, R2 ; POP STACK INTO R2
MOV (SP)+, R1 ; POP STACK INTO R1
MOV (SP)+, R0 ; POP STACK INTO R0
RTI ; RETURN TO MAIN TEST

; *THIS IS A WAIT LOOP WHEN NO P-CLOCK IS AVAILABLE
; *NO TIMING IS DONE
; *CALL IS
; * WAT
; * A : ABSOLUTE REGISTER ADDRESS
; * B ; BIT WAITE) FOR
; * TA ; TIME-NOT USED HERE
; * TO ; TIME-NOT USED HERE
; *R3-IS A TEMPORARY COUNTER
TIMCNT: 177777 ; COUNT FOR WAIT LOOP

WAIT.T: MOV R0, -(SP) ; PUSH R0 ON STACK
MOV R3, -(SP) ; PUSH R3 ON STACK
MOV 4(SP), R0 ; R0 HAS ADDRESS OF NEXT LOCATION
MOV R0, 2#WAITPC ; WAT PC +2 IS IN WAITPC
SUB #2, 2#WAITPC ; WAT PC IS IN WAITPC

```

```

000000 042110 013037 041620 MOV (R0)+,2#WAITRE :WAIT ON REGISTER ADDRESS
000001 042114 012037 041622 MOV (R0)+,2#WAITBT :WAIT ON BIT
000002 042120 022020 CMP (R0)+,(R0)+ :DUMP NEXT TWO WORDS-TA, TC
000003 042122 010066 000004 MOV RO,4(SP) :RESTORE RETURN ON STACK

000004 042126 013703 042064 :*THIS HAS THE TWO COUNT DOWNS FROM 177777
000005 042130 032777 041622 177460 1S: MOV 2#TIMCNT,R3 :R3 HAS TEMPORARY COUNT
000006 042140 001025 BIT 2#WAITBT,2#WAITRE :IS REQUIRED BIT THERE
000007 042142 005303 BNE 4S :BRANCH IF YES
000008 042144 001372 DEC R3 :COUNT IF REQUIRED BIT NOT THERE
000009 042146 013703 042064 BNE 1S
000010 042150 032777 041622 177440 2S: MOV 2#TIMCNT,R3 :SECOND COUNT DOWN FROM 177777
000011 042160 001015 BIT 2#WAITBT,2#WAITRE :IS REQUIRED BIT THERE
000012 042162 005303 BNE 4S :BRANCH IF YES
000013 042164 001372 DEC R3 :COUNT IF REQUIRED BIT NOT THERE
000014 042166 017737 177426 001126 MOV 2#WAITRE,2#SBD0AT :REGISTER CONTENTS FOR TYPEOUT
000015 042174 032777 000100 140076 BIT #IE,2#RHC01 :DID ANY INTERRUPT OCCUR
000016 042202 001405 BEQ 5S :BRANCH IF YES
000017 042204 104001 ERRJR :RPO4 DID NOT INTERRUPT
000018 042206 000414 BR 5S :BIT DID NOT OCCUR
000019 042210 104002 3S: BR 2 :OUT
000020 042212 000412 BR 5S :RPO4 INTERRUPTED BUT
000021 042214 000240 :WAITED ON BIT DID NOT OCCUR
000022 042216 032777 000100 140054 4S: NOP :EVEN AFTER TWO COUNT DOWNS
000023 042224 001405 BEQ 5S :FROM 177777 TO 0
000024 042226 017737 177366 001126 MOV 2#WAITRE,2#SBD0AT :REGISTER CONTENTS FOR TYPEOUT
000025 042234 104003 ERROR 2 :INTERRUPT DID NOT OCCUR
000026 042236 000400 BR 5S :EVEN AFTER ONE ONE OF
000027 042240 000400 :THE WAITED ON BIT OCCURING
000028 042242 000400 BR 5S :OUT
000029 042244 012603 MOV (SP)+,R3 :POP STACK INTO R3
000030 042246 012603 MOV (SP)+,R0 :POP STACK INTO R0
000031 042248 000002 RT :RETURN TO MAIN TEST

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042356 010246  
042360 010346  
042362 010446  
042364 010546  
042366 012001  
042370 012002  
042372 012003  
042374 012004  
042376 011900  
  
042400 004737 043300  
042404 113737 004637 002401  
042412 012705 177776  
  
042416 062705 000002 13:  
042422 022122  
042424 001420  
042426 014137 001124  
042432 014237 001126  
042436 016537 002272 004600  
042444 004714  
  
042446 022122  
042450 017746 136464  
042454 042716 177177  
042460 022726 000200  
042464 001402  
042466 005303 23:  
042470 001352 15  
  
042472 33:  
042472 012605  
042474 012604  
042476 012603  
042500 012602  
042502 012601  
042504 000200

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: *THIS IS A SUBROUTINE TO COMPARE REGISTERS
: *GOOD DATA IS ALREADY SAVED IN 'SAVERE'
: *TEST DATA IS IN THE REGISTERS
: *CALL IS
: *   JSR   RD,2#COMREG
: *   SAVERE
: *   RHCSI
: *   N.
: *   RG
: *ON RETURN WITH ERROR '$GDDAT' HAS GOOD DATA, '$BDDAT' HAS BAD DATA
: *'REGADR' HAS REGISTER ADDRESS

COMREG:
MOV   R1,-(SP)      ;; PUSH R1 ON STACK
MOV   R2,-(SP)      ;; PUSH R2 ON STACK
MOV   R3,-(SP)      ;; PUSH R3 ON STACK
MOV   R4,-(SP)      ;; PUSH R4 ON STACK
MOV   R5,-(SP)      ;; PUSH R5 ON STACK
MOV   (R0)+,R1      ;; R1 HAS ADDRESS OF GOOD DATA
MOV   (R0)+,R2      ;; R2 HAS ADDRESS OF ADDRESS OF TEST DATA
MOV   (R0)+,R3      ;; R3 HAS NUMBER OF WORDS
MOV   (R0)+,R4      ;; R4 HAS RETURN FOR ERROR
MOV   (R0),R0       ;; R0 HAS RETURN ON NO ERROR

: *NOW SAVE REGISTERS
JSR   PC,2#PUTREG   ;; SAVE REGISTERS
MOVB  2#SAVERE+25,2#AS+1 ;; MAKE UPPER BYTE OF R HAS SAME
MOV   #-2,R5        ;; PRESET R5 TO -2

: *NOW COMPARES WILL MADE
13:  ADD   #2,R5      ;; INCREMENT TO INDEX
      CMP   (R1)+,(R2)+ ;; COMPARE REGISTER CONTENTS
      BEQ   25      ;; BRANCH IF GOOD
      MOV   -(R1),2#$GDDAT ;; SAVE GOOD DATA
      MOV   -(R2),2#$BDDAT ;; SAVE BAD DATA
      MOV   RHWC(R5),2#REGADR ;; SAVE ADDRESS OF FAILING REGISTER
      JSR   PC,2#JRY  ;; RETURN TO MAIN PROGRAM
      ;; TO PRINT ERROR
      UNDO -(R1) AND -(R2) FOR ERRORS
      MOV   2#SWR,-(SP) ;; GET SWITCH SETTING
      BIC   #1C600,(SP) ;; KEEP ONLY SWITCH 7 AND 9
      CMP   #SW07,(SP)+ ;; IS 7 SET AND 9 DOWN
      BEQ   35      ;; BRANCH OUT IF YES
23:  DEC   R3        ;; ARE ALL COMPARES DONE
      BNE   15      ;; BRANCH IF NOT COMPLETE

33:  MOV   (SP)+,R5   ;; POP STACK INTO R5
      MOV   (SP)+,R4   ;; POP STACK INTO R4
      MOV   (SP)+,R3   ;; POP STACK INTO R3
      MOV   (SP)+,R2   ;; POP STACK INTO R2
      MOV   (SP)+,R1   ;; POP STACK INTO R1
      RTS   R0        ;; RETURN TO MAIN PROGRAM

```

F07

MAINTEN-11-DIR:1-A. R004 S 6 FUNCT. CONT. TST-PT 1  
SERVIA.P11 JAM CURRENT CYLINDER ROUTINE

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SEQ 0289

:0857 042506 000000 43: .WORD 0 :TEMP STORAGE

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042510 000000  
042512  
042512 005037 177776  
042516 012737 177777 046760  
042524 104400 042532  
042530 000421  
  
042574  
042574 013746 004604  
042600 104401  
042602 104400 042610  
042606 000414  
  
042640  
042640 013746 001110  
042644 104401  
042646 104400 001223  
042652 104400 042660  
042656 000430  
  
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042740 104400 042746  
042744 000430  
  
043026  
043026 104400 043034  
043032 000422  
  
043100  
043100 104411  
043102 062716 000002  
043106 012637 001106

:\*HERE IS A DETAILED EXPLANATION OF HOW THE LOOP ON ERROR WORKS.  
:\*ON HITTING AN ERROR IF THE LOOP ON ERROR SWITCH IS SET, THE  
:\*PROGRAM GOES BACK - USUALLY BACK TO THE BEGINNING OF THE TEST.  
  
:\*WHEN THIS OPERATOR SELECTABLE SCOPE LOOP IS USED THEN THE POINT  
:\*THE PROGRAM GOES BACK TO CAN BE CHANGED.  
:\*THE RESTRICTIONS TO THE POINT WHERE THE PROGRAM CAN GO ARE: -  
:\*1. IT MUST BE WITHIN THE TEST UNDER CONSIDERATION  
:\*2. LOOP ON ERROR SWITCH MUST BE SET  
:\*3. THE ERROR MUST OCCUR WITHIN THE TEST UNDER CONSIDERATION  
:\*IF THE ERROR DOES NOT OCCUR WITHIN THE TEST UNDER CONSIDERATION  
:\*THE PROGRAM WILL REVERT TO NORMAL OPERATION. HOWEVER, IF LOOP ON  
:\*TEST SWITCH IS SET AND THIS OPERATOR SELECTABLE SCOPE LOOP IS USED  
:\*THEN THE PROGRAM WILL LOOP BACK TO THE SELECTED POINT WHEN IT  
:\*COMES TO THE END OF THE TEST UNDER CONSIDERATION.  
:\*  
:\*AFTER LOOPING FOR SOME TIME IF THE LOOP SWITCH IS PUT DOWN THEN  
:\*NORMAL OPERATION WILL CONTINUE.

TESTAD: 0 ;FIRST ADDRESS OF TEST  
OPERSEL:  
CLR PS ;MAKE PROCESSOR STATUS ZERO  
MOV #1,2\*PRITEM ;CLEAR PREVIOUS ITEM NUMBER  
TYPE ,55\$ ;TYPE ASCIZ STRING  
BR 64\$ ;GET OVER THE ASCIZ  
64\$: .ASCIZ <15><12>/THE PROGRAM WAS IN TEST NUMBER /  
MOV 2\*STNM,-(SP) ;GET READY TO TYPE TEST  
TYPOC ;NUMBER  
TYPE ,67\$ ;TYPE ASCIZ STRING  
BR 66\$ ;GET OVER THE ASCIZ  
66\$: .ASCIZ <15><12>/THE LOOP BACK PC WAS /  
MOV 2\*\$LPERR,-(SP) ;GET READY TO TYPE LOOP BACK PC  
TYPOC  
TYPE ,69\$ ;TYPE ASCIZ STRING  
BR 68\$ ;GET OVER THE ASCIZ  
68\$: .ASCIZ <15><12>/SET SWITCH FOR LOOP ON ERROR OR LOOP ON TEST  
TYPE ,71\$ ;TYPE ASCIZ STRING  
BR 70\$ ;GET OVER THE ASCIZ  
70\$: .ASCIZ <15><12>/TYPE THE FIRST PC OF THE TEST TO BE LOOPED ON  
TYPE ,73\$ ;TYPE ASCIZ STRING  
BR 72\$ ;GET OVER THE ASCIZ  
72\$: .ASCIZ <15><12>/ FOLLOWED BY A CARRIAGE RETURN <15><12>  
RDOCT  
ADD #2,(SP) ;GET LPADR  
MOV (SP)+,2\*\$LPADR



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043300 010046  
043302 010146  
043304 010246  
043306 012700 002272  
043312 012701 002354  
043316 012702 000022  
043322 013021  
043324 005302  
043326 001375  
043330 012602  
043332 012601  
043334 012600  
043336 000207

PUTREG:

MOV R0, -(SP) ;: PUSH R0 ON STACK  
MOV R1, -(SP) ;: PUSH R1 ON STACK  
MOV R2, -(SP) ;: PUSH R2 ON STACK  
MOV #RHWC, R0 ;: STARTING ADDRESS OF REG  
MOV #WC, R1 ;: STARTING ADDRESS OF WERE SAVED  
MOV #RHCC-RHWC+2/2, R2 ;: NUMBER OF REG. INTO R2  
JS: MOV @ (R0)+, (R1)+ ;: SAVE HARDWARE REG.  
DEC R2  
BNE JS  
MOV (SP)+, R2 ;: POP STACK INTO R2  
MOV (SP)+, R1 ;: POP STACK INTO R1  
MOV (SP)+, R0 ;: POP STACK INTO R0  
RTS PC

THIS IS A DATA COMMAND SETUP SUBROUTINE

THE CALL IS

JSR R0, @#RUN  
C ;: CYLINDER  
S ;: SECTOR  
T ;: TRACK  
-W ;: WORD COUNT  
B ;: BUS ADDRESS  
BAI ;: BUS ADDRESS INHIBIT  
FMT22!ECI!HCI ;: FMT22=1 =16 BIT WORDS  
ECI = ECC CORRECTION INHIBIT  
HCI = HEADER COMPARE INHIBIT  
COM ;: COMMAND ADDRESS  
RUN: MOV (R0)+, @RHCA ;: CYLINDER  
MOV (R0)+, @RHCS1 ;: DESIRED SECTOR/TRACK  
MOV (R0)+, @RHWC ;: WORD COUNT  
MOV (R0)+, @RHBA ;: BUS ADDRESS  
MOV @#UNIT, -(SP) ;: GET UNIT NO  
BIS (R0)+, (SP) ;: SET BUS ADDRESS INHIBIT  
MOV (SP)+, @RHCS2 ;: UNIT NO AND BAI TO RHCS2  
MOV (R0)+, @RHOF ;: FORMAT, ECC INHIBIT, HEADER  
COMPARE, IF THERE  
MOV @ (R0)+, @RHCS1 ;: COMMAND IN RHCS1  
RTS R0 ;: RETURN TO MAIN PROGRAM

J07

MAINDEC-11-DZRJI-A, RPO4.5/6 FUNCT. CONT. TST-PT 1  
DZRJIA.F11 JAM CURRENT CYLINDER ROUTINE

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SEQ 0293

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J07

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043412 010446  
043414 010546  
043416 012001  
043420 012002  
043422 012003  
043424 012005  
043426 011000  
043430 010304  
043432 005204  
043434 010437 004602  
043440 022122  
043442 001417  
043444 014137 001124  
043450 014237 001126  
043454 160337 004602  
043460 004715  
043462 022122  
043464 017746 135450  
043470 042716 177177  
043474 022726 000200  
043500 001402  
043502 005303  
043504 001353  
043506  
043506 012605  
043510 012604  
043512 012603  
043514 012602  
043516 012601

;\*THIS IS A SUBROUTINE TO COMPARE TWO BLOCKS IN MEMORY

;\*R1 HAS GOOD DATA BUFFER ADDRESS  
;\*R2 HAS TEST DATA BUFFER ADDRESS  
;\*R5 HAS ADDRESS OF RETURN ON ERROR  
;\*R3 HAS NUMBER OF WORDS TO BE COMPARED  
;\*R4 HAS ONE MORE THAN NUMBER OF WORDS TO BE COMPARED

;\*CALL IS:  
;\* JSR R0,3#COMPAR ; ADDRESS OF GOOD DATA  
;\* S ; ADDRESS OF TEST DATA  
;\* T ; NUMBER OF WORDS TO BE COMPARED  
;\* N ; RETURN ON ERROR  
;\* RE ; RETURN ON NO ERROR  
;\* RG

COMPAR:  
MOV R1,-(SP) ;: PUSH R1 ON STACK  
MOV R2,-(SP) ;: PUSH R2 ON STACK  
MOV R3,-(SP) ;: PUSH R3 ON STACK  
MOV R4,-(SP) ;: PUSH R4 ON STACK  
MOV R5,-(SP) ;: PUSH R5 ON STACK  
MOV (R0)+,R1 ;: ADDRESS OF GOOD DATA BUFFER  
MOV (R0)+,R2 ;: ADDRESS OF TEST DATA BUFFER  
MOV (R0)+,R3 ;: NO OF WORDS TO BE COMPARED  
MOV (R0)+,R5 ;: RETURN ON ERROR  
MOV (R0),R0 ;: RETURN ON NO ERROR  
MOV R3,R4 ;: NO OF WORDS TO BE COMPARED  
INC R4  
1\$: MOV R4,2#ERWORD ;: FOR ERROR WORD NO  
CMP (R1)+,(R2)+ ;: COMPARE GOOD WITH TEST DATA  
BEQ 2\$ ;: BRANCH IF GOOD  
MOV -(R1),2#\$GDDAT ;: GOOD DATA  
MOV -(R2),2#\$BDDAT ;: BAD DATA  
SUB R3,2#ERWORD ;: ERROR WORD NO.  
JSR PC,2#RS ;: RETURN TO PRINT ERROR  
CMP (R1)+,(R2)+ ;: UNDO -(R1) AND -(R2) FOR ERRORS  
MOV 2#SWR,-(SP) ;: GET SWITCH SETTING  
BIC #1C600,(SP) ;: KEEP ONLY SWITCH 7 AND 9  
CMP #SW07,(SP)+ ;: IS 7 SET AND 8 RESET  
BEQ 3\$ ;: BRANCH OUT IF YES  
2\$: DEC R3 ;: COUNT  
BNE 1\$ ;: BRANCH IF ALL NOT DEVICE  
3\$: MOV (SP)+,R5 ;: POP STACK INTO R5  
MOV (SP)+,R4 ;: POP STACK INTO R4  
MOV (SP)+,R3 ;: POP STACK INTO R3  
MOV (SP)+,R2 ;: POP STACK INTO R2  
MOV (SP)+,R1 ;: POP STACK INTO R1

L07

MAINDEC-11-DZRJI-A, RPO4/5 6 FUNCT. CONT. TST-PT 1  
DZRJIA.P11 JAM CURRENT CYLINDER ROUTINE

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SEQ 0295

11035 043520 000200

RIS

RO

;RETURN TO MAIN PROGRAM

M07



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11041 043522
11042 043522 104400 043530
11043 043526 000425
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11045 043602
11046 043602 013746 002300
11047 043606 104401
11048 043610 104400 043616
11049 043614 000425
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11051 043670
11052 043670 004737 045352
11053 043674 104411
11054 043676 012700 002270
11055 043702 012701 000024
11056 043706 042710 177700
11057 043712 051620
11058 043714 005301
11059 043716 001373
11060 043720 104400 043726
11061 043724 000417
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11063 043764
11064 043764 013746 002266
11065 043770 104401
11066 043772 104400 044000
11067 043776 000437
11068
11069 044076
11070 044076 104411
11071 044100 012637 002266
11072 044104 104400 044112
11073 044110 000421
11074
11075 044154
11076 044154 104400 044162
11077 044160 000416
11078
11079 044216
11080 044216 013746 002300
11081 044222 104401
11082 044224 104400 044232
11083 044230 000416
11084
11085 044266
11086 044266 013746 002266
11087 044272 104401
11088 044274 104400 044302
11089 044300 000402

;* THIS ROUTINE WILL ALLOW THE CHANGE OF THE BASE
;* ADDRESS FROM 176700 TO ANY TYPED VALUE

BASECH:
TYPE 65$ ;;TYPE ASCIZ STRING
BR 64$ ;;GET OVER THE ASCIZ
;;65$: .ASCIZ <15><12>/PRESENT BASE ADDRESS OF REGISTERS IS /
64$:
MOV 2*RHCS1,-(SP) ;GET READY TO TYPE OLD BASE
TYPOC
TYPE 67$ ;;TYPE ASCIZ STRING
BR 66$ ;;GET OVER THE ASCIZ
;;67$: .ASCIZ <15><12>/TYPE NEW BASE ADDRESS FOLLOWED BY 'CR' /
66$:
JSR PC,2*STKINT ;INITIALIZE THE TTY KEYBOARD
RDOCT
MOV 2*RHDB,RO ;GET STARTING ADDRESS OF REGISTERS
MOV 20,R1 ;NUMBER OF REGISTERS
BIC 1C77,(RO) ;CLEAR OLD BASE
BIS (SP),(RO)+ ;SET NEW BASE
DEC R1 ;COUNT
BNE 1$ ;BRANCH IF 20 NOT DONE
TYPE 69$ ;;TYPE ASCIZ STRING
BR 68$ ;;GET OVER THE ASCIZ
;;69$: .ASCIZ <15><12>/PRESENT VECTOR ADDRESS IS /
68$:
MOV 2*RPVEC,-(SP) ;GET READY TO TYPE OLD VECTOR ADDRESS
TYPOC
TYPE 71$ ;;TYPE ASCIZ STRING
BR 70$ ;;GET OVER THE ASCIZ
;;71$: .ASCIZ <15><12>/TYPE NEW VECTOR ADDRESS OR RETYPE OLD ONE FOLLOWED BY "CR" /
70$:
RDOCT
MOV (SP)+,2*RPVEC ;SETUP VECTOR ADDRESS
TYPE 73$ ;;TYPE ASCIZ STRING
BR 72$ ;;GET OVER THE ASCIZ
;;73$: .ASCIZ <15><12>/RESTART PROGRAM FROM 200 OR 210/
72$:
TYPE 75$ ;;TYPE ASCIZ STRING
BR 74$ ;;GET OVER THE ASCIZ
;;75$: .ASCIZ <15><12>/NEW BASE WILL REMAIN - /
74$:
MOV 2*RHCS1,-(SP)
TYPOC
TYPE 77$ ;;TYPE ASCIZ STRING
BR 76$ ;;GET OVER THE ASCIZ
;;77$: .ASCIZ <15><12>/NEW VECTOR WILL REMAIN - /
76$:
MOV 2*RPVEC,-(SP)
TYPOC
TYPE 79$ ;;TYPE ASCIZ STRING
BR 78$ ;;GET OVER THE ASCIZ

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11091 044306
11092 044306 104400 044314
11093 044312 000416
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11095 044350
11096 044350 000000
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::79$: .ASCIZ <15><12>/ /
78$:
TYPE 81$           :::TYPE ASCIZ STRING
BR 80$           :::GET OVER THE ASCIZ
::91$: .ASCIZ /15><12>/UNTIL PROGRAM IS RELOADED/
80$: HALT

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.SBTTL SCOPE HANDLER ROUTINE  
  
\*\*\*\*\*  
\*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT  
\*AND LOAD THE TEST NUMBER(\$TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)  
\*AND LOAD THE ERROR FLAG (\$ERFLG) INTO DISPLAY<15:08>  
\*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:  
\*SW14=1 LOOP ON TEST  
\*SW11=1 INHIBIT ITERATIONS  
\*SW09=1 LOOP ON ERROR  
\*SW08=1 LOOP ON TEST IN SWR<7:0>  
\*CALL  
\* SCOPE ::SCOPE=I07  
  
\$SCOPE:  
CKSWR  
1\$: BIT #BIT14,\$SWR  
BNE \$OVER  
\*\*\*\*\*START OF CODE FOR THE XOR TESTER\*\*\*\*\*  
\$XTSTR: BR 6\$  
  
MOV \$ERRVEC, -(SP)  
MOV \$S,\$ERRVEC  
TST \$177060  
MOV (SP)+,\$ERRVEC  
BR \$SVLAD  
5\$: CMP (SP)+,(SP)+  
MOV (SP)+,\$ERRVEC  
BR 7\$  
6\$: \*\*\*\*\*END OF CODE FOR THE XOR TESTER\*\*\*\*\*  
BIT #BIT08,\$SWR  
BEQ 2\$  
CMPB \$SWR,\$TSTNM  
BEQ \$OVER  
2\$: TSTB \$ERFLG  
BEQ 3\$  
CMPB \$ERMAX,\$ERFLG  
BHT 3\$  
BIT #BIT09,\$SWR  
BEQ 4\$  
MOV \$LPERR,\$LPADR  
BR \$OVER  
4\$: CLRB \$ERFLG  
CLR \$TIMES  
BR 1\$  
3\$: BIT #BIT11,\$SWR  
BNE 1\$  
TST \$PASS  
BEQ 1\$  
INC \$ICNT  
CMP \$TIMES,\$ICNT  
BGE \$OVER  
1\$: MOV #1,\$ICNT  
MOV \$MAXCNT,\$TIMES

::TEST FOR CHANGE IN SOFT-SWR  
::LOOP ON PRESENT TEST?  
::YES IF SW14=1  
::IF RUNNING ON THE "XOR" TESTER CHANGE  
::THIS INSTRUCTION TO A "NOP" (NOP=240)  
::SAVE THE CONTENTS OF THE ERROR VECTOR  
::SET FOR TIMEOUT  
::TIME OUT ON XOR?  
::RESTORE THE ERROR VECTOR  
::GO TO THE NEXT TEST  
::CLEAR THE STACK AFTER A TIME OUT  
::RESTORE THE ERROR VECTOR  
::LOOP ON THE PRESENT TEST  
::LOOP ON SPEC. TEST?  
::BR IF NO  
::ON THE RIGHT TEST? SWR<7:0>  
::BR IF YES  
::HAS AN ERROR OCCURRED?  
::BR IF NO  
::MAX. ERRORS FOR THIS TEST OCCURRED?  
::BR IF NO  
::LOOP ON ERROR?  
::BR IF NO  
::SET LOOP ADDRESS TO LAST SCOPE  
  
::ZERO THE ERROR FLAG  
::CLEAR THE NUMBER OF ITERATIONS TO MAKE  
::ESCAPE TO THE NEXT TEST  
::INHIBIT ITERATIONS?  
::BR IF YES  
::IF FIRST PASS OF PROGRAM  
::INHIBIT ITERATIONS  
::INCREMENT ITERATION COUNT  
::CHECK THE NUMBER OF ITERATIONS MADE  
::BR IF MORE ITERATION REQUIRED  
::REINITIALIZE THE ITERATION COUNTER  
::SET NUMBER OF ITERATIONS TO 00

```

00000000 044622 105237 001102 $SVLAD: INCB
00000000 044623 011637 001106 MOV
00000000 044624 011637 001110 MOV
00000000 044625 005037 001214 CLR
00000000 044626 112737 000001 $COVER: MOVB
00000000 044627 012737 001102 134264
00000000 044628 012737 001106 MOV
00000000 044629 000037 RTI
00000000 044630 000037

```

```

$STSTM      :: COUNT TEST NUMBERS
(SP), $LPADR :: SAVE SCOPE LOOP ADDRESS
(SP), $LPERP :: SAVE ERROR LOOP ADDRESS
$ESCAPE     :: CLEAR THE ESCAPE FROM ERPOP ADDRESS
#1, $ERMAX  :: ONLY ALLOW ONE(1) ERROR ON NEXT TEST
$STSTM, $DISPLAY :: DISPLAY TEST NUMBER
$LPADR, (SP) :: FUDGE RETURN ADDRESS
FIXES PS    :: FIXES PS
MAX. NUMBER OF ITERATIONS

```

\$MXCNT: 4

.SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

\*\*\*\*\*  
\*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT  
\*SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE  
\*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED  
\*BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE  
\*REPLACED WITH SPACES.  
\*CALL:

\* MOV NUM, -(SP) ;:PUT THE BINARY NUMBER ON THE STACK  
\* TYPDS ;:GO TO THE ROUTINE

\$TYPDS:

11190	044666	010046			MOV	R0, -(SP)	::PUSH R0 ON STACK
11191	044668	010146			MOV	R1, -(SP)	::PUSH R1 ON STACK
11192	044670	010246			MOV	R2, -(SP)	::PUSH R2 ON STACK
11193	044672	010346			MOV	R3, -(SP)	::PUSH R3 ON STACK
11194	044674	010446			MOV	R4, -(SP)	::PUSH R4 ON STACK
11195	044676	010546			MOV	R5, -(SP)	::PUSH R5 ON STACK
11196	044700	012746	020200		MOV	#20200, -(SP)	::SET BLANK SWITCH AND SIGN
11197	044704	015605	000020		MOV	20(SP), R5	::GET THE INPUT NUMBER
11198	044710	100004			BPL	15	::BR IF INPUT IS POS.
11199	044712	005405			NEG	R5	::MAKE THE BINARY NUMBER POS.
11200	044714	112766	000055	000001	MOVB	#'-, 1(SP)	::MAKE THE ASCII NUMBER NEG.
11201	044722	005000		15:	CLR	R0	::ZERO THE CONSTANTS INDEX
11202	044724	012703	045102		MOV	#\$DBLK, R3	::SETUP THE OUTPUT POINTER
11203	044730	112723	000040		MOVB	#', (R3)+	::SET THE FIRST CHARACTER TO A BLANK
11204	044734	005002		25:	CLR	R2	::CLEAR THE BCD NUMBER
11205	044736	016001	045072		MOV	\$D9L(R0), R1	::GET THE CONSTANT
11206	044742	160105		35:	SUB	R1, R5	::FORM THIS BCD DIGIT
11207	044744	002402			BLT	45	::BR IF DONE
11208	044746	005202			INC	R2	::INCREASE THE BCD DIGIT B: 1
11209	044750	000774			BR	35	
11210	044752	050105		45:	ADD	R1, R5	::ADD BACK THE CONSTANT
11211	044754	005702			TST	R2	::CHECK IF BCD DIGIT=0
11212	044756	001002			BNE	55	::FALL THROUGH IF 0
11213	044760	105716			TSTB	(SP)	::STILL DOING LEADING 0'S?
11214	044762	100407			SM*	75	::BR IF YES
11215	044764	106316		55:	ASLB	(SP)	::MSD?
11216	044766	103003			BCC	65	::BR IF NO
11217	044770	116663	000001	:77777	MOVB	1(SP) - 1, R3	::YES--SET THE SIGN
11218	044776	052702	000050	65:	BIS	#'0, R2	::MAKE THE BCD DIGIT ASCII
11219	045002	052702	000040	75:	BIS	#', R2	::MAKE IT A SPACE IF NOT A READ A DIGIT
11220	045006	110223			MOVB	R2, (R3)+	::PUT THIS CHARACTER IN THE OUTPUT BUFFER
11221	045010	005720			TST	(R0)+	::JUST INCREMENTING
11222	045012	020027	000010		CMP	R0, #10	::CHECK THE TABLE INDEX
11223	045016	002746			BLT	25	::GO DO THE NEXT DIGIT
11224	045020	003002			BGT	85	::GO TO EXIT
11225	045022	010502			MOV	R5, R2	::GET THE LSD
11226	045024	000764			BR	65	::GO CHANGE TO ASCII
11227	045026	105726		85:	TSTB	(SP)+	::WAS THE LSD THE FIRST NON-ZERO?
11228	045030	100003			BPL	95	::BR IF NO
11229	045032	116663	177777	177775	MOVB	-1(SP), -2(R3)	::YES--SET THE SIGN FOR TYPING
11230	045040	105013		95:	CLRB	(R3)	::SET THE TERMINATOR
11231	045042	012605			MOV	(SP)+, R5	::POP STACK INTO R5

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045:02  
000002 000004

\$D\*BL: 10000.  
1000.  
100.  
10.  
\$D\*BL: .BL\*W 4

MOV (SP)+,R3  
MOV (SP)+,R2  
MOV (SP)+,R1  
MOV (SP)+,R0  
TYPE \$D\*BL  
MOV 2(SP),4(SP)  
MOV (SP)+,(SP)  
RTI

::POP STACK INTO R3  
::POP STACK INTO R2  
::POP STACK INTO R1  
::POP STACK INTO R0  
::NOW TYPE THE NUMBER  
::ADJUST THE STACK  
::RETURN TO USER

.SBTTL TYPE ROUTINE

```

*****
*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
*NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
*NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
*NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.

```

```

*CALL:
*1) USING A TRAP INSTRUCTION
*   TYPE      .MESADR      ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
*OR
*   TYPE
*   MESADR

```

```

11274
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```

045112 105737 001157
045116 100002
045120 000000
045122 000407
045124 010046
045126 017600 000002
045132 112046
045134 001005
045136 005726
045140 012600
045142 062716 000002
045146 000002
045150 122716 000011
045154 001430
045156 122716 000200
045162 001005
045164 005726
045166 104400
045170 001223
045172 105037 045326
045176 000755
045200 004737 045262
045204 123726 001156
045210 001350
045212 013746 001154
045216 105366 000001
045222 002770
045224 004737 045262
045230 105337 045326
045234 000770
045236 112716 000040
045242 004737 045262
045246 132737 000007 045326

```

```

$TYPE:  TSTB      $TFPL3      ;; IS THERE A TERMINAL?
        BPL       1$          ;; BR IF YES
        HALT      ;; HALT HERE IF NO TERMINAL
        BR        3$          ;; LEAVE
1$:      MOV       RD, -(SP)    ;; SAVE RD
        MOV       32(SP), RD   ;; GET ADDRESS OF ASCIZ STRING
2$:      MOVB     (RD)+, -(SP) ;; PUSH CHARACTER TO BE TYPED ONTO STACK
        BNE      4$          ;; BR IF IT ISN'T THE TERMINATOR
        TST      (SP)+        ;; IF TERMINATOR POP IT OFF THE STACK
60$:     MOV       (SP)+, RD    ;; RESTORE RD
3$:      ADD      #2, (SP)     ;; ADJUST RETURN PC
        RTI          ;; RETURN
4$:      CMPB     #HT, (SP)    ;; BRANCH IF <HT>
        BEQ      8$          ;; BRANCH IF NOT <CRLF>
        CMPB     #CRLF, (SP)
        BNE      5$          ;; POP <CR><LF> EQUIV
        TST      (SP)+        ;; TYPE A CR AND LF
        TYPE     $CRLF
11280:   CLRB     $CHARCNT    ;; CLEAR CHARACTER COUNT
        BR        2$          ;; GET NEXT CHARACTER
11282:   JSR      PC, $TYPEC   ;; GO TYPE THIS CHARACTER
11283:   CMPB     $FILLC, (SP)+ ;; IS IT TIME FOR FILLER CHARS.?
        BNE      2$          ;; IF NO GO GET NEXT CHAR.
        MOV      $NULL, -(SP) ;; GET # OF FILLER CHARS. NEEDED
        AND      THE NULL CHAR.
11287:   DECB     1(SP)       ;; DOES A NULL NEED TO BE TYPED?
        BLT      6$          ;; BR IF NO--GO POP THE NULL OFF OF STACK
        JSR      PC, $TYPEC   ;; GO TYPE A NULL
11290:   DECB     $CHARCNT    ;; DO NOT COUNT AS A COUNT
        BR        7$          ;; LOOP

```

;HORIZONTAL TAB PROCESSOR

```

8$:      MOVB     #' , (SP)    ;; REPLACE TAB WITH SPACE
9$:      JSR      PC, $TYPEC   ;; TYPE A SPACE
        BITB     #7, $CHARCNT ;; BRANCH IF NOT AT

```



11300	045254	001372				BNE	9\$	::TAB STOP
11301	045256	005726				TST	(SP)+	::POP SPACE OFF STACK
11302	045260	000724				BR	2\$	::GET NEXT CHARACTER
11303	045262	105777	133662		\$TYPEC:	TSTB	2\$TPS	::WAIT UNTIL PRINTER IS READY
11304	045266	100375				BPL	\$TYPEC	
11305	045270	116677	000002	133654		MOV8	2(SP), 2\$TPB	::LOAD CHAR TO BE TYPED INTO DATA REG.
11306	045276	122766	000015	000002		CMPB	#CR, 2(SP)	::IS CHARACTER A CARRIAGE RETURN?
11307	045304	001003				BNE	1\$	::BRANCH IF NO
11308	045306	105037	045326			CLRB	\$CHARCNT	::YES--CLEAR CHARACTER COUNT
11309	045312	000406				BR	\$TYPEX	::EXIT
11310	045314	122766	000012	000002	1\$:	CMPB	#LF, 2(SP)	::IS CHARACTER A LINE FEED?
11311	045322	001402				BEQ	\$TYPEX	::BRANCH IF YES
11312	045324	105227				INCB	(PC)+	::COUNT THE CHARACTER
11313	045326	000000			\$CHARCNT:	WORD	0	::CHARACTER COUNT STORAGE
11314	045330	000207			\$TYPEX:	RTS	PC	

```

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11319 045332 000000
11319 045334 000000
11320 045336 000000
11321 045340 000011
11322 045351
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11333 045352 005037 045332
11334 045356 012737 045340 045334
11335 045364 013737 045334 045336
11336 045372 012737 045422 000060
11337 045400 012737 000200 000062
11338 045406 005777 133534
11339 045412 012777 000100 133524
11340 045420 000207
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11349 045422 117746 133520
11350 045426 042716 177600
11351 045432 021627 000003
11352 045436 001007
11353 045440 104400 046411
11354 045444 004737 045352
11355 045450 005726
11356 045452 000137 042512
11357 045456 021627 000007
11358 045462 001004
11359 045464 022737 000176 001140
11360 045472 001500
11361
11362 045474
11363 045474 022737 000011 045332
11364 045502 001004
11365 045504 104400 001216
11366 045510 005726
11367 045512 000451

```

```

.SBTTL TTY INPUT ROUTINE
*****
.ENABL LSB
$TKCNT: .WORD 0          ;; NUMBER OF ITEMS IN QUEUE
$TKQIN: .WORD 0          ;; INPUT POINTER
$TKQOUT: .WORD 0        ;; OUTPUT POINTER
$TKQSRT: .BLKB 9.       ;; TTY KEYBOARD QUEUE
$TKQEND=.
.EVEN

;*TK INITIALIZE ROUTINE
;*THIS ROUTINE WILL INITIALIZE THE TTY KEYBOARD INPUT QUEUE
;*SETUP THE INTERRUPT VECTOR AND TURN ON THE KEYBOARD INTERRUPT
:
*CALL:
*      JSR      PC,$TKINT
*      RETURN
:
$TKINT: CLR      $TKCNT          ;; CLEAR COUNT OF ITEMS IN QUEUE
        MOV      $TKQSRT,$TKQIN ;; MOVE THE STARTING ADDRESS OF THE
        MOV      $TKQIN,$TKQOUT ;; QUEUE INTO THE INPUT & OUTPUT POINTERS.
        MOV      $TKSRV,$TKVEC  ;; INITIALIZE THE KEYBOARD VECTOR
        MOV      #200,$TKVEC+2  ;; "BR" LEVEL 4
        TST     $TKB           ;; CLEAR DONE FLAG
        MOV      #100,$TKS     ;; ENABLE TTY KEYBOARD INTERRUPT
        RTS      PC           ;; RETURN TO CALLER

;*TK SERVICE ROUTINE
;*THIS ROUTINE WILL SERVICE THE TTY KEYBOARD INTERRUPT
;*BY READING THE CHARACTER FROM THE INPUT BUFFER AND PUTTING
;*IT IN THE QUEUE.
;*IF THE CHARACTER IS A "CONTROL-C" (1C) $TKINT IS CALLED AND
;*UPON RETURN EXIT IS MADE TO THE "CONTROL-C" RESTART ADDRESS .OPERSEL
:
$TKSRV: MOVB    $TKB,-(SP)      ;; PICKUP THE CHARACTER
        BIC     #1C177,(SP)    ;; STRIP THE JUNK
        CMP     (SP),#3        ;; IS IT A CONTROL C?
        BNE    1$             ;; BRANCH IF NO
        TYPE   $CNTLC         ;; TYPE A CONTROL-C (1C)
        JSR    PC,$TKINT      ;; INIT THE KEYBOARD
        TST   (SP)+          ;; CLEAN UP STACK
        JMP   OPERSEL         ;; CONTROL C RESTART
1$:     CMP     (SP),#7        ;; IS IT A CONTROL G?
        BNE    2$             ;; BRANCH IF NO
        CMP   $SWREG,$SWR     ;; IS SOFT-SWR SELECTED?
        BEQ   6$             ;; GO TO SWR CHANGE
2$:     CMP     #9,$TKCNT      ;; IS THE QUEUE FULL?
        BNE    3$             ;; BRANCH IF NO
        TYPE   $BELL          ;; RING THE TTY BELL
        TST   (SP)+          ;; CLEAN CHARACTER OFF OF STACK
        BR    5$             ;; EXIT
6$:
5$:

```

```

11368 045514 021627 000023 3$: CMP (SP),#23 ;; IS IT A CONTROL-S?
11369 045520 001021 BNE 32$ ;; BRANCH IF NO
11370 045522 005077 133416 CLR 2$TKS ;; DISABLE TTY KEYBOARD INTERRUPTS
11371 045526 005726 TST (SP)+ ;; CLEAN CHAR OFF STACK
11372 045530 105777 133410 31$: TSTB 2$TKS ;; WAIT FOR A CHAR
11373 045534 100375 BPL 31$ ;; LOOP UNTIL ITS THERE
11374 045536 117746 133404 MOVB 2$TKB,-(SP) ;; GET THE CHARACTER
11375 045542 042716 177600 BIC #1C177,(SP) ;; MAKE IT 7-BIT ASCII
11376 045546 022627 000021 CMP (SP)+,#21 ;; IS IT A CONTROL-Q?
11377 045552 001366 BNE 31$ ;; BRANCH IF NO
11378 045554 012777 000100 133352 MOV #100,2$TKS ;; REENABLE TTY KEYBOARD INTERRUPTS
11379 045556 000002 RTI ;; RETURN
11380 045564 005237 045332 32$: INC $TKCNT ;; COUNT THIS CHARACTER
11381 045570 021627 000140 CMP (SP),#140 ;; IS IT UPPER CASE?
11382 045574 002405 BLT 4$ ;; BRANCH IF YES
11383 045576 021627 000175 CMP (SP),#175 ;; IS IT A SPECIAL CHAR?
11384 045602 003002 BGT 4$ ;; BRANCH IF YES
11385 045604 042716 000040 BIC #40,(SP) ;; MAKE IT UPPER CASE
11386 045610 112677 177520 4$: MOVB (SP)+,2$TKQIN ;; AND PUT IT IN QUEUE
11387 045614 005237 045334 INC $TKQIN ;; UPDATE THE POINTER
11388 045620 023727 045334 045351 CMP $TKQIN,$TKQEND ;; GO OFF THE END?
11389 045626 001003 BNE 5$ ;; BRANCH IF NO
11390 045630 012737 045340 045334 MOV #1,$TKQSRRT,$TKQIN ;; RESET THE POINTER
11391 045636 000002 5$: RTI ;; RETURN
11392
11393 ;;*****
11394 ;;*SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
11395 ;;*ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
11396 ;;*SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP
11397 ;;*CALL WHEN OPERATING IN TTY INTERRUPT MODE.
11398 045640 022737 000176 001140 $CKSWR: CMP #SWREG,SWR ;; IS THE SOFT-SWR SELECTED
11399 045646 001124 BNE 15$ ;; EXIT IF NOT
11400 045650 105777 133270 TSTB 2$TKS ;; IS A CHAR WAITING?
11401 045654 100121 BPL 15$ ;; IF NOT, EXIT
11402 045656 117746 133264 MOVB 2$TKB,-(SP) ;; YES
11403 045662 042716 177600 BIC #1C177,(SP) ;; MAKE IT 7-BIT ASCII
11404 045666 021627 000007 CMP (SP),#7 ;; IS IT A CONTROL-G?
11405 045672 001300 BNE 2$ ;; IF NOT, PUT IT IN THE TTY QUEUE
11406
11407 ;;AND EXIT
11408
11409 ;;*****
11410 ;;*CONTROL IS PASSED TO THIS POINT FROM EITHER THE TTY INTERRUPT SERVICE
11411 ;;*ROUTINE OR FROM THE SOFTWARE SWITCH REGISTER TRAP CALL, AS A RESULT OF A
11412 ;;*CONTROL-G BEING TYPED, AND THE SOFTWARE SWITCH REGISTER BEING SELECTED.
11413 045674 123727 001134 000001 6$: CMPB $AUTOB,#1 ;; ARE WE RUNNING IN AUTO-MODE?
11414 045702 001674 BEQ 2$ ;; BRANCH IF YES
11415 045704 005726 TST (SP)+ ;; CLEAR CONTROL-G OFF STACK
11416 045706 004737 045352 JSR PC,$TKINT ;; FLUSH THE TTY INPUT QUEUE
11417 045712 005077 133226 CLR 2$TKS ;; DISABLE TTY KEYBOARD INTERRUPTS
11418 045716 112737 000001 001135 MOVB #1,$INTAG ;; SET INTERRUPT MODE INDICATOR
11419 045724 104400 046423 $GTSWR: TYPE , $CNTLG ;; ECHO THE CONTROL-G (↑G)
11420 045730 104400 046430 TYPE $MSWR ;; TYPE CURRENT CONTENTS
11421 045734 013746 000176 MOV $WREG,-(SP) ;; SAVE SWREG FOR TYPEOUT

```

11422	045740	104401				TYPOC		::GO TYPE--OCTAL ASCII(ALL DIGITS,
11423	045742	104400	046441			TYPE	.\$MNEW	::PROMPT FOR NEW SWR
11424	045746	005046		19\$:		CLR	-(SP)	::CLEAR COUNTER
11425	045750	005046				CLR	-(SP)	::THE NEW SWR
11426	045752	105777	133166	7\$:		TSTB	2\$TKS	::CHAR THERE?
11427	045756	100375				BPL	7\$	::IF NOT TRY AGAIN
11428								
11429	045760	117746	133162			MOVB	2\$TKB, -(SP)	::PICK UP CHAR
11430	045764	042716	177600			BIC	#1C177, (SP)	::MAKE IT 7-BIT ASCII
11431								
11432	045770	021627	000003			CMP	(SP), #3	::IS IT A CONTROL-C?
11433	045774	001015				BNE	9\$	::BRANCH IF NOT
11434	045776	104400	046411			TYPE	.\$CNTLC	::YES, ECHO CONTROL-C (↑C)
11435	046002	062706	000006			ADD	#6, SP	::CLEAN UP STACK
11436	046006	123727	001135	000001		CMPB	\$INTAG, #1	::REENABLE TTY KEYBOARD INTERRUPTS?
11437	046014	001003				BNE	8\$	::BRANCH IF NO
11438	046016	012777	000100	133120		MOV	#100, 2\$TKS	::ALLOW TTY KEYBOARD INTERRUPTS
11439	046024	000137	042512	8\$:		JMP	OPERSEL	::CONTROL-C RESTART
11440								
11441								
11442	046030	021627	000025	9\$:		CMP	(SP), #25	::IS IT A CONTROL-U?
11443	046034	001005				BNE	10\$	::BRANCH IF NOT
11444	046036	104400	046416			TYPE	.\$CNTLU	::YES, ECHO CONTROL-U (↑U)
11445	046042	062706	000006	20\$:		ADD	#6, SP	::IGNORE PREVIOUS INPUT
11446	046046	000737				BR	19\$	::LET'S TRY IT AGAIN
11447								
11448								
11449	046050	021627	000015	10\$:		CMP	(SP), #15	::IS IT A <CR>?
11450	046054	001022				BNE	16\$	::BRANCH IF NO
11451	046056	005766	000004			TST	4(SP)	::YES, IS IT THE FIRST CHAR?
11452	046062	001403				BEQ	11\$	::BRANCH IF YES
11453	046064	015677	000002	133046		MOV	2(SP), 2\$SWR	::SAVE NEW SWR
11454	046072	062706	000006		11\$:	ADD	#6, SP	::CLEAR UP STACK
11455	046076	104400	001223		14\$:	TYPE	.\$CRLF	::ECHO <CR> AND <LF>
11456	046102	123727	001135	000001		CMPB	\$INTAG, #1	::RE-ENABLE TTY KBD INTERRUPTS?
11457	046110	001003				BNE	15\$	::BRANCH IF NOT
11458	046112	012777	000100	133024		MOV	#100, 2\$TKS	::RE-ENABLE TTY KBD INTERRUPTS
11459	046120	000002			15\$:	RTI		::RETURN
11460	046122	004737	045262		16\$:	JSR	PC, \$TYPEC	::ECHO CHAR
11461	046126	021627	000060			CMP	(SP), #60	::CHAR < C?
11462	046132	002420				BLT	18\$	::BRANCH IF YES
11463	046134	021627	000067			CMP	(SP), #67	::CHAR > ?
11464	046140	003015				BGT	18\$	::BRANCH IF YES
11465	046142	042726	000060			BIC	#60, (SP)+	::STRIP-OFF ASCII
11466	046146	005766	000002			TST	2(SP)	::IS THIS THE FIRST CHAR
11467	046152	001403				BEQ	17\$	::BRANCH IF YES
11468	046154	006316				ASL	(SP)	::NO, SHIFT PRESENT
11469	046156	006316				ASL	(SP)	::CHAR OVER TO MAKE
11470	046160	006316				ASL	(SP)	::ROOM FOR NEW ONE.
11471	046162	005266	000002		17\$:	INC	2(SP)	::KEEP COUNT OF CHAR
11472	046166	056616	177776			BIS	-2(SP), (SP)	::SET IN NEW CHAR
11473	046172	000667				BR	7\$	::GET THE NEXT ONE
11474	046174	104400	001222		18\$:	TYPE	.\$QUES	::TYPE ?<CR><LF>
11475	046200	000720				BR	20\$	::SIMULATE CONTROL-U

```

11476 .DSABL LSB
11477
11478
11479 ;:*****
11480 ;:THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
11481 ;:CALL:
11482 ;: RDCHR ;:GET A CHARACTER FROM THE QUEUE
11483 ;: RETURN HERE ;:CHARACTER IS ON THE STACK
11484 ;: ;:WITH PARITY BIT STRIPPED OFF
11485 ;:
11486
11487 046202 011646 $RDCHR: MOV (SP),-(SP) ;:PUSH DOWN THE PC AND
11488 046204 016666 000004 000002 MOV 4(SP),2(SP) ;:THE PS
11489 046212 005066 000004 CLR 4(SP) ;:GET READY FOR A CHARACTER
11490 046216 005046 CLR -(SP) ;:PUT NEW PS ON STACK
11491 046220 012746 046226 MOV #64$,-(SP) ;:PUT NEW PC ON STACK
11492 046224 000002 RTI ;:POP NEW PC AND PS
11493
11494 046226 005737 045332 64$: TST $TKCNT ;:WAIT ON A CHARACTER
11495 046232 001775 1$: BEQ 1$
11496 046234 005337 045332 DEC $TKCNT ;:DECREMENT THE COUNTER
11497 046240 117766 177072 000004 MOVB $TKQOUT,4(SP) ;:GET ONE CHARACTER
11498 046246 005237 045336 INC $TKQOUT ;:UPDATE THE POINTER
11499 046252 023727 045336 045351 CMP $TKQOUT,#$TKQEND ;:DID IT GO OFF OF THE END?
11500 046260 001003 BNE 2$ ;:BRANCH IF NO
11501 046262 012737 045340 045336 MOV #$TKQSRT,$TKQOUT ;:RESET THE POINTER
11502 046270 000002 2$: RTI ;:RETURN
11503 ;:*****
11504 ;:THIS ROUTINE WILL INPUT A STRING FROM THE TTY
11505 ;:CALL:
11506 ;: RDLIN ;:INPUT A STRING FROM THE TTY
11507 ;: RETURN HERE ;:ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
11508 ;: ;:TERMINATOR WILL BE A BYTE OF ALL 0'S
11509 ;:
11510 046272 010346 $RDLIN: MOV R3, -(SP) ;:SAVE R3
11511 046274 012703 046400 1$: MOV #$TTYIN,R3 ;:GET ADDRESS
11512 046300 022703 046411 2$: CMP #$TTYIN+9.,R3 ;:BUFFER FULL?
11513 046304 101405 BLOS 4$ ;:BR IF YES
11514 046306 104407 RDCHR ;:GO READ ONE CHARACTER FROM THE TTY
11515 046310 112613 MOVB (SP)+,(R3) ;:GET CHARACTER
11516 046312 122713 000177 10$: CMPB #177,(R3) ;:IS IT A RUBOUT
11517 046316 001003 BNE 3$ ;:SKIP IF NOT
11518 046320 104400 001222 4$: TYPE $QUES ;:TYPE A '?'
11519 046324 000763 BR 1$ ;:CLEAR THE BUFFER AND LOOP
11520 046326 111337 046376 3$: MOVB (R3),9$ ;:ECHO THE CHARACTER
11521 046332 104400 046376 TYPE 9$
11522 046336 122723 000015 CMPB #15,(R3)+ ;:CHECK FOR RETURN
11523 046342 001356 BNE 2$ ;:LOOP IF NOT RETURN
11524 046344 105063 177777 CLRB -1(R3) ;:CLEAR RETURN (THE 15)
11525 046350 104400 001224 TYPE $LF ;:TYPE A LINE FEED
11526 046354 012603 MOV (SP)+,R3 ;:RESTORE R3
11527 046356 011646 MOV (SP),-(SP) ;:ADJUST THE STACK AND PUT ADDRESS OF THE
11528 046360 016666 000004 000002 MOV 4(SP),2(SP) ;:FIRST ASCII CHARACTER ON IT
11529 046366 012766 046400 000004 MOV #$TTYIN,4(SP)

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11530 046374 000002          R/I          ;:RETURN
11531 046376          000          ;:STORAGE FOR ASCII CHAR. TO TYPE
11532 046377          000          ;:TERMINATOR
11533 046400 000011          ;:RESERVE 9. BYTES FOR TTY INPUT
11534 046411          136 006503 000012 $TTYIN:          ;:CONTROL "C"
11535 046416 052536 005015          000 $CNTLC: .ASCIZ /TC<15><12> ;:CONTROL "U"
11536 046423          136 006507 000012 $CNTLU: .ASCIZ /TU<15><12> ;:CONTROL "G"
11537 046430 005015 053523 020122 $CNTLG: .ASCIZ /TG<15><12> ;:CONTROL "G"
11538 046436 020075          000 $MSWR: .ASCIZ <15><12>/SWR = /
11539 046441          040 047040 053505 $MNEW: .ASCIZ / NEW = /
11540 046446 036440 000040
11541

```

;FROM THE TTY

```

11542          .SBTTL  READ AN OCTAL NUMBER FROM THE TTY
11543
11544          ;;*****
11545          ;;*THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
11546          ;;*CHANGE IT TO BINARY.
11547          ;;*THE INPUT CHARACTERS WILL BE CHECKED TO INSURED THEY ARE LEGAL
11548          ;;*OCTAL DIGITS. IF AN ILLEGAL CHARACTER IS READ A "?" WILL BE TYPED
11549          ;;*FOLLOWED BY A CARRIAGE RETURN-LINE FEED. THE COMPLETE NUMBER MUST
11550          ;;*THEN BE RETYPED. THE INPUT IS TERMINATED BY TYPING A CARRIAGE RETURN.
11551          ;;*CALL:
11552          ;;*      RDOCT          ;;:READ AN OCTAL NUMBER
11553          ;;*      RETURN HERE  ;;:LOW ORDER BITS ARE ON TOP OF THE STACK
11554          ;;*                  ;;:HIGH ORDER BITS ARE IN $HIOCT
11555
11556 046452 011646 $RDOCT: MOV      (SP),-(SP)  ;;:PROVIDE SPACE FOR THE
11557 046454 016666 000004 000002 MOV      4(SP),2(SP)  ;;:INPUT NUMBER
11558 046462 010046 MOV      R0,-(SP)  ;;:PUSH R0 ON STACK
11559 046464 010146 MOV      R1,-(SP)  ;;:PUSH R1 ON STACK
11560 046466 010246 MOV      R2,-(SP)  ;;:PUSH R2 ON STACK
11561 046470 104410 1$:  RDLIN  ;;:READ AN ASCII LINE
11562 046472 012600 MOV      (SP)+,R0  ;;:GET ADDRESS OF 1ST CHARACTER
11563 046474 010037 046600 MOV      R0,5$  ;;:AND SAVE IT
11564 046500 005001 CLR      R1  ;;:CLEAR DATA WORD
11565 046502 005002 CLR      R2
11566 046504 112346 2$:  MOV8B  (R0)+,-(SP)  ;;:PICKUP THIS CHARACTER
11567 046506 001420 BEQ      3$  ;;:IF ZERO GET OUT
11568 046510 122716 000060 CMPB    #'0,(SP)  ;;:MAKE SURE THIS CHARACTER
11569 046514 003026 BGT      4$  ;;:IS AN OCTAL DIGIT
11570 046516 122716 000067 CMPB    #'7,(SP)
11571 046522 002423 BLT      4$
11572 046524 006301 ASL      R1  ;;:*2
11573 046526 006102 ROL      R2
11574 046530 006301 ASL      R1  ;;:*4
11575 046532 006102 ROL      R2
11576 046534 006301 ASL      R1  ;;:*8
11577 046536 006102 ROL      R2
11578 046540 042716 177770 BIC      #'C7,(SP)  ;;:STRIP THE ASCII JUNK
11579 046544 062601 ADD      (SP)+,R1  ;;:ADD IN THIS DIGIT
11580 046546 000756 BR       2$  ;;:LOOP
11581 046550 005726 3$:  TST      (SP)+  ;;:CLEAN TERMINATOR FROM STACK
11582 046552 010166 000012 MOV      R1,12(SP)  ;;:SAVE THE RESULT
11583 046556 010237 046610 MOV      R2,$HIOCT
11584 046562 012602 MOV      (SP)+,R2  ;;:POP STACK INTO R2
11585 046564 012601 MOV      (SP)+,R1  ;;:POP STACK INTO R1
11586 046566 012600 MOV      (SP)+,R0  ;;:POP STACK INTO R0
11587 046570 000002 RTI     ;;:RETURN
11588 046572 005726 4$:  TST      (SP)+  ;;:CLEAN PARTIAL FROM STACK
11589 046574 105010 CLR8    (R0)  ;;:SET A TERMINATOR
11590 046576 104400 TYPE    ;;:TYPE UP THRU THE BAD CHAR.
11591 046600 000000 5$:  .WORD  0
11592 046602 104400 001222 TYPE    $QUES  ;;:"?" "CR" & "LF"
11593 046606 000730 BR       1$  ;;:TRY AGAIN
11594 046610 000000 $HIOCT: .WORD  0  ;;:HIGH ORDER BITS GO HERE

```

.SBTTL ERROR HANDLER ROUTINE

```

*****
*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT.
*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
*AND GO TO SERATYP ON ERROR
*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
*SW15=1 HALT ON ERROR
*SW13=1 INHIBIT ERROR TYPEOUTS
*SW10=1 BELL ON ERROR
*SW09=1 LOOP ON ERROR
*CALL
*      ERROR      N      ::ERROR=EMT AND N=ERROR ITEM NUMBER

```

```

046612
046612
046614
046614
046622
046622
046626
046630
046636
046644
046646
046652
046656
046662
046670
046676
046704
046706
046712
046716
046716
046722
046724
046726
046730
046736
046740
046744
046750
046752
046756
046756

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104406
012737 177777 004734
105237 001103
001775
013777 001102 132304
032777 002000 132274
001402
104400 001216
005237 001112
011637 001116
162737 000002 001116
117737 132222 001114
032777 020000 132234
001004
004737 046762
104400 001223
005777 132216
100002
000000
104406
032777 001000 132202
001402
013716 001110
005737 001214
001402
013716 001214
000002

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ERROR:      CKSWR      ::TEST FOR CHANGE IN SOFT-SWR

REGSAV:     MOV      #1,2*ERFLG  ;SET ERROR FLAG
REGSA1:     7$:      INCB     SERFLG      ;SET THE ERROR FLAG
              BEQ      7$         ;DON'T LET THE FLAG GO TO ZERO
              MOV     $STNM,2$DISPLAY ;DISPLAY TEST NUMBER AND ERROR FLAG
              BIT     #BIT10,2$SWR  ;BELL ON ERROR?
              BEQ     1$           ;NO - SKIP
              TYPE   3BELL        ;RING BELL
              1$:     INC     $ERTTL  ;COUNT THE NUMBER OF ERRORS
              MOV    (SP), $ERRPC   ;GET ADDRESS OF ERROR INSTRUCTION
              SUB    #2, $ERRPC
              MOVB  2$ERRPC, 2$ITEMB ;STRIP AND SAVE THE ERROR ITEM CODE
              BIT   #BIT13,2$SWR  ;SKIP TYPEOUT IF SET
              BNE   20$           ;SKIP TYPEOUTS
              JSR   PC, $SERATYP  ;GO TO USER ERROR ROUTINE
              TYPE  , $CRLF

              20$:
              2$:     TST     2$SWR  ;HALT ON ERROR
              BPL   3$           ;SKIP IF CONTINUE
              HALT  ;HALT ON ERROR!
              CKSWR ;TEST FOR CHANGE IN SOFT-SWR
              3$:     BIT     #BIT09,2$SWR ;LOOP ON ERROR SWITCH SET?
              BEQ   4$           ;BR IF NO
              MOV   $LPERR, (SP)  ;FUDGE RETURN FOR LOOPING
              4$:     TST     $ESCAPE ;CHECK FOR AN ESCAPE ADDRESS
              BEQ   5$           ;BR IF NONE
              MOV   $ESCAPE, (SP) ;FUDGE RETURN ADDRESS FOR ESCAPE
              5$:
              RTI      ;RETURN

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046760 000000  
046762 017746 132152  
046766 042716 000100  
046772 022726 000100  
046776 001001  
047000 000402  
047002 000137 047722  
047006  
047006 104400 047014  
047012 000406  
047030  
047030 013746 002354  
047034 104401  
047036 104400 047044  
047042 000406  
047060  
047060 013746 002356  
047064 104401  
047066 104400 047074  
047072 000406  
047110  
047110 013746 002360  
047114 104401  
047116 104400 047124  
047122 000406

\*\*\*\*\*

.SBTTL ERROR MESSAGE TYPEOUT ROUTINE

.\*THIS ROUTINE USES THE "ITEM CONTROL BYTE" (SITEMB) TO DETERMINE WHICH  
.\*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" (SEARR3),  
.\*AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.  
.\*IT IS A COPY OF THE SEARTYP SUBROUTINE FROM SYSMAC.  
.\*WITH ONLY MINOR CHANGES  
.\*FIRST IF SWITCH 6 IS SET AND SWITCH 8 RESET THEN  
.\*ALL REGISTER CONTENTS WILL BE TYPED BEFOR REPORTING THE ERROR  
.\*SECOND IF THE CURRENT ERROR HAS THE SAME ITEM NUMBER  
.\*AS THE PREVIOUS ERROR THEN ONLY THE DATA WILL BE TYPED  
.\*AND NOT THE ERROR MESSAGE AND HEADER.

PRITEM: 0 ;PREVIOUS ITEM NO. LOCATION

SEARTYP: MOV @SWR,-(SP) ;GET SWITCH SETTING  
BIC #C500,(SP) ;KEEP ONLY SWITCH 8 AND 6  
CMP #SW06,(SP)+ ;IS 6 SET AND 8 RESET  
BNE 15 ;IF NOT BRANCH  
BR 25 ;BRANCH IF SW 6 IS SET AND 8 RESET  
JMP @TYPERR ;JUMP IF SW 8 IS SET  
 ;OR IF SW 8 IS RESET AND SW 6 IS RESET

25:  
TYPE 655 ;:TYPE ASCIZ STRING  
BR 648 ;:GET OVER THE ASCIZ  
655: .ASCIZ (15)<12> RHWC =  
648: MOV @RWC,-(SP) ;GET READY TO TYPE RHWC CONTENTS  
TYPC

675: .ASCIZ (15)<12> RHBA =  
669: MOV @RBA,-(SP) ;GET READY TO TYPE RHBA CONTENTS  
TYPC

695: .ASCIZ (15)<12> RHCS2 =  
689: MOV @RCS2,-(SP) ;GET READY TO TYPE RHCS2 CONTENTS  
TYPC

715: .ASCIZ (15)<12> RHCS1 =  
709: .ASCIZ (15)<12> RHCS1 =

```

11697 047140          705:
11698 047140 013746 002362      MOV      3#CS1,-(SP)      ;GET READY TO TYPE RHCS1 CONTENTS
11699 047144 104401          TYPOC
11700
11701
11702 047146 104400 047154      TYPE      735           ;;TYPE ASCIZ STRING
11703 047152 000406          BR        725           ;;GET OVER THE ASCIZ
11704 047154          735: .ASCIZ  <15><12>/RHDS1 = /
11705
11706 047170          725:
11707 047170 013746 002404      MOV      3#DS1,-(SP)    ;GET READY TO TYPE RHDS1 CONTENTS
11708 047174 104401          TYPOC
11709
11710
11711 047176 104400 047204      TYPE      755           ;;TYPE ASCIZ STRING
11712 047202 000406          BR        745           ;;GET OVER THE ASCIZ
11713 047204          755: .ASCIZ  <15><12>/RHER1 = /
11714
11715 047220          745:
11716 047220 013746 002364      MOV      3#ER1,-(SP)   ;GET READY TO TYPE RHER1 CONTENTS
11717 047224 104401          TYPOC
11718
11719
11720 047226 104400 047234      TYPE      775           ;;TYPE ASCIZ STRING
11721 047232 000406          BR        765           ;;GET OVER THE ASCIZ
11722 047234          775: .ASCIZ  <15><12>/RHER2 = /
11723
11724 047250          765:
11725 047250 013746 002370      MOV      3#ER2,-(SP)   ;GET READY TO TYPE RHER2 CONTENTS
11726 047254 104401          TYPOC
11727
11728
11729 047256 104400 047264      TYPE      795           ;;TYPE ASCIZ STRING
11730 047262 000406          BR        785           ;;GET OVER THE ASCIZ
11731 047264          795: .ASCIZ  <15><12>/RHER3 = /
11732
11733 047300          785:
11734 047300 013746 002376      MOV      3#ER3,-(SP)   ;GET READY TO TYPE RHER3 CONTENTS
11735 047304 104401          TYPOC
11736
11737
11738 047306 104400 047314      TYPE      915           ;;TYPE ASCIZ STRING
11739 047312 000406          BR        905           ;;GET OVER THE ASCIZ
11740 047314          915: .ASCIZ  <15><12>/RHDST = /
11741
11742 047330          905:
11743 047330 013746 002366      MOV      3#DST,-(SP)   ;GET READY TO TYPE RHDST CONTENTS
11744 047334 104401          TYPOC
11745
11746
11747 047336 104400 047344      TYPE      935           ;;TYPE ASCIZ STRING
11748 047342 000406          BR        925           ;;GET OVER THE ASCIZ
11749 047344          935: .ASCIZ  <15><12>/RHCA = /
11750
11751 047360          925:
11752 047360 013746 002374      MOV      3#CA,-(SP)    ;GET READY TO TYPE RHCA CONTENTS
11753 047364 104401          TYPOC
11754
11755
11756 047366 104400 047374      TYPE      955           ;;TYPE ASCIZ STRING

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11805
11806 047636 104400 047644 TYPE 99$ ::TYPE ASCIZ STRING
11807 047642 000406 BR 99$ ::GET OVER THE ASCIZ
11808 ::99$: .ASCIZ <15><12>RHD =
11809 047660 98$: MOV 3*DT,-(SP) :GET READY TO TYPE RHD CONTENTS
11810 047660 013746 002406 TPOC
11811 047664 104401
11812
11813 047666 104400 047674 TYPE 101$ ::TYPE ASCIZ STRING
11814 047672 000406 BR 100$ ::GET OVER THE ASCIZ
11815 ::101$: .ASCIZ <15><12>RHSN =
11816 100$: MOV 2*SN,-(SP) :GET READY TO TYPE RHSN CONTENTS
11817 047710 TPOC
11818 047710 013746 002410
11819 047714 104401
11820
11821 047716 005037 046760 CLR 2*PRITEM ;CLEAR PREVIOUS ERROR ITEM
11822 047722 TYPERR:
11823 047722 104400 001223 TYPE $CRLF ;"CARRIAGE RETURN" & "LINE FEED"
11824 047726 010046 MOV R0,-(SP) ;SAVE R0
11825 047730 005000 CLR R0 ;PICKUP THE ITEM INDEX
11826 047732 153700 001114 BISB 2*$ITEMB,R0
11827 047736 001004 BNE 1$ ;IF ITEM NUMBER IS ZERO, JUST
11828 ;TYPE THE PC OF THE ERROR
11829 047740 013746 001116 MOV $ERRPC,-(SP) ;SAVE $ERRPC FOR TYPEOUT
11830 ;ERROR ADDRESS
11831 047744 104401 TPOC ;GO TYPE--OCTAL ASCII(ALL DIGITS)
11832 047746 000454 BR 10$ ;GET OUT
11833 047750 005300 1$: DEC R0 ;ADJUST THE INDEX SO THAT IT WILL
11834 047752 006300 ASL R0 ;WORK FOR THE ERROR TABLE
11835 047754 006300 ASL R0
11836 047756 006300 ASL R0
11837 047760 062700 001226 ADD #SERRTB,R0 ;FORM TABLE POINTER
11838 047764 020037 046760 CMP R0,2*PRITEM ;WAS PREVIOUS ERROR SAME
11839 047770 001002 BNE 13$ ;BRANCH IF NOT
11840 047772 022020 CMP (R0)+,(R0)+ ;POP R0 OVER EM AND DH
11841 047774 000420 BR 5$
11842 047776 010037 046760 13$: MOV R0,2*PRITEM ;SAVE NEW ERROR ITEM
11843 050002 012037 050012 MOV (R0)+,2$ ;PICKUP "ERROR MESSAGE" POINTER
11844 050006 001404 BEQ 3$ ;SKIP TYPEOUT IF NO POINTER
11845 050010 104400 TYPE ;TYPE THE "ERROR MESSAGE"
11846 050012 000000 2$: .WORD 0 ;"ERROR MESSAGE" POINTER GOES HERE
11847 050014 104400 001223 TYPE $CRLF ;"CARRIAGE RETURN" & "LINE FEED"
11848 050020 012037 050030 3$: MOV (R0)+,4$ ;PICKUP "DATA HEADER" POINTER
11849 050024 001404 BEQ 5$ ;SKIP TYPEOUT IF 0
11850 050026 104400 TYPE ;TYPE THE "DATA HEADER"
11851 050030 000000 4$: .WORD 0 ;"DATA HEADER" POINTER GOES HERE
11852 050032 104400 001223 TYPE $CRLF ;"CARRIAGE RETURN" & "LINE FEED"
11853 050036 010146 MOV R1,-(SP) ;SAVE R1
11854 050040 012001 MOV (R0)+,R1 ;PICKUP "DATA TABLE" POINTER
11855 050042 001415 BEQ 9$ ;BR IF NO DATA TO BE TYPED
11856 050044 012000 MOV (R0)+,R0 ;PICKUP "DATA FORMAT" POINTER
11857 050046 105720 6$: TSTB (R0)+ ;"OCTAL" OR "DECIMAL"
11858 050050 001003 BNE 7$ ;BR IF DECIMAL
    
```

```

11859 050052 013146      MOV      2(R1)+, -(SP)      :SAVE 2(R1)+ FOR TYPEOUT
11860 050054 104401      TYPDC                                ;GO TYPE--OCTAL ASCII(ALL DIGITS)
11861 050056 000402      BR      9$
11862 050060          7$:
11863 050060 013146      MOV      2(R1)+, -(SP)      :SAVE 2(R1)+ FOR TYPEOUT
11864 050062 104404      TYPDS                                ;GO TYPE--DECIMAL ASCII WITH SIGN
11865 050064 005711      9$: TST      (R1)           :IS THERE ANOTHER NUMBER?
11866 050066 001403      BEQ     9$                 :BR IF NO
11867 050070 104400      TYPE   11$                :TYPE TWO(2) SPACES
11868 050074 000764      BR      6$                 :LOOP
11869 050076 012501      9$: MOV      (SP)+, R1       :RESTORE R1
11870 050080 012500      10$: MOV     (SP)+, R0       : "CARRIAGE RETURN" & "LINE FEED"
11871 050082 000207      RTS     PC                 :RETURN
11872 050084 020040      11$: .ASCII                :TWO(2) SPACES
11873 050100 050100      .EVEN

```

050104

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050110 017646 000000  
050114 116537 000001 050333  
050122 112637 050335  
050126 062716 000002  
050132 000406  
050134 112737 000001 050333  
050142 112737 000006 050335  
050150 112737 000005 050332  
050156 010346  
050160 010446  
050162 010546  
050164 113704 050335  
050170 005404  
050172 062704 000006  
050176 110437 050334  
050202 113704 050333  
050206 016605 000012  
050212 005003  
050214 006105 1\$:  
050216 000404 BR 3\$  
050220 006105 2\$:  
050222 006105 ROL R5  
050224 006105 ROL R5  
050226 010503 MOV R5,R3  
050230 006103 3\$:  
050232 105337 050334 DECB \$OMODE  
050236 100016 BPL 7\$  
050240 042703 177770 BIC #177770,R3

```
.SBTTL BINARY TO OCTAL (ASCII) AND TYPE
*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
*OCTAL (ASCII) NUMBER AND TYPE IT.
*$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPOS   ;;CALL FOR TYPEOUT
*   .BYTE  N               ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*   .BYTE  M               ;;M=1 OR 0
*                               ;;1=TYPE LEADING ZEROS
*                               ;;0=SUPPRESS LEADING ZEROS
*$TYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
*$TYPOS OR $TYPOC
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPON   ;;CALL FOR TYPEOUT
*$TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPOC   ;;CALL FOR TYPEOUT
*$TYPOS: MOV     2(SP),-(SP)  ;;PICKUP THE MODE
        MOV     1(SP), $OFILL ;;LOAD ZERO FILL SWITCH
        MOV     (SP)+, $OMODE+1 ;;NUMBER OF DIGITS TO TYPE
        ADD     #2, (SP)      ;;ADJUST RETURN ADDRESS
        BR     $TYPON
*$TYPOC: MOV     #1, $OFILL   ;;SET THE ZERO FILL SWITCH
        MOV     #6, $OMODE+1  ;;SET FOR SIX(6) DIGITS
*$TYPON: MOV     #5, $OCNT    ;;SET THE ITERATION COUNT
        MOV     R3,-(SP)      ;;SAVE R3
        MOV     R4,-(SP)      ;;SAVE R4
        MOV     R5,-(SP)      ;;SAVE R5
        MOV     $OMODE+1,R4   ;;GET THE NUMBER OF DIGITS TO TYPE
        NEG     R4
        ADD     #6,R4         ;;SUBTRACT IT FOR MAX. ALLOWED
        MOV     R4, $OMODE    ;;SAVE IT FOR USE
        MOV     $OFILL,R4    ;;GET THE ZERO FILL SWITCH
        MOV     12(SP),R5     ;;PICKUP THE INPUT NUMBER
        CLR     R3           ;;CLEAR THE OUTPUT WORD
        ROL    R5            ;;ROTATE MSB INTO "C"
        BR     3$           ;;GO DO MSB
        ROL    R5            ;;FORM THIS DIGIT
        ROL    R5
        ROL    R5
        MOV     R5,R3
        ROL    R3            ;;GET LSB OF THIS DIGIT
        DECB   $OMODE        ;;TYPE THIS DIGIT
        BPL    7$           ;;BR IF NO
        BIC   #177770,R3    ;;GET RID OF JUNK
```

11930	050244	001002		BNE	4\$	:: TEST FOR 0
11931	050246	005704		TSY	R4	:: SUPPRESS THIS 0?
11932	050250	001403		BEQ	5\$	:: BR IF YES
11933	050252	005204	4\$:	INC	R4	:: DON'T SUPPRESS ANYMORE 0'S
11934	050254	052703	000060	BIS	*'0,R3	:: MAKE THIS DIGIT ASCII
11935	050260	052703	000040	BIS	*'0,R3	:: MAKE ASCII IF NOT ALREAD.
11936	050264	110337	050330	MOVB	R3,8\$	:: SAVE FOR TYPING
11937	050270	104400	050330	TYPE	8\$	:: GO TYPE THIS DIGIT
11938	050274	105337	050332	DECB	\$OCNT	:: COUNT BY 1
11939	050300	003347		BGT	2\$	:: BR IF MORE TO DO
11940	050302	002402		BLT	6\$	:: BR IF DONE
11941	050304	005204		INC	R4	:: INSURE LAST DIGIT ISN'T A BLANK
11942	050306	000744		BR	2\$	:: GO DO THE LAST DIGIT
11943	050310	012605	5\$:	MOV	(SP)+,R5	:: RESTORE R5
11944	050312	012604		MOV	(SP)+,R4	:: RESTORE R4
11945	050314	012603		MOV	(SP)+,R3	:: RESTORE R3
11946	050316	015606	000002 000004	MOV	2(SP),4(SP)	:: SET THE STACK FOR RETURNING
11947	050324	012616		MOV	(SP)+,(SP)	
11948	050326	000002		RTI		:: RETURN
11949	050330	000	9\$:	.BYTE	0	:: STORAGE FOR ASCII DIGIT
11950	050331	000		.BYTE	0000	:: TERMINATOR FOR TYPE ROUTINE
11951	050332	000	\$OCNT:	.BYTE	0000	:: OCTAL DIGIT COUNTER
11952	050333	000	\$OFILL:	.BYTE	0	:: ZERO FILL SWITCH
11953	050334	000000	\$OCODE:	.WORD	0	:: NUMBER OF DIGITS TO TYPE

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11991

.SBTTL TRAP DECODER

\*\*\*\*\*  
\*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION  
\*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS  
\*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL  
\*GO TO THAT ROUTINE.

050336 010946  
050340 016600 000002  
050344 005740  
050346 111900  
050350 006300  
050352 016000 050360  
050355 000200

\$TRAP: MOV R0, -(SP) ;;SAVE R0  
MOV 2(SP), R0 ;;GET TRAP ADDRESS  
TST -(R0) ;;BACKUP BY 2  
MOVB (R0), R0 ;;GET RIGHT BYTE OF TRAP  
ASL R0 ;;POSITION FOR INDEXING  
MOV \$TRPAD(R0), R0 ;;INDEX TO TABLE  
RTS R0 ;;GO TO ROUTINE

.SBTTL TRAP TABLE

\*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED  
\*BY THE "TRAP" INSTRUCTION.

: ROUTINE  
:-----  
\$TRPAD:

050360  
050360 045112  
050362 050134  
050364 050110  
050366 050150  
050370 044566  
050372 045730  
050374 045640  
050376 046202  
050400 046272  
050402 046452  
050404 042055

\$TYPE ;;CALL=TYPE TRAP+0(104400) TTY TYPEOUT ROUTINE  
\$TYPOC ;;CALL=TYPOC TRAP+1(104401) TYPE OCTAL NUMBER (WITH LEADING ZEROS)  
\$TYPOS ;;CALL=TYPOS TRAP+2(104402) TYPE OCTAL NUMBER (NO LEADING ZEROS)  
\$TYPON ;;CALL=TYPON TRAP+3(104403) TYPE OCTAL NUMBER (AS PER LAST CALL)  
\$TYPDS ;;CALL=TYPDS TRAP+4(104404) TYPE DECIMAL NUMBER (WITH SIGN)  
\$GTSWR ;;CALL=GTSWR TRAP+5(104405) GET SOFT-SWR SETTING  
\$CKSWR ;;CALL=CKSWR TRAP+6(104406) TEST FOR CHANGE IN SOFT-SWR  
\$RDCHR ;;CALL=RDCHR TRAP+7(104407) TTY TYPEIN CHARACTER ROUTINE  
\$RDLIN ;;CALL=RDLIN TRAP+10(104410) TTY TYPEIN STRING ROUTINE  
\$RDOCT ;;CALL=RDOCT TRAP+11(104411) READ AN OCTAL NUMBER FROM TTY  
WAIT.T ;;CALL=WAT TRAP+12(104412) DONT ADD ABOVE THIS TRAP



```

11992          .SBTTL  POWER DOWN AND UP ROUTINES
11993
11994          ::*****
11995          :POWER DOWN ROUTINE
11996 050406 012737 050552 000024 $PWRDN: MOV    $SILLUP,@#PWRVEC  ;;SET FOR FAST UP
11997 050414 012737 000340 000026      MOV    #340,@#PWRVEC+2  ;;PRIO:7
11998 050422 010046          MOV    R0,-(SP)          ;;PUSH R0 ON STACK
11999 050424 010146          MOV    R1,-(SP)          ;;PUSH R1 ON STACK
12000 050426 010246          MOV    R2,-(SP)          ;;PUSH R2 ON STACK
12001 050430 010346          MOV    R3,-(SP)          ;;PUSH R3 ON STACK
12002 050432 010446          MOV    R4,-(SP)          ;;PUSH R4 ON STACK
12003 050434 010546          MOV    R5,-(SP)          ;;PUSH R5 ON STACK
12004 050436 017746 130476          MOV    @SWR,-(SP)       ;;PUSH @SWR ON STACK
12005 050442 010637 050556          MOV    SP,$SAVR6      ;;SAVE SP
12006 050446 012737 050460 000024      MOV    $PWRUP,@#PWRVEC ;;SET UP VECTOR
12007 050454 000000          HALT
12008 050456 000776          BR      -2              ;;HANG UP
12009
12010          ::*****
12011          :POWER UP ROUTINE
12012 050460 012737 050552 000024 $PWRUP: MOV    $SILLUP,@#PWRVEC  ;;SET FOR FAST DOWN
12013 050466 013706 050556          MOV    $SAVR6,SP      ;;GET SP
12014 050472 005037 050556          CLR    $SAVR6        ;;WAIT LOOP FOR THE TTY
12015 050476 005237 050556          1$:  INC    $SAVR6     ;;WAIT FOR THE INC
12016 050502 001375          BNE    1$            ;;OF WORD
12017 050504 012677 130430          MOV    (SP)+,@SWR    ;;POP STACK INTO @SWR
12018 050510 012605          MOV    (SP)+,R5     ;;POP STACK INTO R5
12019 050512 012604          MOV    (SP)+,R4     ;;POP STACK INTO R4
12020 050514 012603          MOV    (SP)+,R3     ;;POP STACK INTO R3
12021 050516 012602          MOV    (SP)+,R2     ;;POP STACK INTO R2
12022 050520 012601          MOV    (SP)+,R1     ;;POP STACK INTO R1
12023 050522 012600          MOV    (SP)+,R0     ;;POP STACK INTO R0
12024 050524 012737 050406 000024      MOV    $PWRDN,@#PWRVEC ;;SET UP THE POWER DOWN VECTOR
12025 050532 012737 000340 000026      MOV    #340,@#PWRVEC+2 ;;PRIO:7
12026 050540 104400          TYPE                ;;REPORT THE POWER FAILURE
12027 050542 050560          $PWRMG: .WORD    $POWER ;;POWER FAIL MESSAGE POINTER
12028 050544 012716          MOV    (PC)+,(SP)   ;;RESTART AT BEGIN
12029 050546 005012          $PWRAD: .WORD    BEGIN  ;;RESTART ADDRESS
12030 050550 000002          RTI
12031 050552 000000          $SILLUP: HALT
12032 050554 000776          BR      -2              ;;THE POWER UP SEQUENCE WAS STARTED
12033 050556 000000          $SAVR6: 0              ;;BEFORE THE POWER DOWN WAS COMPLETE
12034 050560 005015 047520 042527 $POWER: .ASCIZ  <15><12>"POWER"
12035 050566 000122
12036          .EVEN

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12046 050570 050122 032060 042040
12047 050576 042111 047040 052117
12048 050604 044440 052116 051105
12049 050612 052522 052120 000
12050 050617 111 052116 051105
12051 050624 052522 052120 042440
12052 050632 040516 046102 020105
12053 050640 044502 020124 047504
12054 050646 047127 041040 052125
12055 050654 042440 050130 041505
12056 050662 042524 020104 044502
12057 050670 020124 044504 020104
12058 050676 047516 020124 042523
12059 050704 000124
12060 050706 050122 032060 042040
12061 050714 042111 047040 052117
12062 050722 044440 052116 051105
12063 050730 052522 052120 053440
12064 050736 042510 020116 054105
12065 050744 042520 052103 042105
12066 050752 041040 052111 042040
12067 050760 042111 051440 052105
12068 050766 000
12069 050767 105 050130 041505
12070 050774 042524 020104 044502
12071 051002 020124 044504 020104
12072 051010 042523 020124 052502
12073 051016 020124 044524 042515
12074 051024 044440 020123 047111
12075 051032 042440 051122 051117
12076 051040 024040 044524 042515
12077 051046 044440 020116 030061
12078 051054 046440 041511 047522
12079 051062 042523 026103 042040
12080 051070 041505 046511 046101
12081 051076 000051
12082 051100 044122 051501 042040
12083 051106 042517 020123 047516
12084 051114 020124 046103 040505
12085 051122 020122 054502 046440
12086 051130 053117 047111 020107
12087 051136 047111 040440 046114
12088 051144 047440 042516 000123
12089 051152 047514 042101 047111
12090 051160 020107 044122 051105

```

```

:*****
:*
:*ERROR AND MESSAGE TABLE CONDIMENTS
:*
:*****

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EM1: .ASCIZ /RPO4 DID NOT INTERRUPT/

EM2: .ASCIZ /INTERRUPT ENABLE BIT DOWN BUT EXPECTED BIT DID NOT SET/

EM3: .ASCIZ /RPO4 DID NOT INTERRUPT WHEN EXPECTED BIT DID SET/

EM4: .ASCIZ /EXPECTED BIT DID SET BUT TIME IS IN ERROR (TIME IN 10 MICROSEC, DECIMAL

EM5: .ASCIZ /RHAS DOES NOT CLEAR BY MOVING IN ALL ONES/

EM6: .ASCIZ /LOADING RHER1 FOR ALL UNITS DID NOT SET ANY RHAS BITS/

12091	051166	020061	047506	020122	
12092	051174	046101	020114	047125	
12093	051202	052111	020123	044504	
12094	051210	020104	047516	020124	
12095	051216	042523	020124	047101	
12096	051224	020131	044122	051501	
12097	051232	041040	052111	000123	
12098	051240	050123	041505	043111	EM7: .ASCIZ /SPECIFIED REGISTER NON EXISTANT, SO ABORT PROGRAM/
12099	051246	042511	020104	042522	
12100	051254	044507	052123	051105	
12101	051262	047040	047117	042440	
12102	051270	044530	052123	047101	
12103	051276	026124	051440	020117	
12104	051304	041101	051117	020124	
12105	051312	051120	042517	040522	
12106	051320	000115			
12107					
12108	051322	052123	050117	042520	EM10: .ASCIZ /STOPPED DRIVE HAS MOL BIT IN RHDS1 SET/
12109	051330	020104	051104	053111	
12110	051336	020105	040510	020123	
12111	051344	047515	020114	044502	
12112	051352	020124	047111	051040	
12113	051360	042110	030523	051440	
12114	051366	052105	000		
12115	051371	127	052111	020110	EM11: .ASCIZ /WITH SPINDLE POWERED DOWN RHCS2 SHOULD ONLY HAVE UNIT NO. AND IR SET/
12116	051376	050123	047111	046104	
12117	051404	020105	047520	042527	
12118	051412	042522	020104	047504	
12119	051420	047127	051040	041510	
12120	051426	031123	051440	047510	
12121	051434	046125	020104	047117	
12122	051442	054514	044040	053101	
12123	051450	020105	047125	052111	
12124	051456	047040	027117	040440	
12125	051464	042116	044440	020122	
12126	051472	042523	000124		
12127	051476	043101	042524	020122	EM12: .ASCIZ /AFTER SPINDLE POWERED UP, NO PACK ACKN. RHDS1 SHOULD HAVE MOL=1, VV=0/
12128	051504	050123	047111	046104	
12129	051512	020105	047520	042527	
12130	051520	042522	020104	050125	
12131	051526	020054	047516	050040	
12132	051534	041501	020113	041501	
12133	051542	047113	020056	044122	
12134	051550	051504	020061	044123	
12135	051556	052517	042114	044040	
12136	051564	053101	020105	047515	
12137	051572	036514	026061	053040	
12138	051600	036526	000060		
12139	051604	044527	044124	051440	EM13: .ASCIZ /WITH SPINDLE POWERED UP, NO INTIALIZE, RHCS1 SHOULD HAVE GO=0, DVA=1, R
12140	051612	044520	042116	042514	
12141	051620	050040	053517	051105	
12142	051626	042105	052440	026120	
12143	051634	047040	020117	047111	
12144	051642	044524	046101	055111	

12145	051650	026105	051040	041510
12146	051656	030523	051440	047510
12147	051664	046125	020104	040510
12149	051672	042526	043440	036517
12149	051700	026060	042040	040526
12150	051706	030475	020054	042122
12151	051714	036531	026061	044440
12152	051722	036505	000060	
12153	051726	043101	042524	020122
12154	051734	050123	047111	046104
12155	051742	020105	047520	042527
12156	051750	042522	020104	050125
12157	051756	020054	044122	041503
12158	051764	051440	047510	046125
12159	051772	020104	042502	030075
12160	052000	000		
12161	052001	120	041501	020113
12162	052006	041501	047113	053517
12163	052014	042514	043504	020105
12164	052022	047503	046515	047101
12165	052030	020104	040503	051525
12166	052036	042105	040440	020116
12167	052044	051105	047522	006522
12168	052052	012		
12169	052053	107	047517	020104
12170	052060	040504	040524	044440
12171	052066	020123	042502	047506
12172	052074	042522	041440	046517
12173	052102	040515	042116	020054
12174	052110	042522	020103	040504
12175	052116	040524	044440	020123
12176	052124	043101	042524	020122
12177	052132	047503	046515	047101
12178	052140	000104		
12179	052142	047516	047455	020120
12180	052150	047503	046515	047101
12181	052156	020104	040503	051525
12182	052164	042105	040440	020116
12183	052172	051105	047522	006522
12184	052200	012		
12185	052201	107	047517	020104
12186	052206	040504	040524	044440
12187	052214	020123	042502	047506
12188	052222	042522	041440	046517
12189	052230	040515	042116	020054
12190	052236	042522	020103	040504
12191	052244	040524	044440	020123
12192	052252	043101	042524	020122
12193	052260	047503	046515	047101
12194	052266	000104		
12195	052270	051104	053111	020105
12196	052276	046103	040505	020122
12197	052304	047503	046515	047101
12198	052312	020104	040503	051525

EM14: .ASCIZ /AFTER SPINDLE POWERED UP, RHCC SHOULD BE=0/

EM15: .ASCII /PACK ACKNOWLEDGE COMMAND CAUSED AN ERROR/<15><12>

.ASCIZ /GOOD DATA IS BEFORE COMMAND, REC DATA IS AFTER COMMAND/

EM16: .ASCII /NO-OP COMMAND CAUSED AN ERROR/<15><12>

.ASCIZ /GOOD DATA IS BEFORE COMMAND, REC DATA IS AFTER COMMAND/

EM17: .ASCII /DRIVE CLEAR COMMAND CAUSED AN ERROR/<15><12>



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122000 052757 107 047517 020104
122001 052764 040504 040524 043440
122002 052772 053111 051505 051440
122003 053000 047510 046125 020104
122004 053006 042502 020054 042522
122005 053014 020103 040504 040524
122006 053022 043440 053111 051505
122007 053030 051040 043505 020056
122008 053036 047503 052116 020056
122009 053044 043101 042524 020122
122010 053052 047503 046515 047101
122011 053060 020104
122012 053062 043117 051506 052105
122013 053070 041440 046517 040515
122014 053076 042116 041440 052501
122015 053104 042523 020104 047101
122016 053112 042440 051122 051117
122017 053120 005015
122018 053122 047507 042117 042040
122019 053130 052101 020101 044507
122020 053136 042526 020123 044123
122021 053144 052517 042114 041040
122022 053152 026105 051040 041505
122023 053160 042040 052101 020101
122024 053166 044507 042526 020123
122025 053174 042522 027107 041440
122026 053202 047117 027124 040440
122027 053210 052106 051105 041440
122028 053216 046517 040515 042116
122029 053224 000
122030 053225 122 052105 051125
122031 053232 020116 047524 041440
122032 053240 047105 042524 020122
122033 053246 044514 042516 041440
122034 053254 046517 040515 042116
122035 053262 041440 052501 042523
122036 053270 020104 047101 042440
122037 053276 051122 051117 005015
122038 053304 047507 042117 042040
122039 053312 052101 020101 044507
122040 053320 042526 020123 044123
122041 053326 052517 042114 041040
122042 053334 026105 051040 041505
122043 053342 042040 052101 020101
122044 053350 044507 042526 020123
122045 053356 042522 027107 041440
122046 053364 047117 027124 040440
122047 053372 052106 051105 041440
122048 053400 046517 040515 042116
122049 053406 000
122050 053407 065 030060 047440
122051 053414 043106 042523 020124
122052 053422 047503 046515 047101
122053 053430 051504 047440 042516

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.ASCIZ /GOOD DATA GIVES SHOULD BE. REC DATA GIVES REG. CONT. AFTER COMMAND/

EM24: .ASCII /OFFSET COMMAND CAUSED AN ERROR/ (15) (12)

.ASCIZ GOOD DATA GIVES SHOULD BE. REC DATA GIVES REG. CONT. AFTER COMMAND

EM25: .ASCII RETURN TO CENTER LINE COMMAND CAUSED AN ERROR (15) (12)

.ASCIZ /GOOD DATA GIVES SHOULD BE. REC DATA GIVES REG. CONT. AFTER COMMAND

EM26: .ASCIZ /500 OFFSET COMMANDS ONE AFTER THE OTHER CAUSED AN ERROR

12337	053436	040440	052106	051105	
12338	053444	052040	042510	047440	
12339	053452	044124	051105	041440	
12340	053460	052501	042523	020104	
12341	053466	047101	042440	051122	
12342	053474	051117	000		
12343	053477	127	044522	042524	EM27: .ASCII WRITE HEADER AND DATA CAUSED IMPROPER REGISTER CHANGE (15) (12)
12344	053504	044040	040505	042504	
12345	053512	020122	047101	020104	
12346	053520	040504	040524	041440	
12347	053526	052501	042523	020104	
12348	053534	046511	051120	050117	
12349	053542	051105	051040	043505	
12350	053550	051511	042524	020122	
12351	053556	044103	047101	042507	
12352	053564	005015			
12353	053566	047507	042117	042040	.ASCII GOOD DATA GIVES WHAT SHOULD BE THERE (15) (12)
12354	053574	052101	020101	044507	
12355	053602	042526	020123	044127	
12356	053610	052101	051440	047510	
12357	053616	046125	020104	042502	
12358	053624	052040	042510	042522	
12359	053632	005015			
12360	053634	042522	042503	053111	.ASCII2 RECEIVED DATA GIVES WHAT WAS THERE AFTER COMMAND.
12361	053642	042105	042040	052101	
12362	053650	020101	044507	042526	
12363	053656	020123	044127	052101	
12364	053664	053440	051501	052040	
12365	053672	042510	042522	040440	
12366	053700	052106	051105	041440	
12367	053706	046517	040515	042116	
12368	053714	000			
12369					
12370	053715	127	044522	042524	EM30: .ASCII2 WRITE HEADER AND DATA CHANGED WRITE FROM BUFFER.
12371	053722	044040	040505	042504	
12372	053730	020122	047101	020104	
12373	053736	040504	040524	041440	
12374	053744	040510	043516	042105	
12375	053752	053440	044522	042524	
12376	053760	043040	047522	020115	
12377	053766	052502	043106	051105	
12378	053774	000			
12379					
12380	053775	122	040505	020104	EM31: .ASCII READ HEADER AND DATA CAUSED IMPROPER REGISTER CHANGE (15) (12)
12381	054002	042510	042101	051105	
12382	054010	040440	042116	042040	
12383	054016	052101	020101	040503	
12384	054024	051525	042105	044440	
12385	054032	050115	047522	042520	
12386	054040	020122	042522	044507	
12387	054046	052123	051105	041440	
12388	054054	043510	043516	006505	
12389	054062	012			
12390	054063	107	047517	020104	.ASCII GOOD DATA GIVES WHAT SHOULD BE THERE (15) (12)
12391	054070	040504	040524	043440	

12361	054076	053111	051505	053440
12362	054104	040510	020124	044123
12363	054112	052517	042114	041040
12364	054120	020105	044124	051105
12365	054126	006505	012	
12366	054131	122	041505	044505
12367	054136	042526	020104	040504
12368	054144	040524	043440	053111
12369	054152	051505	053440	040510
12370	054160	020124	040527	020123
12371	054166	044124	051105	020105
12372	054174	043101	042524	020122
12373	054202	047503	046515	047101
12374	054210	050104		
12375	054212	051127	052111	020105
12376	054220	042510	042101	051105
12377	054226	042040	052101	026101
12378	054234	043040	045117	047514
12379	054242	042527	020104	054502
12380	054250	051040	040505	020104
12381	054256	042510	042101	051105
12382	054264	040440	042116	042040
12383	054272	052101	026101	041440
12384	054300	052501	042523	020104
12385	054306	040504	040524	042440
12386	054314	051122	051117	000
12387	054321	122	040505	020104
12388	054326	040504	040524	041440
12389	054334	052501	042523	020104
12390	054342	046511	051120	050117
12391	054350	051105	051040	043505
12392	054356	051511	042524	020122
12393	054364	044103	047101	042507
12394	054372	005015		
12395	054374	047507	042117	042040
12396	054402	052101	020101	044507
12397	054410	042526	020123	044127
12398	054416	052101	051440	047510
12399	054424	046125	020104	042502
12400	054432	052040	042510	042522
12401	054440	005015		
12402	054442	042522	042503	053111
12403	054450	042105	042040	052101
12404	054456	020101	044507	042526
12405	054464	020123	044127	052101
12406	054472	053440	051501	052040
12407	054500	042510	042522	040440
12408	054506	052106	051105	041440
12409	054514	046517	040515	042116
12410	054522	000		
12411	054523	122	040505	020104
12412	054530	040504	040524	044440
12413	054536	041516	051117	042522
12414	054544	052103	000	

.ASCIZ RECEIVED DATA GIVES WHAT WAS THERE AFTER COMMAND/

EM32: .ASCIZ WRITE HEADER DATA, FOLLOWED BY READ HEADER AND DATA, CAUSED DATA ERROR/

EM33: .ASCII READ DATA CAUSED IMPROPER REGISTER CHANGE<<15><12>

.ASCII GOOD DATA GIVES WHAT SHOULD BE THERE<<15><12>

.ASCIZ RECEIVED DATA GIVES WHAT WAS THERE AFTER COMMAND.

EM34: .ASCIZ READ DATA INCORRECT



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0415 054547 127 044522 042524 EM35: .ASCII WRITE DATA COMMAND CAUSED IMPROPER REGISTER CHANGE '15' '12'
0416 054554 042040 052101 020101
0417 054562 047503 046515 047101
0418 054570 020104 042503 051525
0419 054576 042105 044440 050115
0420 054604 047522 042520 020122
0421 054612 042522 044507 052123
0422 054620 051105 041440 040510
0423 054626 043516 006505 012
0424 054633 107 047517 020104 .ASCII GOOD DATA GIVES WHAT SHOULD BE THERE '15' '12'
0425 054640 040504 040524 043440
0426 054646 053111 051505 053440
0427 054654 040510 020124 044123
0428 054662 052517 042114 041240
0429 054670 020105 044124 051105
0430 054676 006505 012
0431 054701 122 041505 044505 .ASCII RECEIVED DATA GIVES REGISTER CONTENTS AFTER COMMAND
0432 054706 042526 020104 040504
0433 054714 040524 043440 053111
0434 054722 051505 051040 043505
0435 054730 051511 042524 020122
0436 054736 047503 052116 047105
0437 054744 051524 040440 052106
0438 054752 051105 041440 046517
0439 054760 040515 042116 000
0440 054765 127 044522 042524 EM36: .ASCII WRITE DATA COMMAND CHANGED WRITE FROM BUFFER
0441 054772 042040 052101 020101
0442 055000 047503 046515 047101
0443 055006 020104 044103 047101
0444 055014 042507 020104 051127
0445 055022 052111 020105 051106
0446 055030 046517 041040 043125
0447 055036 042506 000122
0448 055042 042523 045505 041440 EM37: .ASCII SEEK COMMAND CAUSED IMPROPER REGISTER CHANGE '15' '12'
0449 055050 046517 040515 042116
0450 055056 041440 052501 042523
0451 055064 020104 046511 051120
0452 055072 050117 051105 051040
0453 055100 043505 051511 042524
0454 055106 020122 044103 047101
0455 055114 042507 005015
0456 055120 047507 042117 042040 .ASCII GOOD DATA GIVES WHAT SHOULD BE THERE '15' '12'
0457 055126 052101 020101 044507
0458 055134 042526 020123 044127
0459 055142 052101 051440 047510
0460 055150 046125 020104 042502
0461 055156 052040 042510 042522
0462 055164 005015
0463 055166 042522 042503 053111 .ASCII RECEIVED DATA GIVES REGISTER CONTENTS AFTER SEEK COMMAND
0464 055174 042105 042040 052101
0465 055202 020101 044507 042526
0466 055210 020123 042522 044507
0467 055216 052123 051105 041440
0468 055224 047117 042524 052116

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12469	055232	020123	043101	042524
12470	055240	020122	042523	045505
12471	055246	041440	046517	040515
12472	055254	042116	000	
12473	055257	127	044522	042524
12474	055264	041440	042510	045503
12475	055272	041440	052501	042523
12476	055300	020104	046511	051120
12477	055306	050117	051105	051040
12478	055314	043505	051511	042524
12479	055322	020122	044103	047101
12480	055330	042507	005015	
12481	055334	047507	042117	042040
12482	055342	052101	020101	044507
12483	055350	042526	020123	044127
12484	055356	052101	051440	047510
12485	055364	046125	020104	042502
12486	055372	052040	042510	042522
12487	055400	005015		
12488	055402	042522	042503	053111
12489	055410	042105	042040	052101
12490	055416	020101	044507	042526
12491	055424	020123	042522	044507
12492	055432	052123	051105	041440
12493	055440	047117	042524	052116
12494	055446	020123	043101	042524
12495	055454	020122	047503	046515
12496	055462	047101	000104	
12497	055466	047514	045503	047111
12498	055474	020107	052517	020124
12499	055502	051127	052111	020105
12500	055510	054502	053440	044522
12501	055516	042524	046040	041517
12502	055524	020113	052502	052124
12503	055532	047117	041440	052501
12504	055540	042523	020104	046511
12505	055546	051120	050117	051105
12506	055554	051040	043505	051511
12507	055562	042524	020122	044103
12508	055570	047101	042507	005015
12509	055576	047507	042117	042040
12510	055604	052101	020101	044507
12511	055612	042526	020123	044127
12512	055620	052101	051440	047510
12513	055628	046125	020104	042502
12514	055634	052040	042510	042522
12515	055642	005015		
12516	055644	042522	042503	053111
12517	055652	042105	042040	052101
12518	055660	020101	044507	042526
12519	055666	020123	042522	044507
12520	055674	052123	051105	041440
12521	055702	047117	042524	052116

EM40: .ASCII WRITE CHECK CAUSED IMPROPER REGISTER CHANGE <<15><12>

.ASCII GOOD DATA GIVES WHAT SHOULD BE THERE <<15><12>

.ASCIIZ RECEIVED DATA GIVES REGISTER CONTENTS AFTER COMMAND

EM41: .ASCII LOCKING OUT WRITE BY WRITE LOCK BUTTON CAUSED IMPROPER REGISTER CHANGE

.ASCII GOOD DATA GIVES WHAT SHOULD BE THERE <<15><12>

.ASCIIZ RECEIVED DATA GIVES REGISTER CONTENTS AFTER WRITES WERE LOCKED OUT

12523	055710	020123	043101	042524
12524	055716	020122	051127	052111
12525	055724	051505	053440	051105
12526	055732	020105	047514	045503
12527	055740	042105	047440	052125
12528	055746	000		
12529	055747	101	052124	046505
12530	055754	052120	047111	020107
12531	055762	047524	053440	044522
12532	055770	042524	053440	052111
12533	055776	020110	051127	052111
12534	056004	051505	046040	041517
12535	056012	042513	020104	052517
12536	056020	020124	040503	051525
12537	056026	042105	044440	050115
12538	056034	047522	042520	020122
12539	056042	042522	044507	052123
12540	056050	051105	041440	040510
12541	056056	043516	006505	012
12542	056063	107	047517	020104
12543	056070	040504	040524	043440
12544	056076	053111	051505	053440
12545	056104	040510	020124	044123
12546	056112	052517	042114	041040
12547	056120	020105	044124	051105
12548	056126	006505	012	
12549	056131	122	041505	044505
12550	056136	042526	020104	040504
12551	056144	040524	043440	053111
12552	056152	051505	051040	043505
12553	056160	051511	042524	020122
12554	056166	047503	052116	047105
12555	056174	051524	040440	052106
12556	056202	051105	040440	052124
12557	056210	046505	052120	042105
12558	056216	053440	044522	042524
12559	056224	000		
12560	056225	127	044522	044524
12561	056232	043516	053440	052111
12562	056240	020110	051127	052111
12563	056246	051505	046040	041517
12564	056254	042513	020104	052517
12565	056262	020124	044103	047101
12566	056270	042507	020104	044504
12567	056276	045523	042040	052101
12568	056304	006501	012	
12569	056307	107	047517	020104
12570	056314	040504	040524	043440
12571	056322	053111	051505	053440
12572	056330	040510	020124	040527
12573	056336	020123	047117	042040
12574	056344	051511	020113	042502
12575	056352	047506	042522	053440
12576	056360	044522	042524	024040

EM42: .ASCII /ATTEMPTING TO WRITE WITH WRITES LOCKED OUT CAUSED IMPROPER REGISTER CHA

.ASCII GOOD DATA GIVES WHAT SHOULD BE THERE /15<<12>

.ASCII2 RECEIVED DATA GIVES REGISTER CONTENTS AFTER ATTEMPTED WRITE

EM43: .ASCII /WRITING WITH WRITES LOCKED OUT CHANGED DISK DATA <15><12>

.ASCII /GOOD DATA GIVES WHAT WAS ON DISK BEFORE WRITE (WITH WRITE LOCKED OUT)

12577	056366	044527	044124	053440
12578	056374	044522	042524	046040
12579	056402	041517	042513	020104
12580	056410	052517	024524	005015
12581	056416	040527	020123	052101
12582	056424	042524	050115	042524
12583	056432	006504	012	
12584	056435	122	041505	044505
12585	056442	042526	020104	040504
12586	056450	040524	043440	053111
12587	056456	051505	053440	040510
12588	056464	020124	040527	020123
12589	056472	042522	042101	041040
12590	056500	041501	020113	043101
12591	056506	042524	020122	051127
12592	056514	052111	006505	012
12593	056521	050	044527	044124
12594	056526	053440	044522	042524
12595	056534	046040	041517	042513
12596	056542	020104	052517	024524
12597	056550	053440	051501	040440
12598	056556	052124	046505	052120
12599	056564	042105	000	
12600	056567	105	040516	046102
12601	056574	047111	020107	051127
12602	056602	052111	051505	041040
12603	056610	020131	051127	052111
12604	056616	020105	047514	045503
12605	056624	041040	052125	047524
12606	056632	020116	040503	051525
12607	056640	042105	044440	050115
12608	056646	047522	042520	020122
12609	056654	042522	044507	052123
12610	056662	051105	041440	040510
12611	056670	043516	006505	012
12612	056675	107	047517	020104
12613	056702	040504	040524	043440
12614	056710	053111	051505	053440
12615	056716	040510	020124	044123
12616	056724	052517	042114	041040
12617	056732	020105	044124	051105
12618	056740	006505	012	
12619	056743	122	041505	044505
12620	056750	042526	020104	040504
12621	056756	040524	043440	053111
12622	056764	051505	051040	043505
12623	056772	051511	042524	020122
12624	057000	047503	052116	047105
12625	057006	051524	040440	052106
12626	057014	051105	053440	044522
12627	057022	042524	046040	041517
12628	057030	020113	052502	052124
12629	057036	047117	005015	
12630	057042	047105	041101	042514

.ASCII /WAS ATTEMPTED/<15><12>

.ASCII /RECEIVED DATA GIVES WHAT WAS READ BACK AFTER WRITE <15> 12>

.ASCIZ / (WITH WRITE LOCKED OUT) WAS ATTEMPTED/

EM44: .ASCII /ENABLING WRITES BY WRITE LOCK BUTTON CAUSED IMPROPER REGISTER CHANGE/ !

.ASCII /GOOD DATA GIVES WHAT SHOULD BE THERE/<15><12>

.ASCII /RECEIVED DATA GIVES REGISTER CONTENTS AFTER WRITE LOCK BUTTON/<15><12>

.ASCIZ /ENABLED WRITES/

12631	057050	020104	051127	052111	
12632	057056	051505	000		
12633	057061	124	040522	051516	EM45: .ASCII TRANSFERRING ON LAST BLOCK - CYLINDER 410, SECTOR 21, TRACK 19<<15><12>
12634	057066	042506	051122	047111	
12635	057074	020107	047117	046040	
12636	057102	051501	020124	046102	
12637	057110	041517	020113	020055	
12638	057116	054503	044514	042116	
12639	057124	051105	032040	030061	
12640	057132	020054	042523	052103	
12641	057140	051117	031040	026061	
12642	057146	052040	040522	045503	
12643	057154	030440	006470	012	
12644	057161	103	052501	042523	.ASCII CAUSED IMPROPER REGISTER CHANGE<<15><12>
12645	057166	020104	046511	051120	
12646	057174	050117	051105	051040	
12647	057202	043505	051511	042524	
12648	057210	020122	044103	047101	
12649	057216	042507	005015		
12650	057222	047507	042117	042040	.ASCII GOOD DATA GIVES WHAT SHOULD BE THERE<<15><12>
12651	057230	052101	020101	044507	
12652	057236	042526	020123	044127	
12653	057244	052101	051440	047510	
12654	057252	046125	020104	042502	
12655	057260	052040	042510	042522	
12656	057266	005015			
12657	057270	042522	042503	053111	.ASCII2 RECEIVED DATA GIVES REGISTER CONTENTS AFTER TRANSFER/
12658	057276	042105	042040	052101	
12659	057304	020101	044507	042526	
12660	057312	020123	042522	044507	
12661	057320	052123	051105	041440	
12662	057326	047117	042524	052116	
12663	057334	020123	043101	042524	
12664	057342	020122	051124	047101	
12665	057350	043123	051105	000	
12666	057355	104	052101	020101	EM46: .ASCII DATA READ FROM LAST BLOCK - CYLINDER 410, SECTOR 21, TRACK 19<<15><12>
12667	057362	042522	042101	043040	
12668	057370	047522	020115	040514	
12669	057376	052123	041040	047514	
12670	057404	045503	026440	041440	
12671	057412	046131	047111	042504	
12672	057420	020122	030464	026060	
12673	057426	051440	041505	047524	
12674	057434	020122	030462	020054	
12675	057442	051124	041501	020113	
12676	057450	034061	005015		
12677	057454	051511	044440	020116	.ASCII2 IS IN ERROR/
12678	057462	051105	047522	000122	
12679	057470	051124	047101	043123	EM47: .ASCII TRANSFERRING DATA FROM NONEXISTANT SECTOR CAUSED IMPROPER <<15><12>
12680	057476	051105	044522	043516	
12681	057504	042040	052101	020101	
12682	057512	051106	046517	047040	
12683	057520	047117	054105	051511	
12684	057526	040524	052116	051440	

12685	057534	041505	047524	020122
12686	057542	040503	051525	042105
12687	057550	044440	050115	047522
12688	057556	042520	020122	005015
12689	057564	042522	044507	052123
12690	057572	051105	041440	040510
12691	057600	043516	026105	043440
12692	057606	047517	020104	040504
12693	057614	040524	043440	053111
12694	057622	051505	053440	040510
12695	057630	020124	044123	052517
12696	057636	042114	041040	020105
12697	057644	044124	051105	006505
12698	057652	012		
12699	057653	122	041505	044505
12700	057660	042526	020104	040504
12701	057666	040524	043440	053111
12702	057674	051505	051040	043505
12703	057702	051511	042524	020122
12704	057710	047503	052116	047105
12705	057716	051524	040440	052106
12706	057724	051105	040440	052124
12707	057732	046505	052120	042105
12708	057740	052040	040522	051516
12709	057746	042506	000122	
12710				
12711	057752	051124	047101	043123
12712	057760	051105	044522	043516
12713	057766	043040	047522	020115
12714	057774	047516	042516	044530
12715	060002	052123	047101	020124
12716	060010	042523	052103	051117
12717	060016	041440	052501	042523
12718	060024	020104	040504	040524
12719	060032	042440	051122	051117
12720	060040	005015		
12721	060042	047507	042117	042040
12722	060050	052101	020101	044507
12723	060056	042526	020123	044127
12724	060064	052101	051440	047510
12725	060072	046125	020104	042502
12726	060100	052040	042510	042522
12727	060106	005015		
12728	060110	040502	020104	040504
12729	060116	040524	043440	053111
12730	060124	051505	053440	040510
12731	060132	020124	040527	020122
12732	060140	047111	041040	043125
12733	060146	042506	020122	043101
12734	060154	042524	020122	051124
12735	060162	047101	043123	051105
12736	060170	000		
12737	060171	107	053111	047111
12738	060176	020107	046111	042514

.ASCII /REGISTER CHANGE, GOOD DATA GIVES WHAT SHOULD BE THERE/<15><12>

.ASCIZ /RECEIVED DATA GIVES REGISTER CONTENTS AFTER ATTEMPTED TRANSFER/

EMSD: .ASCII /TRANSFERRING FROM NONEXISTANT SECTOR CAUSED DATA ERROR/<15><12>

.ASCII /GOOD DATA GIVES WHAT SHOULD BE THERE.<15><12>

.ASCIZ /BAD DATA GIVES WHAT WAS IN BUFFER AFTER TRANSFER/

EMSI: .ASCII /GIVING ILLEGAL FUNCTION CAUSED IMPROPER REGISTER CHANGE/<15><12>

12739	060204	040507	020114	052506	
12740	060212	041516	044524	047117	
12741	060220	041440	052501	042523	
12742	060226	020104	046511	051120	
12743	060234	050117	051105	051040	
12744	060242	043505	051511	042524	
12745	060250	020122	044103	047101	
12746	060256	042507	005015		
12747	060262	047507	042117	042040	.ASCII /GOOD DATA GIVES WHAT SHOULD BE THERE/<15><12>
12748	060270	052101	020101	044507	
12749	060276	042526	020123	044127	
12750	060304	052101	051440	047510	
12751	060312	046125	020104	042502	
12752	060320	052040	042510	042522	
12753	060326	005015			
12754	060330	042522	042503	053111	.ASCIZ /RECEIVED DATA GIVES REGISTER CONTENTS AFTER ILLEGAL FUNCTION IS GIVEN/
12755	060336	042105	042040	052101	
12756	060344	020101	044507	042526	
12757	060352	020123	042522	044507	
12758	060360	052123	051105	041440	
12759	060366	047117	042524	052116	
12760	060374	020123	043101	042524	
12761	060402	020122	046111	042514	
12762	060410	040507	020114	052506	
12763	060416	041516	044524	047117	
12764	060424	044440	020123	044507	
12765	060432	042526	000116		
12766	060436	051127	052111	020105	EMS2: .ASCII /WRITE DATA COMMAND ON NONEXISTANT SECTOR CAUSED IMPROPER REG. CHANGE/<1
12767	060444	040504	040524	041440	
12768	060452	046517	040515	042116	
12769	060460	047440	020116	047516	
12770	060466	042516	044530	052123	
12771	060474	047101	020124	042523	
12772	060502	052103	051117	041440	
12773	060510	052501	042523	020104	
12774	060516	046511	051120	050117	
12775	060524	051105	051040	043505	
12776	060532	020056	044103	047101	
12777	060540	042507	005015		
12778	060544	047507	042117	042040	.ASCII /GOOD DATA GIVES WHAT SHOULD BE THERE/<15><12>
12779	060552	052101	020101	044507	
12780	060560	042526	020123	044127	
12781	060566	052101	051440	047510	
12782	060574	046125	020104	042502	
12783	060602	052040	042510	042522	
12784	060610	005015			
12785	060612	042522	042503	053111	.ASCIZ /RECEIVED DATA GIVES REGISTER CONTENTS AFTER ATTEMPTED WRITE DATA/
12786	060620	042105	042040	052101	
12787	060626	020101	044507	042526	
12788	060634	020123	042522	044507	
12789	060642	052123	051105	041440	
12790	060650	047117	042524	052116	
12791	060656	020123	043101	042524	
12792	060664	020122	052101	042524	

12793	060672	050115	042524	020104	
12794	060700	051127	052111	020105	
12795	060706	040504	040524	000	
12796	060713	122	040505	020104	EMS3: .ASCIZ /READ HEADER AND DATA AFTER A SEARCH CAUSED DATA ERROR/
12797	060720	042510	042101	051105	
12798	060726	040440	042116	042040	
12799	060734	052101	020101	043101	
12800	060742	042524	020122	020101	
12801	060750	042523	051101	044103	
12802	060756	041440	052501	042523	
12803	060764	020104	040504	040524	
12804	060772	042440	051122	051117	
12805	061000	000			
12806	061001	101	052124	046505	EMS4: .ASCII /ATTEMPTING COMMAND WITH INVALID ADDRESS CAUSED IMPROPER REGISTER CHANGE
12807	061006	052120	047111	020107	
12808	061014	047503	046515	047101	
12809	061022	020104	044527	044124	
12810	061030	044440	053116	046101	
12811	061036	042111	040440	042104	
12812	061044	042522	051523	041440	
12813	061052	052501	042523	020104	
12814	061060	046511	051120	050117	
12815	061066	051105	051040	043505	
12816	061074	051511	042524	020122	
12817	061102	044103	047101	042507	
12818	061110	005015			
12819	061112	047507	042117	042040	.ASCII /GOOD DATA GIVES WHAT SHOULD BE THERE/<15><12>
12820	061120	052101	020101	044507	
12821	061126	042526	020123	044127	
12822	061134	052101	051440	047510	
12823	061142	046125	020104	042502	
12824	061150	052040	042510	042522	
12825	061156	005015			
12826	061160	042522	042503	053111	.ASCIZ /RECEIVED DATA GIVES REGISTER CONTENTS AFTER OPERATION/
12827	061166	042105	042040	052101	
12828	061174	020101	044507	042526	
12829	061202	020123	042522	044507	
12830	061210	052123	051105	041440	
12831	061216	047117	042524	052116	
12832	061224	020123	043101	042524	
12833	061232	020122	050117	051105	
12834	061240	052101	047511	000116	
12835	061246	051127	052111	047111	EMS5: .ASCII /WRITING OR READING WITH EXPECTED ADDRESS OVERFLOW ERROR/<15><12>
12836	061254	020107	051117	051040	
12837	061262	040505	044504	043516	
12838	061270	053440	052111	020110	
12839	061276	054105	042520	052103	
12840	061304	042105	040440	042104	
12841	061312	042522	051523	047440	
12842	061320	042526	043122	047514	
12843	061326	020127	051105	047522	
12844	061334	006522	012		
12845	061337	103	052501	042523	.ASCII /CAUSED IMPROPER REGISTER CHANGE/<15><12>
12846	061344	020104	046511	051120	



12847	061352	050117	051105	051040
12848	061360	043505	051511	042524
12849	061366	020122	044103	047101
12850	061374	042507	005015	
12851	061400	047507	042117	042040
12852	061406	052101	020101	044507
12853	061414	042526	020123	044127
12854	061422	052101	051440	047510
12855	061430	046125	020104	042502
12856	061436	052040	042510	042522
12857	061444	005015		
12858	061446	042522	042503	053111
12859	061454	042105	042040	052101
12860	061462	020101	044507	042526
12861	061470	020123	042522	044507
12862	061476	052123	051105	041440
12863	061504	047117	042524	052116
12864	061512	020123	043101	042524
12865	061520	020122	050117	051105
12866	061526	052101	047511	000116
12867	061534	040504	040524	051040
12868	061542	040505	020104	044527
12869	061550	044124	040440	020116
12870	061556	054105	042520	052103
12871	061564	042105	040440	042104
12872	061572	042522	051523	047440
12873	061600	042526	043122	047514
12874	061606	020127	051105	047522
12875	061614	020122	051511	044440
12876	061622	041516	051117	042522
12877	061630	052103	005015	
12878	061634	047527	042122	047040
12879	061642	027117	030440	052040
12880	061650	020117	033062	020060
12881	061656	044123	052517	042114
12882	061664	041040	020105	042522
12883	061672	042101	020054	047527
12884	061700	042122	047040	020117
12885	061706	033062	020061	047524
12886	061714	031040	033066	051440
12887	061722	047510	046125	006504
12888	061730	012		
12889	061731	102	020105	044103
12890	061736	047101	042507	000104
12891	061744	052101	042524	050115
12892	061752	044524	043516	042040
12893	061760	052101	020101	047503
12894	061766	046515	047101	020104
12895	061774	044527	044124	053440
12896	062002	047522	043516	043040
12897	062010	051117	040515	020124
12898	062016	044502	020124	040503
12899	062024	051525	042105	005015
12900	062032	046511	051120	050117

.ASCII /GOOD DATA GIVES WHAT SHOULD BE THERE/<15><12>

.ASCIZ /RECEIVED DATA GIVES REGISTER CONTENTS AFTER OPERATION/

EM56: .ASCII /DATA READ WITH AN EXPECTED ADDRESS OVERFLOW ERROR IS INCORRECT/<15><12>

.ASCII /WORD NO. 1 TO 260 SHOULD BE READ.- WORD NO 261 TO 266 SHOULD/<15><12>

.ASCIZ /BE CHANGED/

EM57: .ASCII /ATTEMPTING DATA COMMAND WITH WRONG FORMAT BIT CAUSED/<15><12>

.ASCII /IMPROPER REGISTER CHANGE/<15><12>



129955	062510	052101	047511	020116
129956	062516	040527	020123	052101
129957	062524	042524	050115	042524
129958	062532	006504	012	
129959	062535	042117	042117	043111
129960	062542	044531	043516	051040
129961	062550	043505	043440	053111
129962	062556	051505	040440	042104
129963	062564	042522	051523	047440
129964	062572	020106	042522	044507
129965	062600	052123	051105	041040
129966	062596	044505	043516	046440
129967	062614	042117	043111	042511
129968	062622	020104	044127	041511
129969	062630	020110	040503	051525
129970	062636	042105	042440	051122
129971	062644	051117	000	
129972	062647	122	041510	030523
129973	062654	044040	051501	051440
129974	062662	046517	020105	047111
129975	062670	047503	051122	041505
129976	062676	020124	052123	052101
129977	062704	051525	041040	052111
129978	062712	020123	020075	026061
129979	062720	047440	020122	020075
129980	062726	000060		
129981	062730	044122	051504	020061
129982	062736	040510	020123	047523
129983	062744	042515	044440	041516
129984	062752	051117	042522	052103
129985	062760	051440	040524	052524
129986	062766	020123	044502	051524
129987	062774	036440	030440	020054
129988	063002	051117	036440	030040
129989	063010	000		
129990	063011	122	042110	030523
129991	063016	041440	047117	042524
129992	063024	052116	020123	052504
129993	063032	044522	043516	041440
129994	063040	046517	040515	042116
129995	063046	053440	051105	020105
129996	063054	047111	042440	051122
129997	063062	051117	000	
129998	063065	122	041505	046101
129999	063072	041111	040522	042524
130000	063100	041440	046517	040515
130001	063106	042116	041440	052501
130002	063114	042523	020104	046511
130003	063122	051120	050117	051105
130004	063130	051040	043505	051511
130005	063136	042524	020122	044103
130006	063144	047101	042507	005015
130007	063152	047507	042117	042040
130008	063160	052101	020101	044507

.ASCII MODIFYING REG GIVES ADDRESS OF REGISTER BEING MODIFIED WHICH CAUSED ERR

EM61: .ASCII /RHOS1 HAS SOME INCORRECT STATUS BITS = 1, OR = 0/

EM62: .ASCII /RHOS1 HAS SOME INCORRECT STATUS BITS = 1, OR = 0/

EM63: .ASCII /RHOS1 CONTENTS DURING COMMAND WERE IN ERROR/

EM64: .ASCII /RECALIBRATE COMMAND CAUSED IMPROPER REGISTER CHANGE (15)(12)

.ASCII /GOOD DATA GIVES WHAT SHOULD BE THERE (15)(12)

12009	063166	042526	020123	044127
12010	063174	052101	051440	047510
12011	063202	046125	020104	042502
12012	063210	052040	042510	042522
12013	063216	005015		
12014	063220	042522	042503	053111
12015	063226	042105	042040	052101
12016	063234	020101	044507	042526
12017	063242	020123	042522	044507
12018	063250	052123	051105	041440
12019	063256	047117	042524	052116
12020	063264	020123	043101	042524
12021	063272	020122	047503	046515
12022	063300	047101	000104	
12023	063304	047111	042524	051122
12024	063312	050125	020124	040506
12025	063320	046111	047111	000107
12026	063326	042510	042101	051105
12027	063334	040440	042116	042040
12028	063342	052101	020101	047503
12029	063350	046515	047101	020104
12030	063356	047506	020122	042510
12031	063364	042101	051440	046105
12032	063372	041505	044524	047117
12033	063400	052040	051505	020124
12034	063406	040503	051525	042105
12035	063414	005015		
12036	063416	051105	047522	020122
12037	063424	020055	044122	051504
12038	063432	020124	044507	042526
12039	063440	020123	051124	041501
12040	063446	020113	042502	047111
12041	063454	020107	051127	052111
12042	063462	042524	020116	051117
12043	063470	051040	040505	020104
12044	063476	047117	041440	046131
12045	063504	030040	020054	041523
12046	063512	051124	030040	005015
12047	063520	042522	042101	044040
12048	063526	040505	042504	020122
12049	063534	047101	020104	040504
12050	063542	040524	042440	051122
12051	063550	051117	044440	020116
12052	063556	042510	042101	051440
12053	063564	046105	041505	044524
12054	063572	047117	052040	051505
12055	063600	027124	006412	
12056	063604	044506	051522	020124
12057	063612	047506	051125	053440
12058	063620	051117	020104	052516
12059	063626	041115	051105	020123
12060	063634	051101	020105	044124
12061	063642	020105	042510	042101
12062	063650	051105	005056	015

.ASCII RECEIVED DATA GIVES REGISTER CONTENTS AFTER COMMAND/

EM65: .ASCII INTERRUPT FAILING/

EM66: .ASCII HEADER AND DATA COMMAND FOR HEAD SELECTION TEST CAUSED <15> 12/

.ASCII ERROR - RHOST GIVES TRACK BEING WRITTEN OR READ ON CYL 0, SCTR 0<15> 11

EM67: .ASCII READ HEADER AND DATA ERROR IN HEAD SELECTION TEST. 12<15>

.ASCII FIRST FOUR WORD NUMBERS ARE THE HEADER. 12<15>

13063	063655	127	051117	020104
13064	063662	052516	041115	051105
13065	063670	020123	020065	047524
13066	063676	031040	030066	040440
13067	063704	042522	042040	052101
13068	063712	020101	047527	042122
13069	063720	026123	006412	
13070	063724	047101	020104	047111
13071	063732	042040	052101	020101
13072	063740	047527	042122	020123
13073	063746	044502	051524	032040
13074	063754	032454	033054	033454
13075	063762	034054	043440	053111
13076	063770	020105	051124	041501
13077	063776	020113	052516	041115
13078	064004	051105	000056	
13079				
13080	064010	042522	042101	044040
13081	064016	040505	042504	020122
13082	064024	047101	020104	040504
13083	064032	040524	042440	051122
13084	064040	051117	044440	006516
13085	064046	012		
13086	064047	104	043111	042506
13087	064054	042522	041516	020105
13088	064062	044514	042516	052040
13089	064070	051505	006524	012
13090	064075	127	051117	020104
13091	064102	047516	020123	026461
13092	064110	020064	044507	042526
13093	064116	044040	040505	042504
13094	064124	006522	012	
13095	064127	127	051117	020104
13096	064134	047516	020123	026465
13097	064142	033062	020060	044507
13098	064150	042526	042040	052101
13099	064156	020101	044127	041511
13100	064164	020110	051511	052040
13101	064172	042510	041440	046131
13102	064200	047111	042504	020122
13103	064206	042101	051104	051505
13104	064214	000123		
13105	064216	047506	041522	047111
13106	064224	020107	050117	020111
13107	064232	054502	031440	044440
13108	064240	042116	054105	050040
13109	064246	046125	042523	006523
13110	064254	012		
13111	064255	103	052501	042523
13112	064262	020104	046511	051120
13113	064270	050117	051105	051040
13114	064276	043505	051511	042524
13115	064304	020122	044103	047101
13116	064312	042507	005015	

.ASCII /WORD NUMBERS 5 TO 260 ARE DATA WORDS, /12/15/

.ASCIZ AND IN DATA WORDS BITS 4,5,6,7,8 GIVE TRACK NUMBER./

EM70: .ASCII /READ HEADER AND DATA ERROR IN/15/12/

.ASCII /DIFFERENCE LINE TEST/15/12/

.ASCII /WORD NOS 1-4 GIVE HEADER/15/12/

.ASCIZ /WORD NOS 5-260 GIVE DATA WHICH IS THE CYLINDER ADDRESS

EM71: .ASCII /FORCING OPI BY 3 INDEX PULSES /15/12/

.ASCII /CAUSED IMPROPER REGISTER CHANGE 15/12/

13:17	064316	047507	042117	042040	.ASCII	GOOD DATA GIVES WHAT SHOULD BE THERE (15)(12)
13:18	064324	052101	020101	044507		
13:19	064332	042526	020123	044127		
13:20	064340	052101	051440	047510		
13:21	064346	046125	020104	042502		
13:22	064354	052040	042510	042522		
13:23	064362	005015				
13:24	064364	042522	042503	053111	.ASCIZ	RECEIVED DATA GIVES REGISTER CONTENTS AFTER 3 INDEX PULSES/
13:25	064372	042105	042040	052101		
13:26	064400	020101	044507	042526		
13:27	064406	020123	042522	044507		
13:28	064414	052123	051105	041440		
13:29	064422	047117	042524	052116		
13:30	064430	020123	043101	042524		
13:31	064435	020122	020063	047111		
13:32	064444	042504	020130	052520		
13:33	064452	051514	051505	000		
13:34	064457	127	044510	042514	EM72: .ASCII	WHILE USING UNIBUS B (15)(12)
13:35	064464	052440	044523	043516		
13:36	064472	052440	044516	052502		
13:37	064500	020123	006502	012		
13:38	064505	122	040505	020104	.ASCII	READ DATA CAUSED IMPROPER REGISTER CHANGE (15)(12)
13:39	064512	040504	040524	041440		
13:40	064520	052501	042523	020104		
13:41	064526	046511	051120	050117		
13:42	064534	051105	051040	043505		
13:43	064542	051511	042524	020122		
13:44	064550	044103	047101	042507		
13:45	064556	005015				
13:46	064550	047507	042117	042040	.ASCII	GOOD DATA GIVES WHAT SHOULD BE THERE (15)(12)
13:47	064566	052101	020101	044507		
13:48	064574	042526	020123	044127		
13:49	064602	052101	051440	047510		
13:50	064610	046125	020104	042502		
13:51	064616	052040	042510	042522		
13:52	064624	005015				
13:53	064626	042522	042503	053111	.ASCIZ	RECEIVED DATA GIVES WHAT WAS THERE AFTER COMMAND.
13:54	064634	042105	042040	052101		
13:55	064642	020101	044507	042526		
13:56	064650	020123	044127	052101		
13:57	064655	053440	051501	052040		
13:58	064664	042510	042522	040440		
13:59	064672	052106	051105	041440		
13:60	064700	046517	040515	042116		
13:61	064706	000				
13:62	064707	127	044510	042514	EM73: .ASCII	WHILE USING UNIBUS B (15)(12)
13:63	064714	052440	044523	043516		
13:64	064722	052440	044516	052502		
13:65	064730	020123	006502	012		
13:66	064735	122	040505	020104	.ASCIZ	READ DATA INCORRECT/.
13:67	064742	040504	040524	044440		
13:68	064750	041516	051117	042522		
13:69	064756	052103	000			
13:70	064761	127	044510	042514	EM74: .ASCII	WHILE USING UNIBUS B (15)(12)

13171	064766	052440	044523	043516	
13172	064774	052440	044516	052502	
13173	065002	020123	006502	012	
13174	065007	127	044522	042524	.ASCII /WRITE DATA COMMAND CAUSED IMPROPER REGISTER CHANGE/<15><12>
13175	065014	042040	052101	020101	
13176	065022	047503	046515	047101	
13177	065030	020104	040503	051525	
13178	065036	042105	044440	050115	
13179	065044	047522	042520	020122	
13180	065052	042522	044507	052123	
13181	065060	051105	041440	040510	
13182	065066	043516	006505	012	
13183	065073	107	047517	020104	.ASCII /GOOD DATA GIVES WHAT SHOULD BE THERE/<15><12>
13184	065100	040504	040524	043440	
13185	065106	053111	051505	053440	
13186	065114	040510	020124	044123	
13187	065122	052517	042114	041040	
13188	065130	020105	044124	051105	
13189	065136	006505	012		
13190	065141	122	041505	044505	.ASCIIZ /RECEIVED DATA GIVES REGISTER CONTENTS AFTER COMMAND/
13191	065146	042526	020104	040504	
13192	065154	040524	043440	053111	
13193	065162	051505	051040	043505	
13194	065170	051511	042524	020122	
13195	065176	047503	052116	047105	
13196	065204	051524	040440	052106	
13197	065212	051105	041440	046517	
13198	065220	040515	042116	000	
13199	065225	127	044510	042514	EM75: .ASCII /WHILE USING UNIBUS B/<15><12>
13200	065232	052440	044523	043516	
13201	065240	052440	044516	052502	
13202	065246	020123	006502	012	
13203	065253	127	044522	042524	.ASCIIZ /WRITE DATA COMMAND CHANGED WRITE FROM BUFFER/
13204	065260	042040	052101	020101	
13205	065266	047503	046515	047101	
13206	065274	020104	044103	047101	
13207	065302	042507	020104	051127	
13208	065310	052111	020105	051106	
13209	065316	046517	041040	043125	
13210	065324	042506	000122		
13211	065330	044127	046111	020105	EM76: .ASCII /WHILE USING UNIBUS B/<15><12>
13212	065336	051525	047111	020107	
13213	065344	047125	041111	051525	
13214	065352	041040	005015		
13215	065356	051127	052111	020105	.ASCII /WRITE CHECK CAUSED IMPROPER REGISTER CHANGE/<15><12>
13216	065364	044103	041505	020113	
13217	065372	040503	051525	042105	
13218	065400	044440	050115	047522	
13219	065406	042520	020122	042522	
13220	065414	044507	052123	051105	
13221	065422	041440	040510	043516	
13222	065430	006505	012		
13223	065433	107	047517	020104	.ASCII /GOOD DATA GIVES WHAT SHOULD BE THERE/<15><12>
13224	065440	040504	040524	043440	

13225	065446	053111	051505	053440
13226	065454	040510	020124	044123
13227	065462	052517	042114	041040
13228	065470	020105	044124	051105
13229	065476	006505	012	
13230	065501	122	041505	044505
13231	065506	042526	020104	040504
13232	065514	040524	043440	053111
13233	065522	051505	051040	043505
13234	065530	051511	042524	020122
13235	065536	047503	052116	047105
13236	065544	051524	040440	052106
13237	065552	051105	041440	046517
13238	065560	040515	042116	000
13239	065565	120	042522	047514
13240	065572	042101	047111	020107
13241	065600	051047	041510	023503
13242	065606	050040	044522	051117
13243	065614	052040	020117	047504
13244	065622	047111	020107	042516
13245	065630	052130	052040	051505
13246	065636	020124	047504	051505
13247	065644	047040	052117	050040
13248	065652	047522	052504	042503
13249	065660	041440	051117	042522
13250	065666	052103	051040	051505
13251	065674	046125	006524	012
13252	065701	124	042510	042522
13253	065706	047506	042522	047040
13254	065714	054105	020124	042524
13255	065722	052123	051040	051505
13256	065730	046125	051524	040440
13257	065736	042522	051440	051525
13258	065744	042520	052103	053440
13259	065752	052111	020110	042522
13260	065760	040507	042122	052040
13261	065766	020117	051047	041510
13262	065774	023503	041440	047117
13263	066002	042524	052116	000123
13264				
13265	066010	042101	051104	051505
13266	066016	020123	046120	043525
13267	066024	041440	040510	043516
13268	066032	020105	042522	052523
13269	066040	052114	042105	044440
13270	066046	020116	040502	020104
13271	066054	042522	044507	052123
13272	066062	051105	042040	052101
13273	066070	000101		
13274	066072	047125	052111	042040
13275	066100	042111	047040	052117
13276	066106	043440	020117	043117
13277	066114	046106	047111	020105
13278	066122	044127	047105	040440

.ASCIZ RECEIVED DATA GIVES REGISTER CONTENTS AFTER COMMAND

EM77: .ASCII PRELOADING 'RHCC' PRIOR TO DOING NEXT TEST DOES NOT PRODUCE CORRECT RES

.ASCIZ THEREFORE NEXT TEST RESULTS ARE SUSPECT WITH REGARD TO 'RHCC' CONTENTS

EM100: .ASCIZ ADDRESS PLUG CHANGE RESULTED IN BAD REGISTER DATA

EM101: .ASCIZ UNIT DID NOT GO OFFLINE WHEN ADDRESS PLUG REMOVED



13279	066130	042104	042522	051523
13280	066136	050040	052514	020107
13281	066144	042522	047515	042526
13282	066152	000104		
13283	066154	047125	052111	047040
13284	066162	052117	040440	040526
13285	066170	046111	041101	042514
13286	066176	040440	052106	051105
13287	066204	040440	042104	042522
13289	066212	051523	050040	052514
13289	066220	020107	042522	046120
13290	066226	041501	042105	000
13291	066233	122	043505	051511
13292	066240	042524	020122	047503
13293	066246	052116	047105	051524
13294	066254	044440	041516	051117
13295	066262	042522	052103	041040
13296	066270	043105	051117	020105
13297	066276	020101	044504	043501
13298	066304	046440	042117	020105
13299	066312	042523	045505	000
13300	066317	122	043505	051511
13301	066324	042524	020122	047503
13302	066332	052116	047105	051524
13303	066340	044440	041516	051117
13304	066346	042522	052103	040440
13305	066354	052106	051105	040440
13306	066362	042040	040511	020107
13307	066370	047515	042504	051440
13308	066376	042505	020113	

EM102: .ASCII UNIT NOT AVAILABLE AFTER ADDRESS PLUG REPLACED

EM103: .ASCII REGISTER CONTENTS INCORRECT BEFORE A DIAG MODE SEEK

EM104: .ASCII REGISTER CONTENTS INCORRECT AFTER A DIAG MODE SEEK







13471	070140	020040	042524	052123						
13472	070146	020040	020040	042522						
13473	070154	020107	020040	020040						
13474	070162	047507	042117	020040						
13475	070170	020040	042522	053103						
13476	070176	020104	020040	046111						
13477	070204	042514	046107	005015						
13478	070212	020040	020040	020040	.ASCIZ /	NO	ADDRESS DATA	DATA	FUNCTN/	
13479	070220	020040	047516	020040						
13480	070226	020040	020040	042101						
13481	070234	051104	051505	020123						
13482	070242	040504	040524	020040						
13483	070250	020040	040504	040524						
13484	070256	020040	020040	052506						
13485	070264	041516	047124	000						
13486										
13487	070271	120	020103	020040	DH60: .ASCII /PC	TEST	REG	GOOD	RECVD	MODFING/<15><12>
13488	070276	020040	052040	051505						
13489	070304	020124	020040	051040						
13490	070312	043505	020040	020040						
13491	070320	043440	047517	020104						
13492	070326	020040	051040	041505						
13493	070334	042126	020040	046440						
13494	070342	042117	044506	043516						
13495	070350	005015								
13496	070352	020040	020040	020040	.ASCIZ /	NO	ADDRESS DATA	DATA	REG/	
13497	070360	020040	047516	020040						
13498	070366	020040	020040	042101						
13499	070374	051104	051505	020123						
13500	070402	040504	040524	020040						
13501	070410	020040	040504	040524						
13502	070416	020040	020040	042522						
13503	070424	000107								
13504	070426	041520	020040	020040	DH61: .ASCII /PC	TEST	PC OF	RHCS1/<15><12>		
13505	070434	020040	042524	052123						
13506	070442	020040	020040	041520						
13507	070450	047440	020106	020040						
13508	070456	044122	051503	006461						
13509	070464	012								
13510	070465	040	020040	020040	.ASCIZ /	NO	JSR	WAS/		
13511	070472	020040	047040	020117						
13512	070500	020040	020040	045040						
13513	070506	051123	020040	020040						
13514	070514	053440	051501	000						
13515	070521	120	020103	020040	DH62: .ASCII /PC	TEST	PC OF	RHDS1/<15><12>		
13516	070526	020040	052040	051505						
13517	070534	020124	020040	050040						
13518	070542	020103	043117	020040						
13519	070550	051040	042110	030523						
13520	070556	005015								
13521	070560	020040	020040	020040	.ASCIZ /	NO	JSR	WAS/		
13522	070566	020040	047516	020040						
13523	070574	020040	020040	051512						
13524	070602	020122	020040	020040						



071244	001116	004604	004600	DT6:	.WORD	SERRPC.TSTNM,REGADR,\$BDDAT,C
071252	001126	000000				
071256	001116	004604	001200	DT7:	.WORD	SERRPC.TSTNM,\$TMP1,0
071264	000000					
071266	001116	004604	001122	DT10:	.WORD	SERRPC.TSTNM,\$BDADR,CS1,CS2,DS1,ER1,3
071274	002362	002360	002404			
071302	002364	000000				
071306	001116	004604	002362	DT26:	.WORD	SERRPC.TSTNM,CS1,CS2,DS1,ER1,ER2,ER3,C
071314	002360	002404	002364			
071322	002370	002376	000000			
071330	001116	004604	004602	DT30:	.WORD	SERRPC.TSTNM,ERWORD,\$GDDAT,\$BDDAT,0
071336	001124	001126	000000			
071344	001116	004604	004600	DT51:	.WORD	SERRPC.TSTNM,REGADR,\$GDDAT,\$BDDAT,ILLEGL,C
071352	001124	001126	002464			
071360	000000					
071362	001116	004604	004600	DT63:	.WORD	SERRPC.TSTNM,REGADR,\$GDDAT,\$BDDAT,\$BDADR,C
071370	001124	001126	001122			
071376	000000					
071400	001116	004604	041422	DT61:	.WORD	SERRPC.TSTNM,PCJSR,\$BDADR,C
071406	001122	000000				
071412	001116	004604	041422	DT62:	.WORD	SERRPC.TSTNM,PCJSR,\$BDADR,C
071420	001122	000000				
071424	001116	004604	002362	DT65:	.WORD	SERRPC.TSTNM,CS1,AS,DS1,0
071432	002400	002404	000000			
071440	001116	004604	002364	DT66:	.WORD	SERRPC.TSTNM,ER1,ER2,ER3,CS1,CS2,C
071446	002370	002376	002362			
071454	002360	000000				
071460	001116	004604	041422	DT77:	.WORD	SERRPC.TSTNM,PCJSR,REGADR,\$GDDAT,\$BDDAT,C
071466	004600	001124	001126			
071474	000000					
071476	000	000	000	DF1:	.BYTE	0,0,0,0,0,0,0
071500	000	000	000			
071504	000	000	000	DF4:	.BYTE	0,0,0,0,0,1,0
071508	000	000	001			
071512	000	000	000	DF5:	.BYTE	0,0,0,0,0
071514	000	000	000			
071517	000	000	000	DF6:	.BYTE	0,0,0,0
071521	000	000	000			
071524	000	000	000	DF7:	.BYTE	0,0,0
071526	000	000	000			
071530	000	000	000	DF10:	.BYTE	0,0,0,0,0,0,0
071532	000	000	000			
071536	000	000	000			
071537	000	000	000	CF26:	.BYTE	0,0,0,0,0,0,0,0
071542	000	000	000			
071546	000	000	000			













M61	000000	636	12411*
M62	000000	632	12415*
M63	000000	642	12440*
M64	000000	649	12449*
M65	000000	659	12069*
M66	000000	660	12474*
M67	000000	671	12498*
M68	000000	685	12529*
M69	000000	697	12560*
M70	000000	710	12600*
M71	000000	722	12632*
M72	000000	735	12666*
M73	000000	744	12679*
M74	000000	757	12092*
M75	000000	768	12711*
M76	000000	784	12737*
M77	000000	797	12756*
M78	000000	804	12796*
M79	000000	816	12806*
M80	000000	828	12835*
M81	000000	840	12867*
M82	000000	852	12891*
M83	000000	862	12089*
M84	000000	870	12925*
M85	000000	879	12972*
M86	000000	889	12981*
M87	000000	897	12990*
M88	000000	910	12998*
M89	000000	921	13023*
M90	000000	937	13026*
M91	000000	948	13047*
M92	000000	960	12098*
M93	000000	984	13080*
M94	000000	991	13105*
M95	000000	1002	13124*
M96	000000	1010	984
M97	000000	1022	13162*
M98	000000	1022	991
M99	000000	1022	13170*
M100	000000	1022	13199*
M101	000000	1022	13211*
M102	000000	1022	13239*
M103	000000	1022	13239*
M104	000000	1022	11613*
M105	000000	1166	2712
M106	000000	1155	2781
M107	000000	1443	5029
M108	000000	1369	10042
M109	000000	1391	10334
M110	000000	1394	10346
M111	000000	1224	11135
M112	000000	1227	11136*
M113	000000	1225	11139*
M114	000000	1228	11141*
M115	000000	1226	
M116	000000	1229	
M117	000000	1250	

FER = 000020  
FILL 042312  
FILLRE 041270

1174*													
10791*													
3340	3343	3386	3389	3392	3395	3398	3401	3408	3413	3422	3425	3645	
3648	3817	3820	4056	4059	4066	4069	4247	4954	5218	5221	5224	5365	
5369	5371	5527	5530	5533	5723	5726	5729	5886	5989	5992	6075	6078	
6081	6227	6230	6233	6424	6427	6430	6587	6590	6593	6824	6827	6830	
6989	6992	6995	7234	7237	7240	7401	7404	7407	7648	7651	7654	7657	
7660	7816	7819	7822	8068	8071	8075	9078	8081	8228	8231	8234	8372	
8624	8994	8997	9000	9090	9093	9096	9235	9238	9241	9482	9485	9488	
9747	9750	9753	10024	10432*									

FINACC 004664  
FINALA 004552  
FIRST 004736  
FISH 017316  
FLHEAD 041212

1456*	10588*											
1457*	10589*											
1485*	1573	1610*										
3896	3919*											
5095	5117	7093	7100	7121	7134	7298	7496	7513	7534	7547	7717	7943
7962	8122	8427	8695	8848	8915	10375*						
1271*	3657	3829	4956	5143	5144	5145	5292	5293	5294	5454	5455	5456
5648	5649	5650	5813	5814	5815	6000	6001	6002	6154	6155	6156	6349
6350	6351	6514	6515	6516	6725	6726	6727	6915	6916	6917	7160	7161
7162	7326	7327	7328	7573	7574	7575	7744	7745	7746	7991	7992	7993
8154	8155	8156	8457	8458	8459	8715	8716	8717	8948	8949	8950	9043
9044	9045	9161	9162	9163	9410	9411	9412	9670	9671	9672	9853	9854
9855	9969	9970	9971	10105	10106	10107						

FUTABL 002422  
GNS = \*\*\*\*\* U

1410*												
174	1581	1585	1590	1594	1598	1603	1607	1662	1704	1708	1767	1794
1798	1802	1806	1853	1859	1865	1937	1941	1979	1985	1991	2024	2030
2036	2066	2070	2074	2080	2217	2223	2249	2255	2638	2644	2664	2670
2914	2918	2932	2938	2943	2969	2975	3011	3015	3513	3523	4346	4350
4541	4607	4613	6809	6817	9902	9908	10169	10175	10232	10238	10886	10892
10899	10903	10907	10914	10918	11044	11050	11062	11068	11074	11078	11084	11090
11094	11104	11672	11680	11698	11696	11704	11712	11720	11728	11736	11744	11752
11760	11768	11776	11784	11792	11800	11808	11816	11978	11979	11980	11981	11982
11984	11986	11987	11988	11989	11990							

GC = 000001

1139*	2322	2427	2835	2993	3072	3187	3323	3615	3628	3787	3800	4007
4026	4207	4226	4368	4483	4503	4569	4529	4766	4786	4896	4906	5017
5040	5174	5196	5323	5343	5495	5505	5679	5701	5844	5864	6031	6053
6185	6205	6380	6402	6545	6565	6757	6779	6947	6967	7190	7212	7357
7379	7604	7626	7775	7795	7919	8022	8046	8186	8206	8329	8350	8479
8543	8581	8602	8737	8825	8979	9074	9193	9213	9317	9440	9460	9579
9705	9725	9875	9993	10121	10339	10613						

GRV = 000010  
GTSWR = 004405  
HCF = 000200  
HCF1 = 002000

1155*												
1617	11984*											
1177*												
1269*	3661	3833	4956	5145	5294	5456	5650	5815	6003	6156	6351	6516
6727	6917	7162	7329	7575	7746	7993	8156	8459	8717	8950	9045	9163
9412	9672	9855	9971	10107								

HCRC = 000400  
HT = 000011  
H1 = 003323

1178*												
65*	11273	11314										
2330*	2335	2437*	2442	2844*	2849	3002*	3007	3080*	3085	3195*	3200	3331*
3336	3636*	3641	3808*	3813	4015*	4020	4215*	4220	4376*	4381	4539*	4643
4775*	4780	4895*	4900	5023*	5028	5046*	5051	5185*	5190	5332*	5337	5494*
5499	5690*	5695	5953*	5958	6042*	6047	6194*	6199	6391*	6396	6554*	6559
6768*	6773	6956*	6961	7201*	7206	7368*	7373	7615*	7620	7794*	7799	7952*
7932	8035*	8040	8195*	8200	8339*	8344	8486*	8491	8552*	8557	8591*	8596







PKACK	002460	1426*	2300	2321	2405	2426	2447	2834	2992	4367	4628			
PLU =	020000	1256*												
PRE =	000020	1296*												
PRITEM	046760	1653*	2372*	2948*	3698*	4616*	10210*	10255*	10883*	11659*	11821*	11838	11842*	
PROG =	001000	1161*												
PRJ =	000000	89*												
PR1 =	000040	90*												
PR2 =	000100	91*												
PR3 =	000140	92*												
PR4 =	000200	93*												
PR5 =	000240	94*												
PRE =	000300	95*												
PR7 =	000340	96*												
PS =	177776	69*	70	1565*	2105*	2144*	2199*	10228*	10892*					
PSEL =	002000	1144*	6731	6757	6779	6921	6947	6967	9678	9705	9725			
PSU =	000001	1293*												
PSW =	177776	70*												
PUTREG	043300	2651	2676	2961	10333	10345	10830	10938*						
PWRVEC=	000024	161*	1539*	1540*	11996*	11997*	12006*	12012*	12024*	12025*				
RDCHR =	104407	11514	11987*											
RDLIN =	104410	11561	11988*											
RDOCT =	104411	1664	10909	10920	11053	11070	11989*							
RDY =	000200	1141*	2106	2117	2145	2273	2448	2697	3082	3197	3333	3910	4026	4226
		4494	4503	4786	4906	5187	5334	5496	5692	5855	6044	6196	6393	6556
		6770	6958	7203	7370	7617	7786	8037	8197	8350	8488	8602	8746	8988
		9083	9204	9451	9716	9885	10002	10130	10518					
READAT	002446	1421*	5457	5484	5504	5816	5843	5863	6157	6184	6204	6517	6544	6564
		6918	6946	6966	10108	10120								
READIN	002462	1427*	3591	3614	3627	3763	3786	3799						
RECALI	002426	1413*	3987	4006	4025	4187	4206	4225	5039	7918	8542			
REFOR	002450	1422*	5295	5322	5342	7329	7356	7378	7747	7774	7794	8157	8195	8205
		8460	8478	8718	8736	9046	9073							
REGADR	004600	1442*	1738*	2284*	2454*	2463*	4040*	4049*	4232*	4241*	4517*	4533*	4800*	4809*
		4920*	4929*	5202*	5211*	5349*	5358*	5511*	5520*	5707*	5716*	5870*	5879*	6059*
		6068*	6211*	6220*	6408*	6417*	6571*	6580*	6785*	6794*	6973*	6982*	7218*	7227*
		7385*	7394*	7632*	7641*	7801*	7810*	8052*	8061*	8212*	8221*	8356*	8365*	8509*
		8617*	9219*	9228*	9466*	9475*	9731*	9740*	10356*	10839*	13577	13579	13593	13597
		13610												
REGSAV	046614	11612*												
REGSA1	046622	1685	11614*											
REINTO	003534	1436*	5118	5125	5254	5274	5288	5370	5401	5450	5532	5560	5626	5631
		5757	5778	5783	5809	5891	5920	5983	6109	6129	6150	6232	6261	6327
		6332	6458	6479	6484	6510	6592	6621	6703	6708	6859	6880	6885	6911
		6994	7024	7122	7129	7135	7142	7270	7290	7322	7406	7438	7535	7542
		7548	7555	7690	7710	7740	7821	7852	8113	8150	8233	8262	8420	8453
		8497	8677	8711	8755	8931	9039	9095	9128	10082	10101	10139		
RELEAS	002432	1415*												
RES/EC=	000010	156*												
RETCL	002456	1425*	4864	4885	4905									
RHAS	002316	1343*	1732	1769	3590*	3752*	4851*							
RHBA	002274	1331*	3344	5222	5369	5571	5727	5890	6079	6231	6428	6591	6828	6993
		7238	7405	7652	7820	80 ?	8232	8998	9094	9239	9486	9751	10010	10971*
RHBAE	002340	1355*												
RHCA	002312	1341*	3399	3646	3818	765	8079	10328*	10452*	10470*	10969*			

M1c

RHCC	002334	1350*	2282	2284	2286	3313	4057	4248	7658	8076	8373	8625	10352	10356
RHCS1	002300	10588	10944											
		1336*	2300*	2324*	2405*	2429*	2693	2764	26...*	2995*	3050*	3074*	3081	3165*
		3189*	3196	3325*	3332	3365	3430	3591*	3617*	3630*	3763*	3789*	3802*	3809
		3987*	4009*	4067	4187*	4209*	4263	4370*	4461*	45*	4493	4565	4575	4631*
		4745*	4768*	4818	4864*	4888*	4938	5019*	5042*	51...	5186	5325*	5333	5487*
		5495	5681*	5691	5846*	5854	6033*	6043	6187*	6195	6382*	6392	6547*	6555
		6731*	6759*	6769	6921*	6949*	6957	7192*	7202	7359*	7...	7606*	7616	7777*
		7785	7921*	8024*	8036	8188*	8196	8331*	8388	8481*	8487	8545*	8583*	8640
		8739*	8745	8827*	8981*	8987	9076*	9082	9195*	9203	9319*	942*	9450	9580*
		9678*	9707*	9715	9877*	9884	9995*	10001	10017	10123*	10129	103...	10339*	10454*
		10471*	10480*	10484	10639	10651	10719	10732	10977*	11046	11080			
RHCS2	002276	1332*	1770	1829*	1955*	2717	2784	3354	3375	6804	10011	10485	10...	
RHCS3	002342	1356*												
RHDB	002270	1329*	1690	11054										
RHDS1	002304	1338*	2650	2960	3390	3649	3821	5225	5372	5534	5730	5893	6082	6237
		6431	6594	6831	6996	7241	7408	7661	7823	8082	8235	9001	9097	9242
		9489	9754	10025	10329*	10453*	10969*							
		1345*	2331	2339	2438	2469	2706	2775	2845	3003	3216	3414	3637	3664
		3836	4016	4070	4216	4256	4340	4377	4554	4639	4776	4825	4896	4945
		5024	5029	5047	7928	8340	8381	8553	8592	8633	8835	9325	9586	9915
		10036	10182	10486										
RHDT	002324	1346*	1830	1832	1836	1838	1841	1843	1861	1956	1958	1965	1967	1993
		2000	2002	2005	2007	2010	2012	2044	2046					
RHEC1	002330	1348*	3423											
RHEC2	002332	1349*	3426											
RHER1	002302	1337*	1773	3387	10029	10487								
RHER2	002306	1339*	3393											
RHER3	002314	1342*	2724	2791	3402									
RHLA	002336	1351*	10589											
RHMR	002320	1344*	2524*	2543*	2614*	2798	3409	3890*	3909*	4112*	4131*	10331*		
RHOF	002310	1340*	3396	3653	3825	4060	4955	10479*	10975*					
RHSN	002326	1347*	1987	2043	2045									
RHWC	002272	1330*	2307	2412	2619	3057	3115	3172	3268	3341	3541	3598	3734	3770
		3942	3994	4163	4194	4390	4468	4692	4751	4871	5153	5219	5302	5366
		5464	5528	5658	5724	5823	5887	6010	6076	6164	6228	6359	6425	6524
		6588	6737	6825	6927	6990	7169	7235	7336	7402	7593	7649	7754	7817
		8001	8069	8165	8229	8314	8566	8958	8995	9053	9091	9172	9236	9419
		9483	9683	9748	9894	9978	10009	10160	10437	10756	10839	10942	10944	10970*
RH70	004750	1493*	1624*	3204	3349	3611	3783	6678	9548					
RH70CK	006146	1622*												
RMR =	000004	1172*												
RPTRP1	011370	2102	2116*											
RPTRP2	011476	2141	2155*											
RPVEC	002266	1094*	1567	2101	2140	2313*	2418*	3063*	3178*	3314*	3604*	3776*	3999*	4199*
		4474*	4757*	4877*	5165*	5314*	5476*	5670*	5835*	6022*	6176*	6371*	6535*	6749*
		6939*	7181*	7348*	7595*	7766*	7911*	8013*	8177*	8320*	8470*	8534*	8572*	8728*
		8816*	8970*	9065*	9184*	9308*	9431*	9569*	9695*	9866*	9984*	10112*	11064	11071*
		11086	11107*											
RPVECT	044352	1568	11101*	11107										
RPOS	004746	1492*	1964*	1970*	2505	2576	2869							
RPO6	004744	1491*	1954*	1961*	2503	2574	2867	3872	4095					
RP4VEC	004606	1445*	1652*	2313	2418	3063	3178	3314	3604	3776	3999	4199	4474	4757
		4877	5165	5314	5476	5670	5835	6022	6176	6371	6535	6749	6939	7191

RUN	043340	7349	7595	7766	7911	8013	8177	8320	8470	8534	8572	8728	8816	8970
		9065	9184	9308	9431	9569	9695	9866	9984	10112	6716	6906	7150	7316
		5133	5282	5445	5639	5904	5991	5145	6340	6505	9401	9661	9844	9960
		7563	7734	7981	8144	8447	8705	8938	9033	9151				
RO	=:000000	10096	10968*											
		77*	1567*	1568*	1569*	1689*	1692*	1772*	1776*	1816*	1619*	1624*	1876*	1917*
		1918	1928*	1929	1931	1946	1949*	1955	1976	2101*	2102*	2103*	2140*	2141*
		2142*	2270*	2271*	2273	2306*	2338*	2349*	2411*	2433*	2450	2453	2468*	2480*
		2525*	2544*	2618*	2650*	2692*	2705*	2716*	2723*	2737*	2763*	2774*	2783*	2790*
		2797*	2811*	2841*	2960*	2999*	3056*	3090*	3115*	3116*	3118*	3120*	3121*	3123*
		3125*	3127*	3129*	3131*	3133*	3137	3141*	3144*	3153*	3154	3157*	3171*	3215*
		3227*	3268*	3269*	3271*	3273*	3274*	3276*	3278*	3280*	3282*	3284*	3286*	3290
		3294*	3297*	3306*	3307	3310*	3340*	3343*	3353*	3364*	3374*	3386*	3389*	3392*
		3395*	3398*	3401*	3408*	3413*	3422*	3425*	3429*	3441*	3541*	3546*	3548*	3550*
		3551*	3553*	3555*	3557*	3559*	3561*	3563*	3567	3571*	3574*	3583*	3584	3587*
		3597*	3621*	3645*	3648*	3652*	3663*	3675*	3734*	3739*	3741*	3743*	3744*	3746*
		3748*	3750*	3752*	3754*	3756*	3758*	3759*	3769*	3793*	3817*	3820*	3824*	3835*
		3947*	3891*	3910*	3942*	3943*	3945*	3947*	3948*	3950*	3952*	3954*	3956*	3958*
		3960*	3964	3968*	3971*	3980*	3981	3984*	3993*	4012*	4029	4036	4039	4056*
		4059*	4066*	4069*	4077*	4113*	4132*	4163*	4165*	4167*	4169*	4170*	4172*	4174*
		4176*	4178*	4180*	4182*	4184*	4185*	4193*	4212*	4228	4231	4247*	4255*	4262*
		4272*	4390*	4395*	4399*	4403*	4406*	4410*	4414*	4418*	4422*	4426*	4430*	4436
		4440*	4443*	4452*	4453	4459*	4467*	4489*	4506	4513	4516	4514*	4574*	4589*
		4635*	4692*	4694*	4697*	4700*	4702*	4705*	4708*	4711*	4715*	4717*	4719*	4723
		4727*	4730*	4739*	4740	4743*	4750*	4771*	4789	4796	4799	4817*	4824*	4839*
		4870*	4891*	4909	4916	4919	4937*	4944*	4954*	4960*	5011*	5095*	5105*	5117*
		5124*	5133*	5152*	5179*	5198	5201	5218*	5221*	5224*	5232*	5253*	5273*	5282*
		5301*	5328*	5345	5348	5365*	5368*	5371*	5379*	5399*	5436*	5445*	5463*	5490*
		5507	5510	5527*	5530*	5533*	5541*	5558*	5610*	5615*	5625*	5630*	5639*	5657*
		5684*	5703	5706	5723*	5726*	5729*	5737*	5756*	5777*	5782*	5790*	5795*	5804*
		5822*	5849*	5866	5869	5896*	5889*	5892*	5900*	5918*	5972*	5982*	5991*	6009*
		6036*	6055	6058	6075*	6078*	6081*	6089*	6108*	6128*	6136*	6145*	6163*	6190*
		6207	6210	6227*	6230*	6233*	6241*	6259*	6311*	6316*	6326*	6331*	6340*	6358*
		6385*	6404	6407	6424*	6427*	6430*	6438*	6457*	6478*	6483*	6491*	6496*	6505*
		6523*	6550*	6567	6570	6587*	6590*	6593*	6601*	6619*	6687*	6692*	6702*	6707*
		6716*	6736*	6762*	6781	6784	6824*	6827*	6830*	6838*	6859*	6879*	6884*	6892*
		6897*	6906*	6926*	6952*	6969	6972	6989*	6992*	6995*	7003*	7022*	7083*	7092*
		7100*	7109*	7121*	7128*	7134*	7141*	7150*	7168*	7195*	7214	7217	7234*	7237*
		7240*	7248*	7269*	7289*	7298*	7307*	7316*	7335*	7362*	7381	7384	7401*	7404*
		7407*	7415*	7436*	7496*	7505*	7513*	7522*	7534*	7541*	7547*	7554*	7563*	7582*
		7609*	7629	7631	7648*	7651*	7654*	7657*	7660*	7668*	7689*	7709*	7717*	7726*
		7734*	7753*	7780*	7797	7800	7816*	7819*	7822*	7829*	7850*	7924*	7943*	7953*
		7962*	7972*	7981*	8000*	8027*	8048	8051	8068*	8071*	8075*	8078*	8081*	8089*
		8112*	8122*	8130*	8135*	8144*	8164*	8191*	8209	8211	8228*	8231*	8234*	8242*
		8260*	8309*	8313*	8334*	8352	8355	8372*	8380*	8387*	8396*	8419*	8427*	8434*
		8439*	8447*	8495*	8548*	8561*	8565*	8586*	8604	8607	9624*	8632*	8639*	8648*
		8676*	8685*	8692*	8697*	8705*	8753*	8813*	8930*	8848*	8859*	9864*	9869*	8874*
		8879*	8888*	8892	8893*	8900*	8901	8902*	8909*	8915*	8922*	8930*	8938*	8957*
		8994*	8997*	9000*	9008*	9033*	9052*	9090*	9093*	9096*	9105*	9126*	9151*	9171*
		9198*	9215	9218	9235*	9238*	9241*	9249*	9305*	9339*	9344*	9349*	9354*	9359*
		9368*	9372	9373*	9380*	9381	9382*	9388*	9393*	9401*	9418*	9445*	9462	9465
		9482*	9485*	9488*	9496*	9566*	9600*	9605*	9610*	9615*	9620*	9629*	9633	9634*
		9641*	9642	9643*	9648*	9653*	9661*	9682*	9710*	9727	9730	9747*	9750*	9753*
		9760*	9836*	9844*	9893*	9914*	9924*	9952*	9960*	9977*	10016*	10024*	10029*	10035*





5195*	5196*	5197	5198	5205*	5206	5207	5322*	5323*	5325	5342*	5343*	5344
5345*	5352*	5353	5354	5426*	5484*	5485*	5487	5504*	5505*	5506	5507	5514*
5515*	5516	5600*	5678*	5679*	5681	5700*	5701*	5702	5703	5710*	5711	5712
5704*	5744*	5846	5863*	5864*	5865	5866	5873*	5874	5875	5962*	6030*	6031*
6206	6207	6214*	6215	6255	6062*	6063	6064	6184*	6195*	6187	6204*	6205*
6411*	6412	6413	6544*	6545*	6301*	6379*	6380*	6382	6401*	6402*	6403	6404
6669*	6756*	6757*	6759	6778*	6779*	6780	6781	6788*	6566	6567	6574*	6575
6949*	6966*	6967*	6968	6969	6975*	6977	6978	7073*	7189*	7189*	7190*	6946*
7212*	7213	7214	7221*	7222	7223	7356*	7357*	7359	7378*	7379*	7380	7211*
7388*	7399	7390	7486*	7603*	7604*	7606	7625*	7626*	7627	7628	7635*	7381
7727*	7774*	7775*	7777	7794*	7795*	7796	7797	7904*	7905	7806	7885*	7919*
7919*	7921	8021*	8022*	8024	8045*	8046*	8047	8048	8055*	8056	8057	8185*
8186*	8198	8205*	8206*	8207	8208	8215*	8216	8217	8328*	8329*	8331	8349*
8250*	8251	8252	8259*	8260	8361	8478*	8479*	8481	8525*	8542*	8543*	8545
8550*	8581*	8583	8601*	8602*	8603	8604	8611*	8612	8613	8736*	8737*	8739
8938*	8934*	8925*	8927	8981*	8982*	8979*	8979*	8981	9073*	9074*	9075	9192*
9193*	9195	9212*	9213*	9214	9215	9222*	9223	9224	9289*	9316*	9317*	9319
9267*	9384	9439*	9440*	9442	9459*	9460*	9461	9462	9469*	9470	9471	9538*
9577*	9579*	9580	9628*	9645	9704*	9705*	9707	9724*	9725*	9726	9727	9734*
9735	9736	9826*	9874*	9875*	9877	9904*	9992*	9993*	9995	10011*	10012*	10014
10120*	10121*	10123	10171*	10234*	10240*	10250*	10292*	10324*	10360	10376*	10377*	10386
10387	10405*	10406*	10407*	10414	10415	10416	10433*	10434*	10439	10440	10506	10508*
10509*	10512	10514*	10516*	10517*	10518	10526*	10527	10537*	10552*	10553*	10554*	10561
10562	10563	10614*	10615*	10616*	10617*	10618	10625*	10674	10675	10676	10677	10697*
10698*	10699	10705*	10740	10741	10752*	10753*	10764	10765	10782*	10783*	10784*	10795*
10795	10796	10797	10798	10819*	10820*	10821*	10822*	10823*	10843*	10844*	10845	10851
10852	10853	10854	10855	10888*	10894*	10910*	10911	10921	10922*	10939*	10940*	10941*
10948	10949	10950	10972*	10973*	10974	11002*	11003*	11004*	11005*	11006*	11023*	11024*
11025	11030	11031	11032	11033	11034	11046*	11057	11064*	11071	11080*	11086*	11135*
11138	11140	11141	11169	11170	11174*	11190*	11191*	11192*	11193*	11194*	11195*	11196
11199*	11212	11214*	11216	11226	11228	11230	11231	11232	11233	11234	11236*	11237*
11265*	11266	11267*	11269	11270	11271*	11273	11275	11277	11283	11285*	11297*	11295*
11299	11303	11304	11308	11349*	11350*	11351	11355	11357	11366	11368	11371	11374*
11375*	11376	11381	11383	11385*	11386	11402*	11403*	11404	11414	11421*	11424*	11425*
11429*	11430*	11432	11435*	11442	11445*	11449	11451	11453	11454*	11461	11463	11465*
11466	11468*	11469*	11470*	11471*	11472*	11487*	11488*	11489*	11490*	11491*	11497*	11510*
11515	11526	11527*	11528*	11529*	11556*	11557*	11558*	11559*	11560*	11562	11566*	11568
11570	11578*	11579	11581	11582*	11584	11585	11586	11588	11622	11636*	11639*	11667*
11662*	11663	11674*	11682*	11690*	11698*	11706*	11714*	11722*	11730*	11738*	11746*	11754*
11762*	11770*	11778*	11796*	11794*	11802*	11810*	11818*	11824*	11829*	11853*	11859*	11863*
11870	11871	11902*	11903	11904	11905*	11910*	11911*	11912*	11918	11943	11944	11945
11946*	11947*	11962*	11963	11998*	11999*	12000*	12001*	12002*	12003*	12004*	12005	12013*
12017	12018	12019	12020	12021	12022	12023	12028*					
10452*												
216*												
217*												
218*												
219*												
60*												
3037	1531	1684	1729	1906	2094	2123	2192	2394	2520	2533	2594	2699
5084	3253	3489	3723	3886	3905	3920	4108	4127	4191	4274	4369	4499
9826	5426	5600	5962	6301	6569	7073	7486	7885	8525	8736	9299	9572
1515	1518	1523*										

H 04:316  
 M 177572  
 N 77574  
 N 77576  
 N 72516  
 N 001030  
 005022











VC30	0000010	1295*												
VC30	0000020	1294*												
VC30	0100000	1255*												
VC30	0001000	1158*	2264	2332	2343	2439	2457	2473	2714	2846	3004	3628	3668	3840
		4043	4235	4342	4378	4524	4525	4555	4556	4640	4803	4923	5205	5352
		5514	5710	5873	6062	6214	6411	6574	6788	6976	7221	7388	7635	7804
		8055	8215	8359	8611	9222	9469	9734	10509	10527				
WRITBT	041622	10609*	10622*	10629	10634	10703*	10709	10714	13571	13574				
WRITBT	041615	10607*	10619*	10620*	10700*	10701*	13571	13574						
WRITBT	041620	10608*	10621*	10629	10634	10638	10650	10702*	10709	10714	10718	10734	13571	13574
WRITBT	041624	10597*	10610*	10611*	10658	10668	13574							
WRITBT	041626	10611*												
WRITBT	042038	10696*	11990											
WRITBT	104412	2330	2437	2844	3002	3080	3195	3331	3636	3808	4015	4215	4376	4492
		4638	4775	4895	5023	5046	5185	5332	5494	5690	5853	6042	6194	6291
		6554	6769	6956	7201	7368	7615	7784	7927	8035	8195	8339	8486	8552
		8591	8744	8834	8986	9091	9202	9324	9449	9585	9714	9883	10000	10129
		11990*												
XC	002354	1383*	2352	2483	2740	2814	3093	3230	3444	3678	3850	4080	4275	4592
		4841	4963	5235	5382	5544	5740	5903	6092	6244	6441	6604	6841	7005
		7251	7418	7671	7832	8092	8245	8399	8651	9011	9109	9252	9499	9763
		9927	10055	10195	10943	11674								
XC		1124*												
XC	0000040	1175*												
XC	0000001	1243*												
XC	0004000	1181*	10033											
XC	0002440	1418*	9164	9192	9212									
XC	0004336	1417*	9413	9439	9459	9673	9704	9724						
XC	0004700	1435*	5096	5106	5139	5223	5255	5400	5437	5559	5611	5616	5644	5728
		5758	5791	5796	5919	5973	5996	6080	6110	6137	6260	6312	6317	6345
		6429	6459	6492	6497	6620	6698	6693	6721	6829	6860	6893	6898	7023
		7084	7093	7101	7110	7156	7239	7271	7299	7308	7437	7497	7505	7514
		7523	7569	7653	7691	7718	7727	7851	7944	7954	7963	7973	7987	8073
		8123	8131	8136	8261	8428	8435	8440	8496	8686	8693	8698	8754	8849
		8860	8865	8870	8875	8880	8890	8910	8916	8923	8944	8999	9127	9157
		9240	9340	9345	9350	9355	9360	9370	9399	9394	9406	9487	9601	9506
		9611	9616	9621	9631	9649	9654	9656	9752	9837	9949	9953	9965	10089
		10138												
WRICAT	002442	1419*	5651	5678	5700	6003	6030	6052	6352	6379	6401	6728	6755	6778
		9856	9874	9972	9992									
WRIFOR	002444	1420*	5146	5173	5195	7163	7189	7211	7576	7603	7625	7994	8221	8245
		8951	8978											
		1163*	9919	10186										
WRU	004000	1251*												
WRU	000400	1245*												
WSU	000004	1784	1815*											
XE2	007372	269*	1619*	11412	11541									
SADTCB	001134	264*	2228*	2236*	2266*	2276*	10521*	10529*	13584	13597	13600	13602	4799*	4909*
SBDADR	001122	266*	1736*	2286*	2453*	2462*	4039*	4048*	4231*	4240*	4516*	4532*	5869*	5979*
SBDADR	001126	4319*	4928*	5201*	5210*	5348*	5357*	5510*	5519*	5706*	5715*	5869*	5979*	6039*
		6067*	6210*	6219*	6407*	6416*	6570*	6579*	6784*	6793*	6972*	6981*	7217*	7226*
		7384*	7393*	7631*	7640*	7800*	7809*	8051*	8060*	8211*	8220*	8355*	8464*	8501*
		8616*	9218*	9227*	9465*	9474*	9730*	9739*	10352*	10353*	10638*	10650*	10719*	10724*
		10838*	11019*	13571	13574	13577	13579	13590	13593	13597	13610			







ALLREG	40#	11668	11677	11685	11693	11701	11709	11717	11725	11733	11741	11749	11757	11765	11773
CHANGR	11781	11789	11797	11805	11813										
	40#	2337	2467	2691	2704	2715	2722	2762	2773	2782	2789	2796	3210	3214	3352
	3363	3372	3428	3651	3662	3823	3834	4062	4250	4254	4261	4563	4573	4580	4816
	4823	4830	4936	4943	4950	8375	8379	8386	8627	8631	8638	9913	10015	10027	10034
	10043	10180													
CHECKD	40#	2591	2905	3044	3160	3260	3937	4158	4331	5006	5159	5308	5470	5664	5829
	6016	6170	6365	6530	6743	6933	7175	7342	7589	7760	8007	8171	8464	8722	8805
	9964	9059	9178	9296	9425	9552	9689	9860							
CHECKV	40#	2296	2401	3537	3730	4359	4673								
CHKCNT	40#	4383	4687	4859											
CKCNTV	40#	4618													
CLEARA	40#	5105	5124	5273	5436	5610	5615	5625	5630	5777	5782	5790	5795	5972	5982
	6128	6136	6311	6316	6326	6331	6478	6483	6491	6496	6687	6692	6702	6707	6879
	6884	6892	6897	7092	7109	7129	7141	7289	7307	7505	7522	7541	7554	7709	7726
	7952	7971	8112	8129	8135	8418	8434	8439	8576	8692	8697	8859	8864	8869	8874
	9879	8909	8922	8930	9339	9344	9349	9354	9359	9388	9393	9600	9605	9610	9615
	9620	9648	9653	9836	9952	10081	10089								
CMPBLK	40#	5251	5397	5556	5754	5916	6106	6257	6455	6617	6856	7020	7267	7434	7687
	7848	8258	8494	8752	9124	10136									
CMREGI	40#	2348	2479	2736	2810	3089	3226	3440	3674	3846	4076	4271	4588	4837	4959
	5231	5378	5540	5736	5899	6088	6240	6437	6600	6837	7002	7247	7414	7667	7828
	8088	8241	8395	8647	9007	9104	9248	9495	9759	9923	10051	10191			
COMMEN	13	167#													
DATA00	40#	5132	5281	5444	5638	5803	5990	6144	6339	6504	6715	6905	7149	7315	7562
	7733	7979	8142	8446	8704	8937	9032	9150	9400	9660	9843	9959	10095		
DISREG	40#														
DUM	40#	2300	2405	3050	3165	3591	3763	3987	4187	4461	4745	4864			
ENDCOM	22	167#													
ERROR	61#	1698	1740	2112	2119	2156	2229	2237	2267	2277	2287	2357	2455	2464	2488
	2655	2684	2745	2819	2965	3098	3235	3449	3683	3855	4041	4050	4085	4233	4242
	4280	4518	4534	4597	4801	4810	4846	4921	4930	4967	5031	5203	5212	5240	5261
	5350	5359	5387	5407	5512	5521	5549	5566	5708	5717	5745	5764	5871	5880	5908
	5926	6060	6069	6097	6116	6212	6221	6249	6267	6409	6418	6446	6465	6572	6591
	6609	6627	6786	6795	6846	6866	6974	6993	7011	7030	7219	7228	7256	7277	7386
	7395	7424	7445	7633	7642	7676	7697	7802	7811	7838	7859	8053	8062	8096	8213
	8222	8250	8268	8357	8366	8404	8503	8609	8618	8656	8761	9016	9114	9135	9220
	9229	9257	9467	9476	9503	9732	9741	9768	9932	10060	10145	10200	10336	10348	10357
	10522	10530	10641	10643	10653	10662	10671	10721	10724	10735					
ESCAPE	167#														
FIHEAD	40#	5094	5116	7082	7099	7120	7133	7297	7495	7512	7533	7546	7716	7941	7960
	8120	8426	8684	8847	8914										
FILLBL	40#														
FLSVRE	40#	3340	3343	3386	3389	3392	3395	3398	3401	3408	3412	3422	3425	3645	3648
	3817	3820	4056	4059	4066	4069	4247	4954	5218	5221	5224	5365	5368	5371	5527
	5530	5533	5723	5726	5729	5886	5889	5892	6075	6078	6091	6227	6230	6233	6424
	6427	6430	6587	6590	6593	6824	6827	6830	6989	6992	6995	7234	7237	7240	7401
	7404	7407	7648	7651	7654	7657	7660	7816	7819	7822	8068	9071	8074	8078	8091
	8228	8231	8234	8372	8624	8994	8997	9000	9090	9093	9096	9235	9238	9241	9482
	9485	9488	9747	9750	9753	10024									
GETPRI	167#														
GETSWR	2#	167#	1612												
GOO	40#	2320	2425	2833	2991	3070	3185	3321	3613	3625	3785	3797	4005	4205	4365
	4481	4627	4764	4884	5015	5038	5172	5321	5483	5677	5842	6029	6183	6379	6543

LOAD	6755	6945	7188	7355	7602	7773	7917	8020	8194	8327	8477	8541	8579	8735	8823
	8977	9072	9191	9315	9438	9576	9703	9873	9991	10119					
	40#	3116	3118	3120	3121	3123	3125	3127	3129	3131	3133	3135	3157	3269	3271
	3273	3274	3276	3278	3280	3282	3284	3286	3288	3310	3546	3548	3550	3551	3553
	3555	3557	3559	3561	3563	3565	3587	3739	3741	3743	3744	3746	3748	3750	3752
	3754	3756	3758	3759	3943	3945	3947	3948	3950	3952	3954	3956	3958	3960	3962
	3984	4165	4167	4169	4170	4172	4174	4176	4178	4180	4182	4184	4185	4395	4399
	4403	4406	4410	4414	4418	4422	4426	4430	4434	4458	4694	4697	4700	4702	4705
	4708	4711	4717	4719	4721	4743									
MAKECL	40#	2515	2534	3881	3899	4103	4121								
MANUAL	40#	2201	2596	2884	3496	4310	9812								
MSG	1674#	1677	1720#	1723	1743#	1746	1889#	1892	2050#	2052	2082#	2085	2123#	2126	2164#
	2166	2376#	2379	2551#	2554	2853#	2856	3027#	3030	3245#	3247	3459#	3462	3699#	3703
	3918#	3922	4139#	4143	4287#	4289	4645#	4647	4990#	4992	5057#	5059	5413#	5416	5570#
	5573	5933#	5935	6272#	6274	6635#	6637	7041#	7044	7450#	7453	7865#	7868	8511#	8514
	8772#	8774	9267#	9270	9514#	9517	9783#	9786	10215#	10217					
MULT	167#														
NEWTST	167#	1675	1721	1744	1890	2051	2083	2124	2164	2377	2516	2535	2552	2854	3028
	3245	3460	3701	3882	3901	3919	4104	4123	4140	4287	4645	4990	5057	5414	5571
	5933	6272	6635	7042	7451	7866	8512	8772	9268	9515	9784	10215			
OFFST	40#	5011													
POP	167#	3153	3306	3583	3980	4452	4739	8904	9384	9645	10359	10386	10414	10439	10561
	10673	10739	10764	10795	10850	10948	11029	11230	11584	12017	12018				
PUSH	167#	3137	3290	3567	3964	4436	4723	8887	9367	9628	10323	10375	10404	10432	10551
	10514	10696	10751	10781	10818	10938	11001	11189	11558	11998	12004				
REPORT	167#														
RFORGC	40#														
RHCLEA	40#	1908	2095	2134	2194	2241	2290	2396	2586	2753	2900	2951	3039	3109	3255
	3491	3532	3725	3932	4153	4326	4354	4668	4681	4974	5001	5033	5086	5267	5428
	5602	5771	5964	6122	6303	6472	6670	6873	7075	7283	7488	7703	7887	7934	8106
	8302	8527	8670	8800	8840	9026	9144	9291	9331	9540	9592	9828	9947	10073	
RH70CK	40#	6675	9545												
SAVE	40#	11611													
SAVTST	40#	1683	1730	1759	2060	2093	2132	2521	2540	3887	3906	4109	4128		
SCH	40#														
SCOPE	62#	1681	1727	1757	1903	2059	2091	2130	2191	2386	2519	2538	2570	2963	3036
	3252	3488	3722	3885	3904	3929	4107	4126	4150	4308	4664	4996	5032	5093	5425
	5599	5961	6300	6666	7072	7485	7884	8524	8797	9288	9537	9810	10226	10280	
SEEKCO	40#	8308	8560	8812	9304	9564									
SETPRI	167#	11490													
SETTRA	11970#	11979	11980	11981	11982	11984	11986	11987	11988	11989	11990				
SETUP	167#	1525													
SKIP	40#	167#	1659	1671	1735	1885	2063	2114	2118	2153	4987	5053			
SLASH	167#														
SPACE	167#														
SREGIS	40#	2306	2411	2618	3056	3171	3597	3769	3993	4193	4467	4750	4870	5152	5301
	5463	5657	5822	6009	6163	6358	6523	6736	6926	7168	7335	7592	7753	8000	8164
	8313	8565	8957	9052	9170	9418	9682	9893	9977	10158					
STARS	32	38	167#	180	243	247	302	1089	1091	1131	1133	1374	1379	1626	1648
	1675	1680	1721	1726	1744	1756	1835	1845	1890	1902	1953	1972	1999	2038	2051
	2058	2083	2090	2124	2129	2164	2190	2377	2385	2502	2512	2516	2518	2535	2537
	2552	2569	2573	2582	2854	2862	2866	2875	3028	3035	3245	3251	3460	3487	3701
	3721	3871	3877	3882	3884	3901	3903	3920	3928	4094	4100	4104	4106	4123	4125
	4141	4149	4287	4307	4645	4663	4857	4990	4995	5057	5082	5414	5424	5571	5598





CONT. TST-PT 1  
TABLE

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7943	7953	7962	7972	7981	8000	8007	8068	8071
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8392	8387	8396	8419	8427	8434	8439	8447	8464
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9238	9241	9249	9292	9296	9305	9332	9339	9344
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1733	1736	1738	1758	1759	1769	1770	1772	1773
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1946	1949	1955	1961	1970	1981	1987	1993	2045
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	8558	8596	8597	8749	8750	8839	8840	8951	8992	9086	9087	9207	9208	9329	9330
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.REPT	14	22	174	284	290										
.SBTTL	40	57	168	178	189	208	245	303	1322	1505	1506	1507	1510	1525	1512

	1675	1721	1744	1890	2051	2083	2124	2164	2377	2516	2535	2552	2854	3019	3020
	3021	3028	3245	3460	3701	3882	3901	3920	4104	4123	4141	4287	4645	4990	5056
	5057	5414	5571	5933	6272	6635	7042	7451	7454	7866	8512	8770	8772	9268	9515
	9794	10215	10266	10267	10268	10271	10312	11114	11177	11244	11314	11542	11595	11646	11877
.TITLE	11954	11970	11992												
	2														
.WORD	174	175	176	186	253	256	257	258	259	262	263	264	265	266	267
	268	271	272	273	282	284	285	286	287	288	289	290	291	292	293
	294	295	1429	1473	1474	1476	1480	1481	1502	1503	1504	10286	10289	10304	10857
	11311	11318	11319	11320	11591	11594	11846	11951	11953	12027	12029	13571	13574	13577	13579
	13581	13584	13587	13590	13593	13597	13600	13602	13604	13606	13610				

ERRORS DETECTED: 0

\*DZRJIA,DZRJIA/SOL,CRF+DZRJIA  
RUN-TIME: 151 154 19 SECONDS  
CORE USED: 29K

