

DZ11

ASYNCHRONOUS MULTIPLEXER TESTS
MD-11-DZDZA-D

EP-DZDZA-D-DL-C

APR 1977

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FICHE 1 OF 1

MADE IN USA

This microfiche card contains 100 frames of test data for asynchronous multiplexers. The frames are arranged in a 10x10 grid. Each frame contains a small diagram or waveform, likely representing the timing or logic of the multiplexer tests. The text in the frames is too small to read but appears to be technical specifications or test results.

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZDZA-D-D
PRODUCT NAME: DZ11 8 LINE ASYNC MUX TESTS
DATE RELEASED: MARCH 1977
MAINTAINER: DIAGNOSTIC ENGINEERING

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1. ABSTRACT

The function of the DZ11 diagnostics is to verify the option operates according to specifications. The diagnostics also verify that the DZ11 operates in its environment such as the system in which it is installed.

Parameters may be supplied to the program by either 'AUTO SIZING' or input from the user on the console by having SW00=1 at start time. Auto sizing will be done only the first time the program is started and SW07=0 and SW00=0 and SW03=0. The AUTOSIZER is designed to detect DZ11 device addresses and vectors and to determine whether the DZ11 that is detected is an EIA or 20mA board. All remaining parameters default to certain values (see SEC.8.5). Console input may be controlled at any start time through the use of SW00, SW03, SW04, and SW06 (see SEC. 4.1.1 for a detailed description of these switches).

Currently there is one standalone diagnostic (DZDZA), one system module for DEC X/11 (DZAA), and an online overlay for DZITA (ITEP) - DZDZB. (ITEP) - DZDZB.

DZDZA will test all parts of the DZ11 such as cables, dist pnl., and the interface module itself.

2. REQUIREMENTS

2.1 EQUIPMENT

Any PDP11 family CPU (WITH MINIMUM BK MEMORY)
 ASR 33 (or equivalent for console)
 DZ11 INTERFACE MODULE (M7819(EIA), M7814(20MA))
 H3271 Staggered turnaround connector for EIA module.
 H3190 Staggered turnaround connector for 20mA module.
 H325 Cable turnaround and dist pnl testing for EIA module.
 H315 This may be substituted for H325.

NOTE: A staggered turnaround connector is needed in order to test the PARITY and BREAK logic.

2.2 STORAGE

Program will use all 8K of memory except where ABL and BOOTSTRAP LOADER reside. Location 1500 thru 2000 are especially to be noted and to be untouched by operator after parameters have been input from console (SW00=1); or after the 'AUTO SIZING' has been done. These locations may be changed if the user understands their meaning and different parameters are required.

3. LOADING PROCEEDURE

3.1 METHOD

All programs are in absolute format and are loaded using the ABSOLUTE LOADER. NOTE: if the diagnostics are on a media such as DISK, MAGTAPE, DECTAPE, or CASSETTE; follow instructions for the monitor which has been provided on that specific media.

ABSOLUTE LOADER starting address #500

MEMORY * SIZE

4k	17
8k	37
12k	57
16k	77
20k	117
24k	137
28k	157

- 3.1.1 Place address of ABS loader into switch register.
(also place 'HALT' SW up)
- 3.1.2 Depress 'LOAD ADDRESS' key on console and release.
- 3.1.3 Depress 'START KEY' on console and release (program should be loading into CPU)

4. STARTING PROCEEDURE

- A. Set switch register to 000200
 B. Depress 'LOAD ADDRESS' key and release
 C. Set SWR to zero for 'AUTO SIZING' or set SW00=1 for user parameter input from console terminal. On first start if SW07=1 and SW00=0 the program will default to console parameter input (SW00=1).
 D. Depress 'START KEY' and release, the program will type Maindec Name and program name (if this was the first start up of the program or parameters were changed by SW00=1) and also the following:

```
'MAP OF DZ11 STATUS'
1500 160100
1502 000300
1504 000005
1506 000377
1510 017470
1512 000000
```

The above is only an example! This would indicate the status table starting at add. 1500 in the program. THE STATUS TABLE MUST BE VERIFIED BY THE USER IF AUTO SIZING IS DONE. For information of status table see section 8.4 for help.

The program will type "Running" and proceed to run the diagnostic.

4.1 CONTROL SWITCH SETTINGS

NOTE: If there is no real SWR (177570); SWR may be modified at Loc:176 or by hitting Control "G" (↑G) on console terminal.

```
SW 15 Set: Halt on error
SW 14 Set: Loop on current test
SW 13 Set: Inhibit error print out
SW 12 Set: Inhibit **ALL** type out/bell on error.
SW 11 Set: Inhibit iterations. (quick pass)
SW 10 Set: Escape to next test
SW 09 Set: Loop with current data
SW 08 Set: Catch error and loop on it
SW 07 Set: NO AUTO SIZE. If 1st start of program after loading the
operator must input address and vector from console.
SW 06 Set: Reselect DZ11's desired active
SW 05 Set: Reserved
SW 04 Set: Select delay parameter (see SEC. 4.1.1)
SW 03 Set: Extra parameter input (see SEC. 4.1.1)
SW 02 Set: Lock on selected test
**SW 01 Set: Restart program at selected test
*SW 00 Set: Get users parameters from console
```

* For Echo or Cable tests (program started at loc. 210) this switch set to 1 allows the user to type in the Vector and the CSR for the DZ11 under test.

** For Echo or Cable test this switch set to 1 allows the selection of either the Echo or Cable test, baud rate, and the line number under test.

4.1.1 SWITCH REGISTER CONTROL OF PARAMETER INPUT FROM CONSOLE

SW 00 GET USERS PARAMETERS FROM CONSOLE. Setting this switch at start up time allows the user to input at the Console terminal the following parameters: base device address, base vector address, bus request level, declare EIA or 20mA module, mode of operation (External, Internal, or Staggered), and the number of DZ11's that are running. Using this switch alone defaults the following parameters: all 8 lines are set to be tested on each DZ11, the default baud rate is set at 19.2 kbaud, and the character length for the majority of testing is set at eight bits per character with two stop bits.

SW 03 EXTRA PARAMETER INPUT. Setting this switch at start up time provides the user with the ability to set the lines active for testing and to set the default baud rate used for the majority of the diagnostic tests. The Delay Parameter is automatically adjusted to the baud rate given by the user.

SW 04 SELECT DELAY PARAMETER. The DELAY parameter this switch controls determines the length of time the program stalls waiting for a character to be completely transmitted or received. This delay count is automatically set to provide enough delay time for the default baud rate specified when running the program on an 11/45 with bipolar memory. When running this program on a faster processor the delay parameter should be adjusted proportionally higher than the following defaulted values:

2450	;time for	50	baud
1560	;time for	75	baud
1120	;time for	110	baud
0750	;time for	134	baud
0660	;time for	150	baud
0330	;time for	300	baud
0150	;time for	600	baud
0060	;time for	1200	baud
0040	;time for	1800	baud
0030	;time for	2000	baud
0020	;time for	2400	baud
0010	;time for	3600	baud
0001	;time for	4800	baud
0001	;time for	7200	baud
0001	;time for	9600	baud
0001	;time for	19.2	kbaud

4.1.2 SWITCH REGISTER RESTRICTIONS

SW 06 RESELECT DZ11'S DESIRED ACTIVE. Please note that a message is typed out for setting the switch register equal to DZ11's active. This means if the system has four DZ11s; bits 00,01,02,03 will be set in loc 'DZACTV' from the switch register. Using this switch(SW06) alters that location; therefore if four DZ11s are in the system ***DO NOT*** set switches greater than SW 03 in the up position. This would be a fatal error. do not select more active DZ11s than has been given information about in parameter input (SW00=1)

METHOD: A: Load address 200
 B: Start with SW 06=1
 C: Program will type message
 D: Set the BINARY number of DZ11s desired active EXAMPLE: 1=1
 DZ11; 3=2 DZ11; 7=3 DZ11; 17=4 DZ11 37=5 DZ11 etc/aa PRESS
 CONTINUE.
 E: Number (IF VALID) will be in data lights (excluding 11/05)
 F: Set with any other switch settings desired. PRESS CONTINUE.

SW 01 RESTART PROGRAM AT SELECTED TEST it is strongly suggested that at least one pass has been made before trying to select a test that is not in the order of sequence the reason being is that the program has to clear areas and set up parameters. Note: if running multiple DZ11's; the DZ11 you desire to be under test must be selected by the use of SW06 before locking on the test. In other words; each time the program is started; the first DZ11 will be selected to be under test unless SW06 is used to select only one.

SW 09 LOOP ON CURRENT DATA: this switch will only work if call 'SCOPI' is in that test. The reason being that most tests deal with blocks of different data to be sent or received all at once thus in block data, one pattern can't be singled out. This switch is designed to provide an aid for a trained troubleshooter to sample various signals on the module and is not meant to be used as a general user control switch.

SW 04 SELECT DELAY PARAMETER: THIS SWITCH SHOULD BE USED WITH CARE AS TOO SHORT A DELAY WILL CAUSE VALID TESTS TO FAIL ON CERTAIN PROCESSORS. IT IS RECOMMENDED THAT THIS SWITCH ONLY BE USED IN CONJUNCTION WITH SCOPE LOOPS. E.G. SW 14,9,4,1 SET; SW 9,4,2,1 SET. THE SHORTEST PARAMETER IS 1; THE LONGEST ACCEPTED IS 177776. (see SEC. 4.1.1)

4.1.3 SWITCH REGISTER PRIORITIES

ERROR SWITCHES

1. SW 12 Delete print out/bell on error.
2. SW 13 Delete error printout.
3. SW 15 Halt on the error.
4. SW 08 Goto beginning of the test(on error).
5. SW 10 Goto next test(on error).

SCOPE SWITCHES

1. SW 09 (if enabled by 'SCOPI'). If an '*' is printed in front of the test no. on an error report (ex. *TEST NO. 10) SW09 is incorporated in that test and therefore SW09 is *usually* the best switch for the scope loop (SW14=0, SW10=0, SW09=1, SW08=0) if the program user is technically trained to electronically isolate signal problems on the DZ11 module. If SW09 is not enabled; and there is a *HARD* error (constant); SW08 is best.
2. For intermittent errors either start the program with SWC1 and SW02 set which will allow the user to lock on a selected test, or else set SW14 as an error is being typed out on the terminal. SW14 will continue to loop on that test regardless of whether an error occurs.
3. SW 14 Loop on current test.

4.2 STARTING ADDRESS

SA 200 - Address 200 is for normal execution of the diagnostic. This will do the major testing necessary for verification of hardware.

SA 210 - CABLE/ECHO - Terminal Tests. Starting at address 210 will give the user the option to verify the EIA cables at the dist pnt or verify a true link to any DEC supported terminal supported by the DZ11.

NOTE: If address 000042 is non-zero the program assumes it is under ACT11 or XXDP control and will act accordingly. After *ALL* available DZ11's are tested the program will return to 'XXDP' or 'ACT-11'.

5. OPERATING PROCEDURE

When program is initially started messages as described in section four will be printed and program will begin running the diagnostic.

5.1 NORMAL START OF DIAGNOSTIC

On the first start of the diagnostic at address 200; if auto sizing is not used or whenever SW00=1; the following questions are asked and must be answered.

"1ST CSR ADDRESS (160000:163700): "

You must type in the first DZ11 CSR in the system you wish testing to begin at. RANGE: 160000:163700

"1ST VECTOR ADDRESS (300:770): "

You must type in the vector of the first DZ11 in the system under test. RANGE 300:770

"BR LEVEL (4:6): "

type in the priority level of the DZ11 that the above information has been given about. RANGE 4 or 5 or 6.

"TYPE "A" FOR EIA MODULE OR "B" FOR 20MA (A:B): "

Type "A" if running a DZ11-A,B,E (EIA).
Type "B" if running a DZ11-C,D,F (20MA).
Typing a <CR> defaults to EIA MODULES.

"MAINTENANCE MODE

[EXTERNAL <H325>-EIA ONLY (E)]
[INTERNAL <DZCSR03=1> (I)]
[STAGGERED <H3271>-EIA ONLY (S)]
[STAGGERED <H3190>-20mA ONLY (S)] :

Type "E" or "I" or "S" depending on which mode you wish to run in. If running "EXTERNAL"; all selected lines must be terminated by an A325 test connector.

"# OF DZ11'S <IN OCTAL> (1:20): "

Type total number of DZ11's to be tested in the system. RANGE
is 1 thru 20 in octal.

***** IF SW03=1 THEN *****

If SW03=1 the following will be printed.

"LINES ACTIVE BY BIT <IN OCTAL> (001:377):"

Each bit represents a line and any combination of lines may be
selected (HOWEVER IN STAGGERED MODE TWO ADJACENT LINES MUST BE
SELECTED (0-1, 2-3, 4-5, 6-7))..

"DEFAULT BAUD RATE <IN OCTAL> (00:17): "

This gives the user a chance to change the default baud rate
used in APP. 90% of the test. Baud rate choices are:
"00"(50 baud), "01"(75 baud), "02"(110 baud), "03"(134 baud),
"04"(150 baud), "05"(300 baud), "06"(600 baud), "07"(1200 baud),
"10"(1800 baud), "11"(2000 baud), "12"(2400 baud), "13"(3600 baud),
"14"(4800 baud), "15"(7200 baud), "16"(9600 baud), "17"(19.2 kbaud)
Low default baud rates are not suggested since they lengthen the
time to complete a program pass dramatically.

It is important to note that all DZ11's in the system must be
CONTIGIOUS for both ADDRESS and VECTORS. Also all the EXTRA
PARAMETERS other than CSR and VECTORS are given to the EXISTING
DZ11's in the system. If not all DZ11's are same priority or if
the mode of operation is different for each DZ11; THIS MUST BE
"PATCHED" INTO THE CORRECT STATUS MAP ENTRY which is printed at
start time. An alternative is to put SW00=1 at start time;
answer questions about DZ11 under test and INDICATE ONLY 1 DZ11
in the system. IF THE STATUS MAP IS TO BE "PATCHED" IT MUST BE
DONE AFTER THE QUESTIONS ARE ANSWERED OR AFTER THE AUTO SIZE.

5.2 HOW TO RUN THE "CABLE/ECHO" TESTS.

Normal starting for the first time would be: LOAD ADDRESS 210; START WITH THE SWR EQUAL TO 003.

NOTE: SW00=1 ASKS FOR "VECTOR" AND "CSR"
SW01=1 ASKS FOR "WHICH TEST ECHO OR CABLE", "BAUD RATE", "LINE"
UNDER TEST. Program will print out:

"VECTOR ADDRESS--"

You type vector with a <CR>.

"CONTROL REGISTER ADDRESS--"

You type in DZCSR under test.

"WHICH TEST ? ECHO OR CABLE (E OR C)"

Lets do the CABLE TEST first. **THIS TEST IS ONLY TO BE DONE ON THE EIA VERSION OF THE DZ11 NOT THE 20MA VERSION". Type "C"
<CR>

"BAUD RATE- "

type either 50, 110, 135, 150, 300, 600, 1200 1800, 2000, 2400, 3600, 4800, 7200, 9600 followed by <CR>

"LINE: "

You type the line which has the H325 test connector. (Type either 0, 1, 2, 3, 4, 5, 6, 7) Program will then print:

"CABLE TEST"

and if everything is working; the following will be printed:

"PASS DONE."

"PASS DONE."

etc.

to change lines: HIT ANY PRINTING KEY ON YOUR CONSOLE TERMINAL WHILE THE PROGRAM IS RUNNING and the following will be printed:

"LINE: "

Now change the H325 test connector to another line and type the new line. Program will then print:

"CABLE TEST"

"PASS DONE."

"PASS DONE."

Continue this operation until all lines are tested.

5.3 ECHO TEST

If program has already been started at 210 and the vector and address have been typed in; just load address 210 and start with SWR equal to 002. program will print:

"WHICH TEST ? ECHO OR CABLE (E OR C)"

Now type an "E" to do the ECHO TEST. program will print:

"BAUD RATE--"

Type BAUD RATE at which the terminal is set that is connected to the DZ11 dist pnl. Baud rate choices are: 50, 75, 110, 135, 150, 300, 600, 1200, 1800, 2000, 2400, 3600, 4800, 7200, 9600. The program will then print:

LINE: "

Type the line the terminal is connected to at the dist pnl then the program will print:

"TERMINAL ECHO TEST"

*** AT THIS POINT THE MESSAGE:

"THE QUICK BROWN FOX JUMPED OVER THE LAZY DOGS BACK 0123456789"

SHOULD BE PRINTED ON THE TERMINAL CONNECTED TO THE DZ11. IF THIS MESSAGE IS DESIRED TO BE CONTINUOUSLY OUTPUT; SET THE SWR TO 377 (SWR=377) WHILE IT IS BEING OUTPUT OR WHEN THE LINE NO. IS REQUESTED ABOVE. WHEN THIS MESSAGE IS DONE AND THE SWR IS NOT EQUAL TO 377; THE CONSOLE WILL PRINT:

"TYPE A CHAR. ON DZ11 TERMINAL"

any printable char hit on DZ11 terminal should be echoed back on the terminal. **IF YOU HIT CNTRL C (<C>) ON THE DZ11 TERMINAL THE PROGRAM WILL PRINT:

"PASS DONE."

on the CONSOLE terminal and the "QUICK BROWN FOX" will be printed on DZ11 terminal again and the echo test will be running. TO CHANGE LINES: type any printable character on the CONSOLE TERMINAL (not the DZ11 terminal). The program will again type "LINE: " and wait for a response.

5.4 PROGRAM AND/OR OPERATOR ACTION

The variety of program Control Switches provided in this Diagnostic Package is designed to provide the user with a wide range of troubleshooting techniques. Before the user attempts to run this diagnostic he should become familiar with the use of these Control Switches and their restrictions. (See Sec. 4.1, 4.1.1, 4.1.2, 4.1.3)

When the program detects an error the TEST NUMBER and PC will be typed out and possibly an error message (depending on the particular error). If it is necessary to know more information concerning the error report then look in the program listing for that TEST NUMBER and then note the PC of the error report. The reason for the error report will become clearer when reading the comments in the program listing.

6. ERRORS

As described previously there will always be a TEST NUMBER and PC typed out at the time of an error (providing SW 13=0 and SW 12=0). In most cases additional information will be supplied to the error message which is to give the operator an indication of the error.

6.2 ERROR RECOVERY

If for some reason the DZ11 should 'HANG THE BUS' (gain control of bus so that console manual functions are inhibited) an init or power down/up is necessary for operator to regain control of cpu. If this should happen; look in location 'TSTNO' (address 1216) for the number of the test that was running at the time of the catastrophic error. In this way the operator will have an idea as to what the DZ11 was doing at the time of the error.

7. RESTRICTIONS

7.1 STARTING RESTRICTIONS

See section 4.1.2
Status table should be verified regardless of how program was started. Also it is important to use this listing along with the information printed on the TTY to completely isolate problems.

7.2 OPERATING RESTRICTIONS

Parameter must be input from user OR APT if "AUTO SIZING" is not used.

8. MISCELLANEOUS

8.1 EXECUTION TIME

All DZ11 device diagnostics will give an 'END PASS' message (providing no errors and sw12=0) within 2 min. This is assuming SW11=1 (INHIBIT ITERATIONS) is set to give the fastest possible execution. The actual execution time depends greatly on the PDP11 CPU configuration. An 11/40 with Core memory will take around 100 seconds to execute a pass with no iterations and about 400 seconds to execute a fully iterated pass. Any other PDP11 CPU type will execute a pass in time proportional to the execution speed of the CPU's memory in relation to that of an 11/40.

8.2 PASS COMPLETE

NOTE: *EVERY* time the program is started; the tests will run as if SW11 (delete iterations) was up (=1). This is to 'VERIFY NO *HARD* ERRORS' as soon as possible. Therefore the first pass -EACH TIME PROGRAM IS STARTED- will be a 'QUICK PASS' until all DZ11's in system are tested. When the diagnostic has completed a pass the following is an example of the print out to be expected.

END PASS DZDZA-D CSR: 160010 VEC: 300 PASSES: 000001 ERRORS: 000000

NOTE: The numbers for CSR and VEC are not necessarily the values for the device. They are only for this example.

B.4 KEY LOCATIONS

SLPADR (1126) Contains the address where program will return when iteration count is reached or if loop on test is asserted.

NEXT (1360) Contains the address of the next test to be performed.

STSTNM (1122) Contains the number of the test now being performed.

RUN (1406) The bit in 'RUN' always points one past the DZ11 currently being tested. EXAMPLE: (RUN) 1304/000000000100000 Means that DZ11 no.05 is the DZ11 now running.

STATUS MAP (1500)-(2000) These locations contain the information needed to test up to 16 (decimal) DZ11s sequentially. they contain the CSR, VECTOR and STATUS concerning the configuration of each DZ11.

DZACTV (1404) Each bit set in this location indicates that the associated DZ11 will be tested in turn. EXAMPLE: (DZACTV) 1300/000000000011111 means that DZ11 no. 00,01,02,03,04 will be tested. EXAMPLE: (DZACTV) 1300/000000000010001 Means that DZ11 no. 00,04 will be tested.

\$BASE (1310) Contains the receiver CSR of the current DZ11 under test.

8.4A MORE ON THAT 'STATUS TABLE' (1500-2000)

```
'MAP OF DZ11 STATUS'
1500 160100
1502 000300
1504 000005
1506 000377
1510 017470
1512 000000
```

The above information will be repeated for each of up to 16 DZ11's in the system (these will follow under this table). EXPLANATION:

```
1500 160100 This is the system control register for the 1st DZ11 in
the system.
1502 000300 This is vector 'A' for the first DZ11 in the system.
1504 000005 This represents the bus interrupt priority level of the
DZ11. BIT15 of this location indicates either EIA or
20MA. If BIT15=0 module should be an M7819, if bit15=1
module should be an M7814.
1506 000377 This is the binary representation of what lines are to
be tested.
1510 017470 This is the parameter location used in most of the
tests. It indicates parameters of: RX ON, SPEED SELECT
17 (19.2K BAUD) EIGHT BITS PER CHAR, AND TWO STOP BITS.
The user may alter the stop bits and the speed, but the
remaining parameters should be left alone.
This location is used to load the DZ11 Line Parameter
Register for each line. The meaning of the bits set in
this location is the same as the function of the related
bits in the device Line Parameter Register.
1512 000000 This location will contain either all zeros indicating
that internal loop was selected as mode of operation or
it will contain 10000 indicating that "staggered mode"
was selected or it will contain 000200 indicating that
"external" was the mode selected.
```

The above is repeated for each DZ11 in the system. The table is filled by AUTO SIZING or by the manual parameter input program as described previously. Also if desired by user; the locations may be altered by hand (toggled in) to suit the specific configuration.

B.5 *** METHOD OF AUTO SIZING ***

B.5.1 FINDING THE CONTROL STATUS REGISTER.

The program will start at address 160000 and start 'REFERENCING' the address in the pointer. If a NON-EX MEMORY TRAP occurs, the pointer (holding 160000) is updated by 10 and the above is repeated until address 163700 is reached. If a 'SLAVE SYNC RESPONSE' was issued by the DZ11 (or any other device) (no nzm trap), "MASTER SCAN ENABLE" is attempted to be set and the "TCR" bit for line 7 is set. "TRDY" is then tested to be set and both "TCR07" AND "MASTER SCAN ENABLE" are tested to be still set. If all of this worked; then a "DEVICE CLEAR" is issued testing that the bit can be read back and that after some time it self clears. If all of the above worked; this device is assumed to be a DZ11. If any of the above failed; updating of the pointer is done and the sequence is repeated.

NOTE: If the program does not find your DZ11; something is wrong and AUTO SIZING should not be done.

After identifying a DZ11 the program then attempts to set all DTR bits in Device Register 4. If any DTR bits did set the module is assumed to be an EIA module (M7819) otherwise the status map entry is set for 20mA (M7814).

B.5.2 FINDING THE VECTOR

The vector area (address 300-776) is filled with the instruction IOT and '+2' (next address). Bit14 and Bit15 (TX INTERRUPT ENABLE AND MSTSCAN ENABLE) are set into the DZCSR. "TCR07" is then set. a delay is made and if no interrupt occurs (because of a bad DZ11) the program assumes vector address 300 and the problem should be fixed in the diagnostic. Once the problem is fixed; the program should be re-setup again to get correct vector. If an interrupt occurred; the address to which the DZ11 interrupted to is picked up and reported as the vector. NOTE: if the vector reported is not the vector set up by you; there is a problem and AUTO SIZING should not be done.

B.5.3 PARAMETER ASSUMPTIONS.

Since too much hardware would need to be turned on to SIZE the rest of the parameters; the program must assume the remaining variations. The result if not to your specific configuration may be altered by hand (toggle in) if desired. In this way 95% of the parameter setup was done by the program and 5% by you.

THEREFORE:

- 1) BUS PRIORITY IS SET TO LEVEL5.
- 2) ALL EIGHT LINES ARE ASSUMED TO BE TESTED.
- 3) DEFAULT BAUD RATE IS SET TO 17 (19.2 K).
- 4) MODE OF OPERATION IS "INTERNAL MODE".

For all parameter adjustments please refer to section 8.4a for greater detail.

9.0 RUNNING THE DZ11 DIAGNOSTIC UNDER APT

9.1.1 THE APT INTERFACE

DZDZA has been redesigned to be compatible with the APT-Automated Product Test system. It can be run as a standalone diagnostic or in either of the APT modes. Certain variables in the original APT module were reassigned to the areas set aside for APT interfacing. These new variables generally begin with a dollar sign (\$), e.g., \$DEVN, \$BASE.

9.1.2 SETTING UP THE DIAGNOSTIC USING APT

The diagnostic uses several variables in the region subtitled 'APT Mailbox-Etable'. These variables are:

\$SWREG - used if a software switch register is desired while under apt

\$VECT1 - used to specify the interrupt level and the first vector address

\$BASE - used to indicate bottom address of DZ11 under test

\$DEVN - a bit map representing which DZ11's will be tested

\$CDW1 - used to indicate which lines to run on all DZ11's

\$DDW0 - each of the \$DDW words describes the parameters (LPR) for a particular DZ11, going up to 16 DZ11's

9.1.3 RUNNING UNDER APT

The user should be familiar with the APT system. The APT timing parameters for the DZ11 diagnostic were based on an 11/40 processor. It may be necessary to add a few more seconds if the diagnostic is out on an 11/05 processor.

All of the variables mentioned in section 9.1.2 should be set up prior to running the diagnostic under APT.

NOTE

Be sure \$BASE points to the first DZ11 before running

Based on these values, the diagnostic will set up the status table. The user is then free to monitor under APT as normal.

DZDZAD LST

F02

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DOCUMENT

CZDZAD LST

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THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.

22 INITIAL ADDRESS OF THE STACK POINTER *** 1120 ***

27 MISCELLANEOUS DEFINITIONS

39 GENERAL PURPOSE REGISTER DEFINITIONS

51 PRIORITY LEVEL DEFINITIONS

61 "SWITCH REGISTER" SWITCH DEFINITIONS

89 DATA BIT DEFINITIONS (BIT00 TO BIT15)

117 BASIC "CPU" TRAP VECTOR ADDRESSES

353 THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
USED IN THE PROGRAM.

423 BITS 15-11=CPU TYPE
11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05
11/70=06, PDQ=07, Q=10
BIT 10=REAL TIME CLOCK
BIT 9=FLOATING POINT PROCESSOR
BIT 8=MEMORY MANAGEMENT

431 MEM.TYPE BYTE -- (HIGH BYTE)
900 NSEC CORE=001
300 NSEC BIPOLAR=002
500 NSEC MOS=003

436 MEM.LAST ADDR.=3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE

474 THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

480 EM ::POINTS TO THE ERROR MESSAGE
DH ::POINTS TO THE DATA HEADER
DT ::POINTS TO THE DATA
DF ::POINTS TO THE DATA FORMAT

DZDZAD LST

1098 INCREMENT THE PASS NUMBER (\$PASS)
IF THERES A MONITOR GO TO IT
IF THERE ISN'T JUMP TO CYCLE

1149 THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
AND LOAD THE TEST NUMBER(\$TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
AND LOAD THE ERROR FLAG (\$ERFLG) INTO DISPLAY<15:08>
THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
SW14=1 LOOP ON TEST
SW11=1 INHIBIT ITERATIONS
CALL SCOPE ;;SCOPE=IOT

1225 ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
NOTE1: \$NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
NOTE2: \$FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
NOTE3: \$FILLC CONTAINS THE CHARACTER TO FILL AFTER.
CALL:
1) USING A TRAP INSTRUCTION
TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
OR
TYPE
MESADR

1930 ROUTINE USED TO "AUTO SIZE" THE DZ11
CSR AND VECTOR.
NOTE: THE CSR MAY BE ANY WHERE IN THE FLOATING
ADDRESS RANGE (160000:163700)
AND THE VECTOR MAY BE ANY WHERE IN THE
FLOATING VECTOR RANGE (300:770)

2052 ***** TEST 1 *****
THIS TEST PROVES THE SLAVE SYNC RESPONSE
DURING A READ OR WRITE TO THE FOLLOWING ADDRESS:
DZCSR, DZRBUF, DZTCR, DZMSR

2095 ***** TEST 2 *****
THIS TEST PROVES THAT BIT "DCLR"
CAN BE SET AND THAT IT WILL CLEAR
BY ITSELF AFTER A PERIOD OF TIME.

2125 ***** TEST 3 *****
TEST TO VERIFY THAT BIT "MAINT" CAN
BE SET. THEN VERIFY THAT BIT "MAINT" CAN
BE CLEARED (WRITTEN TO A ZERO). AND FINALLY
VERIFY THAT AFTER BEING SET AGAIN IT CAN BE
CLEARED BY A "DEVICE CLEAR"

- 2157 ***** TEST 4 *****
 TEST TO VERIFY THAT BIT "MSENAB" CAN
 BE SET. THEN VERIFY THAT BIT "MSENAB" CAN
 BE CLEARED (WRITTEN TO A ZERO). AND FINALLY
 VERIFY THAT AFTER BEING SET AGAIN IT CAN BE
 CLEARED BY A "DEVICE CLEAR"
- 2189 ***** TEST 5 *****
 TEST TO VERIFY THAT BIT "SILOEN" CAN
 BE SET. THEN VERIFY THAT BIT "SILOEN" CAN
 BE CLEARED (WRITTEN TO A ZERO). AND FINALLY
 VERIFY THAT AFTER BEING SET AGAIN IT CAN BE
 CLEARED BY A "DEVICE CLEAR"
- 2221 ***** TEST 6 *****
 TEST TO VERIFY THAT BIT "RIE" CAN
 BE SET. THEN VERIFY THAT BIT "RIE" CAN
 BE CLEARED (WRITTEN TO A ZERO). AND FINALLY
 VERIFY THAT AFTER BEING SET AGAIN IT CAN BE
 CLEARED BY A "DEVICE CLEAR"
- 2253 ***** TEST 7 *****
 TEST TO VERIFY THAT BIT "TIE" CAN
 BE SET. THEN VERIFY THAT BIT "TIE" CAN
 BE CLEARED (WRITTEN TO A ZERO). AND FINALLY
 VERIFY THAT AFTER BEING SET AGAIN IT CAN BE
 CLEARED BY A "DEVICE CLEAR"
- 2285 ***** TEST 10 *****
 THIS TESTS THAT ALL OF THE FOLLOWING
 BITS CAN BE: SET, CLEARED, CLEARED BY "DEVICE CLEAR "
 BITS TESTED ARE:
 TCR0, TCR1, TCR2, TCR3, TCR4, TCR5, TCR6, TCR7
- 2327 ***** TEST 11 *****
 THIS TESTS THAT ALL OF THE FOLLOWING
 BITS CAN BE: SET, CLEARED, CLEARED BY "RESET INSTR *NOT* DEVICE CLEAR "
 BITS TESTED ARE:
- 2331 DTR0, DTR1, DTR2, DTR3, DTR4, DTR5, DTR6, DTR7
 THIS TEST IS NOT DONE IF MODULE IS 20MA VERSION
- 2380 ***** TEST 12 *****
 THIS TEST PERFORMS RESET TESTING &
 TESTING OF WRITE ONLY OR READ ONLY BIT
 TEST BITS "RDONE, BIT11, BIT10, BIT9, BIT8, BIT2, BIT1
 BIT0, SILOAL" ARE READ ONLY AND THAT TROY IS
 ZERO UNTIL A LINE IS SELECTED AND MSENAB IS SET.

- 2417 ***** TEST 13 *****
THIS TEST PERFORMS RESET TESTING AND
TESTING OF READ ONLY AND WRITE ONLY BITS
IN REGISTER DZCSR
VERIFY THAT "TIE", "SILOEN", "RIE", "MSENAB", "MAINT"
ARE THE ONLY R/W BITS IN THE DZCSR.
THEN SET "DCLR" AND VERIFY THEY ARE CLEARED
- 2446 ***** TEST 14 *****
THIS TEST PERFORMS RESET TESTING AND
TESTING OF READ ONLY REGISTER DZRBUF
AND TESTING OF WRITE ONLY REGISTER DZLPR
- 2472 ***** TEST 15 *****
THIS TEST PERFORMS RESET TESTING AND
TESTING OF READ ONLY REGISTER DZMSR
AND TESTING OF WRITE ONLY REGISTER DZTDR
- 2498 ***** TEST 16 *****
- 2499 VERIFY THAT IF WE ARE IN "STAGGERED" MODE
THAT SETTING "DTR" FOR A LINE WILL
BRING UP "RING" AND "CARRIER" FOR THE
ASSOCIATED LINE IN WHICH WE ARE STAGGERED!
LINE0 DTR= LINE1 RING AND CARRIER
LINE1 DTR= LINE0 RING AND CARRIER
LINE2 DTR= LINE3 RING AND CARRIER
LINE3 DTR= LINE 4 RING AND CARRIER
ETC...
- 2557 ***** TEST 17 *****
TEST TO VERIFY THAT IF IN "EXTERNAL"
MODE; SETTING DTR FOR SELECTED LINES
WILL BRING UP "CARRIER" AND "RING"
FOR THAT SAME LINE. NOTE: IF YOU HAVE
SELECTED MODE AS "EXTERNAL"; THE H325 TEST CONNECTER
MUST BE USED ON ALL SPECIFIED LINES.
LINES MAY BE SPECIFIED BY SWR03=1
AND SWR00=1 AT START TIME OR ALTERING
STATUS MAP.
- 2604 ***** TEST 20 *****
THIS TEST VERIFIES THAT TRDY IS SET WHEN A LINE
IS READY TO BE LOADED, AND THAT THE LINE SPECI-
FIED IN BITS 8-10 OF DZCSR CORRESPOND
TO THE LINE SELECTED IN DZTCR
- 2640 ***** TEST 21 *****
TEST TO TRANSMIT ONE CHAR AND
RECEIVE ONE CHAR ON ONE LINE
AT A TIME. THE CHAR IS "252" AND
ALL SELECTED LINES WILL BE TURNED ON
ONE AT A TIME. THIS IS THE FIRST TIME ANY
DATA IS CHECKED IN THE RECEIVER.

USING SWITCH NINE WITH THIS TEST CREATES A TIGHT SCOPE LOOP WHICH TRANSMITS A STEADY STREAM OF CHARACTERS.

- 2724 ***** TEST 22 *****
THIS TEST PROVES THAT THE TRANSMITTER TRANSMITS CHARACTERS (FLAG MODE) AND THE RECEIVER RECEIVES (FLAG MODE) (ONE LINE AT A TIME - BASED UPON VALID LINES)
THIS IS THE FIRST TIME THAT ALL DATA IS CHECKED
- 2805 ***** TEST 23 *****
THIS TEST WILL PROVE THAT:
1) THE TRANSMITTER "BREAK BIT" WORKS
2) THE RECEIVER CAN FLAG "FRAMING ERRORS"
3) THE RECEIVER CAN FLAG "PARITY ERRORS"
ONLY ONE LINE AT A TIME WILL BE EXERCISED.
THIS TEST WILL NOT BE EXERCISED UNLESS CONNECTED BY AN H325, H3271, or H3190 CONNECTOR
- 2872 ***** TEST 24 *****
THIS TEST VERIFIES THAT THE DEVICE DOES NOT INTERRUPT WHILE THE PROCESSOR STATUS IS SET EXACTLY TO WHAT THE DZ11 PRIORITY IS SET TO.
DEFAULT PRIORITY IS AT 5 (240).
- 2941 ***** TEST 25 *****
THIS TEST VERIFIES THAT THE DEVICE DOES INTERRUPT WHILE THE PROCESSOR STATUS IS SET TO EXACTLY ONE LEVEL LOWER THAN THE DZ11. DZ11 PRIORITY DEFAULT TO LEVEL 5 MINUS ONE LEVEL IS LEVEL 4.
- 3014 ***** TEST 26 *****
THIS TEST VERIFIES THAT THE RECEIVER WILL INTERRUPT BEFORE THE TRANSMITTER EVEN THOUGH THE TRANSMITTER WAS ENABLED FIRST. SET PS TO LEVEL 7;
GET ROONE AND TROY TO SET;
SET TX IE AND RX IE;
CLEAR PS AND EXPECT RX TO INTERRUPT FIRST
- 3124 ***** TEST 27 *****
THIS TEST VERIFIES OVERRUN AND SILO ALARM ONE LINE AT A TIME - BASED UPON VALID LINES AS EACH OF THE FIRST 16 CHARS ARE SENT; SILO ALARM IS TESTED TO BE CLEARED. ON THE 16TH CHAR THE PROGRAM THEN EXPECTS SILO ALARM TO SET. THEN THE ENTIRE SILO IS FILLED AND AN OVERRUN IS EXPECTED ON THE 65TH CHAR PULLED OUT OUT THE SILO.
USING SWITCH NINE FOR THIS TEST SENDS 20. CHARACTERS ON DZ LINE PREVIOUSLY SELECTED CONTINUOUSLY WHILE SW09=1. USED TO SCOPE SILO ALARM PULSES, ETC.

- 3259 ***** TEST 30 *****
THIS TEST THAT "SILO ENABLE" WILL INHIBIT
RECEIVER INTERRUPTS AND THAT ON THE
16TH CHAR THAT "SILO ALARM" WILL CAUSE AN
INTERRUPT WITH "RIE" SET.
THIS WILL DO ALL SELECTED LINES ONE AT A TIME.
- 3344 ***** TEST 31 *****
THIS TEST RUNS ALL LINES FULL BORE
BASED UPON QUALIFIED LINES
. THIS IS AN INTERRUPT TEST ON THE RECEIVER AND
TRANSMITTER
- 3488 ***** TEST 32 *****
DZ11 RELATIVE TIMING TEST.
EACH SELECTED LINE WILL IN TURN RUN 16. CHARS
AT ALL BAUD RATES AND THEN THE HIGHEST BAUD
WITH ALL CHAR LENGTHS. EACH NEW PARAMETER SHOULD
DECREASE IN TIME FROM THE PREVIOUS PARAMETERS SELECTED.
THE TIME IS CHECKED AGAINST THE LAST PARAMETER USED
AND A LOWER TIME IS EXPECTED ON THE CURRENT PARAMETER.
PARAMETERS ARE:
EIGHT BITS/PER/CHAR - TWO STOP BITS AT
50, 75, 110, 134.5, 150, 300, 600, 1200, 1800, 2000
2400, 3600, 4800, 7200, 9600 BAUD.
19.2 K BAUD - TWO STOP BITS AT
SEVEN, SIX, FIVE BITS/PER/CHAR.
AFTER EACH LINE HAS FINISHED ALL THE ABOVE PARAMETERS
THE NEXT SELECTED LINE IS THE TESTED.
- 3587 ***** TEST 33 *****
THIS TEST VERIFIES THAT EVEN PARITY WORKS
FOR ALL ODD LINES SELECTED AND THAT ODD PARITY WORKS FOR ALL
EVEN LINES SELECTED.
THE MAIN FUNCTION OF THIS TEST IS TO VERIFY
THAT "PE" (PARITY ERROR) CAN BE FLAGGED BY
THE UARTS. THIS TEST WILL NOT BE DONE UNLESS
YOU ARE IN "STAGGERED" MODE.
40(B) CHARS ARE USED FOR THIS TEST.
ALL SELECTED LINES WILL BE ENABLED
AT THE SAME TIME!
- 3686 ***** TEST 34 *****
THIS TEST VERIFIES THAT ODD PARITY WORKS FOR ALL ODD LINES
SELECTED AND THAT EVEN PARITY WORKS FOR ALL EVEN LINES SELECTED
THE MAIN FUNCTION OF THIS TEST IS TO VERIFY
THAT "PE" (PARITY ERROR) CAN BE FLAGGED BY
THE UARTS. THIS TEST WILL NOT BE DONE UNLESS
YOU ARE IN "STAGGERED" MODE.
40(B) CHARS ARE USED FOR THIS TEST.
ALL SELECTED LINES WILL BE ENABLED
AT THE SAME TIME!

3874

STARTING PROCEDURE
 LOAD PROGRAM
 LOAD ADDRESS 000210
 PRESS START
 PROGRAM WILL TYPE DZ11 ECHO/CABLE TEST
 PROGRAM WILL TYPE WHICH TEST- ECHO OR CABLE
 TYPE IN E OR C RESPECTIVELY
 PROGRAM WILL TYPE "VECTOR ADDRESS-"
 TYPE IN THE ADDRESS OF THE RECEIVER INTERRUPT VECTOR
 FOR THE DZ11 TO BE TESTED, FOLLOWED BY <CARRIAGE RETURN>
 PROGRAM WILL TYPE "CONTROL REGISTER ADDRESS-"
 TYPE IN THE ADDRESS OF THE SYSTEM CONTROL REGISTER
 FOR THE DZ11 TO BE TESTED, FOLLOWED BY <CARRIAGE RETURN>
 PROGRAM WILL TYPE "LINE NUMBER-"
 TYPE IN THE LINE NUMBER TO BE TESTED (IN OCTAL)
 FOLLOWED BY <CARRIAGE RETURN>
 PROGRAM WILL TYPE "BAUD RATE-"
 TYPE IN THE BAUD RATE OF THE DZ11 TERMINAL
 FOLLOWED BY <CARRIAGE RETURN>
 THE FOLLOWING BAUD RATES ARE ACCEPTED IN DECIMAL

50
 75
 110
 135 (ROUNDED OFF 134.5)
 150
 300
 600
 1200
 1800
 2000
 2400
 3600
 4800
 7200
 9600

ALL OTHERS ARE REJECTED

3911

PROGRAM WILL TYPE "ECHO" OR "CABLE TEST" TO INDICATE THAT TESTING HAS STARTED

4097

TEST TO VERIFY THAT SETTING DTR FOR A GIVEN LINE
 WILL BRING UP "CO" AND "RING" FOR THE SAME LINE
 THE DIST PNL MUST HAVE JUMPER FROM DTR TO RQST TO SEND
 IN ORDER FOR THIS TEST TO WORK!

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000001

.TITLE MD-11-DZDZA-D
.*COPYRIGHT (C) 1977
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.*MAYNARD, MASS. 01754
.*
.*
.* THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
.* PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.

\$'N=1
; STARTING PROCEDURE
; LOAD PROGRAM
; LOAD ADDRESS 000200
; PRESS START
; PROGRAM WILL TYPE "MAINDEC-11-DZDZAD/<200>/EIGHT LINE ASYNC MUX TESTS"
; PROGRAM WILL TYPE "RUNNING" TO INDICATE THAT TESTING HAS STARTED
; AT THE END OF A PASS, PROGRAM WILL TYPE PASS COMPLETE MESSAGE
; AND THEN RESUME TESTING

.SBTTL BASIC DEFINITIONS

001120

.*INITIAL ADDRESS OF THE STACK POINTER *** 1120 ***
STACK= 1120
.EQUIV EMT,ERROR ;: BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE ;: BASIC DEFINITION OF SCOPE CALL

.*MISCELLANEOUS DEFINITIONS

000011
000012
000015
000200
177776

177774
177772
177570
177570

HT= 11 ;: CODE FOR HORIZONTAL TAB
LF= 12 ;: CODE FOR LINE FEED
CR= 15 ;: CODE FOR CARRIAGE RETURN
CRLF= 200 ;: CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776 ;: PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLMT= 177774 ;: STACK LIMIT REGISTER
PIRQ= 177772 ;: PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570 ;: HARDWARE SWITCH REGISTER
DDISP= 177570 ;: HARDWARE DISPLAY REGISTER

.*GENERAL PURPOSE REGISTER DEFINITIONS

000000
000001
000002
000003
000004
000005
000006
000007
000006
000007

R0= %0 ;: GENERAL REGISTER
R1= %1 ;: GENERAL REGISTER
R2= %2 ;: GENERAL REGISTER
R3= %3 ;: GENERAL REGISTER
R4= %4 ;: GENERAL REGISTER
R5= %5 ;: GENERAL REGISTER
R6= %6 ;: GENERAL REGISTER
R7= %7 ;: GENERAL REGISTER
SP= %6 ;: STACK POINTER
PC= %7 ;: PROGRAM COUNTER

.*PRIORITY LEVEL DEFINITIONS

000000
000040
000100
000140
000200

PR0= 0 ;: PRIORITY LEVEL 0
PR1= 40 ;: PRIORITY LEVEL 1
PR2= 100 ;: PRIORITY LEVEL 2
PR3= 140 ;: PRIORITY LEVEL 3
PR4= 200 ;: PRIORITY LEVEL 4

57	000240	PR5= 240	::PRIORITY LEVEL 5
58	000300	PR6= 300	::PRIORITY LEVEL 6
59	000340	PR7= 340	::PRIORITY LEVEL 7
60			
61		:*"SWITCH REGISTER" SWITCH DEFINITIONS	
62	100000	SW15= 100000	
63	040000	SW14= 40000	
64	020000	SW13= 20000	
65	010000	SW12= 10000	
66	004000	SW11= 4000	
67	002000	SW10= 2000	
68	001000	SW09= 1000	
69	000400	SW08= 400	
70	000200	SW07= 200	
71	000100	SW06= 100	
72	000040	SW05= 40	
73	000020	SW04= 20	
74	000010	SW03= 10	
75	000004	SW02= 4	
76	000002	SW01= 2	
77	000001	SW00= 1	
78		.EQUIV SW09,SW9	
79		.EQUIV SW08,SW8	
80		.EQUIV SW07,SW7	
81		.EQUIV SW06,SW6	
82		.EQUIV SW05,SW5	
83		.EQUIV SW04,SW4	
84		.EQUIV SW03,SW3	
85		.EQUIV SW02,SW2	
86		.EQUIV SW01,SW1	
87		.EQUIV SW00,SW0	
88			
89		:*DATA BIT DEFINITIONS (BIT0 TO BIT15)	
90	100000	BIT15= 100000	
91	040000	BIT14= 40000	
92	020000	BIT13= 20000	
93	010000	BIT12= 10000	
94	004000	BIT11= 4000	
95	002000	BIT10= 2000	
96	001000	BIT09= 1000	
97	000400	BIT08= 400	
98	000200	BIT07= 200	
99	000100	BIT06= 100	
100	000040	BIT05= 40	
101	000020	BIT04= 20	
102	000010	BIT03= 10	
103	000004	BIT02= 4	
104	000002	BIT01= 2	
105	000001	BIT00= 1	
106		.EQUIV BIT09,BIT9	
107		.EQUIV BIT08,BIT8	
108		.EQUIV BIT07,BIT7	
109		.EQUIV BIT06,BIT6	
110		.EQUIV BIT05,BIT5	
111		.EQUIV BIT04,BIT4	
112		.EQUIV BIT03,BIT3	

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113 .EQUIV BIT02,BIT2
114 .EQUIV BIT01,BIT1
115 .EQUIV BIT00,BIT0
116
117 ;*BASIC "CPU" TRAP VECTOR ADDRESSES
118 000004 ERRVEC= 4 ; TIME OUT AND OTHER ERRORS
119 000010 RESVEC= 10 ; RESERVED AND ILLEGAL INSTRUCTIONS
120 000014 TBITVEC=14 ; "T" BIT
121 000014 TRTVEC= 14 ; TRACE TRAP
122 000014 BPTVEC= 14 ; BREAKPOINT TRAP (BPT)
123 000020 IOTVEC= 20 ; INPUT/OUTPUT TRAP (IOT) **SCOPE**
124 000024 PWRVEC= 24 ; POWER FAIL
125 000030 EMTVEC= 30 ; EMULATOR TRAP (EMT) **ERROR**
126 000034 TRAPVEC=34 ; "TRAP" TRAP
127 000060 TKVEC= 60 ; TTY KEYBOARD VECTOR
128 000064 TPVEC= 64 ; TTY PRINTER VECTOR
129 000240 PIRQVEC=240 ; PROGRAM INTERRUPT REQUEST VECTOR

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; INSTRUCTION DEFINITIONS

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131 ;-----
132
133
134
135 005746 PUSH1SP=5746 ; DECREMENT PROCESSOR STACK 1 WORD
136 005726 POP1SP=5726 ; INCREMENT PROCESSOR STACK 1 WORD
137 010046 PUSHRO=10046 ; SAVE RO ON STACK
138 012600 POPRO=12600 ; RESTORE RO FROM STACK
139 024646 PUSH2SP=24646 ; DECREMENT STACK TWICE
140 022626 POP2SP=22626 ; INCREMENT STACK TWICE

```

; DZ11 CONTROL AND STATUS REGISTER DEFINITIONS
(DZCSR) BIT DEFINITIONS

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141 ;-----
142
143
144
145
146 000010 MAINT = BIT3 ; MAINTENANCE MODE ENABLE
147 000020 DCLR=BIT4 ; DEVICE CLEAR
148 000040 MSENAB=BIT5 ; MASTER SCAN ENABLE
149 000100 RIE=BIT6 ; RECEIVER INTERRUPT ENABLE
150 000200 RDOM=BIT7 ; RECEIVER DONE
151 010000 SILOEN= BIT12 ; SILO ALARM ENABLE
152 020000 SILOAL = BIT13 ; SILO ALARM
153 040000 TIE=BIT14 ; TRANSMITTER INTERRUPT ENABLE
154 100000 TRDY=BIT15 ; TRANSMITTER READY

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; DZCSR WORD DEFINITIONS

```

155 ;-----
156
157
158 000000 TLO=0 ; TRANSMIT LINE 0
159 000400 TL1=BIT8 ; TRANSMIT LINE 1
160 001000 TL2=BIT9 ; TRANSMIT LINE 2
161 001400 TL3=BIT9!BIT8 ; TRANSMIT LINE 3
162 002000 TL4=BIT10 ; TRANSMIT LINE 4
163 002400 TL5=BIT10!BIT8 ; TRANSMIT LINE 5
164 003000 TL6=BIT10!BIT9 ; TRANSMIT LINE 6
165 003400 TL7=BIT10!BIT9!BIT8 ; TRANSMIT LINE 7

```

; DZRBUF BIT DEFINITIONS

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166
167
168

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169
170
171      010000      PARER=BIT12      :PARITY ERROR
172      020000      FRMERR=BIT13     :FRAME ERROR
173      040000      OVRRUN=BIT14     :OVERRUN ERROR
174      100000      DVALID=BIT15     :DATA VALID
175
176      ;DZRBUF WORD DEFINITIONS
177      ;-----
178
179      000000      RLO=0              :RECEIVER LINE 0
180      000400      RL1=BIT8          :RECEIVER LINE 1
181      001000      RL2=BIT9          :RECEIVER LINE 2
182      001400      RL3=BIT9!BIT8     :RECEIVER LINE 3
183      002000      RL4=BIT10         :RECEIVER LINE 4
184      002400      RL5=BIT10!BIT8    :RECEIVER LINE 5
185      003000      RL6=BIT10!BIT9    :RECEIVER LINE 6
186      003400      RL7=BIT10!BIT9!BIT8 ;RECEIVER LINE 7
187
188      ;DZLPR WORD DEFINITIONS
189      ;-----
190
191      000000      LPO=0              :LINE PARAMETER 0
192      000001      LP1=BIT0           :LINE PARAMETER 1
193      000002      LP2=BIT1           :LINE PARAMETER 2
194      000003      LP3=BIT1!BIT0     :LINE PARAMETER 3
195      000004      LP4=BIT2           :LINE PARAMETER 4
196      000005      LP5=BIT2!BIT0     :LINE PARAMETER 5
197      000006      LP6=BIT2!BIT1     :LINE PARAMETER 6
198      000007      LP7=BIT2!BIT1!BIT0 ;LINE PARAMETER 7
199
200      000000      FIVE=0             :FIVE BITS/CHAR, 1 STOP BIT
201      000010      SIX=BIT3           :SIX BITS/CHAR, 1 STOP BIT
202      000020      SEVEN=BIT4        :SEVEN BITS/CHAR, 1 STOP BIT
203      000030      EIGHT=BIT4!BIT3   :EIGHT BITS/CHAR, 1 STOP BIT
204      000040      FIVES=BIT5        :FIVE BITS/CHAR, 2 STOP BITS
205      000050      SIXS=BIT5!BIT3    :SIX BITS/CHAR, 2 STOP BITS
206      000060      SEVENS=BIT5!BIT4  :SEVEN BITS/CHAR, 2 STOP BITS
207      000070      EIGHTS=BIT5!BIT4!BIT3 ;EIGHT BITS/CHAR, 2 STOP BITS
208
209      000100      PARITY=BIT6         :PARITY ENABLED
210      000200      ODDPAR=BIT7       :ODD PARITY ENABLED
211      000000      ONESTOP=0          :ONE STOP BIT ENABLED
212      000040      TWOSTOP=BIT5      :TWO STOP BITS ENABLED
213      000000      EVEPAR=0          :EVEN PARITY ENABLED
214      010000      RCVON=BIT12       :ENABLE RECEIVER (RECEIVER ON)
215
216      000000      S50=0              :SPEED 50 BAUD
217      000400      S75=BIT8           :SPEED 75 BAUD
218      001000      S110=BIT9          :SPEED 110 BAUD
219      001400      S134=BIT9!BIT8     :SPEED 134.5 BAUD
220      002000      S150=BIT10         :SPEED 150 BAUD
221      002400      S300=BIT10!BIT8    :SPEED 300 BAUD
222      003000      S600=BIT10!BIT9    :SPEED 600 BAUD
223      003400      S1200=BIT10!BIT9!BIT8 ;SPEED 1200 BAUD
224      004000      S1800=BIT11       :SPEED 1800 BAUD
    
```

225	004400	S2000=BIT11!BIT8	;SPEED 2000 BAUD
226	005000	S2400=BIT11!BIT9	;SPEED 2400 BAUD
227	005400	S3600=BIT11!BIT9!BIT8	;SPEED 3600 BAUD
228	006000	S4800=BIT11!BIT10	;SPEED 4800 BAUD
229	006400	S7200=BIT11!BIT10!BIT8	;SPEED 7200 BAUD
230	007000	S9600=BIT11!BIT10!BIT9	;SPEED 9600 BAUD
231	007400	S19200=BIT11!BIT10!BIT9!BIT8	;SPEED 19200 BAUD

;DZTCR BIT DEFINITIONS

232		-----	
233			
234			
235	000001	TCR0=BIT0	;TCR0
236	000002	TCR1=BIT1	;TCR1
237	000004	TCR2=BIT2	;TCR2
238	000010	TCR3=BIT3	;TCR3
239	000020	TCR4=BIT4	;TCR4
240	000040	TCR5=BIT5	;TCR5
241	000100	TCR6=BIT6	;TCR6
242	000200	TCR7=BIT7	;TCR7
243	000400	DTR0=BIT8	;DTR0
244	001000	DTR1=BIT9	;DTR1
245	002000	DTR2=BIT10	;DTR2
246	004000	DTR3=BIT11	;DTR3
247	010000	DTR4=BIT12	;DTR4
248	020000	DTR5=BIT13	;DTR5
249	040000	DTR6=BIT14	;DTR6
250	100000	DTR7=BIT15	;DTR7

;DZMSR BIT DEFINITIONS

251		-----	
252			
253			
254	000001	RING0=BIT0	;RING INDICATED ON LINE 0
255	000002	RING1=BIT1	;RING INDICATED ON LINE 1
256	000004	RING2=BIT2	;RING INDICATED ON LINE 2
257	000010	RING3=BIT3	;RING INDICATED ON LINE 3
258	000020	RING4=BIT4	;RING INDICATED ON LINE 4
259	000040	RING5=BIT5	;RING INDICATED ON LINE 5
260	000100	RING6=BIT6	;RING INDICATED ON LINE 6
261	000200	RING7=BIT7	;RING INDICATED ON LINE 7
262	000400	C00=BIT8	;CARRIER PRESENT ON LINE 0
263	001000	C01=BIT9	;CARRIER PRESENT ON LINE 1
264	002000	C02=BIT10	;CARRIER PRESENT ON LINE 2
265	004000	C03=BIT11	;CARRIER PRESENT ON LINE 3
266	010000	C04=BIT12	;CARRIER PRESENT ON LINE 4
267	020000	C05=BIT13	;CARRIER PRESENT ON LINE 5
268	040000	C06=BIT14	;CARRIER PRESENT ON LINE 6
269	100000	C07=BIT15	;CARRIER PRESENT ON LINE 7

;DZTDR BIT DEFINITIONS

270		-----	
271			
272			
273			
274	000400	BRK0=BIT8	;BREAK FOR LINE 0
275	001000	BRK1=BIT9	;BREAK FOR LINE 1
276	002000	BRK2=BIT10	;BREAK FOR LINE 2
277	004000	BRK3=BIT11	;BREAK FOR LINE 3
278	010000	BRK4=BIT12	;BREAK FOR LINE 4
279	020000	BRK5=BIT13	;BREAK FOR LINE 5
280	040000	BRK6=BIT14	;BREAK FOR LINE 6

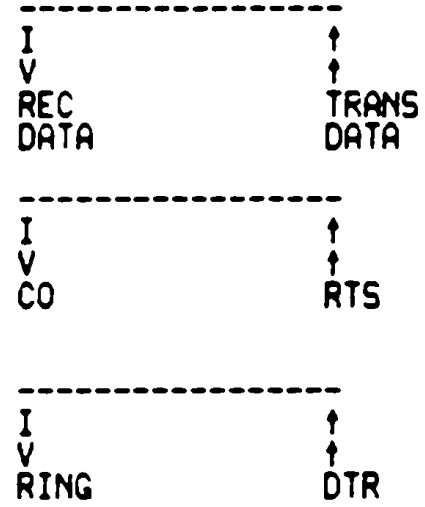
100000

BRK7=BIT15

;BREAK FOR LINE 7

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;TABLE OF LOOP AROUND FUNCTIONS (H325)




```

302 ;*****
303 ;-----
304 ; TRAPCATCHER FOR ILLEGAL INTERRUPTS
305 ; THE STANDARD "TRAP CATCHER" IS PLACED
306 ; BETWEEN ADDRESS 0 TO ADDRESS 776.
307 ; IT LOOKS LIKE "PC+2 HALT".
308 ;-----
309 ;*****
310
311 000000 . =0
312 ; STANDARD INTERRUPT VECTORS
313 ;-----
314
315 000010 000010 . =10
316 000012 010766 SET.PS ; FAKE "MTPS" INSTRUCTION TRAP
317 000012 000340 PR7 ; MAKE SURE PS IS PRIORITY 7
318
319 000020 000020 . =20
320 000020 004772 .SCOPE ; SCOPE LOOP HANDLER
321 000022 000340 PR7 ; HANDLE AT PRIORITY 7
322 000024 007646 $PWRDN ; POWER FAIL HANDLER
323 000026 000340 340 ; SERVICE AT PRIORITY LEVEL 7
324 000030 006736 $ERROR ; ERROR HANDLER
325 000032 000340 340 ; SERVICE AT PRIORITY LEVEL 7
326 000034 006630 .TRPSRV ; GENERAL HANDLER DISPATCH SERVICE
327 000036 000340 340 ; SERVICE AT PRIORITY LEVEL 7
328 .SBTTL ACT11 HOOKS
329
330 ;*****
331 ; HOOKS REQUIRED BY ACT11
332 000040 $SVPC= ; SAVE PC
333 000046 . =46
334 000046 004726 $ENDAD ; ;1) SET LOC.46 TO ADDRESS OF $ENDAD IN .SEOP
335 000052 . =52
336 000052 000000 .WORD 0 ; ;2) SET LOC.52 TO ZERO
337 000040 .=$SVPC ; ; RESTORE PC
338
339 000174 . =174
340 000174 000000 DISPREG:0 ; SOFTWARE DISPLAY REGISTER FOR SWITCHLESS 115
341 000176 000000 SWREG: 0 ; SOFTWARE SWITCH REGISTER FOR SWITCHLESS 115
342 000200 . =200
343 000200 000137 002150 JMP .START ; GO TO START OF PROGRAM
344 000210 . =210
345 000210 000137 023364 JMP XSTART ; GOTO CABLE TEST/ECHO TEST
346
347
348 001000 001000 . =1000
349 001000 005200 040515 047111 MTITLE: .ASCIZ <200><12>/MAINDEC-11-DZDZAD/<200>/EIGHT LINE ASYNC MUX TESTS/<200>
(2)

```

350
351
352
353
354
355
356 001120
357 001120
358 001120 000000
359 001122 000
360 001123 000
361 001124 000000
362 001125 000000
363 001130 000000
364 001132 000000
365 001134 000
366 001135 001
367 001136 000000
368 001140 000000
369 001142 000000
370 001144 000000
371 001146 000000
372 001150 000000
373 001152 000000
374 001154 000
375 001155 000
376 001156 000000
377 001160 177570
378 001162 177570
379 001164 177560
380 001166 177562
381 001170 177564
382 001172 177566
383 001174 000
384 001175 002
385 001176 012
386 001177 000
387 001200 000000
388
389 001202 000000
390 001204 000000
391 001206 000000
392 001210 000000
393 001212 000000
394 001214 000000
395 001216 000000
396 001220 000000
397 001222 000000
398 001224 000000
399 001226 000000
400 001230 077
401 001231 015
402 001232 000012
403
404
405

.SBTTL COMMON TAGS

; THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
; USED IN THE PROGRAM.

SCMTAG: . =1120

;; START OF COMMON TAGS

.WORD 0
; CONTAINS THE TEST NUMBER
.BYTE 00
; CONTAINS ERROR FLAG
.BYTE 00
; CONTAINS SUBTEST ITERATION COUNT
.WORD 00
; CONTAINS SCOPE LOOP ADDRESS
.WORD 00
; CONTAINS SCOPE RETURN FOR ERRORS
.WORD 00
; CONTAINS TOTAL ERRORS DETECTED
.BYTE 00
; CONTAINS ITEM CONTROL BYTE
.BYTE 1
; CONTAINS MAX. ERRORS PER TEST
.WORD 0
; CONTAINS PC OF LAST ERROR INSTRUCTION
.WORD 00
; CONTAINS ADDRESS OF 'GOOD' DATA
.WORD 00
; CONTAINS ADDRESS OF 'BAD' DATA
.WORD 00
; CONTAINS 'GOOD' DATA
.WORD 00
; CONTAINS 'BAD' DATA
.WORD 00
; RESERVED--NOT TO BE USED
.BYTE 00
; AUTOMATIC MODE INDICATOR
.BYTE 00
; INTERRUPT MODE INDICATOR
.WORD 0
; ADDRESS OF SWITCH REGISTER
.WORD DSWR
; ADDRESS OF DISPLAY REGISTER
DDISP
177560
; TTY KBD STATUS
177562
; TTY KBD BUFFER
177564
; TTY PRINTER STATUS REG. ADDRESS
177566
; TTY PRINTER BUFFER REG. ADDRESS
.BYTE 0
; CONTAINS NULL CHARACTER FOR FILLS
.BYTE 2
; CONTAINS # OF FILLER CHARACTERS REQUIRED
.BYTE 12
; INSERT FILL CHARS. AFTER A "LINE FEED"
.BYTE 0
; "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
.WORD 0
; CONTAINS THE ADDRESS FROM
; WHICH (\$REG0) WAS OBTAINED
.WORD 0
; CONTAINS ((\$REGAD)+0)
.WORD 0
; CONTAINS ((\$REGAD)+2)
.WORD 0
; CONTAINS ((\$REGAD)+4)
.WORD 0
; CONTAINS ((\$REGAD)+6)
.WORD 0
; CONTAINS ((\$REGAD)+10)
.WORD 0
; CONTAINS ((\$REGAD)+12)
.WORD 0
; USER DEFINED
.WORD 0
; USER DEFINED
.WORD 0
; USER DEFINED
.WORD 0
; USER DEFINED
0
; MAX. NUMBER OF ITERATIONS
.ASCII ??
; QUESTION MARK
.ASCII <15>
; CARRIAGE RETURN
.ASCII <12>
; LINE FEED

.SBTTL APT MAILBOX-ETABLE

```

406 ;*****
407 .EVEN
408 001234 $MAIL: ; APT MAILBOX
409 001234 000000 $MSGTY: .WORD AMSGTY ; MESSAGE TYPE CODE
410 001236 000000 $FATAL: .WORD AFATAL ; FATAL ERROR NUMBER
411 001240 000000 $TESTN: .WORD ATESTN ; TEST NUMBER
412 001242 000000 $PASS: .WORD APASS ; PASS COUNT
413 001244 000000 $DEVCT: .WORD ADEVCT ; DEVICE COUNT
414 001246 000000 $UNIT: .WORD AUNIT ; I/O UNIT NUMBER
415 001250 000000 $MSGAD: .WORD AMSGAD ; MESSAGE ADDRESS
416 001252 000000 $MSGLG: .WORD AMSGLG ; MESSAGE LENGTH
417 001254 $ETABLE: ; APT ENVIRONMENT TABLE
418 001254 000 $ENV: .BYTE AENV ; ENVIRONMENT BYTE
419 001255 000 $ENVM: .BYTE AENVM ; ENVIRONMENT MODE BITS
420 001256 000000 $SWREG: .WORD ASWREG ; APT SWITCH REGISTER
421 001260 000000 $USWR: .WORD AUSWR ; USER SWITCHES
422 001262 000000 $CPUOP: .WORD ACPUOP ; CPU TYPE, OPTIONS
423 ;*
424 ;* BIT 15-11=CPU TYPE
425 ;* 11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05
426 ;* 11/70=06, PDQ=07, Q=10
427 ;*
428 ;* BIT 10=REAL TIME CLOCK
429 001264 000 $MAMS1: .BYTE AMAMS1 ; HIGH ADDRESS, M.S. BYTE
430 001265 000 $MTYP1: .BYTE AMTYP1 ; MEM. TYPE, BLK#1
431 ;* MEM. TYPE BYTE -- (HIGH BYTE)
432 ;* 900 NSEC CORE=001
433 ;* 300 NSEC BIPOLAR=002
434 ;* 500 NSEC MOS=003
435 001266 000000 $MADR1: .WORD AMADR1 ; HIGH ADDRESS, BLK#1
436 ;* MEM. LAST ADDR.=3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
437 001270 000 $MAMS2: .BYTE AMAMS2 ; HIGH ADDRESS, M.S. BYTE
438 001271 000 $MTYP2: .BYTE AMTYP2 ; MEM. TYPE, BLK#2
439 001272 000000 $MADR2: .WORD AMADR2 ; MEM. LAST ADDRESS, BLK#2
440 001274 000 $MAMS3: .BYTE AMAMS3 ; HIGH ADDRESS, M.S. BYTE
441 001275 000 $MTYP3: .BYTE AMTYP3 ; MEM. TYPE, BLK#3
442 001276 000000 $MADR3: .WORD AMADR3 ; MEM. LAST ADDRESS, BLK#3
443 001300 000 $MAMS4: .BYTE AMAMS4 ; HIGH ADDRESS, M.S. BYTE
444 001301 000 $MTYP4: .BYTE AMTYP4 ; MEM. TYPE, BLK#4
445 001302 000000 $MADR4: .WORD AMADR4 ; MEM. LAST ADDRESS, BLK#4
446 001304 000000 $VECT1: .WORD AVECT1 ; INTERRUPT VECTOR#1, BUS PRIORITY#1
447 001306 000000 $VECT2: .WORD AVECT2 ; INTERRUPT VECTOR#2, BUS PRIORITY#2
448 001310 160010 $BASE: .WORD ABASE ; BASE ADDRESS OF EQUIPMENT UNDER TEST
449 001312 000000 $DEVN: .WORD ADEVN ; DEVICE MAP
450 001314 000000 $CDW1: .WORD ACDW1 ; CONTROLLER DESCRIPTION WORD#1
451 001316 000000 $CDW2: .WORD ACDW2 ; CONTROLLER DESCRIPTION WORD#2
452 001320 000000 $DDW0: .WORD ADDW0 ; DEVICE DESCRIPTOR WORD#0
453 001322 000000 $DDW1: .WORD ADDW1 ; DEVICE DESCRIPTOR WORD#1
454 001324 000000 $DDW2: .WORD ADDW2 ; DEVICE DESCRIPTOR WORD#2
455 001326 000000 $DDW3: .WORD ADDW3 ; DEVICE DESCRIPTOR WORD#3
456 001330 000000 $DDW4: .WORD ADDW4 ; DEVICE DESCRIPTOR WORD#4
457 001332 000000 $DDW5: .WORD ADDW5 ; DEVICE DESCRIPTOR WORD#5
458 001334 000000 $DDW6: .WORD ADDW6 ; DEVICE DESCRIPTOR WORD#6
459 001336 000000 $DDW7: .WORD ADDW7 ; DEVICE DESCRIPTOR WORD#7
460 001340 000000 $DDW8: .WORD ADDW8 ; DEVICE DESCRIPTOR WORD#8
461 001342 000000 $DDW9: .WORD ADDW9 ; DEVICE DESCRIPTOR WORD#9

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.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
 ;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
 ;*LOCATION SITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
 ;*NOTE1: IF SITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
 ;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;* EM ;;POINTS TO THE ERROR MESSAGE
 ;* DH ;;POINTS TO THE DATA HEADER
 ;* DT ;;POINTS TO THE DATA
 ;* DF ;;POINTS TO THE DATA FORMAT

\$ERRTB:

;PROGRAM CONTROL PARAMETERS
 ;-----

NEXT: 0 ;ADDRESS OF NEXT TEST TO BE EXECUTED
 LOCK: 0 ;ADDRESS FOR LOCK ON CURRENT DATA

;PROGRAM VARIABLES
 ;-----

LINE: 377 ;DEFAULT ALL EIGHT LINES RUNNING
 PAR: 17470 ;PARAMETERS: 8 BITS/CHAR,2 STOP BITS,19200 BAUD,NO PARIT
 MODE: 0 ;DEFAULT MAINTENANCE MODE
 SAVLIN: 0 ;LINE NUMBER
 XMTLIN: 0 ;TRANSMISSION LINE NUMBER
 XMTCNT: 0 ;COUNT OF WORDS IN A TRANSMISSION PATTERN
 REGIST: 0 ;DEVICE ADDRESS STORAGE LOCATION
 SAVPC: 0 ;PROGRAM COUNTER STORAGE
 DZACTV: .BLKW 1 ;*DZ11'S SELECTED ACTIVE.
 RUN: 1 ;*POINTER ONE PAST RUNNING DEVICE.
 DZNUM: .BLKB 1 ;*OCTAL NUMBER OF DZ11'S.
 SAVNUM: .BYTE 1 ;*WORKABLE NUMBER.
 .EVEN
 ACTIVE: DZ.MAP ;TABLE POINTER.

001360

001360 000000
001362 000000

001364 000377
001366 017470
001370 000000
001372 000000
001374 000000
001376 000000
001400 000000
001402 000000
001404 000001
001406 000001
001410 000001
001411 001
001412 001500

```

511
512
513
514
515 001414 000
516 001415 000
517 001416 000
518 001417 000
519 001420 000
520 001422
521
522 001422 000000
523 001424 000000
524 001426 000000
525 001430 000000
526 001432 000000
527 001434 000000
528 001436 000000
529 001440 000000
530 001442 000000
531 001444 000000
532 001446 000000
533 001450 000000
534 001452 000000
535 001454 000000
536 001456 000000
537 001460 000000
538 001462
539
540
541
542
543
544 001462
545 000024 000200
546 000024 000200
547 000044 000044
548 000044 001462
549 001462
550
551
552
553
554 001462
555 001462 000000
556 001464 001234
557 001466 000132
558 001470 000137
559 001472 000137
560 001474 000052
561
562
563
564 001500 001500
565
566

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```

;PROGRAM CONTROL FLAGS
-----
EIAFLG: .BYTE 0 ;0=EIA 100000=20MA
INIFLG: .BYTE 0 ;PROGRAM INITIALIZATION FLAG
HDRFLG: .BYTE 0 ;PROGRAM INITIALIZATION FLAG FOR HEADER MAP
MNTFLG: .BYTE 0 ;MAINTENANCE BIT SET FLAG
DONFLG: .BYTE 0 ;TRANSMISSION COMPLETION FLAG
.EVEN
;DATA VARIABLES
T00: .WORD 0
TD1: .WORD 0
TD2: .WORD 0
TD3: .WORD 0
TD4: .WORD 0
TD5: .WORD 0
TD6: .WORD 0
TD7: .WORD 0
TR0: .WORD 0
TR1: .WORD 0
TR2: .WORD 0
TR3: .WORD 0
TR4: .WORD 0
TR5: .WORD 0
TR6: .WORD 0
TR7: .WORD 0
STOP:
.SBTTL APT PARAMETER BLOCK

;*****
;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
;*****
.SX= ;SAVE CURRENT LOCATION
=24 ;SET POWER FAIL TO POINT TO START OF PROGRAM
200 ;FOR APT START UP
=44 ;POINT TO APT INDIRECT ADDRESS PNTR.
$APTHDR ;POINT TO APT HEADER BLOCK
=.SX ;RESET LOCATION COUNTER
;*****
;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
;INTERFACE SPEC.

$APTHD:
$HIBTS: .WORD 0 ;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
$MADDR: .WORD $MAIL ;ADDRESS OF APT MAILBOX (BITS 0-15)
$STMT: .WORD 90 ;RUN TIM OF LONGEST TEST
$PASTM: .WORD 95 ;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
$UNITM: .WORD 95 ;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
.WORD $ETEND-$MAIL/2 ;LENGTH MAILBOX-ETABLE(WORDS)
;DZ11 STATUS TABLE AND ADDRESS ASSIGNMENTS
-----
-1500
DZ.MAP:

```

567	001500	000001	DZCR0:	.BLKW	1	:CONTROL STATUS REGISTER FOR DZ11 NUMBER 0
568	001502	000001	DZVC0:	.BLKW	1	:RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 0
569	001504	000001	DZLV0:	.BLKW	1	:PRIORITY LEVEL AND EIA FLAG SELECTOR
570	001506	000001	LINE0:	.BLKW	1	:ALL LINES SELECTED
571	001510	000001	PAR0:	.BLKW	1	:PARAMETERS
572	001512	000001	MANT0:	.BLKW	1	:MAINTENANCE MODE FOR THIS DEVICE
573						
574	001514	000001	DZCR1:	.BLKW	1	:CONTROL STATUS REGISTER FOR DZ11 NUMBER 1
575	001516	000001	DZVC1:	.BLKW	1	:RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 1
576	001520	000001	DZLV1:	.BLKW	1	:PRIORITY LEVEL AND EIA FLAG SELECTOR
577	001522	000001	LINE1:	.BLKW	1	:ALL LINES SELECTED
578	001524	000001	PAR1:	.BLKW	1	:PARAMETERS
579	001526	000001	MANT1:	.BLKW	1	:MAINTENANCE MODE FOR THIS DEVICE
580						
581	001530	000001	DZCR2:	.BLKW	1	:CONTROL STATUS REGISTER FOR DZ11 NUMBER 2
582	001532	000001	DZVC2:	.BLKW	1	:RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 2
583	001534	000001	DZLV2:	.BLKW	1	:PRIORITY LEVEL AND EIA FLAG SELECTOR
584	001536	000001	LINE2:	.BLKW	1	:ALL LINES SELECTED
585	001540	000001	PAR2:	.BLKW	1	:PARAMETERS
586	001542	000001	MANT2:	.BLKW	1	:MAINTENANCE MODE FOR THIS DEVICE
587						
588	001544	000001	DZCR3:	.BLKW	1	:CONTROL STATUS REGISTER FOR DZ11 NUMBER 3
589	001546	000001	DZVC3:	.BLKW	1	:RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 3
590	001550	000001	DZLV3:	.BLKW	1	:PRIORITY LEVEL AND EIA FLAG SELECTOR
591	001552	000001	LINE3:	.BLKW	1	:ALL LINES SELECTED
592	001554	000001	PAR3:	.BLKW	1	:PARAMETERS
593	001556	000001	MANT3:	.BLKW	1	:MAINTENANCE MODE FOR THIS DEVICE
594						
595	001560	000001	DZCR4:	.BLKW	1	:CONTROL STATUS REGISTER FOR DZ11 NUMBER 4
596	001562	000001	DZVC4:	.BLKW	1	:RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 4
597	001564	000001	DZLV4:	.BLKW	1	:PRIORITY LEVEL AND EIA FLAG SELECTOR
598	001566	000001	LINE4:	.BLKW	1	:ALL LINES SELECTED
599	001570	000001	PAR4:	.BLKW	1	:PARAMETERS
600	001572	000001	MANT4:	.BLKW	1	:MAINTENANCE MODE FOR THIS DEVICE
601						
602	001574	000001	DZCR5:	.BLKW	1	:CONTROL STATUS REGISTER FOR DZ11 NUMBER 5
603	001576	000001	DZVC5:	.BLKW	1	:RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 5
604	001600	000001	DZLV5:	.BLKW	1	:PRIORITY LEVEL AND EIA FLAG SELECTOR
605	001602	000001	LINE5:	.BLKW	1	:ALL LINES SELECTED
606	001604	000001	PAR5:	.BLKW	1	:PARAMETERS
607	001606	000001	MANT5:	.BLKW	1	:MAINTENANCE MODE FOR THIS DEVICE
608						
609	001610	000001	DZCR6:	.BLKW	1	:CONTROL STATUS REGISTER FOR DZ11 NUMBER 6
610	001612	000001	DZVC6:	.BLKW	1	:RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 6
611	001614	000001	DZLV6:	.BLKW	1	:PRIORITY LEVEL AND EIA FLAG SELECTOR
612	001616	000001	LINE6:	.BLKW	1	:ALL LINES SELECTED
613	001620	000001	PAR6:	.BLKW	1	:PARAMETERS
614	001622	000001	MANT6:	.BLKW	1	:MAINTENANCE MODE FOR THIS DEVICE
615						
616	001624	000001	DZCR7:	.BLKW	1	:CONTROL STATUS REGISTER FOR DZ11 NUMBER 7
617	001626	000001	DZVC7:	.BLKW	1	:RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 7
618	001630	000001	DZLV7:	.BLKW	1	:PRIORITY LEVEL AND EIA FLAG SELECTOR
619	001632	000001	LINE7:	.BLKW	1	:ALL LINES SELECTED
620	001634	000001	PAR7:	.BLKW	1	:PARAMETERS
621	001636	000001	MANT7:	.BLKW	1	:MAINTENANCE MODE FOR THIS DEVICE
622						

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 DZDZAO.P11 02-MAR-77 08:20 APT PARAMETER BLOCK

623	001640	000001	DZCR10: .BLKW	1	; CONTROL STATUS REGISTER FOR DZ11 NUMBER 10
624	001642	000001	DZVC10: .BLKW	1	; RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 10
625	001644	000001	DZLV10: .BLKW	1	; PRIORITY LEVEL AND EIA FLAG SELECTOR
626	001646	000001	LINE10: .BLKW	1	; ALL LINES SELECTED
627	001650	000001	PAR10: .BLKW	1	; PARAMETERS
628	001652	000001	MANT10: .BLKW	1	; MAINTENANCE MODE FOR THIS DEVICE
629					
630	001654	000001	DZCR11: .BLKW	1	; CONTROL STATUS REGISTER FOR DZ11 NUMBER 11
631	001656	000001	DZVC11: .BLKW	1	; RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 11
632	001660	000001	DZLV11: .BLKW	1	; PRIORITY LEVEL AND EIA FLAG SELECTOR
633	001662	000001	LINE11: .BLKW	1	; ALL LINES SELECTED
634	001664	000001	PAR11: .BLKW	1	; PARAMETERS
635	001666	000001	MANT11: .BLKW	1	; MAINTENANCE MODE FOR THIS DEVICE
636					
637	001670	000001	DZCR12: .BLKW	1	; CONTROL STATUS REGISTER FOR DZ11 NUMBER 12
638	001672	000001	DZVC12: .BLKW	1	; RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 12
639	001674	000001	DZLV12: .BLKW	1	; PRIORITY LEVEL AND EIA FLAG SELECTOR
640	001676	000001	LINE12: .BLKW	1	; ALL LINES SELECTED
641	001700	000001	PAR12: .BLKW	1	; PARAMETERS
642	001702	000001	MANT12: .BLKW	1	; MAINTENANCE MODE FOR THIS DEVICE
643					
644	001704	000001	DZCR13: .BLKW	1	; CONTROL STATUS REGISTER FOR DZ11 NUMBER 13
645	001706	000001	DZVC13: .BLKW	1	; RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 13
646	001710	000001	DZLV13: .BLKW	1	; PRIORITY LEVEL AND EIA FLAG SELECTOR
647	001712	000001	LINE13: .BLKW	1	; ALL LINES SELECTED
648	001714	000001	PAR13: .BLKW	1	; PARAMETERS
649	001716	000001	MANT13: .BLKW	1	; MAINTENANCE MODE FOR THIS DEVICE
650					
651	001720	000001	DZCR14: .BLKW	1	; CONTROL STATUS REGISTER FOR DZ11 NUMBER 14
652	001722	000001	DZVC14: .BLKW	1	; RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 14
653	001724	000001	DZLV14: .BLKW	1	; PRIORITY LEVEL AND EIA FLAG SELECTOR
654	001726	000001	LINE14: .BLKW	1	; ALL LINES SELECTED
655	001730	000001	PAR14: .BLKW	1	; PARAMETERS
656	001732	000001	MANT14: .BLKW	1	; MAINTENANCE MODE FOR THIS DEVICE
657					
658	001734	000001	DZCR15: .BLKW	1	; CONTROL STATUS REGISTER FOR DZ11 NUMBER 15
659	001736	000001	DZVC15: .BLKW	1	; RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 15
660	001740	000001	DZLV15: .BLKW	1	; PRIORITY LEVEL AND EIA FLAG SELECTOR
661	001742	000001	LINE15: .BLKW	1	; ALL LINES SELECTED
662	001744	000001	PAR15: .BLKW	1	; PARAMETERS
663	001746	000001	MANT15: .BLKW	1	; MAINTENANCE MODE FOR THIS DEVICE
664					
665	001750	000001	DZCR16: .BLKW	1	; CONTROL STATUS REGISTER FOR DZ11 NUMBER 16
666	001752	000001	DZVC16: .BLKW	1	; RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 16
667	001754	000001	DZLV16: .BLKW	1	; PRIORITY LEVEL AND EIA FLAG SELECTOR
668	001756	000001	LINE16: .BLKW	1	; ALL LINES SELECTED
669	001760	000001	PAR16: .BLKW	1	; PARAMETERS
670	001762	000001	MANT16: .BLKW	1	; MAINTENANCE MODE FOR THIS DEVICE
671					
672	001764	000001	DZCR17: .BLKW	1	; CONTROL STATUS REGISTER FOR DZ11 NUMBER 17
673	001766	000001	DZVC17: .BLKW	1	; RECEIVER AND BASE VECTOR FOR DZ11 NUMBER 17
674	001770	000001	DZLV17: .BLKW	1	; PRIORITY LEVEL AND EIA FLAG SELECTOR
675	001772	000001	LINE17: .BLKW	1	; ALL LINES SELECTED
676	001774	000001	PAR17: .BLKW	1	; PARAMETERS
677	001776	000001	MANT17: .BLKW	1	; MAINTENANCE MODE FOR THIS DEVICE
678					

B04

MD-11-DZDZA-D MACY11 27(1006) 02-MAR-77 08:23 PAGE 16
DZDZAD.P11 02-MAR-77 08:20 APT PARAMETER BLOCK
679 002000 177777 DZ.END: 177777

PAGE: 0040

680
681
682
683
684
685
686 002002
687 104400
688 002002 005724
689 104401
690 002004 005236
691 104402
692 002006 005262
693 104403
694 002010 006030
695 104404
696 002012 006134
697 104405
698 002014 006154
699 104406
700 002016 010500
701 104407
702 002020 006354
703 104410
704 002022 006414
705 104411
706 002024 006446
707 104412
708 002026 006452
709 104413
710 002030 006652
711 104414
712 002032 006704
713 104415
714 002034 025112
715 104416
716 002036 025306
717 104417
718 002040 006672
719
720
721

;DEFINITIONS FOR TRAP SUBROUTINE CALLS
;POINTERS TO SUBROUTINES CAN BE FOUND
;IN THE TABLE IMMEDIATELY FOLLOWING THE DEFINITIONS

;:*****

;TRPTAB:
ADVANCE=TRAP+0 ,CALL TO ADVANCE TO NEXT TEST(OR SCOPE THIS ONE)
 .ADVANCE
SCOPI=TRAP+1 ;CALL TO LOOP ON CURRENT DATA HANDLER
 .SCOPI
TYPE=TRAP+2 ;CALL TO TELETYPE OUTPUT ROUTINE
 .TYPE
INSTR=TRAP+3 ;CALL TO ASCII STRING INPUT ROUTINE
 .INSTR
INSTER=TRAP+4 ;CALL TO INPUT ERROR HANDLER
 .INSTER
PARAM=TRAP+5 ;CALL TO NUMERICAL DATA INPUT ROUTINE
 .PARAM
SETFLG=TRAP+6 ;CALL TO SET FLAG ROUTINE
 .SETFLG
SAVDS=TRAP+7 ;CALL TO REGISTER SAVE ROUTINE
 .SAVDS
RESOS=TRAP+10 ;CALL TO REGISTER RESTORE ROUTINE
 .RESOS
CONVRT=TRAP+11 ;CALL TO DATA OUTPUT ROUTINE
 .CONVRT
CNVRT=TRAP+12 ;CALL TO DATA OUTPUT ROUTINE WITHOUT CR/LF.
 .CNVRT
DEVICE.CLR=TRAP+13 ;CALL TO ISSUE A DEVICE CLEAR
 .DEVICE.CLR
DELAY=TRAP+14 ;CALL TO DELAY FOR FAST CPU'S
 .DELAY
PARMD=TRAP+15 ;CONVERT DECIMAL STRING TO OCTAL
 .PARMD
PAWCH=TRAP+16 ;SET FLAG ECHO OR CABLE
 .PAWCH
DCLASM=TRAP+17 ;CLEAR DEVICE, SET MAINT. BIT IF I MODE
 .DCLASM

;:*****

```

722 ;DZ11 VECTOR AND REGISTER INDIRECT POINTERS
723 ;WORKING AREA
724
725 002042 160040 DZCSR: 160040 ;R/W
726 002044 160041 HDZCSR: 160041 ;R/W
727 002046 160042 DZRBUF: 160042 ;READ ONLY
728 002050 160043 HDZRBUF: 160043 ;READ ONLY
729 002052 160042 DZLPR: 160042 ;WRITE ONLY
730 002054 160043 HDZLPR: 160043 ;WRITE ONLY
731 002056 160044 DZTCR: 160044 ;R/W
732 002060 160045 HDZTCR: 160045 ;R/W
733 002062 160046 DZMSR: 160046 ;READ ONLY
734 002064 160047 HDZMSR: 160047 ;READ ONLY
735 002066 160046 DZTDR: 160046 ;WRITE ONLY
736 002070 160047 HDZTDR: 160047 ;WRITE ONLY
737 ;DEFAULT DZ VECTORS
738 002072 000300 DZRIV: 300 ;REC INTR VECTOR
739 002074 000302 DZ RIS: 302 ;REC INTR STATUS
740 002076 000304 DZTIV: 304 ;XMIT INTR VECTOR
741 002100 000306 DZTIS: 306 ;XMIT INTR STATUS
742
743

```

744
745
746
747
748 002102
749 002102 000000
750 002104 000000
751 002106 000000
752 002110 000000
753 002112 000000
754 002114 000000
755 002116 000000
756 002120 000000
757 002122 000000
758 002124 000000
759 002126 000000
760 002130 000000
761 002132 000000
762 002134 000000
763 002136 000000
764 002140 000000
765 002142 000000
766 002144 000000
767 002146 000000

; TIME TABLE FOR RELATIVE TIMING TESTS

TMTBL:
TFO: 0
T75: 0
T110: 0
T134: 0
T150: 0
T300: 0
T600: 0
T1200: 0
T1800: 0
T2000: 0
T2400: 0
T3600: 0
T4800: 0
T7200: 0
T9600: 0
TEIGHT: 0
TSEVEN: 0
TSIX: 0
TFIVE: 0

```

768
769
770
771
772
773
774
775
776 002150
777 002150 000005
778 002152 012706 001120
779 002156 106427 000340
780 002162 012737 007646 000024
781 002170 113737 001410 001411
782 002176 005037 001242
783 002202 105037 001123
784 002206 012737 001500 001412
785 002214 012737 000001 001406
786 002222 005037 001132
787 002226 005037 001136
788 002232 005037 001122
789 002236 012737 002150 001126
790
791
792 002244 013746 000006
793 002250 013746 000004
794 002254 012737 002274 000004
795 002262 022777 177777 176670
796 002270 001402
797 002272 000407
798 002274 022626
799 002276 012737 000176 001160
800 002304 012737 000174 001162
801 002312 012637 000004
802 002316 012637 000006
803 002322 105737 001415
804 002326 001010
805 002330 023727 000042 004726
806 002336 001402
807 002340 104402 001000
808 002344 105337 001415
809 002350 105737 001255
810 002354 100006
811 002356 004737 011440
812 002362 105037 001416
813 002366 000137 004270
814 002372 032777 000001 176560
815 002400 001011
816 002402 122737 000377 001415
817 002410 001003
818 002412 105777 176542
819 002416 100402
820 002420 000137 003114
821 002424 012700 001500
822 002430 105037 001416
823 002434 005020

```

```

;PROGRAM INITIALIZATION
;LOCK OUT INTERRUPTS
;SET UP PROCESSOR STACK
;SET UP POWER FAIL VECTOR
;CLEAR PROGRAM CONTROL FLAGS AND COUNTS
;TYPE TITLE MESSAGE

.START:
RESET
MOV #STACK, SP
MTPS #PR7
MOV #SPWRDN, @#24
MOV#B DZNUM, SAVNUM
CLR SPASS
CLRB #ERRFLG
MOV #DZ.MAP, ACTIVE
MOV #1, RUN
CLR #ERTTL
CLR #ERRPC
CLR #STSTNM
MOV #.START, $LPADR

;CLEAR THE WORLD. START NEW ENVIRONMENT
;SET UP STACK
;LOCK OUT INTERRUPTS
;SET UP POWER FAIL VECTOR
;SAVE NUMBER OF DEVICES IN SYSTEM.
;CLEAR PASS COUNT
;CLEAR ERROR FLAG
;GET MAP POINTER.
;POINT POINTER TO FIRST DEVICE.
;CLEAR ERROR COUNT
;CLEAR LAST ERROR POINTER
;SET UP FOR TEST 1
;SET UP FOR POWER FAIL BEFORE
;TESTING STARTS

;SET UP FOR SMALL 11 SWITCH REGISTER COMPATIBILITY
MOV 6, -(SP)
MOV 4, -(SP)
MOV #20$, 4
CMP #-1, @SWR
BEQ 22$
BR 21$
POP2SP
MOV #SWREG, SWR
MOV #DISPREG.DISPLAY
MOV (SP)+, 4
MOV (SP)+, 6
TSTB INIFLG
BNE 29$
CMP @#42, #SENDAD
BEQ 31$
TYPE #MTITLE
DECB INIFLG
TSTB #ENVNM
BPL 30$
JSR PC, SETAPT
CLRB HDRFLG
JMP 16$
BIT #SW00, @SWR
BNE 32$
CMPB #377, INIFLG
BNE .+10
TSTB @SWR
BMI 32$
JMP 73$
MOV #DZ.MAP, RO
CLRB HDRFLG
CLRB (RO)+

;SAVE BUS ERROR PS
;SAVE BUS ERROR PC
;SET UP TO TRAP TO THIS ROUTINE
;CAN 177570 BE REFERENCED?
;IF SO AND IT IS -1, TREAT LIKE SWITCHLESS
;IF YES, SKIP AROUND THE SETUP
;REMOVE THE TRAP FROM THE STACK
;IF NO TRAP COMES HERE. POINT TO SOFTWARE SWR
;POINT TO SOFTWARE DISPLAY REGISTER
;RESTORE THE BUS ERROR VECTOR

;TITLE ALREADY PRINTED?
;BRANCH IF YES
;RUNNING UNDER ACT?
;IF YES DONT PRINT TITLE
;PRINT THE DIAGNOSTIC'S TITLE
;SET THE ONCE ONLY FLAG
;DETERMINE WHETHER APT SIZING SHOULD BE DONE
;IF NOT, GO CHECK FOR AUTO-SIZING
;OTHERWISE, GO DO APT SIZING FROM ETABLE
;MAKE SURE STATUS TABLE IS PRINTED
;GO PRINT DZ STATUS TABLE
;RESELECT ?
;IF YES, GO SET UP THE INFORMATION
;ON 1ST START: MUST ANSWER QUESTION
;IF NOT ANSWERING QUESTIONS
;ARE U AUTO SIZING?
;NO AUTO SIZE! NO SW00=1 ON 1ST START!
;IF NO, SKIP THE INTERROGATION
;POINT TO THE BEGINNING OF THE MAP TABLE
;MAKE SURE A MAP GETS PRINTED
;CLEAR A TABLE LOCATION

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824 002436 020027 002000      CMP      RO,#DZ.END      ;HAVE THE TABLE BOUNDARIES BEEN EXCEEDED?
825 002442 001374              BNE      65$             ;IF NOT, CLEAR THE NEXT LOCATION IN THE TABLE
826 002444 105337 001415      DECB     INIFLG          ;INSURE NO AUTO SIZING IF QUESTIONS ANSWERED!
827
828                               ;THE FOLLOWING ARE PARAMETERS USED TO FILL IN THE MAP
829                               ;TABLE AND SET UP THE DIAGNOSTIC.
830
831                               ;GET THE BASE ADDRESS OF THE DZ11'S
832
833 002450      33$:
834 002450      INSTR      ;CALL THE STRING INPUT ROUTINE
835 002452      66$        ;POINTER TO MESSAGE TO BE PRINTED
836 002454      104403     PARAM      ;CALL THE OCTAL TO ASCII CONVERT ROUTINE
837 002456      160000     160000     ;LOWEST LEGITIMATE VALUE OF EXPECTED RESPONSE
838 002460      163770     163770     ;HIGHEST LEGITIMATE VALUE OF EXPECTED RESPONSE
839 002462      001500     DZCRO      ;POINTER TO MAP LOCATION TO BE FILLED
840 002464              .BYTE      7      ;MASK OF INVALID BITS FOR THIS PARAMETER
841 002465              .BYTE      1      ;NUMBER OF PARAMETERS TO STORE
842 002466      013737     001500     001310     MOV      DZCRO,$BASE      ;COPY BASE ADDRESS TO ETABLE
843
844                               ;GET THE BASE VECTOR ADDRESS
845
846 002474      34$:
847 002474      INSTR      ;CALL THE STRING INPUT ROUTINE
848 002476      67$        ;POINTER TO MESSAGE TO BE PRINTED
849 002500      104405     PARAM      ;CALL THE OCTAL TO ASCII CONVERT ROUTINE
850 002502      000300     300        ;LOWEST LEGITIMATE VALUE OF EXPECTED RESPONSE
851 002504      000776     776        ;HIGHEST LEGITIMATE VALUE OF EXPECTED RESPONSE
852 002506      001502     DZVCO      ;POINTER TO MAP LOCATION TO BE FILLED
853 002510              .BYTE      3      ;MASK OF INVALID BITS FOR THIS PARAMETER
854 002511              .BYTE      1      ;NUMBER OF PARAMETERS TO STORE
855 002512      013737     001502     001304     MOV      DZVCO,$VECT1    ;COPY VECTOR TO ETABLE
856
857                               ;GET THE BUS REQUEST LEVEL
858
859 002520      INSTR      ;CALL THE STRING INPUT ROUTINE
860 002522      68$        ;POINTER TO MESSAGE TO BE PRINTED
861 002524      104405     PARAM      ;CALL THE OCTAL TO ASCII CONVERT ROUTINE
862 002526      000004     4          ;LOWEST LEGITIMATE VALUE OF EXPECTED RESPONSE
863 002530      000007     7          ;HIGHEST LEGITIMATE VALUE OF EXPECTED RESPONSE
864 002532      001504     DZLVO      ;POINTER TO MAP LOCATION TO BE FILLED
865 002534              .BYTE      0      ;MASK OF INVALID BITS FOR THIS PARAMETER
866 002535              .BYTE      1      ;NUMBER OF PARAMETERS TO STORE
867 002536      113737     001504     001305     MOVB     DZLVO,$VECT1+1 ;GET BUS REQUEST LEVEL INTO ETABLE
868 002544      106337     001305     ASLB     $VECT1+1      ;ALIGN THE BITS PROPERLY
869 002550      106337     001305     ASLB     $VECT1+1      ;ALIGN THE BITS PROPERLY
870 002554      106337     001305     ASLB     $VECT1+1      ;ALIGN THE BITS PROPERLY
P71 002560      106337     001305     ASLB     $VECT1+1      ;ALIGN THE BITS PROPERLY
872 002564      106337     001305     ASLB     $VECT1+1      ;ALIGN THE BITS PROPERLY
873
874                               ;FIND OUT IF MODULE IS EIA OR 20 MA.
875
876 002570      104402     004130     TYPE      74$           ;PRINT EIA MESSAGE
877 002574      005037     001220     CLR      $TMP1         ;USE $TMP1
878 002600      105777     176360     TSTB     $STKS         ;IS KEYBOARD DONE?
879 002604      100375     80$:      BPL      80$           ;IF NOT, WAIT FOR IT

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880 002606 017746 176354      MOV      2$TKB, -(SP)      ; IF YES, PUT CHARACETR ON STACK
881 002612 042716 000240      BIC      #240, (SP)       ; STRIP DOWN CHARACTER
882 002616 122726 000015      CMPB    #15, (SP)+       ; IS IT ?
883 002622 001414              BEQ      81$              ; IF SO, GET OUT
884 002624 014677 176342      MOV      -(SP), 2$TPB     ; IF NOT, PRINT CHARACTER
885 002630 042737 100000 001504  BIC      #BIT15, DZLVD    ; CLEAR EIA FLAG
886 002636 122726 000102      CMPB    #102, (SP)+     ; IS IT A B?
887 002642 001356              BNE      80$              ; IF NOT, GO BACK FOR INPUT
888 002644 052737 100000 001504  BIS      #BIT15, DZLVD    ; IF SO, SET FLAG
889 002652 000752              BR       80$              ; GET MORE INPUT
890 002654              81$:
891
892              ; GET THE MODE OF OPERATION (E,I,S)
893
894 002654 104403      INSTR    ; CALL THE STRING INPUT ROUTINE
895 002656 003652      72$     ; POINTER TO THE MESSAGE TO BE PRINTED
896 002660 104406      SETFLG   ; CALL THE MAINTENANCE FLAG SETUP ROUTINE
897 002662 001512      MANTG    ; THIS IS THE FLAG BEING SETUP
898
899              ; GET THE NUMBER OF DZ11'S RUNNING
900
901 002664 104403      INSTR    ; CALL THE STRING INPUT ROUTINE
902 002666 003610      71$     ; POINTER TO MESSAGE TO BE PRINTED
903 002670 104405      PARAM   ; CALL THE OCTAL TO ASCII CONVERT ROUTINE
904 002672 000001      1       ; LOWEST LEGITIMATE VALUE OF EXPECTED RESPONSE
905 002674 000020      16      ; HIGHEST LEGITIMATE VALUE OF EXPECTED RESPONSE
906 002676 001220      $TMP1   ; POINTER TO MAP LOCATION TO BE FILLED
907 002700          000      .BYTE    0 ; MASK OF INVALID BITS FOR THIS PARAMETER
908 002701          001      .BYTE    1 ; NUMBER OF PARAMETERS TO STORE
909
910 002702 012737 000377 001506      MOV      #377, LINED     ; SET UP DEFAULT LINES
911 002710 012737 017470 001510      MOV      #17470, PARO    ; SET UP DEFAULT LPR PARAMETER
912              ; RECEIVER ON: 19.2 KBAUD; 2$STOP BITS; 8 BIT/CHAR
913 002716 012737 000001 006722      MOV      #1, DLYCNT     ; INITIALIZE DELAY COUNT
914 002724 032777 000010 176226      BIT      #5W03, 2$SWR   ; DO YOU WANT PARAMETERS?
915 002732 001402              BEQ      40$             ; IF NO, SKIP THE PARAMETER CALL
916 002734 004737 003144              JSR      PC, 23$        ; GET PARAMETERS
917 002740 012737 000001 001312 40$:      MOV      #1, $DEVN      ; INITIALIZE ACTIVE DEVICE SELECTION PARAMETER
918 002746 113737 001220 001410      MOV      $TMP1, DZNUM    ; COPY THE NUMBER OF DEVICES
919 002754 113737 001220 001411      MOV      $TMP1, SAVNUM   ; COPY A BACKUP NUMBER
920 002762 005337 001220 62$:      DEC      $TMP1          ; $TMP1 CONTAINS THE COUNT OF UNINITIALIZED
921 002766 001404              BEQ      61$             ; SELECTED DEVICES
922 002770 000261              SEC                      ; SET A BIT FLAG TO INDICATE AN ACTIVE DEVICE
923 002772 006137 001312      ROL      $DEVN          ; POINT TO THE NEXT DEVICE
924 002776 000771              BR       62$             ; GO DO THIS PROCEDURE AGAIN
925 003000 013737 001312 001222 61$:      MOV      $DEVN, $TMP2    ; # OF TIMES
926 003006 013737 001312 001404      MOV      $DEVN, DZACTV   ; COPY THE ACTIVE DEVICE PARAMETER
927 003014 012700 001500              MOV      #DZCR0, R0     ; SET A POINTER TO THE SPECIFIED INFORMATION
928 003020 012701 001514              MOV      #DZCR1, R1     ; POINT R1 TO THE REST OF THE MAP TABLE
929 003024 012702 001320              MOV      #SDDWO, R2     ; POINT TO ETABLE'S DEVICE DESCRIPTOR WORDS
930 003030 000241              CLC                      ; INITIALIZE THE "C" BIT FOR A ROTATION
931 003032 006037 001222      ROR      $TMP2          ; SKIP MAPPING SETUP FOR DEVICE 0- IT'S DONE
932 003036 006237 001222 64$:      ASR      $TMP2          ; ISOLATE A SELECTION FLAG IN THE "C" BIT
933 003042 103404              BCS      41$             ; IS THIS DEVICE SELECTED? IF YES, GO LOAD TABLE
934 003044 012711 177777      MOV      #-1, (R1)      ; TERMINATE THE LIST
935 003050 000137 004244              JMP      63$             ; GO TO THE NEXT BLOCK
    
```

PROGRAM INITIALIZATION AND START UP.

```

936 003054 012011          41$:  MOV      (R0)+,(R1)      ; ADDRESS
937 003056 062721 000010    ADD      #10,(R1)+      ; POINT TO THE NEXT DZ11 ADDRESS VALUE
938 003062 012011          MOV      (R0)+,(R1)      ; VECTOR
939 003064 062721 000010    ADD      #10,(R1)+      ; POINT TO THE NEXT VECTOR VALUE
940 003070 012021          MOV      (R0)+,(R1)+     ; LEVEL
941 003072 012021          MOV      (R0)+,(R1)+     ; LINES
942 003074 016012 177774    MOV      -4(R0),(R2)    ; GET THE EIA FLAG FROM THE PRIORITY WORD
943 003100 042712 077777    BIC      #77777,(R2)    ; ISOLATE THAT FLAG
944 003104 051022          BIS      (R0),(R2)+     ; ADD PARAMETERS TO DEVICE DESCRIPTOR WORD
945 003106 012021          MOV      (R0)+,(R1)+     ; PARAMETERS
946 003110 012021          MOV      (R0)+,(R1)+     ; MAINTENANCE MODE
947 003112 000751          BR       64$
948 003114 032777 000010 176036 73$: BIT      #SW03,DSWR      ; ASK PARAMETERS ?
949 003122 001002          BNE      42$
950 003124 000137 004244    JMP      63$
951 003130 004737 003144    42$:  JSR      PC,23$      ; GO SET UP FOR AUTO SIZING
952 003134 105337 001415    DECB    INIFLG          ; GO ASK PARAMETERS
953 003140 000137 004270    JMP      16$            ; INSURE NO AUTO SIZE IF QUESTIONS ANSWERED
954                                     ; GO TO THE NEXT BLOCK
955                                     ; GET THE ACTIVE LINES PARAMETER
956
957 003144          23$:  INSTR
958 003144 104403          69$
959 003146 003464          PARAM
960 003150 104405          1
961 003152 000001          377
962 003154 000377          LINED
963 003156 001506          .BYTE 0
964 003160          .BYTE 1
965 003161 001
966 003162 105037 001416    CLRB    HDRFLG
967
968                                     ; THIS SEGMENT CHECKS TO MAKE SURE THE LINE PARAMETER JUST ENTERED
969                                     ; IS LEGITIMATE IN STAGGERED MODE OPERATION IF THAT MODE WAS SELECTED
970
971 003166 005737 001512          TST      MANTO
972 003172 100021          BPL      26$
973 003174 013703 001506          MOV      LINED,R3
974 003200 006003          24$:  ROR      R3
975 003202 103410          BCS      25$
976 003204 001414          BEQ      26$
977 003206 006203          ASR      R3
978 003210 103373          BCC      24$
979 003212 104402 001230          TYPE    ,SQUES
980 003216 104402 010424          TYPE    ,MBADLN
981 003222 000750          BR       23$
982 003224 001772          25$:  BEQ      27$
983 003226 006203          ASR      R3
984 003230 103370          BCC      27$
985 003232 000241          CLC
986 003234 000761          BR       24$
987
988                                     ; GET THE LINE PARAMETER REGISTER ARGUMENT
989
990 003236          26$:  INSTR
991 003236 104403          ; CALL THE STRING INPUT ROUTINE
    
```


992	003240	003540			70\$		POINTER TO MESSAGE TO BE PRINTED
993	003242	104405			PARAM		CALL THE OCTAL TO ASCII CONVERT ROUTINE
994	003244	000000			0		LOWEST LEGITIMATE VALUE OF EXPECTED RESPONSE
995	003246	000017			17		HIGHEST LEGITIMATE VALUE OF EXPECTED RESPONSE
996	003250	001510			PARO		POINTER TO MAP LOCATION TO BE FILLED
997	003252	000			.BYTE 0		MASK OF INVALID BITS FOR THIS PARAMETER
998	003253	001			.BYTE 1		NUMBER OF PARAMETERS TO STORE
999	003254	012702	001506		MOV #LINEO,R2		POINT TO THE LINE SELECTION PARAMETER
1000	003260	012703	001510		MOV #PARO,R3		POINT TO THE CHOSEN PARAMETERS
1001	003264	011304			MOV (R3),R4		USE BAUD RATE AS AN INDEX IN DELAY TABLE
1002	003266	006304			ASL R4		ALIGN INDEX ON WORD BOUNDARY
1003	003270	016437	030444	006722	MOV DLYTBL(R4),DLYCNT		SET THE DELAY COUNT FOR THIS BAUD RATE
1004	003276	000313			SWAB (R3)		PLACE IN HIGH BYTE
1005	003300	052713	010070		BIS #10070,(R3)		PLACE EXTRA PARAMETERS INTO LOC
1006	003304	011262	000014	28\$:	MOV (R2),14(R2)		LOAD THE LINES
1007	003310	011363	000014		MOV (R3),14(R3)		LOAD THE PARAMETERS
1008	003314	062702	000014		ADD #14,R2		POINT TO THE NEXT SET
1009	003320	062703	000014		ADD #14,R3		.. OF BOTH PARAMETERS
1010	003324	020327	001774		CMP R3,#PAR17		HAVE THE TABLE BOUNDARIES BEEN EXCEEDED?
1011	003330	001365			BNE 28\$		IF NOT, GO LOAD SOME MORE PARAMETERS
1012	003332	000207			RTS PC		RETURN TO CALLING BLOCK
1013	003334	030600	052123	041440	66\$:	.ASCIZ <200>/1ST CSR ADDRESS (160000:163700): /	
(1)	003400	030600	052123	053040	67\$:	.ASCIZ <200>/1ST VECTOR ADDRESS (300:770): /	
(1)	003441	200	051102	046040	68\$:	.ASCIZ <200>/BR LEVEL (4:6): /	
(1)	003464	046200	047111	051505	69\$:	.ASCIZ <200>/LINES ACTIVE BY BIT <IN OCTAL>(001:377): /	
(1)	003540	042200	043105	052501	70\$:	.ASCIZ <200>/DEFAULT BAUD RATE <IN OCTAL>(00:17): /	
(1)	003610	021600	047440	020106	71\$:	.ASCIZ <200>/# OF DZ11'S <IN OCTAL> (1:20): /	
(1)	003652	046600	044501	052116	72\$:	.ASCII <200>/MAINTENANCE MODE/	
(1)	003673	200	055440	054105		.ASCII <200>/ [EXTERNAL <H325>-EIA ONLY (E)]/	
(1)	003741	200	055440	047111		.ASCII <200>/ [INTERNAL <DZCSR03=1> (I)]/	
(1)	004007	200	055440	052123		.ASCII <200>/ [STAGGERED <H3271>-EIA ONLY (S)]: /	
(1)	004057	200	055440	052123		.ASCII <200>/ [STAGGERED <H3190>-20MA ONLY (S)]: /	
(1)	004130	052200	050131	020105	74\$:	.ASCIZ <200>/TYPE "A" FOR EIA MODULE OR "B" FOR 20 MA (A:B):	
(1)	004212	042600	052116	051105	75\$:	.ASCIZ <200>/ENTER DELAY PARAMETER: -/	
(1)	004244	004244			.EVEN		
(1)	004244				63\$:		
1014	004244	122737	000377	001415	CMPB #377,INIFLG		ONLY DO AUTO SIZE ON 1ST START
1015	004252	001006			BNE 16\$		
1016	004254	032777	000200	174676	BIT #BIT7,@SWR		BIT7=1??
1017	004262	001002			BNE 16\$		BR IF NO AUTO SIZE
1018	004264	004737	011612		JSR PC,AUTO.SIZE		GO DO THE AUTO SIZE
1019	004270	105737	001416	16\$:	TSTB HDRFLG		HAS THE TABLE BEEN TYPED YET?
1020	004274	001021			BNE 1\$		IF SO, DON'T TYPE IT AGAIN
1021	004276	105337	001416		DECB HDRFLG		INDICATE THAT THE TABLE WILL BE TYPED
1022	004302	104402	010377		TYPE ,XHEAD		TYPE MAP HEADER
1023	004306	012700	001500		MOV #DZ.MAP,RO		SET POINTER
1024	004312	010037	001220	5\$:	MOV RO,\$TMP1		POINT TO THE MAP LOCATION
1025	004316	012037	001222		MOV (RO)+,\$TMP2		SET DATA
1026	004322	022737	177777	001222	CMP #-1,\$TMP2		END OF LIST?
1027	004330	001403			BEQ 1\$		BR IF YES
1028	004332	104411		17\$:	CONVRT		CALL THE OCTAL TO ASCII CONVERSION ROUTINE
1029	004334	010466			XSTATQ		CONVERT THE DATA AT THIS ADDRESS
1030	004336	000765			BR 5\$		GO PRINT THE NEXT PARAMETER
1031	004340	005737	000042	1\$:	TST @#42		IS PROGRAM RUNNING UNDER MONITOR
1032	004344	001026			BNE 3\$		YES
1033	004346	032777	000100	174604	BIT #SW06,@SWR		DESELECT SPECIFIC DEVICES??

1034	004354	001422				BEG	3\$: BR IF NO.
1035	004356	104402	010320			TYPE	MNEW		: TYPE THE MESSAGE.
1036	004362	005000				CLR	RO		: ZERO DATA DISPLAY
1037	004364	000000				HALT			: WAIT FOR USER TO TELL WHAT DEVICES TO RUN
1038	004366	027737	174566	001312		CMP	QSWR, \$DEVN		: IS THE NUMBER VALID?
1039	004374	101404				BLOS	2\$: BR IF NUMBER IS OK.
1040	004376	104402	010172			TYPE	, MERR3		: TELL USER OF INVALID NUMBER.
1041	004402	000000			9\$:	HALT			: STOP EVERY THING.
1042	004404	000776				BR	9\$: RESTART THE PROGRAM AGAIN.
1043	004406	017737	174546	001404	2\$:	MOV	QSWR, DZACTV		: GET NEW DEVICE PATTERN
1044	004414	013700	001404			MOV	DZACTV, RO		: SHOW THE USER WHAT HE SELECTED.
1045	004420	000000				HALT			: CONTINUE DYNAMIC SWITCHES.
1046	004422	032777	000020	174530	3\$:	BIT	#SW04, QSWR		: CHECK TO SEE IF DELAY COUNT CHANGES
1047	004430	001407				BEG	18\$: IF NOT, GO CLEAR VECTOR AREA
1048	004432	104403				INSTR			: CALL THE STRING INPUT ROUTINE
1049	004434	004212				75\$: POINTER TO MESSAGE TO BE PRINTED
1050	004436	104405				PARAM			: CALL THE OCTAL TO ASCII CONVERT ROUTINE
1051	004440	000001				1			: LOWEST LEGITIMATE VALUE OF EXPECTED RESPONSE
1052	004442	177777				177777			: HIGHEST LEGITIMATE VALUE OF EXPECTED RESPONSE
1053	004444	006722				DLYCNT			: POINTER TO MAP LOCATION TO BE FILLED
1054	004446	000				.BYTE	0		: MASK OF INVALID BITS FOR THIS PARAMETER
1055	004447	001				.BYTE	1		: NUMBER OF PARAMETERS TO STORE
1056	004450	012700	000300		18\$:	MOV	#300, RO		: PREPARE TO CLEAR THE FLOATING
1057	004454	012701	000302			MOV	#302, R1		: VECTOR AREA. 300-776
1058	004460	010120			4\$:	MOV	R1 (R0)+		: START PUTTING "PC+2 - HALT"
1059	004462	005021				CLR	(R1)+		: IN VECTOR AREA.
1060	004464	022021				CMP	(R0)+, (R1)+		: POP POINTERS
1061	004466	022700	001000			CMP	#1000, RO		: ALL DONE??
1062	004472	001372				BNE	4\$: BR IF NO.
1063									
1064									
1065									
1066									
1067	004474	012706	001120			.BEGIN: MOV	#STACK, SP		: SET UP STACK
1068	004500	106427	000340			MTPS	#PR7		: LOCK OUT INTERRUPTS
1069	004504	005737	000042			TST	Q#42		: IS PROGRAM UNDER MONITOR CONTROL
1070	004510	001015				BNE	2\$: BR IF YES
1071	004512	032777	000004	174440		BIT	#BIT2, QSWR		: CHECK FOR LOCK ON TEST
1072	004520	001406				BEG	1\$: BR IF NO LOCK DESIRED.
1073	004522	104402	010216			TYPE	MLOCK		: TYPE LOCK SELECTED.
1074	004526	012737	000240	005010		MOV	#NOP, TTST		: ADJUST SCOPE ROUTINE.
1075	004534	000403				BR	2\$: CONTINUE ALONG.
1076	004536	013737	005232	005010	1\$:	MOV	BRW, TTST		: PREPARE NORMAL SCOPE ROUTINE
1077	004544	012737	011070	001126	2\$:	MOV	#CYCLE, \$LPADR		: START AT "CYCLE" FIND WHICH DEVICE TO TEST
1078	004552	104402	010107			TYPE	MR		: TYPE "RUNNING"
1079	004556	000177	174344			JMP	Q\$LPADR		: START TESTING

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004562
004562 000004
004564 005037 001136
004570 105037 001123
004574 104402 010063
004600 104402 010245
004604 104412 004742
004610 104402 010253
004614 104412 004750
004620 005237 001242
004624 104402 010261
004630 104412 004756
004634 005337 001242
004640 104402 010272
004644 104412 004764
004650 105337 001411
004654 001030
004656 113737 001410 001411
004664 005037 001226
004670 005237 001242
004674 042737 100000 001242
004702 005327
004704 000001
004706 003013
004710 012737
004712 000001
004714 004704
004716 013700 000042
004722 001405
004724 000005
004726 004710
004730 000240
004732 000240
004734 000240
004736
004736 000137
004740 011070
004742 000001
004744 006 002
004746 002042
004750 000001
004752 003 002
004754 002072

```

;END OF PASS
;TYPE NAME OF TEST
;UPDATE PASS COUNT
;CHECK FOR EXIT TO ACT-11
;RESTART TEST
.SBTTL END OF PASS ROUTINE

;*****
;INCREMENT THE PASS NUMBER ($PASS)
;IF THERES A MONITOR GO TO IT
;IF THERE ISN'T JUMP TO CYCLE

$EOP:
SCOPE
CLR $ERRPC ;CLEAR LAST ERROR PC
CLRB $ERFLG ;CLEAR ERROR FLAG
TYPE ,MEPASS ;TYPE END PASS
TYPE ,MCSRX ;TYPE CSR
CNVRT ,XCSR ;SHOW IT
TYPE ,MVECX ;TYPE VECTOR
CNVRT ,XVEC ;SHOW IT
INC $PASS ;RAISE PASS COUNT
TYPE ,MPASSX ;TYPE PASSES
CNVRT ,XPASS ;SHOW IT
DEC $PASS ;RESTORE PASS COUNT
TYPE ,MERRX ;TYPE ERRORS
CNVRT ,XERR ;SHOW IT
DECB $AVNUM ;ARE ALL DEVICES TESTED?
BNE $DOAGN ;BR IF NO.
MOVB DZNUM, SAVNUM ;RESTORE THE COUNT
CLR $TIMES ;ZERO THE NUMBER OF ITERATIONS
INC $PASS ;INCREMENT THE PASS NUMBER
BIC #100000, $PASS ;DON'T ALLOW A NEG. NUMBER
DEC (PC)+ ;LOOP?

$EOPCT: .WORD 1
BGT $DOAGN ;YES
MOV (PC)+, 2(PC)+ ;RESTORE COUNTER

$ENDCT: .WORD 1

$GET42: MOV 2*42, R0 ;GET MONITOR ADDRESS
BEQ $DOAGN ;BRANCH IF NO MONITOR
RESET ;CLEAR THE WORLD
$ENDAD: JSR PC, (R0) ;GO TO MONITOR
NOP ;SAVE ROOM
NOP ;FOR
NOP ;ACT11

$DOAGN:

$SRTNAD: JMP 2(PC)+ ;RETURN
.WORD CYCLE

XCSR: 1
.BYTE 6, 2
DZCSR

XVEC: 1
.BYTE 3, 2
DZRIV
```

1136 004756 000001
 1137 004760 006 002
 1138 004762 001242
 1139 004764 000001
 1140 004766 006 002
 1141 004770 001132
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 1158 004772
 1159 004772 004737 007360
 1160 004776 005037 001136
 1161 005002 022716 012376
 1162 005006 001413
 1163 005010 000406
 1164 005012 105777 174146
 1165 005016 100067
 1166 005020 017766 174142 177776
 1167 005026 032777 040000 174124
 1168 005034 001060
 1169
 1170 005036 000416
 1171
 1172 005040 013746 000004
 1173 005044 012737 005064 000004
 1174 005052 005737 177060
 1175 005056 012637 000004
 1176 005062 000436
 1177 005064 022626
 1178 005066 012637 000004
 1179 005072 000441
 1180 005074
 1181 005074 105737 001123
 1182 005100 001404
 1183 005102 105037 001123
 1184 005106 005037 001226
 1185 005112 032777 004000 174040
 1186 005120 001011
 1187 005122 005737 001242
 1188 005126 001406
 1189 005130 005237 001124
 1190 005134 023737 001226 001124
 1191 005142 002015

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XPASS: 1
        .BYTE 6,2
        $PASS
XERR: 1
        .BYTE 6,2
        $ERTTL
;SCOPE LOOP AND ITERATION HANDLER
-----
.SBTTL SCOPE HANDLER ROUTINE
;*****
;THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
;AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
;AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
;THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
;SW14=1 LOOP ON TEST
;SW11=1 INHIBIT ITERATIONS
;CALL
;* SCOPE ;;SCOPE=IOT

$SCOPE:
.SCOPE: JSR PC,SERV.G ;FIND OUT IF <IG> WAS HIT
        CLR $ERRPC ;CLEAR LAST ERROR PC
        CMP #TST1+2,(SP) ;IS THIS THE SCOPE AT THE BEGINNING OF TST1?
        BEQ $XTSTR ;IF SO, DON'T LOOP ON IT
TTST: BR 1$ ;GOTO 1$ (IF LOCK SW02=1; THIS LOC =240)
        TSTB $STKS ;KEYBOARD DONE?
        BPL $OVER ;BR IF NO. (LOCK: HIT KEY TO GOTO NEXT TEST)
        MOV $STKB,-2(SP) ;CLEAR DONE BIT
1$: BIT #BIT14,$SWR ;LOOP ON PRESENT TEST?
        BNE $OVER ;YES IF SW14=1
;*****START OF CODE FOR THE XOR TESTER*****
$XTSTR: BR 6$
        MOV @#ERRVEC,-(SP) ;IF RUNNING ON THE "XOR" TESTER CHANGE
        MOV #5$,@#ERRVEC ;THIS INSTRUCTION TO A "NOP" (NOP=240)
        TST @#177060 ;SAVE THE CONTENTS OF THE ERROR VECTOR
        MOV (SP)+,@#ERRVEC ;SET FOR TIMEOUT
        BR $SVLAD ;TIME OUT ON XOR?
        CMP (SP)+,(SP)+ ;RESTORE THE ERROR VECTOR
        MOV (SP)+,@#ERRVEC ;GO TO THE NEXT TEST
        BR $OVER ;CLEAR THE STACK AFTER A TIME OUT
5$: ;RESTORE THE ERROR VECTOR
        BR $OVER ;LOOP ON THE PRESENT TEST
6$;*****END OF CODE FOR THE XOR TESTER*****
2$: TSTB $ERFLG ;HAS AN ERROR OCCURRED?
        BEQ 3$ ;BR IF NO
4$: CLRB $ERFLG ;ZERO THE ERROR FLAG
        CLR $TIMES ;CLEAR THE NUMBER OF ITERATIONS TO MAKE
3$: BIT #BIT11,$SWR ;INHIBIT ITERATIONS?
        BNE 1$ ;BR IF YES
        TST $PASS ;IF FIRST PASS OF PROGRAM
        BEQ 1$ ;INHIBIT ITERATIONS
        INC $ICNT ;INCREMENT ITERATION COUNT
        CMP $TIMES,$ICNT ;CHECK THE NUMBER OF ITERATIONS MADE
        BGE $OVER ;BR IF MORE ITERATION REQUIRED

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1192 005144 012737 000001 001124 1S:  MOV  #1,$ICNT      ; REINITIALIZE THE ITERATION COUNTER
1193 005152 013737 005234 001226      MOV  $MXCNT,$TIMES ; SET NUMBER OF ITERATIONS TO DO
1194 005160 105237 001122      $SVLAD: INCB $STNM      ; COUNT TEST NUMBERS
1195 005164 113737 001122 00124C      MOV  $STNM,$TESTN  ; SET TEST NUMBER IN APT MAILBOX
1196 005172 011637 001126      MOV  (SP),$LPADR   ; SAVE SCOPE LOOP ADDRESS
1197 005176 013777 001122 173756 $OVER: MOV  $STNM,$DISPLAY ; DISPLAY TEST NUMBER
1198 005204 013716 001126      MOV  $LPADR,(SP)  ; FUDGE RETURN ADDRESS
1199 005210 105037 001417 3S:   CLRB  MNTFLG      ; CLEAR THE MAINTENANCE BIT SETTER AFTER EACH TEST
1200 005214 005737 001370      TST  MODE         ; HAS THE MODE BEEN CHANGED?
1201 005220 001003      BNE  #5           ; IF NOT INTERNAL, GO DO A TEST
1202 005222 112737 000010 001417      MOV  #MAINT,MNTFLG ; IF INTERNAL MODE NOW, SET THE MAINTENANCE BIT
1203 005230 000002      4S:   RTI         ; GO DO THE TEST
1204 005232 000406      BRW:  406
1205 005234 000005      $MXCNT: 5          ; MAX. NUMBER OF ITERATIONS
1206
1207      ; CHECK FOR FREEZE ON CURRENT DATA
1208      ; -----
1209
1210 005236 032777 001000 173714 .SCOPI: BIT  #SW09,$SWR  ; IS SW09=1(SET)?
1211 005244 001405      BEQ  #1           ; BR IF NOT SET.
1212 005246 005737 001362      TST  LOCK        ; IS THER A TIGHT LOOP SPECIFIED?
1213 005252 001402      BEQ  #1           ; IF NO, RETURN
1214 005254 013716 001362      MOV  LOCK,(SP)   ; IF YES, GOTO THE ADDRESS IN LOCK.
1215 005260 000002      1S:   RTI         ; GO BACK.
1216
1217 005262 032777 010000 173670 .TYPE:  BIT  #SW12,$SWR  ; INHIBIT ALL PRINTOUT??
1218 005270 001403      BEQ  #1           ; IF NOT, GO TYPE
1219 005272 062716 000002      ADD  #2,(SP)     ; SKIP OVER MESSAGE POINTER
1220 005276 000002      RTI             ; RETURN TO WHERE PROCEDURE WAS INVOKED
1221 005300
1222      1S:
1223      .SBTTL  TYPE ROUTINE
1224
1225      ; *****
1226      ; *ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
1227      ; *THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
1228      ; *NOTE1:      $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
1229      ; *NOTE2:      $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
1230      ; *NOTE3:      $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
1231      ; *
1232      ; *CALL:
1233      ; *1) USING A TRAP INSTRUCTION
1234      ; *      TYPE      ,MESADR      ; ; MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
1235      ; *OR
1236      ; *      TYPE
1237      ; *      MESADR
1238      ; *
1239 005300 105737 001177      $TYPE: TSTB  $TPFLG   ; IS THERE A TERMINAL?
1240 005304 100002      BPL  #1           ; BR IF YES
1241 005306 000000      HALT             ; HALT HERE IF NO TERMINAL
1242 005310 000430      BR  #3           ; LEAVE
1243 005312 010046      1S:   MOV  RO,-(SP)  ; SAVE RO
1244 005314 017600 000002      MOV  #2(SP),RO   ; GET ADDRESS OF ASCIZ STRING
1245 005320 122737 000001 001254      CMPB #APTENV,$ENV ; RUNNING IN APT MODE
1246 005326 001011      BNE  #2S         ; NO, GO CHECK FOR APT CONSOLE
1247 005330 132737 000100 001255      BITB #APTPOOL,$ENVM ; SPOOL MESSAGE TO APT

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1248 005336 001405          BEQ      62$      ; NO GO CHECK FOR CONSOLE
1249 005340 010037 005350    MOV      RD,61$   ; SETUP MESSAGE ADDRESS FOR APT
1250 005344 004737 005570    JSR      PC,$ATY3 ; SPOOL MESSAGE TO APT
1251 005350 000000          .WORD    0        ; MESSAGE ADDRESS
1252 005352 132737 000040 001255 61$: BITB    #APTCSUP,$ENV ; APT CONSOLE SUPPRESSED
1253 005360 001003          BNE      60$      ; YES, SKIP TYPE OUT
1254 005362 112046          MOVB    (RO)+,-(SP) ; PUSH CHARACTER TO BE TYPED ONTO STACK
1255 005364 001005          BNE      4$       ; BR IF IT ISN'T THE TERMINATOR
1256 005366 005726          TST     (SP)+     ; IF TERMINATOR POP IT OFF THE STACK
1257 005370 012600          MOV      (SP)+,RO ; RESTORE RO
1258 005372 062716 000002 3$: ADD    #2,(SP)  ; ADJUST RETURN PC
1259 005376 000002          RTI                     ; RETURN
1260 005400 122716 000011 4$: CMPB   #HT,(SP)  ; BRANCH IF <HT>
1261 005404 001430          BEQ      8$       ;
1262 005406 122716 000200    CMPB   $CRLF,(SP) ; ; BRANCH IF NOT <CRLF>
1263 005412 001006          BNE      5$       ;
1264 005414 005726          TST     (SP)+     ; ; POP <CR><LF> EQUIV
1265 005416 104402          TYPE                    ; ; TYPE A CR AND LF
1266 005420 001231          $CRLF
1267 005422 105037 005556    CLRB   $CHARCNT   ; CLEAR CHARACTER COUNT
1268 005426 000755          BR      2$       ; GET NEXT CHARACTER
1269 005430 004737 005512 5$: JSR    PC,$TYPEC ; GO TYPE THIS CHARACTER
1270 005434 123726 001176 6$: CMPB   $FILLC,(SP)+ ; IS IT TIME FOR FILLER CHARS.?
1271 005440 001350          BNE      2$       ; IF NO GO GET NEXT CHAR.
1272 005442 013746 001174    MOV     $NULL,-(SP) ; GET # OF FILLER CHARS. NEEDED
1273                                     ; AND THE NULL CHAR.
1274 005446 105366 000001 7$: DECB   1(SP)    ; DOES A NULL NEED TO BE TYPED?
1275 005452 002770          BLT     6$       ; BR IF NO--GO POP THE NULL OFF OF STACK
1276 005454 004737 005512    JSR     PC,$TYPEC ; GO TYPE A NULL
1277 005460 105337 005556    DECB   $CHARCNT   ; DO NOT COUNT AS A COUNT
1278 005464 000770          BR      7$       ; LOOP
1279
1280                                     ; HORIZONTAL TAB PROCESSOR
1281
1282 005466 112716 000040 8$: MOVB   #' ,(SP)  ; REPLACE TAB WITH SPACE
1283 005472 004737 005512 9$: JSR    PC,$TYPEC ; TYPE A SPACE
1284 005476 132737 000007 005556 BITB   #7,$CHARCNT ; BRANCH IF NOT AT
1285 005504 001372          BNE      9$       ; TAB STOP
1286 005506 005726          TST     (SP)+     ; POP SPACE OFF STACK
1287 005510 000724          BR      2$       ; GET NEXT CHARACTER
1288 005512 105777 173452 $TYPEC: TSTB  #STPS  ; WAIT UNTIL PRINTER IS READY
1289 005516 100375          BPL     $TYPEC
1290 005520 116677 000002 173444 MOVB   2(SP),#STPB ; LOAD CHAR TO BE TYPED INTO DATA REG.
1291 005526 122766 000015 000002 CMPB   #CR,2(SP)  ; IS CHARACTER A CARRIAGE RETURN?
1292 005534 001003          BNE      1$       ; BRANCH IF NO
1293 005536 105037 005556    CLRB   $CHARCNT   ; YES--CLEAR CHARACTER COUNT
1294 005542 000406          BR      $TYPEX
1295 005544 122766 000012 000002 1$: CMPB   #LF,2(SP) ; IS CHARACTER A LINE FEED?
1296 005552 001402          BEQ     $TYPEX    ; BRANCH IF YES
1297 005554 105227          INCB   (PC)+     ; COUNT THE CHARACTER
1298 005556 000000 $CHARCNT: .WORD 0 ; CHARACTER COUNT STORAGE
1299 005560 000207 $TYPEX: RTS    PC
1300
1301 .SBTTL APT COMMUNICATIONS ROUTINE
1302
1303 ;*****

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1304 005562 112737 000001 006026 SATY1:  MOVB  #1,$FFLG  ;; TO REPORT FATAL ERROR
1305 005570 112737 000001 006024 SATY3:  MOVB  #1,$MFLG  ;; TO TYPE A MESSAGE
1306 005576 000403                BR      SATYC
1307 005600 112737 000001 006026 SATY4:  MOVB  #1,$FFLG  ;; TO ONLY REPORT FATAL ERROR
1308 005606                SATYC:
1309 005606 010046                MOV    RO,-(SP)  ;; PUSH RO ON STACK
1310 005610 010146                MOV    R1,-(SP)  ;; PUSH R1 ON STACK
1311 005612 105737 006024                TSTB  $MFLG      ;; SHOULD TYPE A MESSAGE?
1312 005616 001450                BEQ   $S         ;; IF NOT: BR
1313 005620 122737 000001 001254                CMPB  #APTENV,$ENV  ;; OPERATING UNDER APT?
1314 005626 001031                BNE   $S         ;; IF NOT: BR
1315 005630 132737 000100 001255                BITB  #APTSPOOL,$ENVM  ;; SHOULD SPOOL MESSAGES?
1316 005636 001425                BEQ   $S         ;; IF NOT: BR
1317 005640 017600 000004                MOV    24(SP),RO  ;; GET MESSAGE ADDR.
1318 005644 062766 000002 000004                ADD    #2,4(SP)  ;; BUMP RETURN ADDR.
1319 005652 005737 001234                15:    TST  $MSGTYPE  ;; SEE IF DONE W/ LAST XMISSION?
1320 005656 001375                BNE   $S         ;; IF NOT: WAIT
1321 005660 010037 001250                MOV    RO,$MSGAD  ;; PUT ADDR IN MAILBOX
1322 005664 105720                25:    TSTB  (RO)+    ;; FIND END OF MESSAGE
1323 005666 001376                BNE   $S         ;;
1324 005670 163700 001250                SUB    $MSGAD,RO  ;; SUB START OF MESSAGE
1325 005674 006200                ASR   RO         ;; GET MESSAGE LNTH IN WORDS
1326 005676 010037 001252                MOV    RO,$MSGLGT  ;; PUT LENGTH IN MAILBOX
1327 005702 012737 000004 001234                MOV    #4,$MSGTYPE  ;; TELL APT TO TAKE MSG.
1328 005710 000413                BR    $S         ;;
1329 005712 017637 000004 005736 35:    MOV    24(SP),4S  ;; PUT MSG ADDR IN JSR LINKAGE
1330 005720 062766 000002 000004                ADD    #2,4(SP)  ;; BUMP RETURN ADDRESS
1331 005726 013746 177776                MOV    177776,-(SP)  ;; PUSH 177776 ON STACK
1332 005732 004737 005300                JSR   PC,$TYPE  ;; CALL TYPE MACRO
1333 005736 000000                45:    .WORD  0
1334 005740                55:
1335 005740 105737 006026                105:   TSTB  $FFLG      ;; SHOULD REPORT FATAL ERROR?
1336 005744 001416                BEQ   $S         ;; IF NOT: BR
1337 005746 005737 001254                TST  $ENV        ;; RUNNING UNDER APT?
1338 005752 001413                BEQ   $S         ;; IF NOT: BR
1339 005754 005737 001234                115:  TST  $MSGTYPE  ;; FINISHED LAST MESSAGE?
1340 005760 001375                BNE   $S         ;; IF NOT: WAIT
1341 005762 017637 000004 001236                MOV    24(SP),$FATAL  ;; GET ERROR #
1342 005770 062766 000002 000004                ADD    #2,4(SP)  ;; BUMP RETURN ADDR.
1343 005776 005237 001234                INC   $MSGTYPE  ;; TELL APT TO TAKE ERROR
1344 006002 105037 006026                125:  CLRB  $FFLG      ;; CLEAR FATAL FLAG
1345 006006 105037 006025                CLRB  $LFLG      ;; CLEAR LOG FLAG
1346 006012 105037 006024                CLRB  $MFLG      ;; CLEAR MESSAGE FLAG
1347 006016 012601                MOV    (SP)+,R1  ;; POP STACK INTO R1
1348 006020 012600                MOV    (SP)+,RO  ;; POP STACK INTO RO
1349 006022 000207                RTS   PC         ;; RETURN
1350 006024 000                $MFLG: .BYTE  0  ;; MESSG. FLAG
1351 006025 000                $LFLG: .BYTE  0  ;; LOG FLAG
1352 006026 000                $FFLG: .BYTE  0  ;; FATAL FLAG
1353 006030                .EVEN
1354 000200                APTSIZE=200
1355 000001                APTENV=001
1356 000100                APTSPOOL=100
1357 000040                APTCSUP=040
1358
1359                ;STRING INPUT ROUTINE
    
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1360
1361
1362 006030 010346 .INSTR: MOV R3,-(SP) ;SAVE R3 ON STACK
1363 006032 010446 MOV R4,-(SP) ;SAVE R4 ON STACK
1364 006034 017637 000004 006052 MOV #4(SP),MSG ;GET THE ADDRESS OF THE MESSAGE TO BE PRINTED
1365 006042 062766 000002 000004 ADD #2,4(SP) ;POINT TO INSTRUCTION AFTER ADDRESS POINTER
1366 006050 104402 .INST1: TYPE ;PRINT THE MESSAGE
1367 006052 000000 .MSG: 0 ;MESSAGE IS POINTED TO FROM HERE
1368 006054 012704 010620 MOV #INBUF,R4 ;POINT R4 TO THE INPUT BUFFER
1369 006060 012703 000007 MOV #7,R3 ;SET THE MAXIMUM NUMBER OF CHARACTERS ALLOWED
1370 006064 105777 173074 1$: TSTB #1KS ;HAS A CHARACTER BEEN RECEIVED?
1371 006070 100375 BPL 1$ ;IF NO, KEEP WAITING FOR IT
1372 006072 117714 173070 MOVB #1KB,(R4) ;IF YES, SAVE IT IN THE INPUT BUFFER
1373 006076 142714 000200 BICB #200,(R4) ;KEEP ONLY THE 7-BIT ASCII INFORMATION
1374 006102 122427 000015 CMPB (R4)+,#15 ;IS THIS CHARACTER A LINE FEED?
1375 006106 001417 BEQ INSTR2 ;IF SO, TERMINATE THE INPUT SEQUENCE
1376 006110 105777 173054 2$: TSTB #1PS ;IF NOT, CHECK TO SEE IF THE CHARACTER CAN PRINT
1377 006114 100375 BPL 2$ ;IF WE CAN'T, WAIT UNTIL WE CAN
1378 006116 017777 173044 173045 MOV #1KB,#1PB ;ECHO THE CHARACTER BACK
1379 006124 005303 DEC R3 ;REDUCE THE NUMBER OF CHARACTERS RECEIVED
1380 006126 001356 BNE 1$ ;IF WE DON'T HAVE 7, GO GET SOME MORE
1381 006130 012604 MOV (SP)+,R4 ;IF WE HAVE 7, RESTORE R4
1382 006132 012603 MOV (SP)+,R3 ;RESTORE R3
1383 006134 010346 .INSTE: MOV R3,-(SP) ;SAVE R3 ON THE STACK
1384 006136 010446 MOV R4,-(SP) ;SAVE R4 ON THE STACK
1385 006140 104402 00123C TYPE ,QUES ;PRINT A QUESTION MARK... WHAT'S GOING ON?
1386 006144 000741 BR .INST1 ;GO PRINT THE MESSAGE AGAIN
1387 006146 012604 INSTR2: MOV (SP)+,R4 ;RESTORE R4
1388 006150 012603 MOV (SP)+,R3 ;RESTORE R3
1389 006152 000002 RTI ;RETURN TO THE MAIN PROCEDURE
1390
1391 ;CONVERT ASCII STRING TO OCTAL
1392
1393
1394 006154 010546 .PARAM: MOV R5,-(SP) ;SAVE R5 ON THE STACK
1395 006156 010446 MOV R4,-(SP) ;SAVE R4 ON THE STACK
1396 006160 016605 000004 MOV #4(SP),R5 ;GET THE SETUP INFORMATION POINTER
1397 006164 012537 006344 MOV (R5)+,LOLIM ;SET THE LOW LIMIT FOR THE INPUT
1398 006170 012537 006346 MOV (R5)+,HILIM ;SET THE HIGH LIMIT FOR THE INPUT
1399 006174 012537 006350 MOV (R5)+,DEVADR ;SAVE THE ADDRESS WHERE THE RESULT WILL BE STORED
1400 006200 112537 006352 MOVB (R5)+,LOBITS ;GET THE MASK OF THE INCORRECT BITS
1401 006204 112537 006353 MOVB (R5)+,ADRCNT ;GET THE COUNT OF ITEMS TO BE STORED
1402 006210 010566 000004 MOV R5,#4(SP) ;POINT TO WHERE MAIN LINE PROGRAM WILL RESUME
1403 006214 005005 PARAM1: CLR R5 ;INITIALIZE THE ASCII TO OCTAL RESULT WORD
1404 006216 012704 010620 MOV #INBUF,R4 ;POINT TO THE INPUT BUFFER
1405 006222 122714 000015 CMPB #15,(R4) ;IS THIS CHARACTER A CARRIAGE RETURN?
1406 006226 001420 BEQ PARERR ;IF SO, PRINT THE MESSAGE AGAIN
1407 006230 121427 000060 1$: CMPB (R4),#60 ;IS THIS CHARACTER BELOW THE NUMERIC RANGE?
1408 006234 002415 BLT PARERR ;IF SO, GO PRINT THE MESSAGE AGAIN
1409 006236 121427 000067 CMPB (R4),#67 ;IS THIS CHARACTER ABOVE THE NUMERIC RANGE?
1410 006242 003012 BGT PARERR ;IF SO, GO PRINT THE MESSAGE AGAIN
1411 006244 142714 000060 BICB #60,(R4) ;ISOLATE THE NUMBER THE CHARACTER REPRESENTS
1412 006250 152405 BISB (R4)+,R5 ;CONCATENATE THESE BITS TO THE ALREADY EXISTING STRING
1413 006252 122714 000015 CMPB #15,(R4) ;IS THE NEXT CHARACTER A CARRIAGE RETURN?
1414 006256 001406 BEQ LIMITS ;IF SO, GO SEE IF NUMBER IS WITHIN LIMITS
1415 006260 006305 ASL R5 ;CLEAR BIT POSITION 0, MOVE EXISTING STRING TO LEFT
    
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1416 006262 006305      ASL      R5      ;CLEAR POSITION 1, MOVE STRING TO LEFT AGAIN
1417 006264 006305      ASL      R5      ;MOVE THE STRING ONE MORE TIME TO MAKE ROOM FOR
1418                                     ;NEXT THREE BITS
1419 006266 000760      BR        1$      ;GO GET THE NEXT CHARACTER
1420 006270 104404      PARERR: INSTER   ;THERE WAS AN ERROR... GO PRINT MESSAGE AGAIN
1421 006272 000750      BR        PARAM1 ;TRY GETTING THE PARAMETERS AGAIN
1422
1423                                     ;TEST TO SEE IF NUMBER IS WITHIN LIMITS
1424 -----
1425
1426 006274 020537 006346      LIMITS: CMP      R5,HILIM ;DOES RESULT EXCEED ITS MAXIMUM CORRECT VALUE?
1427 006300 101373      BHI      PARERR   ;IF YES, GO PRINT THE MESSAGE AGAIN
1428 006302 020537 006344      CMP      R5,LOLIM ;IS THE RESULT LOWER THAN ALLOWED?
1429 006306 103770      BLO      PARERR   ;IF YES, GO PRINT THE MESSAGE AGAIN
1430 006310 133705 006352      BITB    LOBITS,R5 ;ARE ANY INCORRECT BITS SET IN THE RESULT?
1431 006314 001365      BNE      PARERR   ;IF SO, GO PRINT THE MESSAGE AGAIN
1432
1433                                     ;STORE NUMBER AT SPECIFIED ADDRESS
1434
1435 006316 013704 006350      1$:      MOV      DEVAOR,R4 ;POINT TO THE LOCATION WHERE THE RESULT WILL BE STORED
1436 006322 010524      MOV      R5,(R4)+ ;STORE THE RESULT
1437 006324 062705 000002      ADD     #2,R5     ;CALCULATE THE NEXT DATUM
1438 006330 105337 006353      DECB   ADCNT     ;REDUCE COUNT OF STORED RESULTS. IS IT EXCEEDED?
1439 006334 001372      BNE      1$      ;IF NOT, GO STORE THE NEXT DATUM
1440 006336 012604      MOV     (SP)+,R4 ;RESTORE R4
1441 006340 012605      MOV     (SP)+,R5 ;RESTORE R5
1442 006342 000002      RTI                    ;RETURN TO THE MAIN PROGRAM
1443
1444 006344 000000      LOLIM:  0          ;LOWEST ACCEPTABLE VALUE
1445 006346 000000      HILIM:  0          ;HIGHEST ACCEPTABLE
1446 006350 000000      DEVAOR: 0          ;LOCATION WHERE RESULT WILL BE STORED
1447 006352      000          ;INCORRECT BITS MASK
1448 006353      000          ;COUNT OF ITEMS TO BE STORED
1449
1450                                     ;SAVE PC OF TEST THAT FAILED AND R0-R5
1451 -----
1452
1453 006354 016637 000004 001402 .SAV05: MOV     4(SP),SAVPC ;SAVE R7 (PC)
1454
1455                                     ;SAVE R0-R5
1456
1457 006362 010537 001214      SV05:  MOV     R5,$REG5 ;SAVE R5
1458 006366 010437 001212      MOV     R4,$REG4 ;SAVE R4
1459 006372 010337 001210      MOV     R3,$REG3 ;SAVE R3
1460 006376 010237 001206      MOV     R2,$REG2 ;SAVE R2
1461 006402 010137 001204      MOV     R1,$REG1 ;SAVE R1
1462 006406 010037 001202      MOV     R0,$REG0 ;SAVE R0
1463 006412 000002      RTI                    ;LEAVE.
1464
1465                                     ;RESTORE R0-R5
1466
1467 006414 013700 001202      .RES05: MOV     $REG0,R0 ;RESTORE R0
1468 006420 013701 001204      MOV     $REG1,R1 ;RESTORE R1
1469 006424 013702 001206      MOV     $REG2,R2 ;RESTORE R2
1470 006430 013703 001210      MOV     $REG3,R3 ;RESTORE R3
1471 006434 013704 001212      MOV     $REG4,R4 ;RESTORE R4

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1528 ; ARGUMENT OF TRAP IS EXTRACTED
1529 ; AND USED AS OFFSET TO OBTAIN POINTER
1530 ; TO SELECTED SUBROUTINE
1531
1532 006630 010046 .TRPSR: MOV RO, -(SP) ; SAVE RO. USE RO TO FIND TRAP ROUTINE
1533 006632 016600 000002 MOV 2(SP), RO ; GET TRAP ADDRESS
1534 006636 005740 TST -(RO) ; GET TRAP
1535 006640 111000 MOV8 (RO), RO ; GET RIGHT BYTE OF TRAP (TRAP OFFSET)
1536 006642 006300 ASL RO ; POSITION OFFSET FOR TABLE INDEXING
1537 006644 016000 002002 MOV .TRPTAB(RO), RO ; PLACE INDEXED ADDRESS OF TABLE IN RO
1538 006650 000200 RTS RO ; TRANSFER TO THAT ADDRESS AND RESTORE OLD RO
1539
1540 ; DEVICE CLEAR ROUTINE
1541 ; ISSUE A DEVICE CLEAR
1542 -----
1543 006652 .DEVICE.CLR:
1544 006652 052777 000020 173162 BIS #DCLR, @DZCSR ; SET DCLR
1545 006660 032777 000020 173154 IS: BIT #DCLR, @DZCSR ; DID IT CLEAR?
1546 006666 001374 BNE IS ; BR IF NO
1547 006670 000002 RTI ; EXIT ROUTINE
1548
1549 ; ROUTINE TO HANDLE MAINTENANCE BIT SETTING WITH DEVICE CLEAR
1550 -----
1551 006672 104413 .DCLASM: DEVICE.CLR ; ISSUE A DEVICE CLEAR
1552 006674 153777 001417 173140 BISB MNTFLG, @DZCSR ; LOAD THE MAINTENANCE BIT IF IT IS I MODE
1553 006702 000002 RTI ; RETURN TO CALLING ROUTINE
1554
1555 .DELAY:
1556 006704 010046 MOV RO, -(SP) ; SAVE RO
1557 006706 013700 006722 MOV DLYCNT, RO ; SET COUNT
1558 006712 005300 IS: DEC RO ; DELAY
1559 006714 001376 BNE IS ;
1560 006716 012600 MOV (SP)+, RO ; RESTORE RO
1561 006720 000002 RTI ; LEAVE ROUTINE
1562 006722 000001 DLYCNT: .WORD 1 ; PATCHABLE LOC FOR MORE TIME
1563
1564 ; ADVANCE TO NEXT TEST HANDLER
1565 -----
1566
1567 006724 013716 001360 .ADVANCE: MOV NEXT, (SP) ; CRUNCH STACK WITH ADDRESS OF SCOPE CALL
1568 006730 005037 001362 CLR LOCK ; RESET TIGHT LOOP ADDRESS
1569 006734 000002 RTI ; CHECK TO SEE IF OLD TEST GETS REPEATED
1570
1571 ; ERROR HANDLER
1572 -----
1573
1574 006736 004737 007360 $ERROR: JSR PC, SERV.G ; FIND OUT IF <IG> WAS HIT
1575 006742 032777 010000 172210 BIT #SW12, @SWR ; BELL ON ERROR?
1576 006750 001406 BEQ XBX ; BR IF NO BELL
1577 006752 105777 172212 TSTB @STPS ; TTY READY.
1578 006756 100003 BPL XBX ; DON'T WAIT IF TTY NOT READY.
1579 006760 112777 000207 172204 MOV8 #207, @STPB ; PUSH A BELL AT THE TTY.
1580 006766 032777 020000 172164 XBX: BIT #SW13, @SWR ; DELETE ERROR PRINT OUT?
1581 006774 001113 BNE HALTS ; BR IF NO PRINT OUT WANTED.
1582 006776 021637 001136 CMP (SP), $ERRPC ; WAS THIS ERROR FOUND LAST TIME?
1583 007002 001404 BEQ IS ; BR IF YES
    
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1584	007004	011637	001136		MOV	(SP), \$ERRPC	: RECORD BEING HERE	
1585	007010	105037	001123		CLRB	\$ERFLG	: PREPARE HEADER	
1586	007014	104407		1S:	SAVOS		: SAVE ALL PROC REGISTERS	
1587	007016	011635			MOV	(SP), R5	: GET THE PC OF ERROR	
1588	007020	162705	000002		SUB	#2, R5	: GET ADDRESS OF TRAP CALL	
1589	007024	011504			MOV	(R5), R4	: GET ERROR INSTRUCTION	
1590	007026	110437	001134		MOVB	R4, \$ITEMB	: COPY TEST NUMBER FOR APT HANDLING	
1591	007032	006304			ASL	R4	: MULT BY TWO	
1592	007034	061504			ADD	(R5), R4	: DOUBLE IT	
1593	007036	006304			ASL	R4	: MULT AGAIN	
1594	007040	042704	177001		BIC	#177001, R4	: CLEAR JUNK	
1595	007044	062704	026460		ADD	\$, ERRTAB, R4	: GET POINTER	
1596	007050	012437	007174		MOV	(R4)+, ERRMSG	: GET ERROR MESSAGE	
1597	007054	012437	007206		MOV	(R4)+, DATAHD	: GET DATA HEADER	
1598	007060	011437	007220		MOV	(R4), DATABP	: GET DATA TABLE	
1599	007064	105737	001123		TSTB	\$ERFLG	: TYPE HEADER	
1600	007070	001403			BEQ	TYPMSG	: BR IF YES	
1601	007072	005737	007220		TST	DATABP	: DOES DATA TABLE EXIST?	
1602	007076	001044			BNE	TYPDAT	: BR IF YES.	
1603	007100	104402	001231	TYPMSG:	TYPE	, \$CRLF	: TYPE A CARRIAGE RETURN	
1604	007104	104402	001231		TYPE	, \$CRLF	: AND TYPE ANOTHER	
1605	007110	005737	001362		TST	LOCK		
1606	007114	001402			BEQ	1S		
1607	007116	104402	010315		TYPE	, MASTEK		
1608	007122	104402	010303	1S:	TYPE	, MTSTN		
1609	007126	104412	007352		CNVRT	, XTSTN	: SHOW IT	
1610	007132	104402	010372		TYPE	, MERRPC	: TYPE PC.	
1611	007136	104412	007344		CNVRT	, ERTABO	: SHOW IT	
1612	007142	104402	010245		TYPE	, MCSRX		
1613	007146	104412	004742		CNVRT	, XCSR		
1614	007152	104402	001231		TYPE	, \$CRLF	: GIVE A CR/LF	
1615	007156	112737	177777	001123	MOVB	#-1, \$ERFLG	: NO MORE HEADER UNLESS NO DATA TABLE.	
1616	007164	005737	007174		TST	ERRMSG	: IS THERE AN ERROR MESSAGE?	
1617	007170	001402			BEQ	WTBS.FM	: BR IF NO.	
1618	007172	104402			TYPE		: TYPE	
1619	007174	000000		ERRMSG: 0			: ERROR MESSAGE	
1620	007176			WTBS.FM:				
1621	007176	005737	007206		TST	DATAHD	: DATA HEADER?	
1622	007202	001402			BEQ	TYPDAT	: BR IF NO	
1623	007204	104402			TYPE		: TYPE	
1624	007206	000000		DATAHD: 0			: DATA HEADER	
1625	007210	005737	007220	TYPDAT:	TST	DATABP	: DATA TABLE?	
1626	007214	001402			BEQ	RESREG	: BR IF NO.	
1627	007216	104411			CONVRT		: SHOW	
1628	007220	000000		DATABP: 0			: DATA TABLE	
1629	007222	104410		RESREG: RES05			: RESTORE PROC REGISTERS	
1630	007224	122737	000001	001254	HALTS:	CMPB	#APTENV, \$ENV	: IS APT RUNNING?
1631	007232	001007			BNE	2S		: SKIP APT CALL IF NOT
1632	007234	113737	001134	007246	MOVB	\$ITEMB, 7S		: COPY ERROR NUMBER
1633	007242	004737	005600		JSR	PC, \$ATY4		: CALL APT SERVICE
1634	007246	000000		7S:	.WORD	0		: ERROR NUMBER STUCK HERE
1635	007250	000777		8S:	BR	8S		: LOCK UP HERE
1636	007252	022737	004726	000042	2S:	CMPL	#SENDAD, 2#42	: CHECK TO SEE IF IN ACT-11 MODE
1637	007260	001403			BEQ	1S		: IF SO, HANDLE ACCORDINGLY
1638	007262	005777	171672		TST	\$SWR		: HALT ON ERROR?
1639	007266	100004			BPL	EXITER		: BR IF NO HALT ON ERROR

1640	007270	016677	000002	171664	1S:	MOV	2(SP), @DISPLAY	: SHOW ERROR PC IN DATA DISPLAY
1641	007276	000000				HALT		: HALT
1642	007300	005237	001132		EXITER:	INC	@ERTTL	: UPDATE ERROR COUNT
1643	007304	032777	000400	171646		BIT	@SW08, @SWR	: GOTO TOP OF TEST?
1644	007312	001007				BNE	1S	: BR IF YES
1645	007314	032777	002000	171636		BIT	@SW10, @SWR	: GOTO NEXT TEST?
1646	007322	001407				BEQ	2S	: BR IF NO
1647	007324	013737	001360	001126		MOV	NEXT, @LPPDR	: SET FOR NEXT TEST
1648	007332	012706	001120		1S:	MOV	@STACK, SP	: RESET SP
1649	007336	000177	171564			JMP	@SLPADR	: GOTO SPECIFIED TEST
1650	007342	000002			2S:	RTI		: RETURN
1651	007344	000001			ERTABO:	1		
1652	007346	006	002			.BYTE	6,2	
1653	007350	001402				SAVPC		
1654	007352	000001			XTSTN:	1		
1655	007354	002	002			.BYTE	2,2	
1656	007356	001122				@STSTNM		
1657	007360	022737	177570	001160	SERV.G:	CMP	@177570, @SWR	: IS THE SWITCH REGISTER HARDWIRED?
1658	007366	001513				BEQ	6S	: IF SO, IGNORE @G
1659	007370	017746	171572			MOV	@STKB, -(SP)	: OTHERWISE, GET THE LAST CHARACTER TYPED
1660	007374	042716	000200			BIC	@BIT7, (SP)	: STRIP PARITY(EIGHTH) BIT
1661	007400	122726	000007			CMPB	@7, (SP)+	: IS IT @G?
1662	007404	001104				BNE	6S	: IF NOT, IGNORE INPUT
1663	007406	032777	004000	171550		BIT	@4000, @STKS	: RX BUSY?
1664	007414	001361				BNE	SERV.G	: BR IF YES
1665	007416	017737	171536	007640		MOV	@SWR, @90S	: SAVE (SWR).
1666	007424	013777	007640	171526	1S:	MOV	@90S, @SWR	
1667	007432	104402	007620			TYPE	@89S	
1668	007436	104412	007632			CNVRT	@88S	
1669	007442	104402	007642			TYPE	@91S	
1670	007446	105777	171512			TSTB	@STKS	: WAIT FOR DONE.
1671	007452	100375				BPL	-4	
1672	007454	017746	171506			MOV	@STKB, -(SP)	
1673	007460	042716	000200			BIC	@BIT7, (SP)	
1674	007464	122726	000015			CMPB	@15, (SP)+	
1675	007470	001450				BEQ	5S	
1676	007472	005077	171462			CLR	@SWR	
1677	007476	105777	171466		2S:	TSTB	@STPS	
1678	007502	100375				BPL	-4	
1679	007504	016677	177776	171460		MOV	-2(SP), @STPB	
1680	007512	000241				CLC		
1681	007514	006177	171440			ROL	@SWR	
1682	007520	006177	171434			ROL	@SWR	
1683	007524	006177	171430			ROL	@SWR	
1684	007530	103735				BCS	1S	: ERROR
1685	007532	026627	177776	000060		CMP	-2(SP), @60	
1686	007540	002731				BLT	1S	
1687	007542	026627	177776	000067		CMP	-2(SP), @67	
1688	007550	003325				BGT	1S	
1689	007552	042766	177770	177776		BIC	@C(7), -2(SP)	
1690	007560	056677	177776	171372		BIS	-2(SP), @SWR	
1691	007566	105777	171372			TSTB	@STKS	
1692	007572	100375				BPL	-4	
1693	007574	017746	171366			MOV	@STKB, -(SP)	
1694	007600	042716	000200			BIC	@BIT7, (SP)	
1695	007604	122726	000015			CMPB	@15, (SP)+	

```

1596 007610 001332
1697 007612 104402 001231
1698 007616 000207
1699
1700 007620 020200 051450 051127 89$: .ASCIZ <200>' (SWR)=/?
1701 007626 036451 000057
1702 .EVEN
1703 007632 000001 88$: 1
1704 007634 006 000 .BYTE 6,0
1705 007636 007640 90$: .WORD 0
1706 007640 000000 91$: .ASCIZ ?/=/?
1707 007642 036457 000057
1708 .EVEN
1709 .SBTTL POWER DOWN AND UP ROUTINES
1710
1711 ::*****
1712 :POWER DOWN ROUTINE
1713 007646 012737 010012 000024 $PWRDN: MOV $SILLUP, @#PWRVEC ;; SET FOR FAST UP
1714 007654 012737 000340 000026 MOV #340, @#PWRVEC+2 ;; PRIO:7
1715 007662 010046 MOV R0, -(SP) ;; PUSH R0 ON STACK
1716 007664 010146 MOV R1, -(SP) ;; PUSH R1 ON STACK
1717 007666 010246 MOV R2, -(SP) ;; PUSH R2 ON STACK
1718 007670 010346 MOV R3, -(SP) ;; PUSH R3 ON STACK
1719 007672 010446 MOV R4, -(SP) ;; PUSH R4 ON STACK
1720 007674 010546 MOV R5, -(SP) ;; PUSH R5 ON STACK
1721 007676 017746 171256 MOV @SWR, -(SP) ;; PUSH @SWR ON STACK
1722 007702 010637 010016 MOV SP, $SAVR6 ;; SAVE SP
1723 007706 012737 007720 000024 MOV $PWRUP, @#PWRVEC ;; SET UP VECTOR
1724 007714 000000 HALT
1725 007716 000776 BR .-2 ;; HANG UP
1726
1727 ::*****
1728 :POWER UP ROUTINE
1729 007720 012737 010012 000024 $PWRUP: MOV $SILLUP, @#PWRVEC ;; SET FOR FAST DOWN
1730 007726 013706 010016 MOV $SAVR6, SP ;; GET SP
1731 007732 005037 010016 CLR $SAVR6 ;; WAIT LOOP FOR THE TTY
1732 007736 005237 010016 1$: INC $SAVR6 ;; WAIT FOR THE INC
1733 007742 001375 BNE 1$ ;; OF WORD
1734 007744 012677 171210 MOV (SP)+, @SWR ;; POP STACK INTO @SWR
1735 007750 012605 MOV (SP)+, R5 ;; POP STACK INTO R5
1736 007752 012604 MOV (SP)+, R4 ;; POP STACK INTO R4
1737 007754 012603 MOV (SP)+, R3 ;; POP STACK INTO R3
1738 007756 012602 MOV (SP)+, R2 ;; POP STACK INTO R2
1739 007760 012601 MOV (SP)+, R1 ;; POP STACK INTO R1
1740 007762 012600 MOV (SP)+, R0 ;; POP STACK INTO R0
1741 007764 012737 007646 000024 MOV $PWRDN, @#PWRVEC ;; SET UP THE POWER DOWN VECTOR
1742 007772 012737 000340 000026 MOV #340, @#PWRVEC+2 ;; PRIO:7
1743 010000 104402 TYPE ;; REPORT THE POWER FAILURE
1744 010002 010020 $PWRMG: .WORD MPFAIL ;; POWER FAIL MESSAGE POINTER
1745 010004 012716 MOV (PC)+, (SP) ;; RESTART AT RESTART
1746 010006 011434 $PWRAD: .WORD RESTART ;; RESTART ADDRESS
1747 010010 000002 RTI
1748 010012 000000 $SILLUP: HALT ;; THE POWER UP SEQUENCE WAS STARTED
1749 010014 000776 BR .-2 ;; BEFORE THE POWER DOWN WAS COMPLETE
1750 010016 000000 $SAVR6: 0 ;; PUT THE SP HERE
1751 010020 050200 051127 043040 MPFAIL: .ASCIZ <200>/PWR FAILED. RESTART AT LAST TEST /

```

```

(2) 010063      200 047105 020104 MEPASS: .ASCIZ <200>/END PASS DZDZA-D /
(2) 010107      200 052522 047116 MR: .ASCIZ <200>/RUNNING /
(2) 010123      200 051120 043517 MERR2: .ASCIZ <200>/PROGRAM INDICATES NO DEVICES PRESENT./
(2) 010172      044600 051516 043125 MERR3: .ASCIZ <200>/INSUFFICIENT DATA!/
(2) 010216      046200 041517 020113 MLOCK: .ASCIZ <200>/LOCK ON SELECTED TEST/
(2) 010245       103 051123 020072 MCSRX: .ASCIZ /CSR: /
(2) 010253       126 041505 020072 MVECX: .ASCIZ /VEC: /
(2) 010261       120 051501 042523 MPASSX: .ASCIZ /PASSES: /
(2) 010272      051105 047522 051522 MERRX: .ASCIZ /ERRORS: /
(2) 010303       :24 051505 020124 NTSTN: .ASCIZ /TEST NO: /
(2) 010315       052 000040 MASTEK: .ASCIZ /* /
(2) 010320      051600 052105 051440 MNEW: .ASCIZ <200>/SET SWITCH REG TO DZ11'S DESIRED ACTIVE./
(2) 010372      041520 020072      000 MERRPC: .ASCIZ /PC: /
(2) 010377       200 040515 020120 XHEAD: .ASCIZ <200>/MAP OF DZ11 STATUS/<200>
(2) 010424      044600 046114 043505 MBADLN: .ASCIZ <200>/ILLEGAL ENTRY IN STAGGERED MODE/<200>

```

```

(2) 010466      000002
1752 010470       006      003
1753 010472      001220
1754 010474       006      002
1755 010476      001222

```

```

.EVEN
XSTATQ: 2
        .BYTE 6,3
        STMP1
        .BYTE 6,2
        STMP2

```

.EVEN

; THIS ROUTINE ESTABLISHES WHICH MAINTENANCE MODE THE DEVICE IS IN

```

1756
1757
1758
1759
1760
1761
1762 010500      017605 000000      .SETFLG: MOV 2(SP), R5 ; PICK UP ADDRESS OF TAG
1763 010504      042737 000040 010620 BIC #40, INBUF ; STRIP LOWER CASE
1764 010512      122737 000105 010620 CMPB #'E, INBUF ; IS IT EXTERNAL LOOP BACK ?
1765 010520      001005 BNE 4$ ; NO
1766 010522      013715 010612 MOV 1$ (R5) ; YES STORE INFO
1767 010526      105037 001417 CLRB MNTFLG ; SET MAINT BIT =0
1768 010532      000422 BR 7$ ; GET OUT
1769 010534      122737 000111 010620 4$: CMPB #'I, INBUF ; IS IT INTERNAL LOOP BACK ?
1770 010542      001006 BNE 5$ ; NO
1771 010544      013715 010614 MOV 2$ (R5) ; YES STORE INFO
1772 010550      112737 000010 001417 MOVB #MAINT, MNTFLG ; SET UP THE MAINTENANCE FLAG LOADER
1773 010556      000410 BR 7$ ; GET OUT
1774 010560      122737 000123 010620 5$: CMPB #'S, INBUF ; IS IT STAGGERED LOOP BACK ?
1775 010566      001007 BNE 6$ ; WHAT ?
1776 010570      013715 010616 MOV 3$ (R5) ; YES STORE INFO
1777 010574      105037 001417 CLRB MNTFLG ; ZERO BITS
1778 010600      062716 000002 7$: ADD #2, (SP) ; POP AROUND
1779 010604      000002 RTI
1780 010606      104404 6$: INSTER ; RETRY
1781 010610      000733 BR .SETFLG ; DITTO
1782 010612      000200 1$: .WORD 200 ; EXTERNAL = E
1783 010614      000000 2$: .WORD 0 ; INTERNAL = I
1784 010616      100000 3$: .WORD 100000 ; STAGGERED = S

```

; BUFFERS FOR INPUT-OUTPUT

```

1785
1786
1787
1788 010620      000000 INBUF: 0
1789          010662 . = +40
1790 010662      000000 TEMP: 0

```

```

1791          010724          . = +40
1792 010724 000000          MDATA: 0
1793          010766          . = +40
1794
1795 010766 011637 011064   SET.PS: MOV      (SP), 3$
1796 010772 162737 000002 011064   SUB      #2, 3$
1797 011000 017737 000060 011066   MOV      23$, 4$
1798 011006 022737 106427 011066   CMP      #106427, 4$
1799 011014 001003          BNE      1$
1800 011016 011637 011064   MOV      (SP), 3$
1801 011022 000412          BR       2$
1802 011024 022737 106437 011066 1$:   CMP      #106437, 4$
1803 011032 001401          BEQ      .+4
1804 011034 000000          HALT
1805 011036 011637 011064   MOV      (SP), 3$
1806 011042 017737 000016 011064   MOV      23$, 3$
1807 011050 062716 000002 2$:   ADD      #2, (SP)
1808 011054 017766 000004 000002   MOV      23$, 2(SP)
1809 011062 000002          RTI
1810 011064 000000          3$:   0
1811 011066 000000          4$:   0

```

;RESERVED INSTRUCTION NOT "MTPS"


```

1812
1813
1814
1815
1816
1817
1818
1819
1820
1821 011070 005737 001404          CYCLE: TST      DZACTV      ;ARE ANY DZ11'S TO BE TESTED?
1822 011074 001004                    BNE      15          ;BR IF OK.
1823 011076 104402 010123          TYPE     ,MERR2     ;NO DZ11'S SELECTED!!
1824 011102 000000                    HALT                                     ;STOP THE SHOW.
1825 011104 000776                    BR      -2          ;DISQUALIFY CONT. SW.
1826 011106 013737 005234 001226 15:  MOV     $MXCNT,$TIMES ;RESTORE THE NUMBER OF ITERATIONS TO MAKE
1827 011114 033737 001406 001404  BIT     RUN,DZACTV  ;IS THIS ONE "ACTIVE"
1828 011122 001020                    BNE     25          ;BR IF GOOD ONE FOUND.
1829 011124 000241                    CLC                                     ;
1830 011126 006137 001406          ROL     RUN         ;UPDATE POINTER
1831 011132 005537 001406          ADC     RUN         ;CATCH CARRY FROM RUN
1832 011136 062737 000014 001412  ADD     #14,ACTIVE  ;UPDATE ADDRESS POINTER.
1833 011144 022737 002000 001412  CMP     #DZ.END,ACTIVE ;HAVE WE PASSED THE END OF THE MAP?
1834 011152 001355                    BNE     15          ;IF NO, KEEP GOING; NOT ALL TESTED FOR.
1835 011154 012737 001500 001412  MOV     #DZ.MAP,ACTIVE ;RESET ADDRESS POINTER.
1836 011162 000751                    BR      15          ;KEEP LOOKING FOR ACTIVE DZ11
1837 011164 000241          25:  CLC                                     ;
1838 011166 006137 001406          ROL     RUN         ;UPDATE POINTER.
1839 011172 005537 001406          ADC     RUN         ;CATCH CARRY.
1840 011176 013700 001412          MOV     ACTIVE,RO   ;GET ADDRESS POINTER.
1841 011202 062737 000014 001412  ADD     #14,ACTIVE  ;UPDATE.
1842 011210 022737 002000 001412  CMP     #DZ.END,ACTIVE ;
1843
1844 011216 001003                    BNE     35          ;ALL DONE?
1845 011220 012737 001500 001412  MOV     #DZ.MAP,ACTIVE ;BR IF NO.
1846 011226 012037 001310          35:  MOV     (RO)+,$BASE ;RESTORE POINTER.
1847 011232 012037 002072          MOV     (RO)+,DZRIV ;LOAD SYSTEM CTRL. REG
1848 011236 012037 026454          MOV     (RO)+,DZPRT ;LOAD VECTOR
1849 011242 117737 026455 001414  MOVB   DZPRT+1,EIAFLG ;LOAD PRIORITY
1850 011250 042737 100000 026454  BIC     #BIT15,DZPRT ;EIA OR 20MA
1851 011256 012037 001364          MOV     (RO)+,LINE  ;CLEAR FLAG
1852 011262 012037 001366          MOV     (RO)+,PAR   ;SET UP LINE DZ LINES ACTIVE
1853 011266 012037 001370          MOV     (RO)+,MODE  ;SET UP PARAMETERIZATION
1854 011272 004737 026246          JSR     PC,DZLEV    ;SET UP MAINTENANCE MODE
1855 011276 005737 000042          TST     #42         ;SET UP
1856 011302 001051                    BNE     45          ;ARE WE UNDER MONITOR CONTROL?
1857 011304 032777 000002 167646  BIT     #SW01,$SWR  ;IF YES, SKIP THIS SETUP
1858 011312 001445                    BEQ     45          ;IF SW01=1, GET STARTING TEST #
1859 011314 104402 001231          75:  TYPE     ,$CRLF    ;BR IF NO TEST IS TO BE INPUTTED
1860 011320 104403                    INSTR
1861 011322 010303                    MTSTN
1862 011324 104405                    PARAM
1863 011326 000001                    1
1864 011330 001000                    1000
1865 011332 001122                    $STNM
1866 011334 000          .BYTE 0
1867 011335 001          .BYTE 1
;CALL THE STRING INPUT ROUTINE
;POINTER TO MESSAGE TO BE PRINTED
;CALL THE OCTAL TO ASCII CONVERT ROUTINE
;LOWEST LEGITIMATE VALUE OF EXPECTED RESPONSE
;HIGHEST LEGITIMATE VALUE OF EXPECTED RESPONSE
;POINTER TO MAP LOCATION TO BE FILLED
;MASK OF INVALID BITS FOR THIS PARAMETER
;NUMBER OF PARAMETERS TO STORE
    
```

MD-11-DZDZA-D MACY11 27(1006) 02-MAR-77 08:23 PAGE 41
 DZDZAD.P11 02-MAR-77 08:20 POWER DOWN AND UP ROUTINES

1868	011336	012700	012374			MOV	#TST1,RO	
1869	011342	022710	000004		5\$:	CMP	#4,(RO)	
1870	011346	001020				BNE	6\$	
1871	011350	022760	012737	000002		CMP	#12737,2(RO)	
1872	011356	001014				BNE	6\$	
1873	011360	023760	001122	000004		CMP	\$TSTNM,4(RO)	; IS THIS THE TEST ?
1874	011366	001010				BNE	6\$; IF NOT, DON'T PROCESS NUMBER
1875	011370	010037	001126			MOV	RO,\$LPADR	; SAVE PC
1876	011374	062737	000002	001126		ADD	#2,\$LPADR	; POP OVER SCOPE
1877	011402	104402	001231			TYPE	\$CRLF	
1878	011406	000412				BR	8\$	
1879	011410	005720			6\$:	TST	(RO)+	
1880	011412	020027	022364			CMP	RO,#TLAST+10	
1881	011416	001351				BNE	5\$	
1882	011420	104402	001230			TYPE	\$QUES	
1883	011424	000733				BR	7\$	
1884	011426	012737	012374	001126	4\$:	MOV	#TST1,\$LPADR	; PREPARE TEST ADDRESS
1885	011434				8\$:			
1886	011434	000177	167466			RESTART: JMP	\$LPADR	; GO START TESTING.***WARNING!***
1887								; THIS JUMP IS USED BY POWER UP ROUTINE!!!!
1888								

```

1889                                     ;--ROUTINE USED TO SET UP THE DIAGNOSTIC VIA APT.
1890                                     ; IF BIT7 IN THE ENVIRONMENT MODE ($ENVM) BYTE IS SET.
1891                                     ; THE PROGRAM WILL LOAD ITS PARAMETERS FROM THE ETABLE.
1892
1893 011440 012700 001500      SETAPT: MOV    #DZ.MAP,R0      ;POINT TO THE DEVICE MAP TABLE
1894 011444 013701 001310      MOV    $BASE,R1      ;BUILD DEVICE ADDRESSES IN R1
1895 011450 013702 001304      MOV    $VECT1,R2     ;BUILD DEVICE VECTORS IN R2
1896 011454 042702 177007      BIC    #1C<770>,R2  ;STRIP AWAY OTHER INFORMATION
1897
1898 011460 113703 001305      MOVB   $VECT1+1,R3   ;LOAD THE INTERRUPT PRIORITY FROM R3
1899 011464 106003              RORB   R3             ;ALIGN THE NUMBER
1900 011466 106003              RORB   R3             ;ALIGN THE NUMBER
1901 011470 106003              RORB   R3             ;ALIGN THE NUMBER
1902 011472 106003              RORB   R3             ;ALIGN THE NUMBER
1903 011474 106003              RORB   R3             ;ALIGN THE NUMBER
1904 011476 042703 177770      BIC    #1C<7>,R3    ;REMOVE ALL BUT BUS LEVEL NUMBER
1905 011502 012704 001320      MOV    #SDOW0,R4    ;POINT TO THE BEGINNING OF DEVICE PARAMETERS
1906 011506 013705 001312      MOV    $DEV0,R5     ;GET THE MAP OF ACTIVE DEVICES
1907 011512 010537 001404      MOV    R5,DZACTV   ;SAVE THE BIT MAP
1908 011516 006005              15:   ROR    R5             ;GET A DEVICE SELECTION BIT
1909 011520 103407              BCS   3$            ;IF IT IS SELECTED, GO SET UP A MAP
1910 011522 001425              BEQ   5$            ;IF NO MORE ARE SELECTED, GET OUT OF SETUP
1911 011524 005724              TST   (R4)+         ;POINT TO NEXT DEVICE DESCRIPTOR
1912 011526 062701 000010      25:   ADD    #10,R1    ;SET UP THE NEXT ADDRESS
1913 011532 062702 000010      ADD    #10,R2       ;SET UP THE NEXT VECTOR GROUP
1914 011536 000767              BR    1$            ;GO SEE IF MORE DEVICES REMAIN
1915 011540 010120              35:   MOV    R1,(R0)+   ;LOAD DEVICE ADDRESS
1916 011542 010220              MOV    R2,(R0)+   ;LOAD THE VECTOR ADDRESS
1917 011544 010320              MOV    R3,(R0)+   ;LOAD THE INTERRUPT PRIORITY LEVEL
1918 011546 013720 001314      MOV    $CDW1,(R0)+ ;GET THE NUMBER OF LINES IN OPERATION
1919 011552 012420              MOV    (R4)+,(R0)+ ;LOAD DEVICE PARAMETERS
1920 011554 100006              BPL   4$            ;IF Z0MA MODE SELECTED, SET IT UP
1921 011556 052760 100000 177772  BIS    #100000,-6(R0) ;SET THE Z0MA FLAG IN DZLVN
1922 011564 042760 100000 177776  BIC    #100000,-2(R0) ;CLEAR THE FLAG IN DZPARM
1923 011572 005020              45:   CLR    (R0)+       ;DEFAULT OPERATION TO INTERNAL MAINTENANCE MODE
1924 011574 000754              BR    2$            ;GO BUILD THE NEXT ADDRESS
1925 011576 012710 177777      55:   MOV    #-1,(R0)   ;TERMINATE THE DEVICE MAP
1926 011602 012737 001256 001160  MOV    #$$SWREG,SWR ;SET TO SOFTWARE APT SWITCH REGISTER
1927 011610 000207              RTS    PC           ;RETURN TO PRINT STATUS TABLE
1928
1929
1930                                     ;*ROUTINE USED TO "AUTO SIZE" THE DZ11
1931                                     ;*CSR AND VECTOR.
1932                                     ;*NOTE: THE CSR MAY BE ANY WHERE IN THE FLOATING
1933                                     ;* ADDRESS RANGE (160000:163700)
1934                                     ;* AND THE VECTOR MAY BE ANY WHERE IN THE
1935                                     ;* FLOATING VECTOR RANGE (300:770)
1936                                     ;*
1937
1938 011612              AUTO.SIZE:
1939 011612 000005              RESET
1940 011614 105337 001415      DECB   INIFLG       ;INSURE A BUS INIT.
1941 011620 012702 001500      CSRMAP: MOV    #DZ.MAP,R2 ;SHOW THAT I WAS HERE
1942 011624 012703 001320      MOV    #SDOW0,R3    ;LOAD MAP POINTER.
1943 011630 005022              15:   CLR    (R2)+       ;POINT TO ETABLE DEVICE DESCRIPTOR WORDS
1944 011632 022702 002000      CMP    #DZ.END,R2   ;ZERO ENTIRE MAP
                                     ;ALL DONE?
    
```

1945	011636	001374			BNE	1\$;BR IF NO
1946	011640	105037	001410		CLRB	DZNUM		;SET OCTAL NUMBER OF DZ11'S TO 0
1947	011644	012702	001500		MOV	#DZ,MAP,R2		
1948	011650	012701	160000		MOV	#160000,R1		;SET FOR FIRST ADDRESS TO BE TESTED
1949	011654	012737	012174	000004	MOV	#6\$,2#4		;SET FOR NON-EXISTENT DEVICE TIME OUT
1950	011662	052711	000040		2\$: BIS	#BIT5,(R1)		;TRY TO SET MASTER SCAN ENABLE
1951	011666	052761	000200	000004	BIS	#BIT7,4(R1)		;TRY TO TRANSMIT ON LINE 7
1952	011674	005000			CLR	RO		;USE RO AS A COUNTER
1953	011676	005711			7\$: TST	(R1)		;HAS TRANSMITTER READY COME UP?
1954	011700	100403			BMI	8\$;IF SO, GO GET A FINAL CHECK
1955	011702	005300			DEC	RO		;REDUCE COUNT. TIME UP?
1956	011704	001374			BNE	7\$;IF NOT, KEEP WAITING
1957	011706	000463			BR	3\$;ASSUME IT'S NOT A DZ11
1958	011710	032761	000200	000004	6\$: BIT	#BIT7,4(R1)		;IS LINE 7 ENABLE STILL SET? IT SHOULD BE
1959	011716	001457			BEQ	3\$;IF IT'S NOT, ASSUME IT'S NOT A DZ11
1960	011720	032711	000040		BIT	#BIT5,(R1)		;IS MASTER SCAN ENABLE STILL SET?
1961	011724	001454			BEQ	3\$;IF NOT, ASSUME IT'S NOT A DZ11
1962	011726	005000			CLR	RO		
1963	011730	052711	000020		BIS	#20,(R1)		;SET DEVICE CLEAR
1964	011734	032711	000020		BIT	#20,(R1)		;SHOULD STAY SET FOR A WHILE IF DZ
1965	011740	001446			BEQ	3\$;BR IF NOT DZ11
1966	011742	032711	000020		BIT	#20,(R1)		;WAIT FOR BIT TO CLEAR
1967	011746	001404			BEQ	+.12		;BR WHEN CLEARED
1968	011750	104414			DELAY			
1969	011752	005200			INC	RO		
1970	011754	001372			BNE	.-12		
1971	011756	000437			BR	3\$;BIT NOT CLEARED! MUST NOT BE DZ11
1972	011760	005011			CLR	(R1)		;GET RID OF MASTER SCAN ENABLE
1973	011762	005061	000004		CLR	4(R1)		;GET RID OF LINE 7 ENABLE
1974								;AT THIS POINT IT IS ASSUMED THAT R1 HOLDS A DZ11 CSR ADDRESS.
1975	011766	010122			MOV	R1,(R2)+		;STORE CSR IN CORE TABLE.
1976	011770	005722			TST	(R2)+		;POP OVER VECTOR STORE AREA
1977	011772	012722	000005		MOV	#5,(R2)+		;SET THE DEFAULT BUS LEVEL
1978	011776	052761	177400	000004	BIS	#177400,4(R1)		;TRY TO SET ALL DTR BITS
1979	012004	032761	177400	000004	BIT	#177400,4(R1)		;IF ANY SET ASSUME EIA BOARD
1980	012012	001003			BNE	9\$;IF NONE SET ASSUME C'ARD IS
1981	012014	052762	100000	177776	BIS	#BIT15,-2(R2)		;20 MA, SET 20 MA FLAG
1982	012022	012722	000377		9\$: MOV	#377,(R2)+		;SET THE DEFAULT LINE SELECTION PARAMETER
1983	012026	012712	017470		MOV	#17470,(R2)		;SET THE DEFAULT PARAMETERS
1984	012032	012223			MOV	(R2)+,(R3)+		;COPY PARAMETERS INTO ETABLE DESCRIPTOR
1985	012034	005022			CLR	(R2)+		;SET THE DEFAULT MODE OF OPERATION
1986	012036	012712	177777		MOV	#-1,(R2)		;TERMINATE LIST
1987	012042	105237	001410		INCB	DZNUM		;UPDATE DEVICE COUNTER
1988	012046	122737	000020	001410	CMPB	#20,DZNUM		;ARE MAX. NO. OF DEV FOUND?
1989	012054	001405			BEQ	100\$;YES DON'T LOOK FOR ANY MORE.
1990	012056	062701	000010		3\$: ADD	#10,R1		;UPDATE CSR POINTER ADDRESS
1991	012062	022701	163700		CMP	#163700,R1		
1992	012066	001275			BNE	2\$;BR IF MORE ADDRESS TO CHECK.
1993	012070				100\$:			
1994	012070	105737	001410		TSTB	DZNUM		;WERE ANY DZ11'S FOUND AT ALL?
1995	012074	001432			BEQ	5\$;ERROR AUTO SIZER FOUND NO DZ11'S IN THIS SYS.
1996	012076	113701	001410		MOV	DZNUM,R1		
1997	012102	110137	001411		MOV	R1,SAVNUM		;SAVE NUMBER OF DEVICES
1998	012106	012737	000001	001404	MOV	#1,DZACTV		
1999	012114	005301			4\$: DEC	R1		
2000	012116	001404			BEQ	98\$		


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2058 012374 000004
2059 012376 012737 000001 001122
2060 012404 012737 012564 001360
2061 012412 012737 012552 000004
2062 012420 012737 000340 000006
2063 012426 012737 012434 001362
2064 012434 013700 002042
2065 012440 011001
2066 012442 000240
2067 012444 005010
2068 012446 000240
2069 012450 012737 012456 001362
2070 012456 013700 002046
2071 012462 011001
2072 012464 000240
2073 012466 005010
2074 012470 000240
2075 012472 012737 012500 001362
2076 012500 013700 002056
2077 012504 011001
2078 012506 000240
2079 012510 005010
2080 012512 000240
2081 012514 012737 012522 001362
2082 012522 013700 002062
2083 012526 011001
2084 012530 000240
2085 012532 005010
2086 012534 000240
2087 012536 012737 000006 000004
2088 012544 005037 000006
2089 012550 104400
2090 012552 011601
2091 012554 022626
2092 012556 104001
2093 012560 104401
2094 012562 000111
2095
2096
2097
2098
2099
2100
2101 012564 000004
2102 012566 012737 000002 001122
2103 012574 012737 012650 001360
2104 012602 013700 002042
2105 012606 012705 000020
2106 012612 010510

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***** TEST 1 *****
;THIS TEST PROVES THE SLAVE SYNC RESPONSE
;DURING A READ OR WRITE TO THE FOLLOWING ADDRESS:
; DZCSR, DZRBUF, DZTCR, DZMSR
::* TEST 1
*****
TST1: SCOPE
MOV #1,STSTNM ;LOAD THE NUMBER OF THIS TEST
MOV #TST2,NEXT ;POINT TO THE START OF THE NEXT TEST
MOV #5,4 ;SET TRAP VECTOR
MOV #PR7,6 ;SET PRIORITY TO LEVEL 7
MOV #1$,LOCK ;SET RETURN IF SW09=11
1$: MOV DZCSR,RO ;SET ADDRESS TO TEST
MOV (RO),R1 ;READ THE ADDRESS
NOP ;WASTE TIME
CLR (RO) ;WRITE THE ADDRESS
NOP ;WASTE TIME
MOV #2$,LOCK ;SET RETURN ADDRESS FOR SW09
2$: MOV DZRBUF,RO ;SET ADDRESS TO TEST
MOV (RO),R1 ;READ THE ADDRESS
NOP ;WRITE THE ADDRESS
CLR (RO) ;WASTE TIME
NOP ;SET RETURN ADDRESS FOR SW09
3$: MOV DZTCR,RO ;SET ADDRESS TO TEST
MOV (RO),R1 ;READ THE ADDRESS
NOP ;WRITE THE ADDRESS
CLR (RO) ;WRITE THE ADDRESS
NOP ;SET RETURN ADDRESS
4$: MOV DZMSR,RO ;SET ADDRESS TO TEST
MOV (RO),R1 ;READ FROM ADDRESS
NOP ;WRITE THE ADDRESS
CLR (RO) ;WRITE THE ADDRESS
MOV #6,4 ;SET TRAP CATCHER BACK TO NORMAL
5$: ADVANCE 6 ;SCOPE THIS TEST
MOV (SP),R1 ;SAVE PC OF TRAP
CMP (SP)+,(SP)+ ;POP TRAP OFF STACK
ERROR 1 ;*NO SLAVE SYNC RESPONSE.
SCOPI ;SW09=1?
JMP (R1) ;RTI
***** TEST 2 *****
;THIS TEST PROVES THAT BIT "DCLR"
;CAN BE SET AND THAT IT WILL CLEAR
;BY ITSELF AFTER A PERIOD OF TIME.
::* TEST 2
*****
TST2: SCOPE
MOV #2,STSTNM ;LOAD THE NUMBER OF THIS TEST
MOV #TST3,NEXT ;POINT TO THE START OF THE NEXT TEST
MOV DZCSR,RO ;SET POINTER
MOV #DCLR,R5 ;SET DCLR
MOV R5,(RO) ;WRITE DCLR INTO DZCSR

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2107 012614 011004      MOV      (R0),R4      ;READ BACK DZCSR
2108 012616 020504      CMP      R5,R4      ;DZCSR OK?
2109 012620 001401      BEQ      1$         ;IF IT IS SET SKIP THE ERROR CALL
2110 012622 104002      ERROR   2         ;*DCLR SHOULD BE SET..MOMENTARILY
2111                                     ;NOW LETS WATCH IT DISAPPEAR
2112 012624 005002      1$: CLR      R2         ;SET COUNTER TO 0
2113 012626 005005      CLR      R5         ;SET EXPECTED TO 0
2114 012630 005003      CLR      R3         ;DUAL LOOP COUNTER
2115 012632 011004      2$: MOV      (R0),R4  ;IS DCLR CLEAR?
2116 012634 001405      BEQ      3$         ;IF YES GO TO THE NEXT TEST
2117 012636 005203      INC      R3         ;IF NO COUNT 1 OF 65535 TICKS
2118                                     ;THE WORD CREATED BY THE IMMEDIATE 0 WILL BE
2119                                     ;THE COUNTER
2120 012640 001374      BNE      2$         ;HAS THE TIME EXPIRED? IF NO, GO TEST BIT AGAIN
2121 012642 005302      DEC      R2         ;HAS THE TOTAL TIME EXPIRED?
2122 012644 001372      BNE      2$         ;IF NO, CHECK THE BIT AGAIN
2123 012646 104002      ERROR   2         ;*DCLR FAILED TO CLEAR
2124 012650
2125                                     ;***** TEST 3 *****
2126                                     ;*TEST TO VERIFY THAT BIT "MAINT" CAN
2127                                     ;*BE SET. THEN VERIFY THAT BIT "MAINT" CAN
2128                                     ;*BE CLEARED (WRITTEN TO A ZERO). AND FINALLY
2129                                     ;*VERIFY THAT AFTER BEING SET AGAIN IT CAN BE
2130                                     ;*CLEARED BY A "DEVICE CLEAR"
2131 ;:* TEST 3
2132 ;*****
2133 012650 000004      ;*TEST 3
2134 012652 012737      ;*TEST 3
2135 012660 012737      ;*TEST 3
2136 012666 013700      ;*TEST 3
2137 012672 012705      ;*TEST 3
2138 012676 010510      ;*TEST 3
2139 012700 011004      ;*TEST 3
2140 012702 020504      ;*TEST 3
2141 012704 001401      ;*TEST 3
2142 012706 104002      ;*TEST 3
2143 012710 040510      ;*TEST 3
2144 012712 011004      ;*TEST 3
2145 012714 001404      ;*TEST 3
2146 012716 010546      ;*TEST 3
2147 012720 005005      ;*TEST 3
2148 012722 104002      ;*TEST 3
2149 012724 012605      ;*TEST 3
2150 012726 010510      ;*TEST 3
2151 012730 104413      ;*TEST 3
2152 012732 011004      ;*TEST 3
2153 012734 001402      ;*TEST 3
2154 012736 005005      ;*TEST 3
2155 012740 104002      ;*TEST 3
2156 012742
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2158
2159
2160
2161
2162

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2163
2164
2165 012742 000004
2166 012744 012737 000004 001122
2167 012752 012737 013034 001360
2168 012760 013700 002042
2169 012764 012705 000040
2170 012770 010510
2171 012772 011004
2172 012774 020504
2173 012776 001401
2174 013000 104002
2175 013002 040510
2176 013004 011004
2177 013006 001404
2178 013010 010546
2179 013012 005005
2180 013014 104002
2181 013016 012605
2182 013020 010510
2183 013022 104413
2184 013024 011004
2185 013026 001402
2186 013030 005005
2187 013032 104002
2188 013034
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2194
2195
2196
2197 013034 000004
2198 013036 012737 000005 001122
2199 013044 012737 013126 001360
2200 013052 013700 002042
2201 013056 012705 010000
2202 013062 010510
2203 013064 011004
2204 013066 020504
2205 013070 001401
2206 013072 104002
2207 013074 040510
2208 013076 011004
2209 012100 001404
2210 013102 010546
2211 013104 005005
2212 013106 104002
2213 013110 012605
2214 013112 010510
2215 013114 104413
2216 013116 011004
2217 013120 001402
2218 013122 005005

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```

;:* TEST 4
;*****
†ST4: SCOPE
MOV #4,$STSTNM ;LOAD THE NUMBER OF THIS TEST
MOV #TST5,NEXT ;POINT TO THE START OF THE NEXT TEST
MOV DZCSR,R0 ;GET BASE ADDRESS
MOV #MSENAB,R5 ;SET BIT
MOV R5,(R0) ;SET SET IN DEVICE
MOV (R0),R4 ;READ THE BIT FROM DEVICE
CMP R5,R4 ;WAS BIT SET?
BEQ 1$ ;BR IF YES
ERROR 2 ;*BIT R/W FAILURE
1$: BIC R5,(R0) ;CLEAR THE BIT.
MOV (R0),R4 ;READ DEVICE
BEQ 2$ ;BR IF BITS WERE CLEARED.
MOV R5,-(SP) ;SAVE THE BIT
CLR R5 ;SET EXPECTED RESULTS TO 0
ERROR 2 ;*BIT FAILED TO CLEAR
MOV (SP)+,R5 ;RESTORE THE BIT.
2$: MOV R5,(R0) ;SET THE BIT AGAIN
DEVICE.CLR ;ISSUE DEVICE CLEAR
MOV (R0),R4 ;READ THE BIT.
BEQ 3$ ;BR IF BIT CLEARED BY INIT (DEVICE CLEAR)
CLR R5 ;SET EXPECTED TO ZERO
ERROR 2 ;*BIT NOT CLEARED BY DEVICE CLEAR
3$:
;***** TEST 5 *****
;*TEST TO VERIFY THAT BIT "SILOEN" CAN
;*BE SET. THEN VERIFY THAT BIT "SILOEN" CAN
;*BE CLEARED (WRITTEN TO A ZERO). AND FINALLY
;*VERIFY THAT AFTER BEING SET AGAIN IT CAN BE
;*CLEARED BY A "DEVICE CLEAR"
;:* TEST 5
;*****
†ST5: SCOPE
MOV #5,$STSTNM ;LOAD THE NUMBER OF THIS TEST
MOV #TST6,NEXT ;POINT TO THE START OF THE NEXT TEST
MOV DZCSR,R0 ;GET BASE ADDRESS
MOV #SILOEN,R5 ;SET BIT
MOV R5,(R0) ;SET SET IN DEVICE
MOV (R0),R4 ;READ THE BIT FROM DEVICE
CMP R5,R4 ;WAS BIT SET?
BEQ 1$ ;BR IF YES
ERROR 2 ;*BIT R/W FAILURE
1$: BIC R5,(R0) ;CLEAR THE BIT.
MOV (R0),R4 ;READ DEVICE
BEQ 2$ ;BR IF BITS WERE CLEARED.
MOV R5,-(SP) ;SAVE THE BIT
CLR R5 ;SET EXPECTED RESULTS TO 0
ERROR 2 ;*BIT FAILED TO CLEAR
MOV (SP)+,R5 ;RESTORE THE BIT.
2$: MOV R5,(R0) ;SET THE BIT AGAIN
DEVICE.CLR ;ISSUE DEVICE CLEAR
MOV (R0),R4 ;READ THE BIT.
BEQ 3$ ;BR IF BIT CLEARED BY INIT (DEVICE CLEAR)
CLR R5 ;SET EXPECTED TO ZERO

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2219 013124 104002
2220 013126
2221
2222
2223
2224
2225
2226
2227
2228
2229 013126 000004
2230 013130 012737 000006 001122
2231 013136 012737 013220 001360
2232 013144 013700 002042
2233 013150 012705 000100
2234 013154 010510
2235 013156 011004
2236 013160 020504
2237 013162 001401
2238 013164 104002
2239 013166 040510
2240 013170 011004
2241 013172 001404
2242 013174 010546
2243 013176 005005
2244 013200 104002
2245 013202 012605
2246 013204 010510
2247 013206 104413
2248 013210 011004
2249 013212 001402
2250 013214 005005
2251 013216 104002
2252 013220
2253
2254
2255
2256
2257
2258
2259
2260
2261 013220 000004
2262 013222 012737 000007 001122
2263 013230 012737 013312 001360
2264 013236 013700 002042
2265 013242 012705 040000
2266 013246 010510
2267 013250 011004
2268 013252 020504
2269 013254 001401
2270 013256 104002
2271 013260 040510
2272 013262 011004
2273 013264 001404
2274 013266 010546

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ERROR 2 ;*BIT NOT CLEARED BY DEVICE CLEAR
3$:
;***** TEST 6 *****
;*TEST TO VERIFY THAT BIT "RIE" CAN
;*BE SET. THEN VERIFY THAT BIT "RIE" CAN
;*BE CLEARED (WRITTEN TO A ZERO). AND FINALLY
;*VERIFY THAT AFTER BEING SET AGAIN IT CAN BE
;*CLEARED BY A "DEVICE CLEAR"
;:* TEST 6
;*****
†ST6: SCOPE
MOV #6,STSTNM ;LOAD THE NUMBER OF THIS TEST
MOV #†ST7,NEXT ;POINT TO THE START OF THE NEXT TEST
MOV DZCSR,R0 ;GET BASE ADDRESS
MOV #RIE,R5 ;SET BIT
MOV R5,(R0) ;SET SET IN DEVICE
MOV (R0),R4 ;READ THE BIT FROM DEVICE
CMP R5,R4 ;WAS BIT SET?
BEQ 1$ ;BR IF YES
ERROR 2 ;*BIT R/W FAILURE
1$: BIC R5,(R0) ;CLEAR THE BIT.
MOV (R0),R4 ;READ DEVICE
BEQ 2$ ;BR IF BITS WERE CLEARED.
MOV R5,-(SP) ;SAVE THE BIT
CLR R5 ;SET EXPECTED RESULTS TO 0
ERROR 2 ;*BIT FAILED TO CLEAR
MOV (SP)+,R5 ;RESTORE THE BIT.
2$: MOV R5,(R0) ;SET THE BIT AGAIN
DEVICE.CLR ;ISSUE DEVICE CLEAR
MOV (R0),R4 ;READ THE BIT.
BEQ 3$ ;BR IF BIT CLEARED BY INIT DEVICE CLEAR)
CLR R5 ;SET EXPECTED TO ZERO
ERROR 2 ;*BIT NOT CLEARED BY DEVICE CLEAR
3$:
;***** TEST 7 *****
;*TEST TO VERIFY THAT BIT "TIE" CAN
;*BE SET. THEN VERIFY THAT BIT "TIE" CAN
;*BE CLEARED (WRITTEN TO A ZERO). AND FINALLY
;*VERIFY THAT AFTER BEING SET AGAIN IT CAN BE
;*CLEARED BY A "DEVICE CLEAR"
;:* TEST 7
;*****
†ST7: SCOPE
MOV #7,STSTNM ;LOAD THE NUMBER OF THIS TEST
MOV #†ST10,NEXT ;POINT TO THE START OF THE NEXT TEST
MOV DZCSR,R0 ;GET BASE ADDRESS
MOV #TIE,R5 ;SET BIT
MOV R5,(R0) ;SET SET IN DEVICE
MOV (R0),R4 ;READ THE BIT FROM DEVICE
CMP R5,R4 ;WAS BIT SET?
BEQ 1$ ;BR IF YES
ERROR 2 ;*BIT R/W FAILURE
1$: BIC R5,(R0) ;CLEAR THE BIT.
MOV (R0),R4 ;READ DEVICE
BEQ 2$ ;BR IF BITS WERE CLEARED.
MOV R5,-(SP) ;SAVE THE BIT

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2275 013270 005005          CLR      R5          ;SET EXPECTED RESULTS TO 0
2276 013272 104002          ERROR    2          ;*BIT FAILED TO CLEAR
2277 013274 012605          MOV      (SP)+,R5    ;RESTORE THE BIT.
2278 013276 010510          2$:     MOV      R5,(R0) ;SET THE BIT AGAIN
2279 013300 104413          DEVICE.CLR ;ISSUE DEVICE CLEAR
2280 013302 011004          MOV      (R0),R4    ;READ THE BIT.
2281 013304 001402          BEQ     3$          ;BR IF BIT CLEARED BY INIT (DEVICE CLEAR)
2282 013306 005005          CLR      R5          ;SET EXPECTED TO ZERO
2283 013310 104002          ERROR    2          ;*BIT NOT CLEARED BY DEVICE CLEAR
2284 013312
2285
2286
2287
2288
2289
2290
2291
2292 013312 000004          ;***** TEST 10 *****
2293 013314 012737 000010 001122 ;*THIS TESTS THAT ALL OF THE FOLLOWING
2294 013322 012737 013450 001360 ;*BITS CAN BE: SET, CLEARED, CLEARED BY "DEVICE CLEAR "
2295 013330 013700 002056 ;*BITS TESTED ARE:
2296 013334 012705 000001 ;* TCRO, TCR1, TCR2, TCR3, TCR4, TCR5, TCR6, TCR7
2297 013340 012737 013346 001362 ;:* TEST 10
2298 013346 010510          ;*****
2299 013350 011004          TST10: SCOPE
2300 013352 042704 177400 MOV      #10,$STSTM ;LOAD THE NUMBER OF THIS TEST
2301 013356 020504          MOV      #TST11,NEXT ;POINT TO THE START OF THE NEXT TEST
2302 013360 001401          MOV      DZTCR,R0   ;SET DEVICE ADDRESS
2303 013362 104002          MOV      #TCRO,R5   ;SET EXPECTED RESULTS
2304 013364 040510          1$:     MOV      #1$,LOCK  ;SET FOR SW09
2305 013366 011004          MOV      R5,(R0)   ;SET THE BIT
2306 013370 042704 177400 MOV      (R0),R4    ;READ THE BIT FROM THE DEVICE
2307 013374 005704          BIC     #1C<377>,R4 ;CLEAR HIGH BYTE
2308 013376 001404          CMP     R5,R4      ;WAS BIT OK?
2309 013400 010546          BEQ     2$          ;BR IF YES
2310 013402 005005          ERROR    2          ;*BIT FAILED TO SET.
2311 013404 104002          2$:     BIC     R5,(R0)   ;CLEAR THE BIT
2312 013406 012605          MOV      (R0),R4   ;READ THE REGISTER
2313 013410 010510          BIC     #1C<377>,R4 ;CLEAR HIGH BYTE
2314 013412 104413          TST     R4          ;BITS CLEAR?
2315 013414 011004          BEQ     3$          ;BR IF YES
2316 013416 042704 177400 MOV      R5,-(SP)   ;SAVE GOOD RESULTS
2317 013422 005704          CLR     R5          ;SET EXPECTED TO 0
2318 013424 001404          ERROR    2          ;*REPORT BIT NOT CLEAR
2319 013426 010546          MOV      (SP)+,R5  ;RESTORE R5
2320 013430 005005          3$:     MOV      R5,(R0)   ;SET THE BIT AGAIN.
2321 013432 104002          DEVICE.CLR ;ISSUE DEVICE CLEAR
2322 013434 012605          MOV      (R0),R4   ;READ THE REGISTER
2323 013436 104401          BIC     #1C<377>,R4 ;CLEAR HIGH BYTE
2324 013440 106305          TST     R4          ;BITS CLEAR?
2325 013442 001341          BEQ     4$          ;BR IF YES
2326 013444 005037 001362 MOV      R5,-(SP)   ;SAVE GOOD RESULTS
2327
2328
2329
2330          CLR     R5          ;SET EXPECTED TO 0
          ERROR    2          ;*REPORT BIT NOT CLEAR
          MOV      (SP)+,R5 ;RESTORE R5
          4$:     SCOP1 ;LOCK ON BIT? SET SW09=1
          ASLB    R5      ;CHANGE TO NEXT BIT
          BNE     1$      ;CONTINUE TESTING
          CLR     LOCK    ;MAKE SURE TIGHT LOOP IS CLEANED UP
          ;***** TEST 11 *****
          ;*THIS TESTS THAT ALL OF THE FOLLOWING
          ;*BITS CAN BE: SET, CLEARED, CLEARED BY "RESET INSTR *NOT* DEVICE CLEAR "
          ;*BITS TESTED ARE:

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2331                                     ; * DTR0, DTR1, DTR2, DTR3, DTR4, DTR5, DTR6, DTR7
2332                                     ; * THIS TEST IS NOT DONE IF MODULE IS 20MA VERSION
2333                                     ; : * TEST 11
2334                                     ; : *****
2335 013450 000004          †ST11: SCOPE
2336 013452 012737 000011 001122      MOV      #11, $STNM      ; LOAD THE NUMBER OF THIS TEST
2337 013460 012737 013632 001360      MOV      †ST12, NEXT   ; POINT TO THE START OF THE NEXT TEST
2338 013466 013700 002056              MOV      DZTCR, R0     ; SET DEVICE ADDRESS
2339 013472 012705 000400              MOV      #DTR0, R5    ; SET EXPECTED RESULTS
2340 013476 012737 013514 001362      MOV      #1$, LOCK    ; SET FOR SW09
2341 013504 105737 001414              TSTB    EIAFLG        ; 20MA OR EIA
2342 013510 100001              BPL     1$            ; BR IF EIA
2343 013512 104400              ADVANCE              ; EXIT TEST
2344 013514 010510          1$: MOV      R5, (R0)      ; SET THE BIT
2345 013516 011004              MOV      (R0), R4     ; READ THE BIT FROM THE DEVICE
2346 013520 105004              CLRB    R4            ; CLEAR LOW BYTE
2347 013522 020504              CMP     R5, R4        ; WAS BIT OK?
2348 013524 001401              BEQ     2$            ; BR IF YES
2349 013526 104002              ERROR   2             ; *BIT FAILED TO SET.
2350 013530 040510          2$: BIC     R5, (R0)      ; CLEAR THE BIT
2351 013532 011004              MOV      (R0), R4     ; READ THE REGISTER
2352 013534 105004              CLRB    R4            ; CLEAR LOW BYTE
2353 013536 005704              TST     R4            ; BITS CLEAR?
2354 013540 001404              BEQ     3$            ; BR IF YES
2355 013542 010546              MOV      R5, -(SP)    ; SAVE GOOD RESULTS
2356 013544 005005              CLR     R5            ; SET EXPECTED TO 0
2357 013546 104002              ERROR   2             ; *REPORT BIT NOT CLEAR
2358 013550 012605              MOV      (SP)+, R5    ; RESTORE R5
2359 013552 010510          3$: MOV      R5, (R0)      ; SET THE BIT AGAIN.
2360 013554 104413              DEVICE.CLR          ; ISSUE DEVICE CLEAR
2361 013556 011004              MOV      (R0), R4     ; READ THE REGISTER
2362 013560 105004              CLRB    R4            ; CLEAR LOW BYTE
2363 013562 030510              BIT     R5, (R0)      ; WAS BIT CLEARED BY DEVICE.CLR?
2364 013564 001001              BNE     4$            ; BR IF NO (IT SHOULDN'T BE CLEAR)
2365 013566 104002              ERROR   2             ; *BIT CLEARED BY DEVICE.CLR
2366 013570 104401          4$: SCOPI          ; LOCK ON BIT? SW09=1
2367 013572 006305              ASL     R5            ; CHANGE TO NEXT BIT
2368 013574 001347              BNE     1$            ; IF NOT DONE LOOP
2369 013576 012710 177400              MOV      #177400, (R0) ; SET ALL DTR BITS
2370 013602 005005              CLR     R5            ; CLEAR LOCATION FOR ERROR PRINTOUT
2371 013604 005227 000000          5$: INC     #0          ; ACT DELAY LOOP FOR
2372 013610 001375              BNE     5$            ; RESET INSTRUCTION
2373 013612 000005              RESET              ; ISSUE A BUS INIT
2374 013614 011004              MOV      (R0), R4     ; READ REGISTER
2375 013616 105004              CLRB    R4            ; CLEAR LOW BYTE
2376 013620 005704              TST     R4            ; DTR BITS CLEAR?
2377 013622 001401              BEQ     .+4           ; IF YES CONTINUE
2378 013624 104002              ERROR   2             ; IF NO PRINT ERROR
2379 013626 005037 001362              CLR     LOCK          ; MAKE SURE TIGHT LOOP IS CLEANED UP
2380                                     ; ***** TEST 12 *****
2381                                     ; * THIS TEST PERFORMS RESET TESTING &
2382                                     ; * TESTING OF WRITE ONLY OR READ ONLY BIT
2383                                     ; * TEST BITS "RDONE, BIT11, BIT10, BIT9, BIT8, BIT2, BIT1
2384                                     ; * BIT0, SILOAL" ARE READ ONLY AND THAT †RDY IS
2385                                     ; * ZERO UNTIL A LINE IS SELECTED AND MSENAB IS SET.
2386                                     ; *

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2387          ;:* TEST 12
2388          ;:*****
2389 013632 000004          †TST12: SCOPE
2390 013634 012737 000012 001122      MOV      #12,$STSTNM      ;LOAD THE NUMBER OF THIS TEST
2391 013642 012737 013750 001360      MOV      #TST13,NEXT    ;POINT TO THE START OF THE NEXT TEST
2392 013650 013700 002042          MOV      DZCSR,R0      ;SET ADDRESS TO R0
2393 013654 005005          CLR      R5            ;SET EXPECTED TO 0
2394 013656 012710 027607          MOV      #RDONE+BIT11+BIT10+BIT9+BIT8+BIT2+BIT1+BIT0+SILOAL,(R0)
2395          ;WRITE THE BITS
2396 013662 011004          MOV      (R0),R4      ;READ BACK THE BITS
2397 013664 001401          BEQ     1$           ;BR IF NONE ARE SET.
2398 013666 104002          ERROR   2           ;*BITS WERE SET.
2399 013670 012710 100000          1$: MOV     #TRDY,(R0)  ;ATTEMPT TO WRITE TRDY
2400 013674 011004          MOV     (R0),R4      ;READ TRDY
2401 013676 001401          BEQ     2$           ;BR IF NOT SET
2402 013700 104002          ERROR   2           ;*
2403 013702 012705 100000          2$: MOV     #TRDY,R5    ;SET EXPECTED BIT
2404 013706 005077 166140          CLR     @DZLPR        ;LOAD LINE 0
2405 013712 052777 000001 166135      BIS     #TCRD,@DZTCR  ;SET TCR BIT
2406 013720 052710 000040          BIS     #MSENAB,(R0)
2407 013724 052705 000040          BIS     #MSENAB,R5   ;SET SCAN ENABLE
2408 013730 005002          CLR     R2           ;SET COUNTER TO ZERO
2409 013732 011004          3$: MOV     (R0),R4    ;READ THE REGISTER
2410 013734 020504          CMP     R5,R4        ;BIT SET?
2411 013736 001404          BEQ     4$           ;BR IF YES
2412 013740 104414          DELAY   ;STALL TIME
2413 013742 005202          INC     R2           ;UPDATE COUNTER
2414 013744 001372          BNE     3$          ;BR IF COUNTER NOT DONE.
2415 013746 104002          ERROR   2           ;*TRDY NOT SET!
2416 013750          4$:
2417          ;***** TEST 13 *****
2418          ;*THIS TEST PERFORMS RESET TESTING AND
2419          ;*TESTING OF READ ONLY AND WRITE ONLY BITS
2420          ;* IN REGISTER DZCSR
2421          ;*VERIFY THAT "TIE", "SILOEN", "RIE", "MSENAB", "MAINT"
2422          ;*ARE THE ONLY R/W BITS IN THE DZCSR.
2423          ;*THEN SET "DCLR" AND VERIFY THEY ARE CLEARED
2424          ;:* TEST 13
2425          ;:*****
2426 013750 000004          †TST13: SCOPE
2427 013752 012737 000013 001122      MOV      #13,$STSTNM  ;LOAD THE NUMBER OF THIS TEST
2428 013760 012737 014034 001360      MOV      #TST14,NEXT  ;POINT TO THE START OF THE NEXT TEST
2429 013766 104413          DEVICE.CLR
2430 013770 013700 002042          MOV      DZCSR,R0    ;SET UP FOR ERROR MESSAGE
2431 013774 012710 177757          MOV      #TC<DCLR>,(R0) ;TRY TO WRITE
2432 014000 012705 050150          MOV      #TIE!SILOEN!RIE!MSENAB!MAINT,R5 ;MAKE EXPECTED
2433 014004 011004          MOV     (R0),R4      ;ACTUAL
2434 014006 020405          CMP     R4,R5        ;CMP EXPECTED VS ACTUAL
2435 014010 001401          BEQ     1$           ;YES
2436 014012 104002          ERROR   2           ;*NO
2437 014014 012705 000020          1$: MOV     #DCLR,R5  ;EXPECTED...NOTE THAT DCLR REMAINS
2438          ;SET LONG ENOUGH TO READ IT...HOWEVER
2439          ;IF YOU EXAMINE THIS BIT IT SHOULD BE CLEAR.
2440 014020 052710 000020          BIS     #DCLR,(R0)   ;DEVICE MASTER RESET
2441 014024 011004          MOV     (R0),R4      ;ACTUAL
2442 014026 020405          CMP     R4,R5        ;CMP ACTUAL VS EXPECTED

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2444 014032 104002
2445 014034
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2452 014034 000004
2453 014036 012737 000014 001122
2454 014044 012737 014124 001360
2455 014052 104413
2456 014054 013700 002046
2457 014060 011005
2458 014062 012777 177777 165762
2459 014070 011004
2460 014072 042705 104000
2461 014076 020405
2462 014100 001401
2463 014102 104002
2464 014104 010403
2465 014106 005103
2466 014110 010377 165736
2467 014114 011004
2468 014116 020405
2469 014120 001401
2470 014122 104002
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2478 014124 000004
2479 014126 012737 000015 001122
2480 014134 012737 014210 001360
2481 014142 104413
2482 014144 013700 002062
2483 014150 011005
2484 014152 012777 177777 165706
2485 014160 011004
2486 014162 020405
2487 014164 001401
2488 014166 104002
2489 014170 010403
2490 014172 005103
2491 014174 010377 165666
2492 014200 011004
2493 014202 020405
2494 014204 001401
2495 014206 104002
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                BEQ      25          ;YES
                ERROR    2          ;NO
25:
                ;***** TEST 14 *****
                ;*THIS TEST PERFORMS RESET TESTING AND
                ;*TESTING OF READ ONLY REGISTER DZRBUF
                ;*AND TESTING OF WRITE ONLY REGISTER DZLPR
                ;:* TEST 14
                ;*****
                TST14: SCOPE
                MOV      #14,$STNM    ;LOAD THE NUMBER OF THIS TEST
                MOV      #TST15,NEXT ;POINT TO THE START OF THE NEXT TEST
                DEVICE.CLR          ;CLEAR DZ11
                MOV      DZRBUF,R0   ;SET UP FOR ERROR MESSAGE
                MOV      (R0),R5     ;SET EXPECTED
                MOV      #-1,DZLPR   ;TRY TO WRITE ALL 1'S
                MOV      (R0),R4     ;ACTUAL
                BIC      #DVALID!BIT11,R5 ;DITTO
                CMP      R4,R5       ;CMP ACTUAL VS EXPECTED
                BEQ      15          ;IF YES,GO CONTINUE PROCESSING
                ERROR    2          ;*ERROR- BIT PATTERN NOT CORRECT
25:
                MOV      R4,R3       ;GET A COPY OF THE ACTUAL BIT PATTERN
                COM      R3          ;GET THE LOGICAL INVERSE OF THE BIT PATTERN
                MOV      R3,DZLPR   ;TRY TO WRITE
                MOV      (R0),R4     ;ACTUAL
                CMP      R4,R5       ;CMP ACTUAL VS EXPECTED
                BEQ      25          ;IF YES, GET OUT OF THIS TEST
                ERROR    2          ;*NO
                ;***** TEST 15 *****
                ;*THIS TEST PERFORMS RESET TESTING AND
                ;*TESTING OF READ ONLY REGISTER DZMSR
                ;*AND TESTING OF WRITE ONLY REGISTER DZTDR
                ;:* TEST 15
                ;*****
                TST15: SCOPE
                MOV      #15,$STNM    ;LOAD THE NUMBER OF THIS TEST
                MOV      #TST16,NEXT ;POINT TO THE START OF THE NEXT TEST
                DEVICE.CLR          ;CLEAR DZ11
                MOV      DZMSR,R0   ;SET UP FOR ERROR MESSAGE
                MOV      (R0),R5     ;SET EXPECTED
                MOV      #-1,DZTDR   ;TRY TO WRITE ALL 1'S
                MOV      (R0),R4     ;ACTUAL
                CMP      R4,R5       ;CMP ACTUAL VS EXPECTED
                BEQ      15          ;IF YES,GO CONTINUE PROCESSING
                ERROR    2          ;*ERROR- BIT PATTERN NOT CORRECT
25:
                MOV      R4,R3       ;GET A COPY OF THE ACTUAL BIT PATTERN
                COM      R3          ;GET THE LOGICAL INVERSE OF THE BIT PATTERN
                MOV      R3,DZTDR   ;TRY TO WRITE
                MOV      (R0),R4     ;ACTUAL
                CMP      R4,R5       ;CMP ACTUAL VS EXPECTED
                BEQ      25          ;IF YES, GET OUT OF THIS TEST
                ERROR    2          ;*NO
                ;***** TEST 16 *****

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014210 000004
014212 012737 000016 001122
014220 012737 014404 001360
014226 012737 014300 001362
014234 105737 001414
014240 100001
014242 104400
014244 013700 002062
014250 104413
014252 005003
014254 012702 000001
014260 005737 001370
014264 100405
014266 013737 001360 001126
014274 000177 164626
014300 130237 001364
014304 001004
014306 005203
014310 106302
014312 103372
014314 104400
014316 010204
014320 032703 000001
014324 001402
014326 006204
014330 000401
014332 006304
014334 005005
014336 150405
014340 000305
014342 150405
014344 150277 165510
014350 104414
014352 011004
014354 020504
014356 001401
014360 104002
014362 140277 165472
014366 104414
014370 011004
014372 001402
014374 005005
014376 104002
014400 104401

*VERIFY THAT IF WE ARE IN "STAGGERED" MODE
*THAT SETTING "DTR" FOR A LINE WILL
*BRING UP "RING" AND "CARRIER" FOR THE
*ASSOCIATED LINE IN WHICH WE ARE STAGGERED!
* LINE0 DTR= LINE1 RING AND CARRIER
* LINE1 DTR= LINE0 RING AND CARRIER
* LINE2 DTR= LINE3 RING AND CARRIER
* LINE3 DTR= LINE 4 RING AND CARRIER
*
ETC...

;;* TEST 16

TST16: SCOPE
MOV #16,\$TSTNM ;LOAD THE NUMBER OF THIS TEST
MOV #TST17,NEXT ;POINT TO THE START OF THE NEXT TEST
MOV #15,LOCK ;USE THIS ADDRESS IF A TIGHT SCOPE LOOP IS SELECTED
TSTB EIAFLG ;EIA OR ZOMA?
BPL 10\$;BR IF EIA
ADVANCE ;EXIT TEST
10\$: MOV DZMSR,R0 ;SET REGISTER
DEVICE.CLR ;INIT DZ11
CLR R3 ;ZERO LINE NUMBER
MOV #1,R2 ;SET POINTER
TST MODE ;ARE WE IN STAGGERED MODE?
BMI 1\$;YES WE ARE!
MOV NEXT,\$LPADR ;LEAVE THIS TEST! NOT STAGGERED
JMP \$LPADR ;EXIT
1\$: BITB R2,LINE ;TEST THIS LINE?
BNE 3\$;YES
2\$: INC R3 ;LINE #
ASLB R2 ;GET NEXT LINE
BCC 1\$;KEEP TESTING
ADVANCE ;ADVANCE THIS TEST
3\$: MOV R2,R4 ;SAVE BINARY BIT FOR LINE #
BIT #BIT0,R3 ;GET STAGGERED COMPANION LINE
BEQ 4\$;BR IF LINE EVEN
ASR R4 ;ADJUST LINE
BR 5\$
4\$: ASL R4 ;ADJUST LINE
5\$: CLR R5 ;SET EXPECTED
BISB R4,R5
SWAB R5
BISB R4,R5
BISB R2,\$HDZTCR ;SET DTR
DELAY ;CABLE DELAY
MOV (R0),R4 ;READ MSR REGISTER
CMP R5,R4 ;OK?
BEQ 6\$;YES
ERROR 2 ;*ERROR IN RING OR CARRIER
6\$: BICB R2,\$HDZTCR ;CLEAR DTR
DELAY ;CABLE DELAY
MOV (R0),R4 ;READ MSR
BEQ 7\$;BR IF THEY CLEARED
CLR R5 ;SET EXPECTED TO 0
ERROR 2 ;*BITS NOT CLEARED
7\$: SCOPI ;LOCK ON SIGNAL?

2555 014402 000741

BR 2\$;CONTINUE TEST

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014404 000304
014406 012737 000017 001122
014414 012737 014542 001360
014422 012737 014456 001362
014430 105737 001370
014434 100401
014436 104400
014440 105737 001414
014444 100774
014446 013700 002062
014452 012702 000001
014456 130237 001364
014462 001003
014464 106302
014466 103373
014470 104400
014472 005005
014474 150205
014476 000305
014500 150205
014502 150277 165352
014506 104414
014510 011004
014512 020504
014514 001401
014516 104002
014520 140277 165334
014524 104414
014526 011004
014530 001402
014532 005005
014534 104002
014536 104401
014540 000751

***** TEST 17 *****
*TEST TO VERIFY THAT IF IN "EXTERNAL"
*MODE; SETTING DTR FOR SELECTED LINES
*WILL BRING UP "CARRIER" AND "RING"
*FOR THAT SAME LINE. NOTE: IF YOU HAVE
*SELECTED MODE AS "EXTERNAL"; THE H325 TEST CONNECTER
*MUST BE USED ON ALL SPECIFIED LINES.
*LINES MAY BE SPECIFIED BY SWR03=1
*AND SWR00=1 AT START TIME OR ALTERING
*STATUS MAP.

::* TEST 17

↑ST17: SCOPE
MOV #17, STSTNM ;LOAD THE NUMBER OF THIS TEST
MOV #TS120, NEXT ;POINT TO THE START OF THE NEXT TEST
MOV #3\$, LOCK ;USE THIS ADDRESS IF A TIGHT SCOPE LOOP IS SELECTED
TSTB MODE ;EXTERNAL?
BMI 2\$;BR IF YES
1\$: ADVANCE ;EXIT TEST
2\$: TSTB EIAFLG ;YOU BETTER BE IN
BMI 1\$;EIA MODE FOR THIS TEST.
MOV DZMSR, R0 ;SET REGISTER
MOV #1, R2 ;SET LINE POINTER
3\$: BITB R2, LINE ;LINE SELECTED?
BNE 5\$;BR IF YES
4\$: ASLB R2 ;NEXT LINE
BCC 3\$;CONTINUE TEST
5\$: ADVANCE ;ADVANCE THIS TEST
CLR R5 ;SET EXPECTED
BISB R2, R5
SWAB R5
BISB R2, R5
BISB R2, QHDZTCR ;SET DTR
DELAY ;CABLE DELAY
MOV (R0), R4 ;READ MSR
CMP R5, R4 ;BITS OK?
BEQ 6\$;BR IF YES
6\$: ERROR 2 ;CARRIER OR RING ERROR
BICB R2, QHDZTCR ;CLEAR DTR
DELAY ;CABLE DELAY
MOV (R0), R4 ;READ MSR
BEQ 7\$;BR IF BITS CLEARED
CLR R5 ;CLEAR EXPECTED LOC.
7\$: ERROR 2 ;BITS NOT CLEARED.
SCOPI ;LOCK ON LINE?
BR 4\$;CONTINUE TEST

***** TEST 20 *****
* THIS TEST VERIFIES THAT TRDY IS SET WHEN A LINE
* IS READY TO BE LOADED, AND THAT THE LINE SPECI-
* FIED IN BITS 8-10 OF DZCSR CORRESPOND
* TO THE LINE SELECTED IN DZTCR

::* TEST 20

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2611 014542 000004          TST20: SCOPE
2612 014544 012737 000020 001122  MOV      #20,$STSTNM      ;LOAD THE NUMBER OF THIS TEST
2613 014552 012737 014666 001360  MOV      #TST21,NEXT    ;POINT TO THE START OF THE NEXT TEST
2614 014560 104413          DEVICE.CLR              ;ISSUE A "DEVICE CLEAR" (RESET)
2615 014562 013700 002042  MOV      DZCSR,R0       ;SET POINTER
2616 014566 012705 100040  MOV      #MSENAB!TRDY,R5 ;START THE EXPECTED LINE NUMBER AT 0
2617 014572 005037 001372  CLR      SAVLIN         ;SET UP FOR ERROR PRINTOUTS
2618 014576 012702 000001  MOV      #1,R2          ;USING R2 AS A BIT POINTER, POINT TO LINE 0
2619 014602 130237 001364  1$: BIT   R2,LINE        ;IS THIS LINE SELECTED?
2620 014606 001420          BEQ      5$             ;IF NO, SKIP THE STARTUP
2621 014610 050277 165242  2$: BIS   R2,@DZTCR      ;SET THE GO BIT FOR THIS LINE
2622 014614 052710 000040  BIS     #MSENAB,(R0)    ;START THE SCANNER
2623 014620 005074          CLR      R4             ;SET FOR DELAY
2624 014622 032710 100000  3$: BIT   #TRDY,(R0)    ;TX READY?
2625 014626 001004          BNE     4$             ;BR IF YES
2626 014630 104414          DELAY                          ;DELAY
2627 014632 005204          INC     R4             ;COUNTER
2628 014634 001372          BNE     3$             ;BR IF <>0!
2629 014636 104003          ERROR  3              ;*TX NOT READY!
2630 014640 011004  4$: MOV   (R0),R4        ;GET THE LINE POINTED TO BY THE SCANNER
2631 014642 020405          CMP    R4,R5           ;IS THE LINE NUMBER WHAT IT SHOULD BE?
2632 014644 001401          BEQ    5$             ;IF YES, GO WORK ON THE NEXT LINE
2633 014646 104002          ERROR  2              ;*LINE NUMBER DID NOT MATCH TCR BIT
2634 014650 062705 000400  5$: ADD   #400,R5        ;POINT TO THE NEXT EXPECTED LINE
2635 014654 104413          DEVICE.CLR              ;ISSUE A "DEVICE CLEAR" (RESET)
2636 014656 005237 001372  INC     SAVLIN         ;ADJUST FOR NEXT LINE
2637 014662 106302          ASLB   R2              ;POINT TO THE NEXT LINE. ARE ALL LINES TESTED?
2638 014664 103346          BCC    1$             ;IF NOT, GO DO THE NEXT LINE
2639 014666          6$:
2640          ;***** TEST 21 *****
2641          ;*TEST TO TRANSMIT ONE CHAR AND
2642          ;*RECEIVE ONE CHAR ON ONE LINE
2643          ;*AT A TIME. THE CHAR IS "252" AND
2644          ;*ALL SELECTED LINES WILL BE TURNED ON
2645          ;*ONE AT A TIME. THIS IS THE FIRST TIME ANY
2646          ;*DATA IS CHECKED IN THE RECEIVER.
2647          ;*USING SWITCH NINE WITH THIS TEST CREATES A TIGHT SCOPE LOOP
2648          ;*WHICH TRANSMITS A STEADY STREAM OF CHARACTERS.
2649          ;:* TEST 21
2650          ;*****
2651 014666 000004          TST21: SCOPE
2652 014670 012737 000021 001122  MOV      #21,$STSTNM      ;LOAD THE NUMBER OF THIS TEST
2653 014676 012737 015202 001360  MOV      #TST22,NEXT    ;POINT TO THE START OF THE NEXT TEST
2654 014704 012737 015160 001362  MOV      #16$,LOCK      ;USE THIS ADDRESS IF A TIGHT SCOPE LOCK IS SELECTED
2655 014712 104417          DCLASM                    ;CLEAR DEVICE AND SET MAINT BIT IF I MODE
2656 014714 013701 001366  MOV      PAR,R1         ;PICK UP PARAMETERS
2657 014720 012702 000001  MOV      #1,R2          ;PICK UP INIT POINTER
2658 014724 030237 001364  1$: BIT   R2,LINE        ;SHOULD THIS LINE BE SET UP ?
2659 014730 001402          BEQ    2$             ;NO
2660 014732 010177 165114  2$: MOV   R1,@DZLPR      ;SET UP LINE PARAMETERS
2661 014736 005201          INC    R1              ;POSITION POINTER TO THE NEXT LINE
2662 014740 106302          ASLB   R2              ;GOT 'EM ALL ?
2663 014742 103370          BCC    1$             ;IF NO, GO SET UP THE NEXT LINE
2664 014744 005037 001372  CLR     SAVLIN         ;CLEAR LINE # INDICATOR
2665 014750 012702 000001  MOV     #1,R2          ;LINE POINTER
2666 014754 052777 000040 165060  BIS     #MSENAB,@DZCSR  ;START SCANNER

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2667 014762 030237 001364      3$: BIT      R2,LINE      ;VALID LINE ?
2668 014766 001462                BEQ      14$      ;NO SET UP NEXT LINE
2669 014770 010277 165062      MOV      R2,0DZTCR ;SET TCR BIT
2670 014774 032777 000200 165040  4$: BIT      #RDONE,0DZCSR ;IS REC DONE = 0 ?
2671 015002 001401                BEQ      5$      ;IF YES, ALLOW TIME FOR TRDY TO SET
2672 015004 104020                ERROR    20      ;*REC DONE SHOULD = 0
2673 015006 005005                CLR      R5
2674 015010 032777 100000 165024  5$: BIT      #TRDY,0DZCSR
2675 015016 001004                BNE      7$
2676 015020 104414                DELAY
2677 015022 105205                INCB     R5
2678 015024 001371                BNE      6$
2679 015026 104003                ERROR    3
2680 015030 112777 000252 165030  7$: MOVB     #252,0DZTDR ;*TRDY FAILED TO SET!
2681 015036 013705 001372                MOV      SAVLIN,R5 ;LOAD CHARACTER
2682 015042 105737 001371                TSTB    MODE+1   ;MAKE EXPECTED LINE #
2683 015046 001406                BEQ      10$     ;IS THIS TEST IN STAGGERED MODE?
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2687 015050 006205                ASR      R5      ;WE MUST NOW INVERT THE LAST BIT OF THE LINE NUMBER
2688 015052 103402                BCS      8$      ;GET THE LAST BIT INTO THE CARRY BIT
2689 015054 000261                SEC
2690 015056 000401                BR       9$      ;IF IT IS SET GO CLEAR IT
2691 015060 000241                8$: CLC
2692 015062 006105                9$: ROL      R5   ;IF IT IS CLEAR SET IT HERE
2693 015064 000305                10$: SWAB    R5   ;SKIP THE CLEARING
2694 015066 152705 000252                BISB    #252,R5  ;CLEAR THE CARRY BIT (INVERSION OF LINE PARITY)
2695 015072 052705 100000                BIS     #DVALID,R5 ;GET THE NEW BIT BACK INTO R5
2696 015076 005003                CLR      R3      ;MOVE THE LINE NUMBER TO THE UPPER BYTE
2697 015100 032777 000200 164734  11$: BIT     #RDONE,0DZCSR ;ADD CHARACTER
2698 015106 001004                BNE      12$     ;ADD DATA VALID
2699 015110 104414                DELAY
2700 015112 005203                INC      R3
2701 015114 001371                BNE      11$
2702 015116 104004                ERROR    4
2703 015120 017704 164722  12$: MOV     0DZRBUF,R4 ;*RDONE FAILED TO SET!
2704 015124 020405                CMP      R4,R5   ;LOAD THE VALUE ACTUALLY RECEIVED
2705 015126 001401                BEQ      13$     ;COMPARE ACTUAL VS EXPECTED. ARE THEY THE SAME?
2706 015130 104006                ERROR    6
2707 015132 104401                13$: SCOP1 ;IF YES GO DO THE NEXT LINE
2708 015134 040277 164716  14$: BIC     R2,0DZTCR ;*NO DATA/CONTENTS DID NOT COMPARE
2709 015140 005237 001372                INC     SAVLIN  ;CHECK TO SEE IF SWITCH NINE IS SET
2710 015144 013700 001372                MOV     SAVLIN,R0 ;CLEAR TCR BIT FOR THAT LINE.
2711 015150 006300                ASL     R0      ;INC EXPECTED LINE
2712 015152 106302                ASLB    R2      ;SET UP CHARACTER OFFSET
2713 015154 103302                BCC     3$      ;MAKE THE OFFSET A POWER OF TWO
2714 015156 104400                ADVANCE ;SHIFT THE LINE POINTER. ARE WE ALL DONE?
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2718 015160 032777 100000 164654  16$: BIT     #TRDY,0DZCSR ;IS TRANSMITTER READY?
2719 015166 001774                BEQ     16$     ;IF NOT, WAIT FOR IT
2720 015170 112777 000252 164670  15$: MOVB    #252,0DZTDR ;LOAD THE CHARACTER
2721 015176 104401                SCOP1 ;LOOP AGIN IF SW09=1
2722 015200 000755                BR      14$     ;OTHERWISE, GO PICK UP THE TEST NORMALLY
    
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015202 000004
015204 012737 000022 001122
015212 012737 015530 001360
015220 012737 015334 001362
015226 104417
015230 013701 001366
015234 012702 000001
015240 030237 001364
015244 001402
015246 010177 164600
015252 005201
015254 106302
015256 103370
015260 005037 001372
015264 012700 001422
015270 005020
015272 022700 001462
015276 001374
015300 005000
015302 013737 002046 001400
015310 012702 000001
015314 052777 000040 164520
015322 030237 001364
015326 001465
015330 010277 164522
015334 032777 000200 164500
015342 001401
015344 104020
015346 005005
015350 032777 100000 164464
015362 001004
015362 104414
015362 105205
015364 001371
015366 104003
015370 116077 001422 164470
015376 013705 001372
015402 105737 001371
015406 001406
015410 006205
015412 103402
015414 000261
015416 000401
015420 000241
015422 006105

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***** TEST 22 *****  
* THIS TEST PROVES THAT THE TRANSMITTER TRANSMITS  
* CHARACTERS (FLAG MODE) AND THE RECEIVER RECEIVES (FLAG MODE)  
* (ONE LINE AT A TIME BASED UPON VALID LINES)  
* THIS IS THE FIRST TIME THAT ALL DATA IS CHECKED  
::* TEST 22  
*****  
TST22: SCOPE  
MOV #22, $TSTNM ; LOAD THE NUMBER OF THIS TEST  
MOV #TST23, NEXT ; POINT TO THE START OF THE NEXT TEST  
MOV #4$, LOCK ; USE THIS ADDRESS IF A TIGHT SCOPE LOOP IS SELECTED  
DCLASM ; CLEAR DEVICE AND SET MAINT BIT IF I MODE  
MOV PAR, R1 ; PICK UP PARAMETERS  
MOV #1, R2 ; PICK UP INIT POINTER  
1$: BIT R2, LINE ; SHOULD THIS LINE BE SET UP ?  
BEQ 2$ ; NO  
MOV R1, @DZLPR ; SET UP LINE PARAMETERS  
2$: INC R1 ; POSITION POINTER TO THE NEXT LINE  
ASLB R2 ; GOT 'EM ALL ?  
BCC 1$ ; IF NO, GO SET UP THE NEXT LINE  
CLR SAVLIN ; CLEAR LINE # INDICATOR  
MOV #TDO, RO ; POINT TO THE DATA AREA  
CLR (RO) ; CLEAR A DATA WORD  
CMP #STOP, RO ; FINISHED ?  
BNE -6 ; NO  
CLR RO ; CLEAR OFFSET  
MOV DZRBUF, REGIST ; SAVE FOR ERROR MSG  
MOV #1, R2 ; LINE POINTER  
BIS #MSENAB, @DZCSR ; START SCANNER  
3$: BIT R2, LINE ; VALID LINE ?  
BEQ 14$ ; NO SET UP NEXT LINE  
MOV R2, @DZTCR ; SET TCR BIT  
4$: BIT #RDONE, @DZCSR ; IS REC DONE = 0 ?  
BEQ 5$ ; IF YES, ALLOW TIME FOR TRDY TO SET  
ERROR 20 ; *REC DONE SHOULD = 0  
5$: CLR R5  
6$: BIT #TRDY, @DZCSR  
BNE 7$  
DELAY  
INCB R5  
BNE 6$  
ERROR 3 ; *TRDY FAILED TO SET!  
7$: MOVB TDO(RO), @DZTDR ; LOAD CHARACTER  
MOV SAVLIN, R5 ; MAKE EXPECTED LINE #  
TSTB MODE+1 ; IS THIS TEST IN STAGGERED MODE?  
BEQ 10$ ; IF NOT, SKIP STAGGERED SETUP  
  
; WE MUST NOW INVERT THE LAST BIT OF THE LINE NUMBER  
ASR R5 ; GET THE LAST BIT INTO THE CARRY BIT  
BCS 8$ ; IF IT IS SET, GO CLEAR IT  
SEC ; IF IT IS CLEAR SET IT HERE  
BR 9$ ; SKIP THE CLEARING  
8$: CLC ; CLEAR THE CARRY BIT (INVERSION OF LINE PARITY)  
9$: ROL R5 ; GET THE NEW BIT BACK INTO R5
```

```

2779 015424 000305          10$: SWAB R5 ;MOVE THE LINE NUMBER TO THE UPPER BYTE
2780 015426 156005 001422   BISB T00(R0),R5 ;ADD CHARACTER
2781 015432 052705 100000   BIS #DVALID,R5 ;ADD DATA VALID
2782 015436 005003          CLR R3
2783 015440 032777 000200 164374 11$: BIT #RDONE, @DZCSR
2784 015446 001004          BNE 12$
2785 015450 104414          DELAY
2786 015452 005203          INC R3
2787 015454 001371          BNE 11$
2788 015456 104004          ERROR 4 ;*RDONE FAILED TO SET!
2789 015460 017704 164362 12$: MOV @DZRBUF,R4 ;LOAD THE VALUE ACTUALLY RECEIVED
2790 015464 020405          CMP R4,R5 ;COMPARE ACTUAL VS EXPECTED. ARE THEY THE SAME?
2791 015466 001401          BEQ 13$ ;IF YES, GO DO THE NEXT LINE
2792 015470 104006          ERROR 6 ;*NO DATA/CONTENTS DID NOT COMPARE
2793 015472 104401          13$: SCOP1 ;CHECK TO SEE IF SWITCH NINE IS SET
2794 015474 105260 001422   INCB T00(R0) ;INCREMENT BINARY PATTERN FOR THIS LINE
2795 015500 001315          BNE 4$ ;GO 'ROUND AGAIN FOR NEXT CHARACTER
2796 015502 040277 164350 14$: BIC R2,@DZTCR ;CLEAR TCR BIT FOR THAT LINE.
2797 015506 005237 001372 15$: INC SAVLIN ;INC EXPECTED LINE
2798 015512 013700 001372   MOV SAVLIN,R0 ;SET UP CHARACTER OFFSET
2799 015516 006300          ASL R0 ;MAKE THE OFFSET A POWER OF TWO
2800 015520 106302          ASLB R2 ;SHIFT THE LINE POINTER. ARE WE ALL DONE?
2801 015522 103277          BCC 3$ ;IF NO, GO AROUND AGAIN FOR NEXT LINE
2802 015524 005037 001362   CLR LOCK ;MAKE SURE LOCK IS CLEAR FOR NEXT TEST

```

```

2803
2804
2805 ;***** TEST 23 *****
2806 ;*THIS TEST WILL PROVE THAT:
2807 ;* 1) THE TRANSMITTER "BREAK BIT" WORKS
2808 ;* 2) THE RECEIVER CAN FLAG "FRAMING ERRORS"
2809 ;* 3) THE RECEIVER CAN FLAG "PARITY ERRORS"
2810 ;*ONLY ONE LINE AT A TIME WILL BE EXERCISED.
2811 ;*THIS TEST WILL NOT BE EXERCISED UNLESS
2812 ;*CONNECTED BY AN H325, H3271, OR H3190 CONNECTOR

```

```

2813 ;:* TEST 23
2814 ;*****
2815 015530 000004          †TST23: SCOPE
2816 015532 012737 000023 001122   MOV #23,$TSTNM ;LOAD THE NUMBER OF THIS TEST
2817 015540 012737 016006 001360   MOV #TST24,NEXT ;POINT TO THE START OF THE NEXT TEST
2818 015546 012737 015644 001362   MOV #3$,LOCK ;SET FOR LOOP
2819 015554 005737 001370          TST MODE ;ARE WE RUNNING IN INTERNAL MODE?
2820 015560 001510          BEQ 12$ ;IF SO, SKIP THIS TEST
2821 015562 104417          DCLASM ;CLEAR DEVICE AND SET MAINT BIT IF I MODE
2822 015564 013701 001366   MOV PAR,R1 ;PICK UP PARAMETERS
2823 015570 052701 000300   BIS #ODDPAR!PARITY,R1 ;FORCE ODD PARITY
2824 015574 012700 000001   MOV #1,R0 ;PICK UP INI1 POINTER
2825 015600 030037 001364 1$: BIT R0,LINE ;SHOULD THIS LINE BE SET UP ?
2826 015604 001402          BEQ 2$ ;IF NOT,DON'T SET IT UP
2827 015606 010177 164240   MOV R1,@DZLPR ;OTHERWISE, SET UP LINE PARAMETERS
2828 015612 005201          2$: INC R1
2829 015614 106300          ASLB R0 ;GOT 'EM ALL ?
2830 015616 103370          BCC 1$ ;NO
2831 015620 005037 001372   CLR SAVLIN ;CLEAR LINE #
2832 015624 012702 000001   MOV #1,R2 ;LINE POINTER
2833 015630 052777 000040 164204   BIS #MSENAB,@DZCSR ;SET MASTER SCAN ENABLE
2834 015636 013737 002046 001400   MOV DZRBUF,REGIST ;SAVE FOR ERRR MESSAGE

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2935 015644 030237 001364 3$: BIT R2,LINE
2936 015650 001446 BEQ 11$
2937 015652 010277 164200 MOV R2,JDZTCR ;SET TCR BIT
2938 015656 110277 164206 MOV R2,JDZTDR ;SET BREAK BIT
2939 015662 112777 000377 164176 4$: MOV R3,JDZTDR ;LOAD CHARACTER
2940 015670 013705 001372 MOV SAVLIN,R5 ;MAKE EXPECTED DATA
2941 015674 105737 001371 TSTB MODE+1 ;IS THIS TEST IN STAGGERED MODE?
2942 015700 001406 BEQ 7$ ;IF NOT, SKIP STAGGERED SETUP
2943
2944 ;WE MUST NOW INVERT THE LAST BIT OF THE LINE NUMBER
2945
2946 015702 006205 ASR R5 ;GET THE LAST BIT INTO THE CARRY BIT
2947 015704 103402 BCS 5$ ;IF IT IS SET, GO CLEAR IT
2948 015706 000261 SEC ;IF IT IS CLEAR SET IT HERE
2949 015710 000401 BR 6$ ;SKIP THE CLEARING
2950 015712 000241 5$: CLC ;CLEAR THE CARRY BIT (INVERSION OF LINE PARITY)
2951 015714 006105 6$: ROL R5 ;GET THE NEW BIT BACK INTO R5
2952 015716 000305 7$: SWAB R5 ;PUT LINE NUMBER IN UPPER BYTE
2953 015720 052705 130000 BIS #DVALID!PARER!FRMERR,R5 ;ADD EXPECTED
2954 015724 005004 CLR R4
2955 015726 032777 000200 164106 8$: BIT #RDONE,JDZCSR
2956 015734 001004 BNE 9$
2957 015736 104414 DELAY
2958 015740 005204 INC R4
2959 015742 001371 BNE 8$
2960 015744 104004 ERROR 4 ;*RDONE FAILED TO SET!
2961 015746 017704 164074 9$: MOV JDZRBUF,R4 ;ACTUAL
2962 015752 020405 CMP R4,R5 ;CMP ACTUAL VS EXPECTED. DO THEY MATCH?
2963 015754 001401 BEQ 10$ ;IF YES, GO CLEAN UP
2964 015756 104006 ERROR 6 ;*DATA/CONTENTS FAILED TO COMPARE
2965 015760 105077 164104 10$: CLRB JDZTDR ;CLEAR BREAK BITS
2966 015764 104401 SCOP1 ;LOOP?
2967 015766 005237 001372 11$: INC SAVLIN ;INC LINE #
2968 015772 040277 164060 BIC R2,JDZTCR ;CLEAR TCR BIT
2969 015776 106302 ASLB R2
2970 016000 103321 BCC 3$
2971 016002 005037 001362 12$: CLR LOCK ;MAKE SURE LOCK IS CLEAR FOR NEXT TEST
2972 ;***** TEST 24 *****
2973 ;* THIS TEST VERIFIES THAT THE DEVICE DOES NOT INTERRUPT
2974 ;*WHILE THE PROCESSOR STATUS IS SET EXACTLY
2975 ;*TO WHAT THE DZ11 PRIORITY IS SET TO.
2976 ;*DEFAULT PRIORITY IS AT 5 (240).
2977 ;:* TEST 24
2978 ;*:* *****
2979 016006 000004 15:24: SCOPE
2980 016010 012737 000024 001122 MOV #24,$STSTNM ;LOAD THE NUMBER OF THIS TEST
2981 016016 012737 016316 001360 MOV #TST25,NEXT ;POINT TO THE START OF THE NEXT TEST
2982 016024 104417 DCLASM ;CLEAR DEVICE AND SET MAINT BIT IF I MODE
2983 016026 013701 001366 MOV PAR,R1 ;PICK UP PARAMETERS
2984 016032 012702 000001 MOV #1,R2 ;PICK UP INIT POINTER
2985 016036 030237 001364 1$: BIT R2,LINE ;SHOULD THIS LINE BE SET UP ?
2986 016042 001402 BEQ 2$ ;NO
2987 016044 010177 164002 MOV R1,JDZLPR ;SET UP LINE PARAMETERS
2988 016050 005201 2$: INC R1 ;POSITION POINTER TO THE NEXT LINE
2989 016052 106302 ASLB R2 ;GOT 'EM ALL ?
2990 016054 103370 BCC 1$ ;IF NO, GO SET UP THE NEXT LINE

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2891 016056 005037 001372          CLR      SAVLIN          ;CLEAR LINE # INDICATOR
2892 016062 106437 026454          MTPS    DZDZPRT        ;SET CPU STATUS TO DZ11 PRIO,
2893 016066 113777 001364 163762      MOVB    LINE,DZDZTCR   ;ENABLE THE VALID LINES
2894 016074                                     3$:
2895 016074 012777 016164 163774      MOV     #65,DZDZTIV    ;SET UP THE TRANSMITTER INTERRUPT VECTOR
2896 016102 012777 016172 163762      MOV     #75,DZDZRIV    ;SET UP THE RECEIVER INTERRUPT VECTOR
2897 016110 013777 026454 163756      MOV     DZDZPRT,DZDZRIS ;SET THE INTERRUPT VECTOR STATUS
2898 016116 013777 026454 163754      MOV     DZDZPRT,DZDZTIS ;SET TRANSMITTER INTERRUPT PRIORITY
2899 016124 052777 040040 163710      BIS     #TIE!MSENAB,DZDZCSR ;ENABLE THE DEVICE
2900 016132 005005                                     CLR      R5
2901 016134 032777 100000 163700      4$:    BIT     #TRDY,DZDZCSR
2902 016142 001403                                     BEQ     5$
2903 016144 000240                                     NOP
2904 016146 000240                                     NOP
2905 016150 000412                                     BR      8$
2906 016152 104414                                     5$:    DELAY
2907 016154 005205                                     INC     R5
2908 016156 001366                                     BNE    4$
2909 016160 104003                                     ERROR  3                ;*TRDY NOT SET!
2910 016162 000405                                     BR      8$
2911 016164 104010                                     6$:    ERROR 10                ;*TRANSMITTER SHOULD NOT INTERRUPT
2912 016166 022626                                     CMP     (SP)+,(SP)+    ;POP FOR FAKE RTI
2913 016170 000402                                     BR      8$                ;CONTINUE TEST
2914 016172 104012                                     7$:    ERROR 12                ;*RECEIVER SHOULD NOT INTERRUPT
2915 016174 022626                                     CMP     (SP)+,(SP)+    ;POP FOR FAKE RTI
2916 016176 042777 040000 163636      8$:    BIC     #TIE,DZDZCSR ;RESET TRANSMITTER INTERRUPT ENABLE
2917 016204 012777 016302 163664      MOV     #115,DZDZTIV   ;SET UP THE TRANSMITTER INTERRUPT VECTOR
2918 016212 012777 016310 163652      MOV     #125,DZDZRIV   ;SET UP THE RECEIVER INTERRUPT VECTOR
2919 016220 013777 026454 163646      MOV     DZDZPRT,DZDZRIS ;SET THE INTERRUPT VECTOR STATUS
2920 016226 013777 026454 163644      MOV     DZDZPRT,DZDZTIS ;SET TRANSMITTER INTERRUPT PRIORITY
2921 016234 052777 000140 163600      BIS     #RIE!MSENAB,DZDZCSR ;ENABLE THE DEVICE
2922 016242 113777 001422 163616      MOVB    TDD,DZDZTDR    ;PUT ANY RANDOM CHARACTER IN TRANSMITTER BUFFER
2923 016250 005005                                     CLR      R5
2924 016252 032777 000200 163562      9$:    BIT     #RDONE,DZDZCSR
2925 016260 001403                                     BEQ     10$
2926 016262 000240                                     NOP
2927 016264 000240                                     NOP
2928 016266 000412                                     BR      13$
2929 016270 104414                                     10$:   DELAY
2930 016272 005205                                     INC     R5
2931 016274 001366                                     BNE    9$
2932 016276 104004                                     ERROR  4                ;*NO RX DONE! (NOT SET)
2933 016300 000405                                     BR      13$                ;CONTINUE TEST
2934 016302 104010                                     11$:   ERROR 10                ;*TRANSMITTER SHOULD NOT INTERRUPT
2935 016304 022626                                     CMP     (SP)+,(SP)+    ;POP FOR FAKE RTI
2936 016306 000402                                     BR      13$                ;CONT TEST
2937 016310 104012                                     12$:   ERROR 12                ;*RECEIVER SHOULD NOT INTERRUPT
2938 016312 022626                                     CMP     (SP)+,(SP)+    ;POP FOR FAKE RTI
2939 016314                                     13$:
2940 016314 104413          DEVICE.CLR          ;ISSUE DEVICE CLEAR (RESET)
2941                                     ;***** TEST 25 *****
2942                                     ;* THIS TEST VERIFIES THAT THE DEVICE DOES INTERRUPT
2943                                     ;*WHILE THE PROCESSOR STATUS IS SET TO EXACTLY
2944                                     ;*ONE LEVEL LOWER THAN THE DZ11. DZ11 PRIORITY
2945                                     ;*DEFAULT TO LEVEL 5 MINUS ONE LEVEL IS LEVEL 4.
2946 ;:* TEST 25
    
```

```

2947
2948 016316 000004
2949 016320 012737 000025 001122
2950 016326 012737 016644 001360
2951 016334 104417
2952 016336 013701 001366
2953 016342 012702 000001
2954 016346 030237 001364 15:
2955 016352 001402
2956 016354 010177 163472
2957 016360 005201 25:
2958 016362 106302
2959 016364 103370
2960 016366 005037 001372
2961 016372 106437 026456
2962 016376 113777 001364 163452
2963 016404
2964 016404 012777 016476 163464 35:
2965 016412 012777 016514 163452
2966 016420 013777 026454 163446
2967 016426 013777 026454 163444
2968 016434 052777 040040 163400
2969 016442 005005
2970 016444 032777 100000 163370 45:
2971 016452 001404
2972 016454 000240
2973 016456 000240
2974 016460 104007
2975 016462 000416
2976 016464 104414 55:
2977 016466 005205
2978 016470 001365
2979 016472 104003
2980 016474 000411
2981 016476 022626
2982 016500 042777 040000 163334 65:
2983 016506 106437 026456
2984 016512 000402
2985 016514 104012 75:
2986 016516 022626
2987 016520 042777 040000 163314 85:
2988 016526 012777 016626 163342
2989 016534 012777 016634 163330
2990 016542 013777 026454 163324
2991 016550 013777 026454 163322
2992 016556 052777 000140 163256
2993 016564 113777 001422 163274
2994 016572 005005
2995 016574 032777 000200 163240 95:
2996 016602 001404
2997 016604 000240
2998 016606 000240
2999 016610 104011
3000 016612 000413
3001 016614 104414 105:
3002 016616 005205

```

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*****
↑ST25: SCOPE
MOV #25, $STNM ;LOAD THE NUMBER OF THIS TEST
MOV #↑ST26, NEXT ;POINT TO THE START OF THE NEXT TEST
OCLASM ;CLEAR DEVICE AND SET MAINT BIT IF I MODE
MOV PAR, R1 ;PICK UP PARAMETERS
MOV #1, R2 ;PICK UP INIT POINTER
15: BIT R2, LINE ;SHOULD THIS LINE BE SET UP ?
BEQ 25 ;NO
MOV R1, @DZLPR ;SET UP LINE PARAMETERS
25: INC R1 ;POSITION POINTER TO THE NEXT LINE
ASLB R2 ;GOT 'EM ALL ?
BCC 15 ;IF NO, GO SET UP THE NEXT LINE
CLR SAVLIN ;CLEAR LINE # INDICATOR
MTPS @#LESS1 ;MAKE CPU ONE LEVEL LOWER THAN DZ11
MOV#B LINE, @DZTCR ;ENABLE THE VALID LINES
35: MOV #65, @DZTIV ;SET UP THE TRANSMITTER INTERRUPT VECTOR
MOV #75, @DZRV ;SET UP THE RECEIVER INTERRUPT VECTOR
MOV DZPRT, @DZRV ;SET THE INTERRUPT VECTOR STATUS
MOV DZPRT, @DZTIV ;SET TRANSMITTER INTERRUPT PRIORITY
BIS #TIE!#SENAB, @DZCSR ;ENABLE THE DEVICE
CLR R5
45: BIT #TRDY, @DZCSR
BEQ 55
NOP
ERROR 7 ;*TRANSMITTER FAILED TO INTERRUPT
BR 85
55: DELAY
INC R5
BNE 45
ERROR 3 ;*TRDY NOT SET!
BR 85
65: POP2SP ;REMOVE THE INTERRUPT FROM THE STACK
BIC #TIE, @DZCSR ;DON'T LET ANY MORE INTERRUPTS OCCUR
MTPS @#LESS1 ;MAKE CPU ONE LEVEL LOWER THAN DZ11
BR 85 ;RETURN TO THE NORMAL FLOW
75: ERROR 12 ;*RECEIVER SHOULD NOT INTERRUPT
CMP (SP)+, (SP)+ ;POP FOR FAKE RTI
BIC #TIE, @DZCSR ;RESET TRANSMITTER INTERRUPT ENABLE
MOV #115, @DZTIV ;SET UP THE TRANSMITTER INTERRUPT VECTOR
MOV #125, @DZRV ;SET UP THE RECEIVER INTERRUPT VECTOR
MOV DZPRT, @DZRV ;SET THE INTERRUPT VECTOR STATUS
MOV DZPRT, @DZTIV ;SET TRANSMITTER INTERRUPT PRIORITY
BIS #RIE!#SENAB, @DZCSR ;ENABLE THE DEVICE
MOV#B TDO, @DZTDR ;PUT ANY RANDOM CHARACTER IN TRANSMITTER BUFFER
CLR R5
95: BIT #RDONE, @DZCSR
BEQ 105
NOP
NOP
ERROR 11 ;*RECEIVER FAILED TO INTERRUPT
BR 135
105: DELAY
INC R5

```

```

3003 016620 001365
3004 016622 104004
3005 016624 000406
3006 016626 104010
3007 016630 022626
3008 016632 000403
3009 016634 022626
3010 016636 005077 163200
3011 016642
3012 016642 104413
3013
3014
3015
3016
3017
3018
3019
3020
3021
3022
3023
3024 016644 000004
3025 016646 012737 000026 001122
3026 016654 012737 017276 001360
3027 016662 104417
3028 016664 013701 001366
3029 016670 012702 000001
3030 016674 030237 001364
3031 016700 001402
3032 016702 010177 163144
3033 016706 005201
3034 016710 106302
3035 016712 103370
3036 016714 005037 001372
3037 016720 012777 017150 163144
3038 016726 013777 026454 163140
3039 016734 012777 017240 163134
3040 016742 013777 026454 163130
3041 016750 052777 000040 163064
3042 016756 012702 000001
3043 016762 030237 001364
3044 016766 001004
3045 016770 005237 001372
3046 016774 106302
3047 016776 000771
3048 017000 106427 000340
3049 017004 000240
3050 017006 000240
3051 017010 110277 163042
3052 017014 005777 163026
3053 017020 100001
3054 017022 104017
3055 017024 105777 163012
3056 017030 100001
3057 017032 104020
3058 017034 005005

```

```

BNE 95
ERROR 4 ;*NO RX DONE! (NOT SET)
BR 135 ;CONTINUE TEST
115: ERROR 10 ;*TRANSMITTER SHOULD NOT INTERRUPT
CMP (SP)+,(SP)+ ;POP FOR FAKE RTI
BR 135 ;CONT TEST
125: POP2SP ;REMOVE THE INTERRUPT FROM THE STACK
CLR 2DZCSR ;DON'T ALLOW ANY MORE INTERRUPTS
135: DEVICE.CLR ;ISSUE DEVICE CLEAR (RESET)

;***** TEST 26 *****
;THIS TEST VERIFIES THAT THE RECEIVER WILL
;INTERRUPT BEFORE THE TRANSMITTER EVEN
;THOUGH THE TRANSMITTER WAS ENABLED
;FIRST. SET PS TO LEVEL 7;
;GET RDONE AND TRY TO SET;
;SET TX IE AND RX IE;
;CLEAR PS AND EXPECT RX TO INTERRUPT FIRST
;:* TEST 26
;*****
TST26: SCOPE
MOV #26,$STNM ;LOAD THE NUMBER OF THIS TEST
MOV #TST27,NEXT ;POINT TO THE START OF THE NEXT TEST
DCLASM ;CLEAR DEVICE AND SET MAINT BIT IF I MODE
MOV PAR,R1 ;PICK UP PARAMETERS
MOV #1,R2 ;PICK UP INIT POINTER
15: BIT R2,LINE ;SHOULD THIS LINE BE SET UP ?
BEQ 25 ;NO
MOV R1,2DZLPR ;SET UP LINE PARAMETERS
25: INC R1 ;POSITION POINTER TO THE NEXT LINE
ASLB R2 ;GOT 'EM ALL ?
BCC 15 ;IF NO, GO SET UP THE NEXT LINE
CLR SAVLIN ;CLEAR LINE # INDICATOR
MOV #85,2DZRIV ;SETUP INTERRUPT STUFF
MOV DZPR1,2DZRIS
MOV #125,2DZTIV
MOV DZPR1,2DZTIS
BIS #MSENAB,2DZCSR
MOV #1,R2 ;LINE POINTER
35: BIT R2,LINE ;VALID LINE ?
BNE 45
INC SAVLIN
ASLB R2
BR 35
45: MTPS #PR7
NOP
NOP
MOV R2,2DZTCR ;SET TCR BIT
TST 2DZRBUF ;VALID DATA?
BPL .+4 ;IT BETTER NOT BE SET
ERROR 17 ;DATA VALID SHOULD NOT BE SET
55: TSTB 2DZCSR ;RECEIVER DONE ?
BPL .+4
ERROR 20 ;RECEIVER DONE BIT SHOULD NOT BE SET
CLR R5

```

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3059 017036 005004
3060 017040 005777 162776          99$: CLR R4
3061 017044 100404          TST 2DZCSR ;WAIT FOR TRDY
3062 017046 104414          BMI 100$ ;BR IF READY
3063 017050 005204          DELAY ;STALL TIME
3064 017052 001372          INC R4
3065 017054 104003          BNE 99$
3066 017056 105077 163004          ERROR 3 ;TRDY FAILED TO SET
3067 017062 005004          CLRB 2DZTDR
3068 017064 032777 000200 162750 6$: CLR R4
3069 017072 001004          BIT 2RDONE,2DZCSR
3070 017074 104414          BNE 7$
3071 017076 005204          DELAY
3072 017100 001371          INC R4
3073 017102 104004          BNE 6$
3074 017104 005777 162732          7$: ERROR 4 ;*RDONE FAILED TO SET!
3075 017110 100401          TST 2DZCSR ;TRANS DONE BIT = 1 ?
3076 017112 104003          BMI +4 ;YES
3077          ERROR 3 ;*NO TRANS DONE FAILED TO SET
3078          ;NOW THAT BOTH TRANSMITTER AND RECEIVER DONE BIT =1
3079 017114 052777 040000 162720          ;SET INTERRUPT ENABLES AND WATCH THE FUR FLY
3080 017122 052777 000100 162712          BIS 2TIE,2DZCSR
3081 017130 106427 000000          BIS 2RIE,2DZCSR
3082 017134 000240          MTPS 0
3083 017136 000240          NOP
3084 017140 104007          ERROR 7 ;*TRANSMITTER FAILED TO INTERRUPT
3085 017142 104011          ERROR 11 ;*RECEIVER FAILED TO INTERRUPT
3086          ;CHECK BR LEVEL
3087 017144 000137 017244          JMP 13$ ;GET OUT
3088
3089          ;RECEIVER INTERRUPT ROUTINE
3090 017150 017704 162672          8$: MOV 2DZRBUF,R4 ;ACTUAL
3091 017154 010403          MOV R4,R3
3092 017156 000303          SWAB R3
3093 017160 042703 177770          BIC 2C<7>,R3 ;STRIP JUNK
3094 017164 105737 001371          TSTB MODE+1 ;IS THIS TEST IN STAGGERED MODE?
3095 017170 001406          BEQ 11$ ;IF NOT, SKIP STAGGERED SETUP
3096
3097          ;WE MUST NOW INVERT THE LAST BIT OF THE LINE NUMBER
3098
3099 017172 006203          ASR R3 ;GET THE LAST BIT INTO THE CARRY BIT
3100 017174 103402          BCS 9$ ;IF IT IS SET GO CLEAR IT
3101 017176 000261          SEC ;IF IT IS CLEAR SET IT HERE
3102 017200 000401          BR 10$ ;SKIP THE CLEARING
3103 017202 000241          9$: CLC ;CLEAR THE CARRY BIT (INVERSION OF LINE PARITY)
3104 017204 006103          10$: ROL R3 ;GET THE NEW BIT BACK INTO R3
3105 017206 020337 001372          11$: CMP R3,2SAVLIN ;IS THIS A VALID LINE
3106 017212 001401          BEQ +4 ;YES
3107 017214 104015          ERROR 15 ;*INVALID LINE
3108 017216 042704 177400          BIC 2C<37>,R4 ;STRIP JUNK
3109 017222 120504          CMPB R5,R4 ;DATA COMPARE ?
3110 017224 001401          BEQ +4 ;YES
3111 017226 104005          ERROR 5 ;*DATA DOES NOT COMPARE
3112 017230 040277 162622          BIC R2,2DZTCR ;CLEAR TCR BIT
3113 017234 022626          POP2SP ;REMOVE THE INTERRUPT VECTOR FROM THE STACK
3114 017236 000402          BR 13$ ;GO GET OUT OF INTERRUPT MODE
    
```



```

3115      : TRANSMITTER INTERRUPT SVC ROUTINE
3116 017240 104011      12$:  ERROR 11      : THE RECEIVER INTERRUPT FAILED
3117      : TO OVERRIDE THE TRANSMITTER
3118 017242 022626      POP2SP      : REMOVE THE INTERRUPT VECTOR FROM THE STACK
3119 017244 042777 040100 162570 13$:  BIC #TIE!RIE, DZCSR : CLEAR INTERRUPT ENABLES
3120 017252 013777 002074 162612      MOV DZRI5, DZRIV : RESTORE TRAPCATCHER
3121 017260 005077 162610      CLR DZRI5
3122 017264 013777 002100 162604      MOV DZTI5, DZTIV
3123 017272 005077 162602      CLR DZTI5
3124      : ***** TEST 27 *****
3125      : *THIS TEST VERIFIES OVERRUN AND SILO ALARM
3126      : *ONE LINE AT A TIME - BASED UPON VALID LINES
3127      : *AS EACH OF THE FIRST 16 CHARS ARE SENT; SILO ALARM IS
3128      : *TESTED TO BE CLEARED. ON THE 16TH CHAR THE PROGRAM THEN
3129      : *EXPECTS SILO ALARM TO SET. THEN THE ENTIRE
3130      : *SILO IS FILLED AND AN OVERRUN IS EXPECTED ON THE 65TH
3131      : *CHAR PULLED OUT OUT THE SILO.
3132      : *USING SWITCH NINE FOR THIS TEST SENDS 20. CHARACTERS
3133      : *ON DZ LINE PREVIOUSLY SELECTED CONTINUOUSLY WHILE SW09=1.
3134      : *USED TO SCOPE SILO ALARM PULSES, ETC.
3135      :;* TEST 27
3136      :*****
3137 017276 000004      †ST27: SCOPE
3138 017300 012737 000027 001122      MOV #27, STSTNM : LOAD THE NUMBER OF THIS TEST
3139 017306 012737 020024 001360      MOV #ST30, NEXT : POINT TO THE START OF THE NEXT TEST
3140 017314 012737 01773C 001362      MOV #18$, LOCK : SET FOR LOOP
3141 017322 104417      DCLASM      : CLEAR DEVICE AND SET MAINT BIT IF I MODE
3142 017324 013701 001366      MOV PAR, R1 : PICK UP PARAMETERS
3143 017330 012702 000001      MOV #1, R2 : PICK UP INIT POINTER
3144 017334 030237 001364      1$:  BIT R2, LINE : SHOULD THIS LINE BE SET UP ?
3145 017340 001402      BEQ 2$ : NO
3146 017342 010177 162504      MOV R1, DZLPR : SET UP LINE PARAMETERS
3147 017346 005201      2$:  INC R1 : POSITION POINTER TO THE NEXT LINE
3148 017350 106302      ASLB R2 : GOT 'EM ALL ?
3149 017352 103370      BCC 1$ : IF NO, GO SET UP THE NEXT LINE
3150 017354 005037 001372      CLR SAVLIN : CLEAR LINE # INDICATOR
3151 017360 012700 001422      MOV #TDO, RO : POINT TO THE DATA AREA
3152 017364 005020      CLR (RO)+ : CLEAR A DATA WORD
3153 017366 022700 001462      CMP #STOP, RO : FINISHED ?
3154 017372 001374      BNE .-6 : NO
3155 017374 005000      CLR RO : CLEAR OFFSET
3156 017376 012702 000001      MOV #1, R2 : LINE POINTER
3157 017402 052777 010040 162432      BIS #MSENAB!SILOEN, DZCSR : START SCANNER & SET SILO ENABLE
3158 017410 030237 001364      3$:  BIT R2, LINE : VALID LINE?
3159 017414 001002      BNE .+6 : YES
3160 017416 003137 017712      JMP 22$ : TRY NEXT LINE
3161 017422 013700 001372      MOV SAVLIN, RO : MAKE OFFSET
3162 017426 006300      ASL RO : MAKE POWER OF TWO
3163 017430 010277 162422      MOV R2, DZTCR : SET TCR BIT
3164 017434 105777 162402      4$:  TSTB DZCSR : REC DONE = 1 ?
3165 017440 100001      BPL .+4
3166 017442 104020      ERROR 20 : REC DONE SHOULD NOT = 1
3167 017444 005003      CLR R3 : SET CHARACTER COUNT
3168 017446 005004      5$:  CLR R4
3169 017450 032777 100000 162364      6$:  BIT #TRDY, DZCSR
3170 017456 001004      BNE 7$

```

3171	017460	104414				DELAY				
3172	017462	105204				INCB	R4			
3173	017464	001371				BNE	6\$			
3174	017466	104003				ERROR	3			
3175	017470	115077	001422	162370	7\$:	MOV B	TDO(R0),DZTDR			; *TRDY FAILED TO SET
3176	017476	005260	001422			INC	TDO(R0)			; LOAD A CHARACTER
3177	017502	020327	000017			CMP	R3,#15.			; SET UP NEXT CHARACTER
3178	017506	103006				BHIS	8\$; 16 CHARACTERS ?
3179	017510	032777	020000	162324		BIT	#SILOAL,DZCSR			; SILO ALARM = 0 ?
3180	017516	001401				BEQ	+4			; YES
3181	017520	104013				ERROR	13			; *SILO ALARM SHOULD NOT = 1
3182										; UNTIL 16. DATA CHARACTERS
3183	017522	000411				BR	10\$			
3184	017524	005004			8\$:	CLR	R4			
3185	017526	032777	020000	162306	9\$:	BIT	#SILOAL,DZCSR			
3186	017534	001004				BNE	10\$			
3187	017536	104414				DELAY				
3188	017540	005204				INC	R4			
3189	017542	001371				BNE	9\$			
3190	017544	104014				ERROR	14			; *SILO ALARM FAILED TO SET!
3191										; SILO ALARM SHOULD =1 AFTER 16.
3192										; DATA CHARACTERS
3193	017546	005203			10\$:	INC	R3			; INC CHAR COUNT
3194	017550	022703	000102			CMP	#66.,R3			; FINISHED SENDING CHARACTERS ?
3195	017554	001334				BNE	5\$; NO
3196	017556	005004				CLR	R4			
3197	017560	104414				DELAY				
3198	017562	105204				INCB	R4			
3199	017564	001375				BNE	-4			
3200										; NOW LETS READ THE SILO
3201	017566	013705	001372			MOV	SAVLIN,R5			; MAKE EXPECTED LINE #
3202	017572	105737	001371			TSTB	MODE+1			; IS THIS TEST IN STAGGERED MODE?
3203	017576	001406				BEQ	13\$; IF NOT, SKIP STAGGERED SETUP
3204										
3205										; WE MUST NOW INVERT THE LAST BIT OF THE LINE NUMBER
3206										
3207	017600	006205				ASR	R5			; GET THE LAST BIT INTO THE CARRY BIT
3208	017602	103402				BCS	11\$; IF IT IS SET, GO CLEAR IT
3209	017604	000261				SEC				; IF IT IS CLEAR SET IT HERE
3210	017606	000401				BR	12\$; SKIP THE CLEARING
3211	017610	000241			11\$:	CLC				; CLEAR THE CARRY BIT (INVERSION OF LINE PARITY)
3212	017612	006105			12\$:	ROL	R5			; GET THE NEW BIT BACK INTO R5
3213	017614	000305			13\$:	SWAB	R5			; PUT IN UPPER BYTE
3214	017616	052705	100000			BIS	#OVALID,R5			; ADD DATA VALID
3215	017622	017704	162220		14\$:	MOV	DZRBUF,R4			; ACTUAL
3216	017626	020405				CMP	R4,R5			; ACTUAL VS. EXPECTED
3217	017630	001401				BEQ	15\$; YES
3218	017632	104006				ERROR	6			; *DATA/CONTENTS DID NOT COMPARE
3219	017634	032777	020000	162200	15\$:	BIT	#SILOAL,DZCSR			; SILO ALARM= 0 ?
3220	017642	001401				BEQ	16\$; YES
3221	017644	104016				ERROR	16			; READING DZRBUF DID NOT CLEAR SILO ALARM
3222	017646	005205			16\$:	INC	R5			; UP CHARACTER
3223	017650	120527	000077			CMPB	R5,#63.			; LAST SILO CHAR ?....64TH CHAR
3224	017654	101762				BLOS	14\$			
3225	017656	005205				INC	R5			; ADD 1 MORE FOR THE CLOBBEDED CHAR
3226	017660	052705	040000			BIS	#OVRUN,R5			; ADD OVERRUN TO EXPECTED

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3227 017664 120527 000101      CMPB   R5,#65.      ;LAST CHARACTER ?
3228 017670 001754              BEQ    14$
3229 017672 017704 162150      MOV    @DZRBUF,R4  ;FOR GOOD MEASURE
3230 017676 005704              TST    R4          ;DATA VALID SHOULD = 0
3231 017700 100001              BPL    17$        ;YES
3232 017702 104017              ERROR  17         ;DATA VALID SHOULD = 0
3233 017704 040277 162146      17$:  BIC    R2,@DZTCR ;CLR TCR BIT
3234 017710 104401              SCOPI  ;LOOP?
3235 017712 005237 001372      22$:  INC    SAVLIN  ;INC EXPECTED LINE
3236 017716 106302              ASLB   R2         ;NEXT LINE
3237 017720 103402              BCS    .+6       ;NO
3238 017722 000137 017410      JMP    3$        ;YES
3239 017726 104400              ADVANCE ;GO TO NEXT TEST
3240
3241
3242 ;TIGHT SCOPE LOOP FOR THIS TEST. SENDS 20. CHARACTERS
3243 ;ON DZ LINE PREVIOUSLY SELECTED CONTINUOUSLY WHILE SW09=1.
3244 ;USED TO SCOPE SILO ALARM PULSES, ETC.
3245 017730 052777 010040 162104 18$:  BIS    #MSENAB!SILOEN,@DZCSR ;SETUP DEVICE
3246 017736 012777 020014 162132      MOV    #20,@DZTIV ;SETUP TRANSMITTER VECTOR
3247 017744 012737 000024 001216      MOV    #20,@STMPO ;TEMPORARY COUNT OF CHARACTER BURST
3248 017752 050277 162100      BIS    R2,@DZTCR  ;ENABLE LINE
3249 017756 052777 040000 162056      BIS    #TIE,@DZCSR ;ENABLE INTERRUPTS
3250 017764 106427 000000      MTPS   #0        ;LOWER PRIORITY
3251 017770 000001              WAIT   ;ALLOW INTERRUPTS
3252 017772 005337 001216      19$:  DEC    @STMPO   ;REDUCE COUNT. ALL CHARACTERS SENT?
3253 017776 001374              BNE    19$       ;IF NO, WAIT FOR MORE
3254 020000 042777 050040 162034      BIC    #SILOEN!MSENAB!TIE,@DZCSR ;RESET SILO COUNTER, CLEAR STROBE
3255 020006 104401              SCOPI  ;LOOP AGAIN?
3256 020010 000137 017704              JMP    17$      ;IF NOT, RETURN TO WHERE YOU LEFT OFF
3257 020014 112777 000252 162044 20$:  MOVB   #252,@DZTDR ;SEND A CHARACTER
3258 020022 000002              RTI    ;ALLOW MORE CHARACTERS TO COME
3259
3260 ;***** TEST 30 *****
3261 ;*THIS TEST THAT "SILO ENABLE" WILL INHIBIT
3262 ;*RECEIVER INTERRUPTS AND THAT ON THE
3263 ;*16TH CHAR THAT "SILO ALARM" WILL CAUSE AN
3264 ;*INTERRUPT WITH "RIE" SET.
3265 ;*THIS WILL DO ALL SELECTED LINES ONE AT A TIME.
3266 ;:* TEST 30
3267 ;*****
3268 ;*ST30: SCOPE
3269 ;*****
3270 ;*****
3271 ;*****
3272 ;*****
3273 ;*****
3274 ;*****
3275 ;*****
3276 ;*****
3277 ;*****
3278 ;*****
3279 ;*****
3280 ;*****
3281 ;*****
3282 ;*****
3267 020024 000004              MOV    #30,@STNM  ;LOAD THE NUMBER OF THIS TEST
3268 020026 012737 000030 001122      MOV    #TST31,NEXT ;POINT TO THE START OF THE NEXT TEST
3269 020034 012737 020406 001360      MOV    #35,LOCK  ;SET FOR LOOP
3270 020042 012737 020130 001362      DCLASM ;CLEAR DEVICE AND SET MAINT BIT IF I MODE
3271 020050 104417              MOV    PAR,R1    ;PICK UP PARAMETERS
3272 020052 013701 001366      MOV    #1,R2    ;PICK UP INIT POINTER
3273 020056 012702 000001              BIT    R2,LINE  ;SHOULD THIS LINE BE SET UP ?
3274 020062 030237 001364      1$:  BEQ    2$        ;NO
3275 020066 001402              MOV    R1,@DZLPR ;SET UP LINE PARAMETERS
3276 020070 010177 161756      2$:  INC    R1        ;POSITION POINTER TO THE NEXT LINE
3277 020074 005201              ASLB   R2        ;GOT 'EM ALL ?
3278 020076 106302              BCC    1$       ;IF NO, GO SET UP THE NEXT LINE
3279 020100 103370              CLR    SAVLIN   ;CLEAR LINE # INDICATOR
3280 020102 005037 001372      MOV    #TDD,RO  ;POINT TO THE DATA AREA
3281 020106 012700 001422      CLR    (RO)+    ;CLEAR A DATA WORD
3282 020112 005020

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3283	020114	022700	001462			CMP	#STOP, R0	; FINISHED ?
3284	020120	001374				BNE	-6	; NO
3285	020122	005000				CLR	R0	; CLEAR OFFSET
3286	020124	012702	000001			MOV	#1, R2	; LINE POINTER
3287	020130	012777	020350	161734	3\$:	MOV	#11\$, @DZRIV	; SET FOR UNEXPECTED INTER.
3288	020136	012777	000340	161730		MOV	#PR7, @DZRI5	; SET PRIO.
3289	020144	052777	010140	161670		BIS	#MSENAB!SILOEN!R1E, @DZCSR	; START SCANNER & SET SILO ENABLE
3290								; VALID LINE?
3291	020152	030237	001364			BIT	R2, LINE	; YES
3292	020156	001002				BNE	+6	; TRY NEXT LINE
3293	020160	000137	020366			JMP	22\$; EMPTY THE SILO
3294	020164	005777	161656			TST	@DZRBUF	; BR IF DATA VALID IS SET!
3295	020170	100775				BMI	-4	; SET PROCESSOR PRIORITY TO 0
3296	020172	106427	000000			MTPS	#0	; MAKE OFFSET
3297	020176	013700	001372			MOV	SAVLIN, R0	; MAKE POWER OF TWO
3298	020202	006300				ASL	R0	; SET TCR BIT
3299	020204	010277	161646			MOV	R2, @DZTCR	
3300	020210	005004			5\$:	CLR	R4	
3301	020212	032777	100000	161622	6\$:	BIT	#TRDY, @DZCSR	
3302	020220	001004				BNE	7\$	
3303	020222	104414				DELAY		
3304	020224	005204				INC	R4	
3305	020226	001371				BNE	6\$	
3306	020230	104003				ERROR	3	; *TRDY FAILED TO SET
3307	020232	116077	001422	161626	7\$:	MOVB	TDO(R0), @DZTDR	; LOAD A CHARACTER
3308	020240	005260	001422			INC	TDO(R0)	; SET UP NEXT CHARACTER
3309	020244	022760	000017	001422		CMP	#15., TDO(R0)	; 15 CHARS YET?
3310	020252	001406				BEQ	8\$	
3311	020254	032777	020000	161560		BIT	#SILOAL, @DZCSR	; SILO ALARM = 0 ?
3312	020262	001401				BEQ	+4	; YES
3313	020264	104013				ERROR	13	; *SILO ALARM SHOULD NOT = 1
3314								; UNTIL 16. DATA CHARACTERS
3315	020266	000750				BR	5\$	
3316	020270	012777	020356	161574	8\$:	MOV	#12\$, @DZRIV	; SET NEW VECTOR
3317	020276	032777	100000	161536		BIT	#TRDY, @DZCSR	; READY FOR 16TH CHAR
3318	020304	001774				BEQ	-6	
3319	020306	016077	001422	161552		MOV	TDO(R0), @DZTDR	; LOAD THE 16TH CHAR.
3320	020314	005004				CLR	R4	
3321	020316	032777	020000	161516	9\$:	BIT	#SILOAL, @DZCSR	
3322	020324	001005				BNE	10\$	
3323	020326	104414				DELAY		
3324	020330	005204				INC	R4	
3325	020332	001371				BNE	9\$	
3326	020334	104014				ERROR	14	; *SILO ALARM FAILED TO SET!
3327	020336	000410				BR	17\$; SILO ALARM SHOULD =1 AFTER 16.
3328								; DATA CHARACTERS
3329	020340	000240			10\$:	NOP		; STALL
3330	020342	000240				NOP		
3331	020344	104000				ERROR		; SILO ALARM NOT INTERRUPTING.
3332	020346	000404				BR	17\$; CONTINUE TEST.
3333	020350	022626			11\$:	CMP	(SP)+, (SP)+	; FAKE RTI
3334	020352	104012				ERROR	12	; RX SHOULD NOT INTERRUPT
3335	020354	000401				BR	17\$; CONTINUE
3336	020356	022626			12\$:	CMP	(SP)+, (SP)+	; GOOD INTERRUPT TO HERE.
3337	020360	040277	161472		17\$:	BIC	R2, @DZTCR	; CLR TCR BIT
3338	020364	104401				SCOP1		; LOOP?

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3339	020366	005237	001372
3340	020372	106302	
3341	020374	103402	
3342	020376	000137	020130
3343	020402	005037	001362

225:	INC	SAVLIN
	ASLB	R2
	BCS	.+6
	JMP	3\$
	CLR	LOCK

:	INC EXPECTED LINE
:	NEXT LINE
:	NO
:	YES
:	CLEAR TIGHT LOOP FOR NEXT TEST

```

3344
3345
3346
3347
3348
3349
3350
3351 020406 000004
3352 020410 012737 000031 001122
3353 020416 012737 021214 001360
3354 020424 104417
3355 020426 013737 001364 021212
3356 020434 013701 001366
3357 020440 012700 000001
3358 020444 030037 001364
3359 020450 001402
3360 020452 010177 161374
3361 020456 005201
3362 020460 106300
3363 020462 103370
3364 020464 012700 001422
3365 020470 005020
3366 020472 022700 001462
3367 020476 001374
3368 020500 012777 020734 161364
3369 020506 012777 000340 161360
3370 020514 012777 020636 161354
3371 020522 012777 000340 161350
3372 020530 052777 000100 161304
3273 020536 052777 040000 161276
3374 020544 052777 000040 161270
3375 020552 113777 001364 161276
3376 020560 106437 026456
3377
3378
3379 020564 005037 020634
3380 020570 013727 006722
3381 020574 000000
3382 020576 005337 020574
3383 020602 001375
3384 020604 105737 021212
3385 020610 001002
3386 020612 000137 021112
3387 020616 005237 020634
3388 020622 001362
3389 020624 104007
3390 020626 104011
3391 020630 000137 021164
3392 020634 000000
3393
3394
3395 020636 005777 161200
3396 020642 100401
3397 020644 104003
3398 020646 117703 161172
3399

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***** TEST 31 *****
*THIS TEST RUNS ALL LINES FULL BORE
*BASED UPON QUALIFIED LINES
*..THIS IS AN INTERRUPT TEST ON THE RECEIVER AND
*TRANSMITTER
::* TEST 31
*****
TST31: SCOPE
MOV #31,STSTNM ;LOAD THE NUMBER OF THIS TEST
MOV #TST32,NEXT ;POINT TO THE START OF THE NEXT TEST
DCLASM ;CLEAR DEVICE AND SET MAINT BIT IF I MODE
MOV LINE,RXTCR ;SET IMAGE OF TCR BITS
RSTART: MOV PAR,R1 ;PICK UP PARAMETER
MOV #1,R0 ;PICK UP INIT POINTER
INIT: BIT R0,LINE ;SHOULD THIS LINE BE SET UP
BEQ 1$ ;NO
MOV R1,ADZLPR ;SET UP LINE PARAM REGISTER
1$: INC R1
ASLB R0 ;GOT 'EM ALL ?
BCC INIT ;NO
MOV #TDD,R0 ;CLEAR TRANS DATA POINTER & REC POINTERS
INIT1: CLR (R0)+
CMP #STOP,R0 ;FINISHED ?
BNE INIT1 ;NO CONTINUE CLEARING
MOV #RXSVC,ADZRIV ;SET UP REC INTR VECTOR
MOV #PR7,ADZRIS ;STATUS
MOV #TXSVC,ADZTIV ;SET UP TRANS INTR VECTOR
MOV #PR7,ADZTIS ;STATUS
BIS #RIE,ADZCSR ;SET REC INTR ENABLE
BIS #TIE,ADZCSR ;SET TRANS INTR ENABLE
BIS #MSENAB,ADZCSR ;SET MASTER SCAN ENABLE
MOVVB LINE,ADZTCR ;SET TCR BITS...UP UP AND AWAY !
MTPS #LESS1 ;ALLOW INTERRUPTS

SNAP: CLR 66$
67$: MOV DLYCNT,(PC)+ ;SET FOR DELAY
68$: 0
DEC 68$
BNE -4
TSTB RXTCR ;WAIT FOR ALL RECIEVERS TO FINISH
BNE 3$
JMP OUT
3$: INC 66$
BNE 67$
ERROR 7 ;*TRANSMITTER FAILED TO INTERRUPT
ERROR 11 ;*RECEIVER FAILED TO INTERRUPT
JMP FINI
66$: 0

;TRANS INTR SVC ROUTINE
TXSVC: TST ADZCSR ;TRANS INTR ?
BMI +4
ERROR 3 ;*TRANSMITTER FAILED
MOVVB ADZCSR,R3 ;SAVE IT
;NOW TEST FOR LINE # ETC

```

3400	020652	042703	177770		BIC	#1C<7>,R3	:STRIP JUNK
3401	020656	010304			MOV	R3,R4	:SAVE
3402	020660	010337	001372		MOV	R3,SAVLIN	:ADJUST LOCATION FOR ERROR PRINTOUT
3403	020664	012702	000001		MOV	#1,R2	:SET UP POSITION POINTER
3404	020670	105303		3\$:	DECB	R3	:IS IT THIS LINE ?
3405	020672	100402			BMI	4\$:YES
3406	020674	006302			ASL	R2	:UP THE LINE #
3407	020676	000774			BR	3\$:GO 'ROUND AGAIN
3408	020700	030237	001364	4\$:	BIT	R2,LINE	:VALID LINE?
3409	020704	001001			BNE	.+4	:YES
3410	020706	104010			ERROR	10	:NO,INVALID LINE!!!!
3411	020710	006304			ASL	R4	:MAKE POWER OF 2
3412	020712	116477	001422	161146	MOVB	T00(R4),DZTDR	:LOAD CHARACTER
3413	020720	105264	001422		INCB	T00(R4)	:SET UP NEXT CHARACTER
3414	020724	001002			BNE	5\$:LAST CHARACTER ?
3415	020726	040277	161124		BIC	R2,DZTCR	:YES,CLEAR TCR BIT
3416	020732	000002		5\$:	RTI		
3417							
3418							
3419							
3420	020734	105777	161102		:REC INTR SVC ROUTINE		
3421	020740	100401			AXSVC: TSTB	DZCSR	:REC DONE ?
3422	020742	104004			BMI	.+4	:YES
3423	020744	017704	161076		ERROR	4	:FALSE INTERRUPT
3424	020750	010403			MOV	DZRBUF,R4	:SAVE IT
3425	020752	000303			MOV	R4,R3	
3426	020754	042703	177770		SWAB	R3	
3427	020760	010337	001372		BIC	#1C<7>,R3	:STRIP JUNK
3428	020764	032777	020000	161050	MOV	R3,SAVLIN	:SAVE LINE NUMBER
3429	020772	001401			BIT	#SILOAL,DZCSR	:SILO ALARM?
3430	020774	104000			BEQ	.+4	:NO
3431	020776	005704			ERROR		:SILO ALARM SHOULD NOT =1
3432	021000	100401			TST	R4	:DATA VALID SET?
3433	021002	104023			BMI	.+4	:YES
3434	021004	032704	070000		ERROR	23	:YOU LOSE ...DATA VALID WAS'NT SET
3435	021010	001401			BIT	#OVRUN!FRMERR!PARER,R4	
3436	021012	104000			BEQ	.+4	
3437	021014	012702	000001		ERROR		:RECEIVER ERROR FLAG/S WERE SET
3438	021020	105303		5\$:	MOV	#1,R2	:SET UP POSITION POINTER
3439	021022	100402			DECB	R3	
3440	021024	006302			BMI	6\$	
3441	021026	000774			ASL	R2	:RE POSITION POINTER
3442	021030	030237	001364	6\$:	BR	5\$:GO 'ROUND AGAIN
3443	021034	001001			BIT	R2,LINE	:LINE VALID ?
3444	021036	104011			BNE	.+4	:YES
3445	021040	013703	001372		ERROR	11	:INVALID LINE #
3446	021044	006303			MOV	SAVLIN,R3	:GET THE LINE NUMBER AGAIN
3447	021046	126304	001442		ASL	R3	:USE R3 AS A POINTER IN THE DATA TABLE
3448	021052	001405			CMPB	TRO(R3),R4	:DOES THE DATA CHARACTER COMPARE ?
3449	021054	016305	001442		BEQ	2\$:YES
3450	021060	042704	177400		MOV	TRO(R3),R5	:SAVE EXPECTED
3451					BIC	#1C<377>,R4	:CLEAR JUNK
3452							:R2 = LINE # BY BIT POSITION
3453							:R4 = ACTUAL DATA
3454	021064	104005			ERROR	5	:R5 = EXPECTED DATA
3455	021066	005263	001442	2\$:	INC	TRO(R3)	:*NO, DATA DOES NOT COMPARE :SET UP FOR NEXT CHARACTER

```

3456 021072 105763 001442      TSTB   TRO(R3) ;ALL CHARS DONE?
3457 021076 001002              BNE    +6
3458 021100 040237 021212      BIC    R2,RXTCR ;ZERO LINE DONE INDICATOR.
3459 021104 012716 020564      MOV    #SNAP,(SP) ;RESET THE BACKGROUND TIMING LOOP
3460 021110 000002              RTI
3461
3462
3463
3464 021112 106427 000340      OUT:   MTPS   #PR7 ;STOP ALL INTERRUPTS
3465 021116 104413              DEVICE.CLR ;CLEAR ALL INTERRUPTS AWAY
3466 021120 005003              CLR    R3
3467 021122 005037 001372      CLR    SAVLIN
3468 021126 012702 000001      MOV    #1,R2
3469 021132 030237 001364      1$:   BIT    R2,LINE ;VALID LINE ?
3470 021136 001405              BEQ    2$ ;NO
3471 021140 022763 000400 001442      CMP    #400,TRO(R3) ;RECEIVED A BINARY COUNT PATTERN ?
3472 021146 001401              BEQ    +4 ;YES
3473 021150 104027              ERROR  27 ;THE LINE FAILED TO RECEIVE A FULL
3474 ;BINARY COUNT PATTERN
3475 021152 005237 001372      2$:   INC    SAVLIN ;SET UP FOR NEXT LINE
3476 021156 005723              TST   (R3)+ ;ADD 2
3477 021160 106302              ASLB  R2 ;SET UP NEXT LINE POINTER
3478 021162 103363              BCC   1$ ;FINISHED ?
3479
3480 021164 013777 002074 160700      FINI:  MOV    DZRI$ ,DZRIV ;RESTORE TRAPCATCHER
3481 021172 005077 160676              CLR   DZRIS
3482 021176 013777 002100 160672      MOV    DZTI$ ,DZTIV
3483 021204 005077 160670              CLR   DZTI$
3484 021210 104400              ADVANCE
3485 021212 000000      RXTCR: 0 ;GO TO THE NEXT TEST
3486 ;RX IMAGE OF TCR BITS
3487
3488
3489
3490
3491
3492
3493
3494
3495
3496
3497
3498
3499
3500
3501
3502
3503
3504
3505
3506 021214 000004              $T32: SCOPE
3507 021216 012737 000032 001122      MOV    #32,$STNM ;LOAD THE NUMBER OF THIS TEST
3508 021224 012737 000002 001226      MOV    #2,$TIMES
3509 021232 012737 021724 001360      MOV    #T$33,NEXT ;POINT TO THE START OF THE NEXT TEST
3510 021240 012737 021364 001362      MOV    #3$,LOCK ;SET FOR LOOP
3511 021246 005037 023362              CLR    OFFSET ;RESET THIS VARIABLE

```

```

***** TEST 32 *****
* DZ11 RELATIVE TIMING TEST.
* EACH SELECTED LINE WILL IN TURN RUN 16. CHARS
* AT ALL BAUD RATES AND THEN THE HIGHEST BAUD
* WITH ALL CHAR LENGTHS. EACH NEW PARAMETER SHOULD
* DECREASE IN TIME FROM THE PREVIOUS PARAMETERS SELECTED.
* THE TIME IS CHECKED AGAINST THE LAST PARAMETER USED
* AND A LOWER TIME IS EXPECTED ON THE CURRENT PARAMETER.
* PARAMETERS ARE:
* EIGHT BITS/PER/CHAR - TWO STOP BITS AT
* 50, 75, 110, 134.5, 150, 300, 600, 1200, 1800, 2000
* 2400, 3600, 4800, 7200, 9600 BAUD.
* 19.2 K BAUD - TWO STOP BITS AT
* SEVEN, SIX, FIVE BITS/PER/CHAR.
* AFTER EACH LINE HAS FINISHED ALL THE ABOVE PARAMETERS
* THE NEXT SELECTED LINE IS THE TESTED.

```

```

::* TEST 32
*****
$T32: SCOPE
MOV    #32,$STNM ;LOAD THE NUMBER OF THIS TEST
MOV    #2,$TIMES
MOV    #T$33,NEXT ;POINT TO THE START OF THE NEXT TEST
MOV    #3$,LOCK ;SET FOR LOOP
CLR    OFFSET ;RESET THIS VARIABLE

```


3512	021252	005037	001372			CLR	SAVLIN	; RESET LINE NUMBER INDICATOR
3513	021256	005037	001374			CLR	XMTLIN	; USE THIS WORD TO TELL WHAT LINE TRANSMITTED
3514	021262	012737	000001	001216		MOV	#1, \$TMPD	; USE \$TMPD AS A BIT POINTER
3515	021270	012737	010070	021722		MOV	#RCVON!\$S0!EIGHT	; TWOSTOP, 7\$; BUILD TEMPORARY PARAMETERS
3516	021276	033737	001216	001364	1\$:	BIT	\$TMPD, LINE	; IS THIS LINE ACTIVE?
3517	021304	001027				BNE	3\$; IF SO, GO GET STARTED
3518	021306	012737	010070	021722	2\$:	MOV	#RCVON!\$S0!EIGHT	; TWOSTOP, 7\$; LOAD PARAMETERS TEMPORARILY
3519	021314	012700	001422			MOV	#TDD, RO	; POINT TO THE DATA AREA
3520	021320	005020				CLR	(RO)↓	; CLEAR A DATA WORD
3521	021322	022700	001462			CMP	#STOP, RO	; FINISHED ?
3522	021326	001374				BNE	.-6	; NO
3523	021330	005237	001374			INC	XMTLIN	; POINT TO THE NEXT LINE TO TRANSMIT
3524	021334	042737	000007	021722		BIC	#7, 7\$; MAKE SURE TEMPORARY PARAMETERS POINT TO 0
3525	021342	053737	001374	021722		BIS	XMTLIN, 7\$; ADD DESIRED LINE NUMBER
3526	021350	005037	023362			CLR	OFFSET	
3527	021354	106337	001216			ASLB	\$TMPD	; POINT TO THE NEXT LINE
3528	021360	103346				BCC	1\$; PROCESS THE NEXT LINE
3529	021362	104400				ADVANCE		; TEST TO SEE IF THIS TEST GETS REPEATED
3530	021364				3\$:			
3531	021364	104417				DCLASM		; CLEAR DEVICE AND SET MAINT BIT IF I MODE
3532	021366	042737	010000	021722		BIC	#RCVON, 7\$; ZERO PARAMTERS FOR TX LINE
3533	021374	013777	021722	160450		MOV	7\$, #DZLPR	; LOAD PARAMTERS FOR TX
3534	021402	005737	001370			TST	MODE	; STAGGERED?
3535	021406	100011				BPL	10J\$; BR IF NO
3536	021410	000241				CLC		; SET UP LINE
3537	021412	006037	021722			ROR	7\$	
3538	021416	103002				BCC	98\$; BR IF LINE WAS EVEN
3539	021420	000241				CLC		; PREPARE TO MKE LINE EVEN
3540	021422	000401				BR	99\$; CONTINUE
3541	021424	000261			98\$:	SEC		; PREPARE TO MAKE LINE ODD
3542	021426	006137	021722		99\$:	ROL	7\$; SET ALTERED LINE
3543	021432	052737	010000	021722	100\$:	BIS	#RCVON, 7\$; SET RX ON
3544	021440	013777	021722	160404		MOV	7\$, #DZLPR	; LOAD RX PARAMETERS
3545	021446	013737	021722	001372		MOV	7\$, SAVLIN	; ADJUST LOCATION FOR ERROR PRINTOUT
3546	021454	042737	177770	001372		BIC	#↑C<7>, SAVLIN	; STRIP JUNK
3547	021462	042737	000007	021722		BIC	#7, 7\$; CLEAR OLD LINE #
3548	021470	053737	001374	021722		BIS	XMTLIN, 7\$; SET LINE UP AGAIN
3549	021476	013737	021722	001400		MOV	7\$, REGIST	; SAVE PARAMETERS FOR PRINTOUT
3550	021504	012700	001422			MOV	#TDD, RO	; POINT TO THE DATA AREA
3551	021510	005020				CLR	(RO)↓	; CLEAR A DATA WORD
3552	021512	022700	001462			CMP	#STOP, RO	; FINISHED ?
3553	021516	001374				BNE	.-6	; NO
3554	021520	005002				CLR	R2	; USE R2 TO COUNT TOTAL NUMBER OF TRANSMISSIONS
3555	021522	005003				CLR	R3	; USE R3 TO COUNT TOTAL NUMBER OF RECEPTIONS
3556	021524	005037	001220			CLR	\$TMP1	; INITIALIZE THE TIMER
3557	021530	005037	001224			CLR	\$TMP3	; INITIALIZE THESE BITS ALSO
3558	021534	012737	000020	001376		MOV	#20, XMTCNT	; SET HOW MANY CHARACTERS TO TRANSMIT
3559	021542	012777	023004	160326		MOV	#XMTSRV, #DZTIV	
3560	021550	012777	023150	160314		MOV	#RXISR1, #DZRIV	
3561	021556	013777	026454	160310		MOV	DZPRT, #DZRIS	
3562	021564	013777	026454	160306		MOV	DZPRT, #DZTIS	
3563	021572	113777	001216	160256		MOV	\$TMPD, #DZTCP	; START THE VALID LINE
3564	021600	052777	040140	160234		BIS	#TIE!#RIE!#MSENAB, #DZCSR	
3565	021606	106427	000000			MTPS	#0	; LOWER THE PRIORITY TO ALLOW INTERRUPTS
3566	021612	032777	000100	160222	4\$:	BIT	#RIE, #DZCSR	; IS ROUTINE DONE?
3567	021620	001407				BEQ	5\$; WHEN ALL IS DONE RX IE IS CLEARED IN ISR.

```

3568 021622 005237 001220      INC      $TMP1      ;COUNT TIME
3569 021626 001371              BNE      4$        ;CONTINUE TEST
3570 021630 105237 001224      INCB     $TMP3     ;DOUBLE COUNT
3571 021634 001366              BNE      4$        ;CONTINUE TEST
3572 021636 104011              ERROR     11       ;INTERRUPTS NOT FINISHED
3573 021640 004737 007360      JSR      PC, SERV.G ;<↑G>?
3574 021644 104401              SCOPI                    ;LOOP?
3575 021646 062737 000002 023362  ADD      #2, OFFSET
3576 021654 013700 021722      MOV      7$, R0
3577 021660 042700 170377      BIC      #↑C<17*400>, R0
3578 021664 022700 007400      CMP      #<17*400>, R0
3579 021670 001010              BNE      6$
3580 021672 032737 000030 021722  BIT      #BIT4+BIT3, 7$
3581 021700 001602              BEQ      2$
3582 021702 162737 000010 021722  SUB      #BIT3, 7$
3583 021710 000625              BR       3$
3584 021712 062737 000400 021722  6$: ADD      #400, 7$
3585 021720 000621              BR       3$
3586 021722 000000      7$: 0
3587
3588 ;***** TEST 33 *****
3589 ; THIS TEST VERIFIES THAT EVEN PARITY WORKS
3590 ; FOR ALL ODD LINES SELECTED AND THAT ODD PARITY WORKS FOR ALL
3591 ; EVEN LINES SELECTED.
3592 ;THE MAIN FUNCTION OF THIS TEST IS TO VERIFY
3593 ;THAT "PE" (PARITY ERROR) CAN BE FLAGGED BY
3594 ;THE UARTS. THIS TEST WILL NOT BE DONE UNLESS
3595 ;YOU ARE IN "STAGGERED" MODE.
3596 ;40(8) CHARS ARE USED FOR THIS TEST.
3597 ;ALL SELECTED LINES WILL BE ENABLED
3598 ;AT THE SAME TIME!
3599
3600 ;:* TEST 33
3601 ;*****
3602 ;↑ST33: SCOPE
3603 MOV      #33, $STSTNM ;LOAD THE NUMBER OF THIS TEST
3604 MOV      #↑ST34, NEXT ;POINT TO THE START OF THE NEXT TEST
3605 TST      MODE ;IS THIS STAGGERED MODE?
3606 BPL      6$ ;IF NOT, DON'T DO THIS TEST
3607 DCLASM ;CLEAR DEVICE AND SET MAINT BIT IF I MODE
3608 MOV      PAR, R1 ;USE R1 TO BUILD PARAMETERS TO BE LOADED
3609 BIC      #ODDPAR, R1 ;MAKE SURE ODD PARITY ISN'T SET
3610 BIS      #PARITY, R1 ;MAKE SURE PARITY IS TURNED ON
3611 MOV      #1, R2 ;USE R2 AS A LINE POINTER
3612 1$: BIT      R2, LINE ;IS THIS A VALID LINE?
3613 BEQ      3$ ;IF NOT, SKIP TO THE NEXT LINE
3614 BIT      #BIT0, R1 ;IS THIS LINE AN ODD LINE?
3615 BNE      2$ ;IF IT'S ODD, USE EVEN PARITY
3616 BIS      #ODDPAR, R1 ;IF IT'S EVEN, USE ODD PARITY
3617 2$: MOV      R1, $DZLPR ;LOAD THE LINE PARAMETER REGISTER
3618 BIC      #ODDPAR, R1 ;SET UP THE NEXT PARITY TO EVEN
3619 3$: INC      R1 ;POINT TO THE NEXT LINE
3620 ASLB     R2 ;MOVE THE BIT POINTER IN R2 TO THE NEXT LINE
3621 BCC      1$ ;IF WE'RE NOT DONE, GO CHECK THE NEXT LINE
3622 CLR      $AVLIN ;CLEAR THE LINE NUMBER INDICATOR
3623 CLR      R2 ;USE R2 TO COUNT TOTAL NUMBER OF TRANSMISSIONS
3624 CLR      R3 ;USE R3 TO COUNT TOTAL NUMBER OF RECEPTIONS
3625 MOV      #40, XMTCNT ;TRANSMIT A BINARY COUNT PATTERN(00-40)

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3624	022046	012700	001422		MOV	#T00,R0	;POINT TO THE DATA AREA	
3625	022052	005020			CLR	(R0)↓	;CLEAR A DATA WORD	
3626	022054	022700	001462		CMP	#STOP,R0	;FINISHED ?	
3627	022060	001374			BNE	-6	;NO	
3628	022062	005000			CLR	R0	;CLEAR OFFSET	
3629	022064	012777	023004	160004	MOV	#XMTSRV,∂ZTIV	;SET UP THE TRANSMITTER INTERRUPT VECTOR	
3630	022072	012777	022200	157772	MOV	#95,∂ZRV	;SET UP THE RECEIVER INTERRUPT VECTOR	
3631	022100	013777	026454	157766	MOV	DZPRT,∂ZRVIS	;SET THE INTERRUPT VECTOR STATUS	
3632	022106	013777	026454	157764	MOV	DZPRT,∂ZTIS	;SET TRANSMITTER INTERRUPT PRIORITY	
3633	022114	052777	040140	157720	BIS	#RIE!#TIE!#MSENAB,∂ZCSR	;ENABLE THE DEVICE	
3634	022122	113777	001364	157726	MOV	B LINE,∂ZTCR	;ENABLE ALL SELECTED LINES	
3635	022130	106427	000000		MTPS	#0	;ALLOW INTERRUPTS	
3636	022134	005037	022174		45:	CLR	75	
3637	022140	005037	022176		CLR	85		
3638	022144	032777	000100	157670	55:	BIT	#RIE,∂ZCSR	;WHEN RX DONE; RIE WILL =0
3639	022152	001407			BEQ	65	;BR IF ALL DONE	
3640	022154	005237	022174		INC	75		
3641	022160	001371			BNE	55		
3642	022162	105237	022176		INCB	85		
3643	022166	100366			BPL	55		
3644	022170	104011			ERROR	11	;*RX FAILED TO FINISH (INTERRUPT)	
3645	022172	104400			65:	ADVANCE	;ADVANCE LOOP	
3646	022174	000000			75:	0		
3647	022176	000000			85:	0		
3648								
3649								
3650								
3651							:RECEIVER SERVICE ROUTINE	
3652	022200	017704	157642		95:	MOV	∂ZRBUF,R4	;GET THE CHARACTER
3653	022204	010401			MOV	R4,R1	;COPY THE RECEIVED INFORMATION	
3654	022206	000301			SWAB	R1	;GET THE LINE NUMBER IN THE LOWER BYTE	
3655	022210	042701	177770		BIC	#1C<7>,R1	;ISOLATE THE LINE NUMBER	
3656	022214	010137	001372		MOV	R1,SAVLIN	;FILL LOC. FOR ERROR PRINTOUT	
3657	022220	005704			TST	R4	;WAS DATA VALID?	
3658	022222	100401			BMI	105	;BRANCH IF YES	
3659	022224	104023			ERROR	23	;ERROR - DATA VALID NOT SET!	
3660	022226	006301			105:	ASL	R1	;ALIGN IT ON A WORD BOUNDARY
3661	022230	032704	010000		BIT	#PARER,R4	;PARITY ERROR SHOULD BE SET. IS IT?	
3662	022234	001013			BNE	115	;IF SO, GO CHECK CHARACTER	
3663	022236	013737	002046	001400	MOV	DZRBUF,REGIST	;SET UP FOR THE ERROR MESSAGE	
3664	022244	010405			MOV	R4,R5		
3665	022246	042705	000377		EIC	#377,R5		
3666	022252	156105	001442		BISB	TRO(R1),R5	;GET THE CORRECT CHARACTER	
3667	022256	052705	110000		BIS	#0VALID!PARER,R5	;BUILD WHAT WAS EXPECTED	
3668	022262	104006			ERROR	6	;*ERROR- DID NOT GET CORRECT INFORMATION	
3669	022264	126104	001442		115:	CMPB	TRO(R1),R4	;CHECK THE CHARACTER. IS IT CORRECT?
3670	022270	001407			BEQ	125	;IF SO, GO SET UP NEXT CHARACTER	
3671	022272	116105	001442		MOV	B TRO(R1),R5	;LOAD THE CHARACTER FOR ERROR REPORTING	
3672	022276	042705	177400		BIC	#1C<377>,R5	;CLEAR SIGN EXTEND	
3673	022302	042704	177400		BIC	#1C<377>,R4	;REMOVE THE JUNK FROM R4, THE ACTUAL CHARACTER	
3674	022306	104005			ERROR	5	;DATA ERROR	
3675	022310	005261	001442		125:	INC	TRO(R1)	;SET UP THE NEXT CHARACTER
3676	022314	005203			INC	R3	;ADD TO THE TOTAL RECEIVED COUNT	
3677	022316	032777	040000	157516	BIT	#TIE,∂ZCSR	;ARE TRANSMISSIONS DONE?	
3678	022324	001010			BNE	'5	;IF NO, GO RECEIVE SOME MORE	
3679	022326	020203			CMP	R2,R3	;ARE ALL CHARACTERS RECEIVED?	

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3680 022330 001006
3681 022332 042777 000100 157502
3682 022340 012716 022172
3683 022344 000002
3684 022346 012716 022134
3685 022352 000002
3686
3687
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3698 022354 000004
3699 022356 012737 000034 001122
3700 022364 012737 004562 001360
3701 022372 005737 001370
3702 022376 100111
3703 022400 104417
3704 022402 013701 001366
3705 022406 042701 000200
3706 022412 052701 000100
3707 022416 012702 000001
3708 022422 030237 001364
3709 022426 001411
3710 022430 032701 000001
3711 022434 001402
3712 022436 052701 000200
3713 022442 010177 157404
3714 022446 042701 000200
3715 022452 005201
3716 022454 106302
3717 022456 103361
3718 022460 005037 001372
3719 022464 005002
3720 022466 005003
3721 022470 012737 000040 001376
3722 022476 012700 001422
3723 022502 005020
3724 022504 022700 001462
3725 022510 001374
3726 022512 005000
3727 022514 012777 023004 157354
3728 022522 012777 022630 157342
3729 022530 013777 026454 157336
3730 022536 013777 026454 157334
3731 022544 052777 040140 157270
3732 022552 113777 001364 157276
3733 022560 106427 000000
3734 022564 005037 022624
3735 022570 005037 022626

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BNE 13$ ; IF NO, GO RECEIVE SOME MORE
BIC #RIE, DZCSR ; DISABLE RECEIVER INTERRUPTS
MOV #6$, (SP) ; CRUNCH THE STACK
RTI ; RETURN AND FINISH
MOV #4$, (SP) ; CRUNCH THE STACK
RTI ; GO BACK TO RECEIVER WAIT LOOP
***** TEST 34 *****
*THIS TEST VERIFIES THAT ODD PARITY WORKS FOR ALL ODD LINES
*SELECTED AND THAT EVEN PARITY WORKS FOR ALL EVEN LINES SELECTED
*THE MAIN FUNCTION OF THIS TEST IS TO VERIFY
*THAT "PE" (PARITY ERROR) CAN BE FLAGGED BY
*THE UARTS. THIS TEST WILL NOT BE DONE UNLESS
*YOU ARE IN "STAGGERED" MODE.
*40(8) CHARS ARE USED FOR THIS TEST.
*ALL SELECTED LINES WILL BE ENABLED
*AT THE SAME TIME!
::* TEST 34
*****
↑ST34: SCOPE
MOV #34, STSTNM ; LOAD THE NUMBER OF THIS TEST
MOV #SEOP, NEXT ; POINT TO THE END-OF-PASS HANDLER
TST MODE ; IS THIS STAGGERED MODE?
BPL 6$ ; IF NOT, DON'T DO THIS TEST
DCLASM ; CLEAR DEVICE AND SET MAINT BIT IF I MODE
MOV PAR, R1 ; USE R1 TO BUILD PARAMETERS TO BE LOADED
BIC #ODDPAR, R1 ; MAKE SURE ODD PARITY ISN'T SET
BIS #PARITY, R1 ; MAKE SURE PARITY IS TURNED ON
MOV #1, R2 ; USE R2 AS A LINE POINTER
1$: BIT R2, LINE ; IS THIS A VALID LINE?
BEQ 3$ ; IF NOT, SKIP TO THE NEXT LINE
BIT #BIT0, R1 ; IS THIS LINE AN ODD LINE?
BEQ 2$ ; IF IT'S EVEN, USE EVEN PARITY
BIS #ODDPAR, R1 ; IF IT'S ODD, USE ODD PARITY
MOV R1, DZLPR ; LOAD THE LINE PARAMETER REGISTER
2$: BIC #ODDPAR, R1 ; SET UP THE NEXT PARITY TO EVEN
3$: INC R1 ; POINT TO THE NEXT LINE
ASLB R2 ; MOVE THE BIT POINTER IN R2 TO THE NEXT LINE
BCC 1$ ; IF WE'RE NOT DONE, GO CHECK THE NEXT LINE
CLR SAVLIN ; CLEAR THE LINE NUMBER INDICATOR
CLR R2 ; USE R2 TO COUNT TOTAL NUMBER OF TRANSMISSIONS
CLR R3 ; USE R3 TO COUNT TOTAL NUMBER OF RECEPTIONS
MOV #40, XMTCNT ; TRANSMIT A BINARY COUNT PATTERN(00-40)
MOV #TDD, RO ; POINT TO THE DATA AREA
CLR (RO) ; CLEAR A DATA WORD
CMP #STOP, RO ; FINISHED ?
BNE -6 ; NO
CLR RO ; CLEAR OFFSET
MOV #XMTSRV, DZTIV ; SET UP THE TRANSMITTER INTERRUPT VECTOR
MOV #9$, DZRIV ; SET UP THE RECEIVER INTERRUPT VECTOR
MOV DZPRT, DZRTS ; SET THE INTERRUPT VECTOR STATUS
MOV DZPRT, DZRTIS ; SET TRANSMITTER INTERRUPT PRIORITY
BIS #RIE!#IE!#MSENAB, DZCSR ; ENABLE THE DEVICE
MOV# LINE, DZTCH ; ENABLE ALL SELECTED LINES
MTPS #0 ; ALLOW INTERRUPTS
4$: CLR 7$
CLR 8$

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3736 022774 032777 000100 157240 5$: BIT #RIE, DZCSR ; WHEN RX DONE: RIE WILL =0
3737 022602 001407 BEQ 6$ ; BR IF ALL DONE
3738 022604 005237 022624 INC 7$
3739 022610 001371 BNE 5$
3740 022612 105237 022626 INCB 8$
3741 022616 100366 BPL 5$
3742 022620 104011 ERROR 11 ; *RX FAILED TO FINISH (INTERRUPT)
3743 022622 104400 6$: ADVANCE ; ADVANCE LOOP
3744 022624 000000 7$: 0
3745 022626 000000 8$: 0
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3748 ; RECEIVER SERVICE ROUTINE
3749
3750 022630 017704 157212 9$: MOV DZRBUF, R4 ; GET THE CHARACTER
3751 022634 010401 MOV R4, R1 ; COPY THE RECEIVED INFORMATION
3752 022636 000301 SWAB R1 ; GET THE LINE NUMBER IN THE LOWER BYTE
3753 022640 042701 177770 BIC #C(7), R1 ; ISOLATE THE LINE NUMBER
3754 022644 010137 001372 MOV R1, SAVLIN ; FILL LOC. FOR ERROR PRINTOUT
3755 022650 005704 TST R4 ; WAS DATA VALID?
3756 022652 100401 BMI 10$ ; BRANCH IF YES
3757 022654 104023 ERROR 23 ; ERROR - DATA VALID NOT SET!
3758 022656 006301 10$: ASL R1 ; ALIGN IT ON A WORD BOUNDARY
3759 022660 032704 010000 BIT #PARER, R4 ; PARITY ERROR SHOULD BE SET. IS IT?
3760 022664 001013 BNE 11$ ; IF SO, GO CHECK CHARACTER
3761 022666 013737 002046 001400 MOV DZRBUF, REGIST ; SET UP FOR THE ERROR MESSAGE
3762 022674 010405 MOV R4, R5
3763 022676 042705 000377 BIC #377, R5
3764 022702 156105 001442 BISB TRO(R1), R5 ; GET THE CORRECT CHARACTER
3765 022706 052705 110000 BIS #DVALID!PARER, R5 ; BUILD WHAT WAS EXPECTED
3766 022712 104006 ERROR 6 ; *ERROR- DID NOT GET CORRECT INFORMATION
3767 022714 126104 001442 11$: CMPB TRO(R1), R4 ; CHECK THE CHARACTER. IS IT CORRECT?
3768 022720 001407 BEQ 12$ ; IF SO, GO SET UP NEXT CHARACTER
3769 022722 116105 001442 MOVB TRO(R1), R5 ; LOAD THE CHARACTER FOR ERROR REPORTING
3770 022726 042705 177400 BIC #C(377), R5 ; CLEAR SIGN EXTENS
3771 022732 042704 177400 BIC #C(377), R4 ; REMOVE THE JUNK FROM R4, THE ACTUAL CHARACTER
3772 022736 104005 ERROR 5 ; DATA ERROR
3773 022740 005261 001442 12$: INC TRO(R1) ; SET UP THE NEXT CHARACTER
3774 022744 005203 INC R3 ; ADD TO THE TOTAL RECEIVED COUNT
3775 022746 032777 040000 157066 BIT #TIE, DZCSR ; ARE TRANSMISSIONS DONE?
3776 022754 001010 BNE 13$ ; IF NO, GO RECEIVE SOME MORE
3777 022756 020203 CMP R2, R3 ; ARE ALL CHARACTERS RECEIVED?
3778 022760 001006 BNE 13$ ; IF NO, GO RECEIVE SOME MORE
3779 022762 042777 000100 157052 BIC #RIE, DZCSR ; DISABLE RECEIVER INTERRUPTS
3780 022770 012716 022522 MOV #6$, (SP) ; CRUNCH THE STACK
3781 022774 000002 RTI ; RETURN AND FINISH
3782 022776 012716 022564 13$: MOV #4$, (SP) ; CRUNCH THE STACK
3783 023002 000002 RTI ; GO BACK TO RECEIVER WAIT LOOP

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023004 117701 157034
 023010 100411
 023012 013700 001372
 023016 042701 177770
 023022 010137 001372
 023026 104003
 023030 010037 001372
 023034 042701 177770
 023040 006301
 023042 116177 001422 157016
 023050 005261 001422
 023054 005202
 023056 023761 001376 001422
 023064 001015
 023066 012700 000001
 023072 006201
 023074 005301
 023076 100402
 023100 006300
 023102 000774
 023104 140077 156746
 023110 001003
 023112 042777 040000 156722
 023120 000002

: TRANSMITTER INTERRUPT SERVICE

XMTSRV: MOVB @HDZCSR,R1 ; GET THE LINE NUMBER. IS THE TRANSMITTER
 BMI 1\$; REALLY READY? IF SO, GO LOAD THE CHARACTER
 MOV SAVLIN,R0 ; ADJUST LOCATION SAVLIN
 BIC #1C<7>,R1 ; ISOLATE THE LINE NUMBER
 MOV R1,SAVLIN ; FOR ERROR PRINTOUT
 ERROR 3 ; *TRANSMITTER NOT READY- FALSE INTERRUPT
 MOV R0,SAVLIN ; RESET SAVLIN TO PREVIOUS VALUE
 1\$: BIC #1C<7>,R1 ; ISOLATE THE LINE NUMBER
 ASL R1 ; MAKE SURE IT REFERENCES A WORD BOUNDARY
 MOVB TDO(R1),@DZTDR ; LOAD THE CURRENT CHARACTER FOR THIS LINE
 INC TDO(R1) ; SET UP NEXT CHARACTER FOR THIS LINE
 INC R2 ; UP THE NUMBER OF TRANSMISSIONS
 CMP XMTCNT,TDO(R1) ; HAVE WE DONE ALL PATTERNS ON THIS LINE?
 BNE 4\$; IF NOT, KEEP ON TRANSMITTING
 MOV #1,R0 ; SET UP A DESELECTION POINTER
 ASL R1 ; GET THE LINE NUMBER AGAIN
 2\$: DEC R1 ; REDUCE THE COUNT. WAS THIS THE LINE?
 BMI 3\$; IF SO, GO DISABLE THE ENABLE BIT FOR IT
 ASL R0 ; MOVE THE POINTER TO THE NEXT LINE
 BR 2\$; GO CHECK THE NEXT LINE
 3\$: BICB R0,@DZTCR ; DISABLE THE LINE POINTED TO BY R0
 BNE 4\$; IF MORE LINES ARE ACTIVE, GO CONTINUE TRANSMIT
 BIC #TIE,@DZCSR ; IF NOT, DISABLE TRANSMITTER INTERRUPTS
 4\$: RTI ; RETURN TO THE TIMING LOOP

: RELATIVE TIME BUILDING ROUTINE

BUILD: MOV #4,\$TMP2 ; ROTATE 4 BITS BACK INTO \$TMP1
 1\$: ROR \$TMP3 ; GET THE BITS FROM \$TMP3, THE HIGH BYTE
 ROR \$TMP1 ; OF THE RELATIVE TIME COUNTER. PUT THEM BACK
 DEC \$TMP2 ; INTO \$TMP1 USING THE CARRY BIT WITH
 ; ROTATE INSTRUCTIONS
 BNE 1\$; REDUCE COUNT. ALL BITS BACK? IF NOT, GET MORE
 RTS PC ; RETURN TO CALLING TEST

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;RECEIVER SERVICE ROUTINE
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3826 023150 105777 156666 RXISR1: TSTB 0DZCSR ; IS THE RECEIVER REALLY READY?
3827 023154 100401 BMI 15 ; IF SO, GO SERVICE IT
3828 023156 104004 ERROR 4 ; *ERROR- RECEIVER DONE FLAG ISN'T SET
3829 023160 017704 156662 15: MOV 0DZRBUF,R4 ; SAVE THE RECEIVER INFORMATION
3830 023164 100401 BMI 25 ; IF IT WAS VALID, GO PROCESS IT
3831 023166 104023 ERROR 23 ; ERROR- DATA VALID WASN'T SET
3832 023170 032704 070000 25: BIT 0OVRRUN!FRMERR!PARER,R4 ; ARE ANY ERROR FLAGS SET?
3833 023174 001403 BEQ 35 ; IF NOT, GO CONTINUE PROCESSING
3834 023176 013700 002046 MOV 0ZRBUF,RO ; SET UP FOR ERROR REPORTING
3835 023202 104002 ERROR 2 ; ERROR- RECEIVER ERROR FLAG SET
3836 023204 010401 35: MOV R4,R1 ; COPY THE RECEIVER INFORMATION
3837 023206 000301 SWAB R1 ; GET THE LINE NUMBER IN THE LOWER BYTE
3838 023210 042701 177770 BIC 0IC(7),R1 ; ISOLATE THE LINE NUMBER
3839 023214 006301 ASL R1 ; ALIGN IT ON A WORD BOUNDARY
3840 023216 120461 001442 CMPB R4,TR0(R1) ; IS THE CHARACTER WHAT IT SHOULD BE?
3841 023222 001413 BEQ 45 ; IF SO, GO CONTINUE PROCESSING
3842 023224 116105 001442 MOVB TR0(R1),R5 ; GET WHAT WAS EXPECTED FOR ERROR REPORTING
3843 023230 042705 177400 BIC 0IC(377),R5 ; ELIMINATE PROPAGATED SIGN
3844 023234 042704 177400 BIC 0IC(377),R4 ; ISOLATE THE ACTUAL CHARACTER
3845 023240 010137 001372 MOV R1,SAVLIN ; GET THE LINE NUMBER OF THE RECEIVER ERROR
3846 023244 006237 001372 ASR SAVLIN ; ALIGN IT CORRECTLY FOR REPORTING
3847 023250 104005 ERROR 5 ; *DATA ERROR
3848 023252 005261 001442 45: INC TR0(R1) ; SET UP THE NEXT EXPECTED CHARACTER
3849 023256 005203 INC R3 ; INCREMENT THE COUNT OF RECEIVED CHARACTERS
3850 023260 032761 000020 001442 BIT 020,TR0(R1) ; HAVE ALL CHARACTERS BEEN RECEIVED?
3851 023266 001402 BEQ 55 ; IF NOT, GO RECEIVE SOME MORE
3852 023270 020203 CMP R2,R3 ; HAVE WE RECEIVED ALL CHARACTERS?
3853 023272 001401 BEQ 65 ; IF SO, GO DETERMINE THE TIMING
3854 023274 000002 55: RTI ; GO CONTINUE TIMING AND ALLOW INTERRUPTS
3855 023276 004737 023122 65: JSR PC,BUILD ; GET THE RELATIVE TIME (SIGNIFICANT BITS)
3856
3857 023302 013700 023362 MOV OFFSET,RO ; GET POINTER
3858 023306 013760 001220 002102 MOV $TMP1,0TMTBL(RO) ; SAVE THIS TEST'S TIME
3859 023314 005737 023362 TST OFFSET ; FIRST TEST?
3860 023320 001414 BEQ 75 ; IF NOT, GO CHECK THE TIME
3861 023322 005740 TST -(RO) ; POINT TO THE PREVIOUS TIME TAKEN
3862 023324 026037 002102 001220 CMP TMTBL(RO),$TMP1 ; IS THIS TIME WHAT IT SHOULD BE?
3863 023332 101007 BHI 75 ; IF SO, GO TO THE NEXT TEST
3864 023334 016005 002102 MOV TMTBL(RO),R5 ; PLACE WHAT WAS EXPECTED IN R5
3865 023340 010137 001372 MOV R1,SAVLIN ; GET THE LINE NUMBER OF THE RECEIVER
3866 023344 006237 001372 ASR SAVLIN ; MAKE SURE IT'S THE LINE NUMBER
3867 023350 104021 ERROR 21 ; TIMING ERROR
3868 023352 042777 000140 156462 75: BIC 0RIE!MSENAB,0DZCSR ; DISABLE THE DEVICE
3869 023360 000002 RTI ; RETURN TO THE PROGRAM
3870 023362 000000 OFFSET: 0
    
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;DZ11 ECHO/CABLE TEST
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;*STARTING PROCEDURE
;*LOAD PROGRAM
;*LOAD ADDRESS 000210
;*PRESS START
;*PROGRAM WILL TYPE DZ11 ECHO/CABLE TEST
;*PROGRAM WILL TYPE WHICH TEST- ECHO OR CABLE
;*TYPE IN E OR C RESPECTIVELY
;*PROGRAM WILL TYPE "VECTOR ADDRESS-"
;*TYPE IN THE ADDRESS OF THE RECEIVER INTERRUPT VECTOR
;*FOR THE DZ11 TO BE TESTED, FOLLOWED BY <CARRIAGE RETURN>
;*PROGRAM WILL TYPE "CONTROL REGISTER ADDRESS-"
;*TYPE IN THE ADDRESS OF THE SYSTEM CONTROL REGISTER
;*FOR THE DZ11 TO BE TESTED, FOLLOWED BY <CARRIAGE RETURN>
;*PROGRAM WILL TYPE "LINE NUMBER-"
;*TYPE IN THE LINE NUMBER TO BE TESTED (IN OCTAL)
;* FOLLOWED BY <CARRIAGE RETURN>
;*PROGRAM WILL TYPE "BAUD RATE-"
;*TYPE IN THE BAUD RATE OF THE DZ11 TERMINAL
;* FOLLOWED BY <CARRIAGE RETURN>
;*THE FOLLOWING BAUD RATES ARE ACCEPTED IN DECIMAL

* 50
* 75
* 110
* 135 (ROUNDED OFF 134.5)
* 150
* 300
* 600
* 1200
* 1800
* 2000
* 2400
* 3600
* 4800
* 7200
* 9600

;*ALL OTHERS ARE REJECTED

;*PROGRAM WILL TYPE "ECHO" OR "CABLE TEST" TO INDICATE THAT TESTING HAS STARTED

;PROGRAM INITIALIZATION
;LOCK OUT INTERRUPTS
;SET UP PROCESSOR STACK
;SET UP POWER FAIL VECTOR
;CLEAR PROGRAM FLAGS AND COUNTS

023364 012706 001120
023370 106427 000340
023374 012737 023364 001126
023402 005037 025560
023406 005037 001242
023412 005037 001132
023416 105037 001123

XSTART: MOV #STACK, SP ;SET UP PROCESSOR STACK
MTPS #PR7 ;LOCK OUT INTERRUPTS
MOV #XSTART, \$LPADR ;SET UP IN CASE OF POWER FAIL
CLR STFLG ;CLEAR TEST START FLAG
CLR \$PASS ;CLEAR PASS COUNT
CLR \$ERTTL ;CLEAR ERROR COUNT
CLRB \$ERFLG ;CLEAR ERROR FLAG

3927	023422	005037	025564		CLR	LAST	;CLEAR LAST ERROR PC
3928	023426	032777	000001	155524	VEC1: BIT	#SW00, #SWR	;IF SW00=1, GET NEW VECTOR
3929	023434	001465			BEQ	OTHER	;AND CSR
3930	023436	012701	000300		VEC2: MOV	#300, R1	
3931	023442	012702	000302		MOV	#302, R2	
3932	023446	010221			IS: MOV	R2, (R1)+	;RESTORE TRAPCATCHER
3933	023450	005022			CLR	(R2)+	;IN FLOATING VECTOR AREA
3934	023452	022122			CMP	(R1)+, (R2)+	;UPDATE THE POINTERS
3935	023454	020127	001000		CMP	R1, #1000	
3936	023460	001372			BNE	IS	
3937	023462	104403			INSTR		;INPUT ADDRESS OF DEVICE VECTOR
3938	023464	025612			MVECTOR		;MESSAGE "VECTOR ADDRESS-"
3939	023466	104405			PARAM		;CONVERT STRING TO OCTAL
3940	023470	000300			300		;LOW LIMIT
3941	023472	000770			770		;HIGH LIMIT
3942	023474	002072			DZRIV		;LOCATIONS TO BE FILLED
3943	023476	003			.BYTE 3		;LSB MASK
3944	023477	004			.BYTE 4		;NUMBER OF LOCATIONS
3945	023500	104403			INSTR		;INPUT ADDRESS OF DEVICE CSR
3946	023502	025634			MREGAD		;MESSAGE "CONTROL REGISTER ADDRESS-"
3947	023504	104405			PARAM		;CONVERT STRING TO OCTAL
3948	023506	160000			160000		;LOW LIMIT
3949	023510	163700			163700		;HIGH LIMIT
3950	023512	002042			DZCSR		;LOCATIONS TO BE FILLED
3951	023514	007			.BYTE 7		;LSB MASK
3952	023515	001			.BYTE 1		;NUMBER OF LOCATIONS
3953	023516	013737	002042	002046	MOV	DZCSR, DZRBUF	;BEGIN BUILDING DEVICE ADDRESSES
3954	023524	062737	000002	002046	ADD	#2, DZRBUF	;FORM THE READ BUFFER ADDRESS
3955	023532	013737	002046	002052	MOV	DZRBUF, DZLPR	;REMEMBER THAT THIS IS ALSO LINE PARAMETER REG.
3956	023540	013737	002046	002056	MOV	DZRBUF, DZTCR	;BEGIN BUILDING TRANSMITTER CONTROL REGISTER
3957	023546	062737	000002	002056	ADD	#2, DZTCR	;FORM THE TRANSMITTER CONTROL REGISTER POINTER
3958	023554	013737	002056	002060	MOV	DZTCR, HDZTCR	
3959	023562	005237	002060		INC	HDZTCR	
3960	023566	013737	002056	002066	MOV	DZTCR, DZTDR	;BEGIN FORMING TRANSMITTER DATA REGISTER
3961	023574	062737	000002	002066	ADD	#2, DZTDR	;FORM THE TRANSMITTER DATA REGISTER
3962	023602	013737	002066	002062	MOV	DZTDR, DZMSR	
3963	023610	032777	000002	155342	OTHER: BIT	#SW01, #SWR	;RESELECT OF TEST?
3964	023616	001427			BEQ	XBEGIN	;IF NOT, SKIP ASKING WHICH ONE
3965	023620	104403			INSTR		;INPUT WHICH TEST YOU ARE RUNNING
3966	023622	026020			MWHICH		;ECHO OR CABLE
3967	023624	104416			PAWCH		;SET FLAG
3968	023626	025556			WCHFLG		;THIS FLAG
3969	023630	104403			BAUD: INSTR		;INPUT BAUD RATE
3970	023632	025742			MSPEED		;MESSAGE "BAUD RATE-"
3971	023634	104415			PARMD		;CONVERT DECIMAL STRING TO OCTAL
3972	023636	000062			50.		;LOW LIMIT
3973	023640	022600			9600.		;HIGH LIMIT
3974	023642	025574			LINESP		;LOCATION TO BE FILLED
3975	023644	000			.BYTE 0		;LSB MASK
3976	023645	001			.BYTE 1		;NUMBER OF LOCATIONS
3977	023646	104413			LINEX: DEVICE.CLR		;CLEAR DEVICE
3978	023650	005037	025560		CLR	STFLG	;CLEAR PROGRAM START FLAG
3979	023654	104403			INSTR		;INPUT LINE NUMBER
3980	023656	025732			MLINE		;MESSAGE "LINE NUMBER-"
3981	023660	104405			PARAM		;CONVERT STRING TO OCTAL
3982	023662	000000			0		;LOW LIMIT

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3983 023664 000007          7          ;HIGH LIMIT
3984 023666 001372          SAVLIN    ;LOCATION TO BE FILLED
3985 023670          000          .BYTE    ;LSB MASK
3986 023671          001          .BYTE    ;NUMBER OF LOCATIONS
3987 023672 004537 025362    JSR      R5,SET
3988
3989 023676 106427 000340    XBEGIN: MTPS  #PR7          ;LOCK OUT INTERRUPTS
3990 023702 012706 001120    MOV      #STACK,SP    ;SET UP PROCESSOR STACK
3991 023706 005037 025562    CLR      LOCKUP        ;CLEAR TIMEOUT
3992 023712 005737 025556    TST     WCHFLG        ;ECHO OR CABLE TEST ?
3993 023716 001413          BEQ      2$           ;ECHO
3994 023720 012737 024434 001126  MOV      #TEST2,$LPADR ;CABLE TEST
3995 023726 005737 025560    TST     STFLG         ;ARE YOU LOOPING ?
3996 023732 001017          BNE     1$           ;YES
3997 023734 005137 025560    COM     STFLG         ;NO
3998 023740 104402 026113    TYPE   MCABLE        ;TYPE CABLE TEST
3999 023744 000412          BR      1$
4000 023746 012737 023776 001126 2$:  MOV      #TEST1,$LPADR ;SET UP ECHO TEST
4001 023754 005737 025560    TST     STFLG         ;ARE YOU LOOPING ?
4002 023760 001004          BNE     1$           ;YES
4003 023762 005137 025560    COM     STFLG         ;NO
4004 023766 104402 026066    TYPE   MTERM        ;TYPE ECHO TEST
4005 023772 000177 155130    1$:  JMP      $SLPADR     ;START TESTING
4006
4007
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4009
4010 023776 104413          ;THIS TEST WILL ACCEPT 1 CHARACTER AT A TIME
4011 024000 012737 000001 001122 ;((IN INTERRUPT MODE) AND TRANSMIT THAT SAME CHARACTER,
4012 024006 013777 025602 156042 ;ONE LINE AT A TIME, ANY LINE 0 THRU 7 (OCTAL)
4013 024014 013737 025600 001366
4014 024022 053737 025576 001366
4015 024030 013777 001366 156014
4016 024036 012777 000040 155776
4017 024044 005004          TEST1: DEVICE.CLR    ;CLEAR DZ11
4018 024046 012705 026130    MOV      #1,$STNM     ;SET TCR BIT
4019 024052 005777 155764    3$:  MOV      NUMTCR,$DZTCR ;SET PARAMETERS
4020 024056 100404          MOV      NUMLIN,PAR   ;SET BAUD RATE
4021 024060 104414          BIS      SPEED,PAR    ;LOAD PARAM.
4022 024062 005304          MOV      PAR,$DZLPR  ;SET SCANN ENABLE
4023 024064 001372          CLR      R4
4024 024066 104003          MOV      #MQUICK,R5  ;SET MESSAGE BUFFER
4025 024070 005004          2$:  TST     $DZCSR       ;TRDY?
4026 024072 112577 155770    BMI     2$           ;BR IF YES
4027 024076 001365          DELAY   2$           ;WAIT
4028 024100 004737 007360          DEC     R4
4029 024104 122777 000377 155046    BNE     3$           ;NO TRDY SET! WHY?
4030 024112 001731          ERROR   3$           ;RESET COUNTER TO 0
4031 024114 104413          CLR     R4           ;LOAD CHAR
4032 024116 106427 000340    2$:  MOV     (R5)+,$DZTDR ;<↑G>?
4033 024122 012737 025072 001360    MOV     PC,SERV.G    ;RE-DO QUICK BROWN?
4034 024130 104413          BNE     3$           ;BR IF REPEAT PATTERN
4035 024132 013737 025600 001366    JSR    PC,SERV.G
4036 024140 053737 025576 001366    CMPB   #377,$SWR
4037
4038 024146 052737 010000 001366    BEQ    TEST1
          DEVICE.CLR
          MTPS  #PR7          ;LOCK OUT INTERRUPTS
          MOV   #XEOP,NEXT
          DEVICE.CLR
          MOV   NUMLIN,PAR   ;SELECT LINE # & SET INTERRUPT ENABLE
          BIS   SPEED,PAR   ;SET LINE SPEED AND
          ;CHARACTER LENGTH (TRANS. & REC.)
          BIS   #RCVON,PAR  ;MAKE SURE RECEIVER IS TURNED ON

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4039 024154 013777 001366 155670      MOV      PAR,JDZLPR      ;LOAD THE LINE PARAMETER REGISTER
4040 024162 012777 024236 155702      MOV      #INTSVC,JDZRIV ;SET UP INTERRUPT SERVICE
4041 024170 013777 025604 155676      MOV      PRIO,JDZRRIS   ;AND LEVEL
4042 024176 106437 026456      MTPS     #LESS1        ;ALLOW INTERRUPTS
4043 024202 012777 000140 155632      MOV      #RIE!MSENAB,JDZCSR ;SET RECEIVER INTERRUPT ENABLE
4044 024210 104402 025760      TYPE     MCHAR         ;TYPE "ANY CHARACTER"
4045 024214 105777 154744      1$:     TSTB     #STKS   ;IF SOMEBODY HITS A KEY- GET NEW LINE #
4046 024220 100375      BPL      1$            ;LOOP HERE
4047 024222 005777 154740      TST      #STKB        ;CLEAR CHAR
4048 024226 004737 007360      JSR      PC,SERV.G    ;MAKE SURE IT WASN'T <↑G>
4049 024232 000137 023646      JMP      LINEX        ;
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4053 024236 105777 155600      INTSVC:  TSTB     #JDZCSR   ;TEST REC. FLAG
4054 024242 100401      BMI      .+4
4055 024244 104004      ERROR   4             ;ERROR - INTERRUPT NOT CAUSED BY FLAG
4056 024246 017737 155574 025606      MOV      #JDZRBUF,RECDAT
4057 024254 100401      BMI      .+4
4058 024256 104023      ERROR   23            ;NON- VALID CHARACTER
4059 024260 032737 020000 025606      BIT      #BIT13,RECDAT ;CHECK FOR FRAMING ERROR
4060 024266 001401      BEQ     .+4           ;BR IF NO ERROR
4061 024270 104025      ERROR   25            ;EITHER SOMEBODY HIT THE
4062                                     ;"BREAK KEY" OR YOU HAVE AN ERROR!
4063 024272 113737 025606 025610      MOV      RECDAT,TBUF   ;MOVE CHARACTER TO OUTPUT AREA
4064 024300 113737 025606 010620      MOV      RECDAT,INBUF  ;MOVE CHARACTER TO CHECK FOR ↑C
4065 024306 042737 177600 010620      BIC     #↑C<177>,INBUF ;STRIP JUNK PLUS PARITY
4066 024314 042737 174377 025606      BIC     #174377,RECDAT ;SAVE ONLY LINE NUMBER
4067 024322 000337 025606      SWAB    RECDAT
4068 024326 023737 001372 025606      CMP     SAVLIN,RECDAT ;DOES THE LINE # COMPARE?
4069 024334 001401      BEQ     .+4
4070 024336 104015      ERROR   15            ;*WRONG LINE NUMBER
4071 024340 012777 000040 155474      MOV      #MSENAB,JDZCSR ;START THE TRANSMITTERS SCANNER
4072 024346 123727 010620 000003      CMP     INBUF,#3      ;IS IT A ↑C ?
4073 024354 001004      BNE     1$            ;NO
4074 024356 104413      DEVICE.CLR
4075 024360 012716 025072      MOV      #XEOP,(SP)   ;CRUNCH STACK
4076 024364 000002      RTI
4077 024366 005003      1$:     CLR      R3      ;INITIALIZE DELAY
4078 024370 013777 025602 155460      MOV      NUMTCR,#JDZTCR ;ENABLE THE LINE
4079 024376 005777 155440      10$:    TST      #JDZCSR   ;TRANSMITTER READY?
4080 024402 100403      BMI      2$           ;IF YES BRANCH
4081 024404 005203      INC     R3            ;INCREMENT DELAY
4082 024406 001373      BNE     10$          ;DELAY DONE?
4083 024410 104003      ERROR   3            ;TRANSMIT READY NOT SET!
4084 024412 113777 025610 155446      2$:     MOV      TBUF,JDZTDR ;TRANSMIT THE CHARACTER
4085 024420 012777 000140 155414      MOV      #RIE!MSENAB,JDZCSR ;RESTART THE RECEIVER
4086 024426 005077 155424      CLR     #JDZTCR      ;CLEAR TCR BIT
4087 024432 000002      RTI
4088
4089
4090
4091
4092

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;THIS TEST TRANSMITS A BINARY COUNT PATTERN
;VIA INTERRUPT MODE TO THE RECEIVER
;THE LINE UNDER TEST MUST BE TERMINATED WITH THE TEST CONNECTOR
TEST2: MTPS     #PR7      ;DISABLE INTERRUPT S
        MOV      #2,$STNM

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4095 024446 012737 025072 001360 MOV #XEOP,NEXT
4096 024454 104413 DEVICE.CLR
4097 ;*TEST TO VERIFY THAT SETTING DTR FOR A GIVEN LINE
4098 ;*WILL BRING UP "CO" AND "RING" FOR THE SAME LINE
4099 ;*THE DIST PNL MUST HAVE JUMPER FROM DTR TO RQST TO SEND
4100 ;*IN ORDER FOR THIS TEST TO WORK!
4101 024456 012737 024464 001362 MOV #1$ LOCK ;LOOP
4102 024464 113777 025602 155366 1$: MOV#B NUMTCR,JDZTCR ;SET DTR
4103 024472 005005 CLR R5
4104 024474 153705 025602 BISB NUMTCR,R5 ;BUILD EXPECTED
4105 024500 000305 SWAB R5 ;PUT IN HIGH BYTE
4106 024502 153705 025602 BISB NUMTCR,R5
4107 024506 104414 DELAY ;WAIT FOR CABLE DELAY
4108 024510 017704 155346 MOV JDZMSR,R4 ;READY MODEM BITS
4109 024514 020504 CMP R5,R4 ;ARE THEY OK?
4110 024516 001401 BEQ 2$ ;BR IF YES
4111 024520 104022 ERROR 22 ;IS THE TEST CONNECTOR ON?
4112 ;HAS RIGHT LINE BEEN SELECTED?
4113 ;IF SO- YOU HAVE A PROBLEM!
4114 ;MODEM BITS NOT RIGHT
4115 024522 104401 2$: SCOP1 ;LOOP
4116 024524 104413 3$: DEVICE.CLR ;INIT DZ11
4117 024526 013737 025576 001366 MOV SPEED,PAR ;SET LINE SPEED
4118 024534 053737 025600 001366 BIS NUMLIN,PAR ;SELECT LINE # & REC. INTERRUPT ENABLE
4119 024542 052737 010000 001366 BIS #RCVON,PAR ;ENABLE THE RECEIVER FOR THIS LINE
4120 024550 052777 040140 155264 BIS #TIE!RIE!MSENAB,JDZCSR ;SET TRANSMITTER INTERRUPT ENABLE
4121 024556 012777 024672 155306 MOV #INTREC,JDZRIV ;SET UP INTR SERVICE
4122 024564 013777 025604 155302 MOV PRIO,JDZRIS ;SET UP LEVEL
4123 024572 012777 025052 155276 MOV #INTRAN,JDZTIV ;SET UP INTR SERVICE
4124 024600 013777 025604 155272 MOV PRIO,JDZTIS ;SET UP LEVEL
4125 024606 005001 CLR R1 ;RX DATA POINTER- SET TO 0
4126 024610 005002 CLR R2 ;TX DATA POINTER- SET TO 0
4127 024612 013777 001366 155232 MOV PAR,JDZLPR ;SET THE PARAMETERS AND TURN ON RECEIVER
4128 024620 106437 026456 MTPS #LESS1 ;ALLOW INTERRUPTS
4129 024624 013777 025602 155224 MOV NUMTCR,JDZTCR ;SET UP TCR BIT
4130
4131 ; YOU RETURN HERE AFTER EVERY RECEIVER INTERRUPT
4132 024632 105777 154326 SPIN: TSTB JDZTKS ;IF SOMEBODY HITS A KEY- GET A NEW LINE #
4133 024636 100006 BPL 1$ ;BR IF NO KEY HIT
4134 024640 005777 154322 TST JDZTKB ;CLEAR CHAR
4135 024644 004737 007360 JSR PC,SERV.G ;MAKE SURE IT WASN'T (<G)
4136 024650 000137 023646 JMP LINEX ;SWD2=1
4137 024654 005237 025562 1$: INC LOCKUP ;INC TIMEOUT FLAG
4138 024660 001364 BNE SPIN ;IF NOT 0 RETURN SPINNING
4139 024662 104011 ERROR 11 ;*RECEIVER FAILED TO INTERRUPT CHECK CABLE/TERMINATOR
4140 024664 104413 QUIT: DEVICE.CLR
4141 024666 000137 025072 JMP XEOP ;CALL FOR END OF PASS
4142 024672 005037 025562 INTREC: CLR LOCKUP ;CLEAR TIMEOUT FLAG
4143 024676 105777 155140 TSTB JDZCSR ;TEST REC DONE
4144 024702 100401 BMI .+4 ;YES
4145 024704 104004 ERROR 4 ;*FALSE INTERRUPT
4146 024706 017737 155134 025606 MOV JDZRBUF,RECDAT ;SAVE WORD
4147 024714 100401 BMI .+4
4148 024716 104023 ERROR 23 ;*NON VALID CHARACTER
4149 024720 032737 040000 025606 BIT #BIT14,RECDAT ;DATA OVERRUN ?
4150 024726 001401 BEQ .+4 ;NO

```

4151	024730	104024			ERROR	24		; *YES
4152	024732	032737	020000	025606	BIT	#BIT13, RECDAT		; FRAMING ERROR ?
4153	024740	001401			BEQ	.+4		; NO
4154	024742	104025			ERROR	25		; *YES
4155	024744	032737	010000	025606	BIT	#BIT12, RECDAT		; PARITY ERROR ?
4156	024752	001401			BEQ	.+4		; NO
4157	024754	104026			ERROR	26		; *YES
4158	024756	110105			MOVB	R1, R5		; SET EXPECTED
4159	024760	042705	177400		BIC	#C<377>, R5		; CLEAR HIGH BYTE
4160	024764	113704	025606		MOVB	RECDAT, R4		; GET FOUND
4161	024770	042704	177400		BIC	#C<377>, R4		; CLEAR HIGH BYTE
4162	024774	020504			CMP	R5, R4 ; OK?		
4163	024776	001401			BEQ	.+4		
4164	025000	104005			ERROR	5		; DATA ERROR
4165	025002	042737	174377	025606	BIC	#174377, RECDAT		; SAVE ONLY LINE NUMBER
4166	025010	000337	025606		SWAB	RECDAT		
4167	025014	023737	001372	025606	CMP	SAVLIN, RECDAT		; DOES THE LINE # COMPARE ?
4168	025022	001401			BEQ	.+4		; YES
4169	025024	104015			ERROR	15		; *WRONG LINE #
4170	025026	120127	000377		CMPB	R1, #377		; LAST CHARACTER ?
4171	025032	001003			BNE	15		; NO
4172	025034	012716	024664		MOV	#QUITS, (SP)		; CRUNCH STACK
4173	025040	000403			BR	25		
4174	025042	105201			15: INCB	R1		; UPDATE EXPECTED DATA
4175	025044	012716	024632		MOV	#SPIN, (SP)		; CRUNCH STACK
4176	025050	000002			25: RTI			
4177								
4178	025052	005777	154764		INTRAN: TST	@DZCSR ; TEST TRANSMIT FLAG		
4179	025056	100401			BMI	.+4		
4180	025060	104003			ERROR	3		; *FALSE INTERRUPT
4181	025062	110277	155000		MOVB	R2, @DZTDR		; TRANSMIT A CHARACTER
4182	025066	105202			INCB	R2		; UPDATE TX DATA
4183	025070	000002			RTI	; RETURN		

4184					
4185					;END OF PASS
4186					;RESTART TEST
4187					
4188	025072	104402			XEOP: TYPE ;TYPE NAME OF TEST
4189	025074	025670			MPASS
4190	025076	005037	025564		CLR LAST ;CLEAR LAST ERROR PC
4191	025102	105037	001123		CLRB \$ERFLG ;CLEAR ERROR FLAG
4192	025106	000137	023676		RSTRT: JMP XBEGIN
4193					
4194					;CONVERT DECIMAL ASCII STRING TO OCTAL
4195	025112	011605			.PARMD: MOV (SP),R5
4196	025114	012537	025276		MOV (R5)+,6\$
4197	025120	012537	025300		MOV (R5)+,7\$
4198	025124	012537	025302		MOV (R5)+,8\$
4199	025130	112537	025304		MOV (R5)+,9\$
4200	025134	112537	025305		MOV (R5)+,10\$
4201	025140	010516			MOV R5,(SP)
4202	025142	005005			2\$: CLR R5
4203	025144	012704	010620		MOV #INBUF,R4
4204	025150	122714	000015		CMPB #15,(R4)
4205	025154	001424			BEQ 3\$
4206	025156	121427	000060		1\$: CMPB (R4),#'0
4207	025162	002421			BLT 3\$
4208	025164	121427	000071		CMPB (R4),#'9
4209	025170	003016			BGT 3\$
4210	025172	142714	000060		BICB #'0,(R4)
4211	025176	005002			CLR R2
4212	025200	152402			BISB (R4)+,R2
4213	025202	060205			ADD R2,R5
4214	025204	122714	000015		CMPB #15,(R4)
4215	025210	001410			BEQ 4\$
4216	025212	006305			ASL R5 ;X2
4217	025214	010502			MOV R5,R2 ;SAVE X2
4218	025216	006305			ASL R5 ;X4
4219	025220	006305			ASL R5 ;X8
4220	025222	060205			ADD R2,R5 ;TIMES 10
4221	025224	000754			BR 1\$
4222	025226	104404			3\$: INSTER
4223	025230	000744			BR 2\$
4224					
4225					;TEST TO SEE IF NUMBER IS WITHIN LIMITS
4226					
4227	025232	020537	025300		4\$: CMP R5,7\$
4228	025236	101373			BHI 3\$
4229	025240	020537	025276		CMP R5,6\$
4230	025244	103770			BLO 3\$
4231	025246	133705	025304		BITB 9\$,R5
4232	025252	001365			BNE 3\$
4233					
4234					;STORE NUMBER AT SPECIFIED ADDRESS
4235					
4236	025254	013704	025302		5\$: MOV 8\$,R4
4237	025260	010524			MOV R5,(R4)+
4238	025262	062705	000002		ADD #2,R5
4239	025266	105337	025305		DECB 10\$

4240 025272 001372
 4241 025274 000002
 4242 025276 000000
 4243 025300 000000
 4244 025302 000000
 4245 025304 000
 4246 025305 000

BNE 55
 RTI
 65: 0
 75: 0
 85: 0
 95: .BYTE 0
 105: .BYTE 0

;COMPARE THE FIRST CHARACTER IN THE TELETYPE INPUT
 ;BUFFER TO THE CHARACTERS "E" AND "C".
 ;IF THE CHARACTER IS "E" CLEAR THE FLAG
 ;IF THE CHARACTER IS "C" SET THE FLAG

4254 025306 017605 000000
 4255 025312 142737 000040 010620
 4256 025320 122737 000105 010620
 4257 025326 001002
 4258 025330 105015
 4259 025332 000406
 4260 025334 122737 000103 010620
 4261 025342 001005
 4262 025344 112715 177777
 4263 025350 062716 000002
 4264 025354 000002
 4265 025356 104404
 4266 025360 000752

.PAWCH:MOV 2(SP),R5
 BICB #40,INBUF ;SET FOR LOWER CASE INPUT
 CMPB #'E',INBUF ;IS IT "E" ?
 BNE 15
 CLRB (R5) ;000
 BR 25
 15: CMPB #'C',INBUF ;IS IT "C" ?
 BNE 35
 MOVB #-1,(R5) ;3177
 25: ADD #2,(SP)
 RTI
 35: INSTER ;RETRY
 BR .PAWCH

;THIS ROUTINE CONVERTS LINE SPEED (LINESP) AND
 ;LINE NUMBER (SAVLIN) FOR DZLPR, DZTCR AND DZCSR
 ;REGISTER USAGE.

4274 025362 013737 001372 025600
 4275 025370 013700 001372
 4276 025374 005037 025602
 4277 025400 012702 000001
 4278 025404 005300
 4279 025406 100402
 4280 025410 006302
 4281 025412 000774
 4282 025414 012701 025456
 4283 025420 010237 025602
 4284 025424 022137 025574
 4285 025430 001407
 4286 025432 005721
 4287 025434 001373
 4288 025436 104402 025704
 4289 025442 012705 023630
 4290 025446 000402
 4291 025450 011137 025576
 4292 025454 000205

SET: MOV SAVLIN,NUMLIN ;SAVE SAVLIN
 XTCRO: MOV SAVLIN,R0 ;COPY THE LINE NUMBER FOR LOOP CONTROL
 CLR NUMTCR ;SET A DEFAULT OF LINE 0 OR NO LINES
 MOV #1,R2 ;SET A BIT POINTER TO THE FIRST LINE
 XTCR1: DEC R0 ;REDUCE THE INDICATOR. IS IT MINUS YET?
 BMI SET1 ;IF SO, R2 POINTS TO THE RIGHT LINE
 ASL R2 ;IF NOT, MOVE THE POINTER TO THE NEXT LINE
 BR XTCR1 ;GO SEE IF THIS LINE IS THE ONE
 SET1: MOV #TABLE2,R1
 MOV R2,NUMTCR ;COPY THE CORRECT BIT POINTER
 15: CMP (R1)+,LINESP
 BEQ 25
 TST (R1)+ ;IS IT THE END OF TABLE?
 BNE 15 ;NO
 TYPE ,MINVAL ;INVALID BAUD RATE,BEGIN AGAIN
 MOV #BAUD,R5 ;JUMP TO BAUD THRU R5
 BR 35
 25: MOV (R1),SPEED ;SET UP BAUD RATE
 35: RTS R5

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 4294
 4295

4296
4297 025456 000062
4298 025460 01007C
4299 025462 000113
4300 025464 010470
4301 025466 000156
4302 025470 011070
4303 025472 000207
4304 025474 011470
4305 025476 000226
4306 025500 012070
4307 025502 000454
4308 025504 012430
4309 025506 001130
4310 025510 013030
4311 025512 002260
4312 025514 013430
4313 025516 003410
4314 025520 014030
4315 025522 003720
4316 025524 014430
4317 025526 004540
4318 025530 015030
4319 025532 007020
4320 025534 015430
4321 025536 011300
4322 025540 016030
4323 025542 016040
4324 025544 016430
4325 025546 022600
4326 025550 017070
4327 025552 177777 000000
4328
4329
4330 025556 000000
4331 025560 000000
4332 025562 000000
4333 025564 000000
4334 025566 000000
4335 025570 000000
4336 025572 000000
4337 025574 000156
4338 025576 006307
4339
4340 025600 000100
4341
4342 025602 000001
4343 025604 000240
4344 025606 000000
4345 025610 000000
4346 025612 053200 041505 047524
025634 041600 047117 051124
025670 050200 051501 020123
025704 044600 053116 046101
025732 046200 047111 035105
025742 041200 052501 020104

TABLE2: ;THE FOLLOWING IS A TABLE OF LEGAL BAUD RATES (8 BITS/CHAR)

.WORD 50. ;50 BAUD
.WORD 10070 ;
.WORD 75. ;75 BAUD
.WORD 10470 ;
.WORD 110. ;110 BAUD
.WORD 11070 ;TWO STOP BITS
.WORD 135. ;134.5 BAUD
.WORD 11470 ;TWO STOP BITS
.WORD 150. ;150 BAUD
.WORD 12070 ;TWO STOP BITS
.WORD 300. ;300 BAUD
.WORD 12430 ;ONE STOP BIT
.WORD 600. ;600 BAUD
.WORD 13030 ;ONE STOP BIT
.WORD 1200. ;1200 BAUD
.WORD 13430 ;ONE STOP BIT
.WORD 1800. ;1800 BAUD
.WORD 14030 ;ONE STOP BIT
.WORD 2000. ;2000 BAUD
.WORD 14430 ;ONE STOP BIT
.WORD 2400. ;2400 BAUD
.WORD 15030 ;ONE STOP BIT
.WORD 3600. ;3600 BAUD
.WORD 15430 ;ONE STOP BIT
.WORD 4800. ;4800 BAUD
.WORD 16030 ;ONE STOP BIT
.WORD 7200. ;7200 BAUD
.WORD 16430 ;ONE STOP BIT
.WORD 9600. ;9600 BAUD
.WORD 17070 ;
.WORD -1,0 ;TABLE TERMINATOR

WCHFLG: 0 ;ECHO OR CABLE FLAG
STFLG: 0 ;PROGRAM START FLAG
LOCKUP: 0 ;TIMEOUT FLAG
LAST: 0 ;LAST ERROR PC
TDATA: 0
RDATA: 0
BYTCNT: 0
LINESP: 110. ;DEFAULT BAUD RATE
SPEED: 6307 ;DEFAULT 110 BAUD, 8 BITS/CHAR,
;FDX, 2 STOP BITS
NUMLIN: 100 ;DEFAULT VALUE, REC. INTERRUPT ENABLED
NUMTCR: 1 ;DEFAULT VALUE, TCR BIT 0
PRIO: 240 ;DEFAULT DEVICE PRIORITY 5
RECDAT: 0
TBUF: 0
MVECTO: .ASCIZ <200>/VECTOR ADDRESS- /
MREGAD: .ASCIZ <200>/CONTROL REGISTER ADDRESS- /
MPASS: .ASCIZ <200>/PASS DONE./
MINVAL: .ASCIZ <200>/INVALID BAUD RATE - /
MLINE: .ASCIZ <200>/LINE: /
MSPEED: .ASCIZ <200>/BAUD RATE - /

025760	052200	050131	020105
026020	053600	044510	044103
026066	052200	051105	044515
026113	200	040503	046102
026130	006777	177777	177412
026137	124	042510	050440
026234	006777	177777	177412
	026246		

```

MCHAR: .ASCIZ <200>/TYPE A CHAR. ON DZ11 TERMINAL /
MWHICH: .ASCIZ <200>/WHICH TEST ? ECHO OR CABLE (E OR C) /
MTERM: .ASCIZ <200>/TERMINAL ECHO TEST /
MCABLE: .ASCIZ <200>/CABLE TEST /
MQUICK: .ASCII <377><15><377><377><12><377><377>
        .ASCII /THE QUICK BROWN FOX JUMPED OVER THE LAZY DOGS BACK 0123456789/
        .ASCII <377><15><377><377><12><377><377><377><0>

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026246	006337	026454	
026252	006337	026454	
026256	006337	026454	
026262	006337	026454	
026266	006337	026454	
026272	013737	026454	026455
026300	162737	000001	026456
026306	042737	000037	026456
026314	013700	002072	
026320	062700	000002	
026324	010037	002074	
026330	062700	000002	
026334	010037	002076	
026340	062700	000002	
026344	010037	002100	
026350	013700	001310	
026354	010037	002042	
026360	005200		
026362	010037	002044	
026366	005200		
026370	010037	002046	
026374	010037	002052	
026400	005200		
026402	010037	002050	
026406	010037	002054	
026412	005200		
026414	010037	002056	
026420	005200		
026422	010037	002060	
026426	005200		
026430	010037	002062	
026434	010037	002066	
026440	005200		
026442	010037	002064	
026446	010037	002070	
026452	000207		
026454	000240		
026456	000200		

```

.EVEN
;*****
;UTILITIES
;*****
;THIS UTILITY CALCULATES PRIORITY LEVEL,SETS UP CSR'S,SETS UP VECTORS.
DZLEV: ASL DZPRT ;BUILD PRIORITY IN THIS LOCATION
        ASL DZPRT ;USING ARITHMETIC SHIFTS, ROTATE
        ASL DZPRT ; THE PRIORITY LEVEL PAST
        ASL DZPRT ; THE BIT POSITIONS COPRE-
        ASL DZPRT ;SPONDING TO THE CONDITION CODES
        MOV DZPRT,LESS1 ;MOVE THIS TO LESS1
        SUB #1,LESS1 ;CREATE THE NEXT LOWEST PRIORITY
        BIC #37,LESS1 ;INSURE THAT THE TNZVC BITS ARE CLEAR
        MOV DZRIV,RO ;PLACE THE BASE VECTOR ADDRESS IN RO
        ADD #2,RO ;CALCULATE THE RECEIVER INTERRUPT STATUS ADDR.
        MOV RO,DZRVIS ;STORE IT HERE
        ADD #2,RO ;CALCULATE THE TRANSMITTER INTERRUPT VECTOR
        MOV RO,DZTIV ;STORE IT HERE
        ADD #2,RO ;CALCULATE THE TRANSMITTER VECTOR STATUS ADDRESS
        MOV RO,DZTIS ;STORE IT HERE

```

```

;THIS SEGMENT SETS UP POINTERS FOR THE GIVEN DZ11. $BASE IS THE BASE ADDRESS
;OF THE DEVICE
MOV $BASE,RO ;COPY THE ADDRESS BEING LOADED
MOV RO,DZCSR ;XXX0
INC RO
MOV RO,HDZCSR ;XXX1
INC RO
MOV RO,DZRBUF ;XXX2
MOV RO,DZLPR ;XXX2
INC RO
MOV RO,HDZRBUF ;XXX3
MOV RO,HDZLPR ;XXX3
INC RO
MOV RO,DZTCR ;XXX4
INC RO
MOV RO,HDZTCR ;XXX5
INC RO
MOV RO,DZMSR ;XXX6
MOV RO,DZTDR ;XXX6
INC RO
MOV RO,HDZMSR ;XXX7
MOV RO,HDZTDR ;XXX7
RTS PC
DZPRT: PR5
LESS1: PR4 ;LEVEL TO ALLOW INTERRUPTS

```

			:ERROR ERROR TABLE	
			.ERRTAB:	
4393				
4394	026460	000000	0	;ERROR 0
4395	026462	000000	0	
4396	026464	000000	0	
4397				
4398	026466	026700	EM1	;ERROR
4399	026470	030150	DH1	
4400	026472	030346	DT1	
4401				
4402	026474	026753	EM2	;ERROR 2
4403	026476	030173	DH2	
4404	026500	030360	DT2	
4405				
4406	026502	027001	EM3	;ERROR 3
4407	026504	030226	DH3	
4408	026506	030376	DT3	
4409				
4410	026510	027040	EM4	;ERROR 4
4411	026512	030226	DH3	
4412	026514	030376	DT3	
4413				
4414	026516	027067	EM5	;ERROR 5
4415	026520	030240	DH4	
4416	026522	030404	DT4	
4417				
4418	026524	027116	EM6	;ERROR 6
4419	026526	030240	DH4	
4420	026530	030404	DT4	
4421				
4422	026532	027154	EM7	;ERROR 7
4423	026534	030226	DH3	
4424	026536	030376	DT3	
4425				
4426	026540	027215	EM8	;ERROR 10
4427	026542	030226	DH3	
4428	026544	030376	DT3	
4429				
4430	026546	027257	EM9	;ERROR 11
4431	026550	030226	DH3	
4432	026552	030376	DT3	
4433				
4434	026554	027315	EM10	;ERROR 12
4435	026556	030226	DH3	
4436	026560	030376	DT3	
4437				
4438	026562	027354	EM13	;ERROR 13
4439	026564	030226	DH3	
4440	026566	030376	DT3	
4441				
4442	026570	027405	EM14	;ERROR 14
4443	026572	030226	DH3	
4444	026574	030376	DT3	
4445				
4446	026576	027437	EM15	;ERROR 15
4447	026600	000000	0	
4448	026602	000000	0	

K09

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DZ11 DEVICE DIAGNOSTICS.

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4449				
4450	026604	027501	EM16	
4451	026606	030226	DH3	
4452	026610	030376	DT3	
4453				
4454	026612	027552	EM17	;ERROR 17
4455	026614	030226	DH3	
4456	026616	030376	DT3	
4457				
4458	026620	027610	EM20	
4459	026622	030226	DH3	
4460	026624	030376	DT3	
4461				
4462	026626	027651	EM21	;ERROR 21
4463	026630	030267	DH5	
4464	026632	030422	DT5	
4465				
4466	026634	027701	EM22	;ERROR 22
4467	026636	030240	DH4	
4468	026640	030404	DT4	
4469				
4470	026642	027743	EM23	;ERROR 23
4471	026644	030226	DH3	
4472	026646	030376	DT3	
4473				
4474	026650	027773	EM24	
4475	026652	030226	DH3	
4476	026654	030376	DT3	
4477				
4478	026656	030021	EM25	
4479	026660	030226	DH3	
4480	026662	030376	DT3	
4481				
4482	026664	030051	EM26	
4483	026666	030226	DH3	
4484	026670	030376	DT3	
4485				
4486	026672	030100	EM27	
4487	026674	030226	DH3	
4488	026676	030376	DT3	

4489
4490

```

026700 047200 020117 046123 EM1: .ASCIZ <200>/NO SLAVE SYNC RESPONSE FROM DZ11 REGISTER/
026753 200 042522 044507 EM2: .ASCIZ <200>'REGISTER R/W FAILURE'
027001 200 051124 047101 EM3: .ASCIZ <200>/TRANSMIT READY (TRDY) NOT SET/
027040 051200 041505 044505 EM4: .ASCIZ <200>/RECEIVER DONE NOT SET/
027067 200 040504 040524 EM5: .ASCIZ <200>/DATA COMPARISON ERROR/
027116 042200 030532 020061 EM6: .ASCIZ <200>/DZ11 *RECEIVER BUFFER* ERROR/
027154 052200 040522 051516 EM7: .ASCIZ <200>/TRANSMITTER FAILED TO INTERRUPT/
027215 200 047125 054105 EM8: .ASCIZ <200>/UNEXPECTED TRANSMITTER INTERRUPT/
027257 200 042522 042503 EM9: .ASCIZ <200>/RECEIVER FAILED TO INTERRUPT/
027315 200 047125 054105 EM10: .ASCIZ <200>/UNEXPECTED RECEIVER INTERRUPT/
027354 051600 046111 020117 EM13: .ASCIZ <200>/SILO ALARM SET TOO SOON/
027405 200 044523 047514 EM14: .ASCIZ <200>/SILO ALARM FAILED TO SET/
027437 200 041501 044524 EM15: .ASCIZ <200>/ACTION DETECTED ON INVALID LINE./
027501 200 042522 042101 EM16: .ASCIZ <200>/READING DZRBUF DID NOT CLEAR SILO ALARM/
027552 042200 052101 020101 EM17: .ASCIZ <200>/DATA VALID SHOULD NOT BE SET/
027610 051200 041505 044505 EM20: .ASCIZ <200>/RECEIVER DONE SHOULD NOT BE SET/
027651 200 042522 040514 EM21: .ASCIZ <200>/RELATIVE TIMING ERROR./
027701 200 047515 042504 EM22: .ASCIZ <200>/MODEM SIGNAL ERROR ON CABLE TEST/
027743 200 040504 040524 EM23: .ASCIZ <200>/DATA VALID IS NOT SET!/
027773 200 040504 040524 EM24: .ASCIZ <200>/DATA OVERRUN IS SET!/
030021 200 051106 046501 EM25: .ASCIZ <200>/FRAMING ERROR OCCURRED/
030051 200 040520 044522 EM26: .ASCIZ <200>/PARITY ERROR OCCURRED/
030100 043200 046125 020114 EM27: .ASCIZ <200>/FULL BINARY COUNT PATTERN NOT RECEIVED/
    
```

```

030150 052200 040522 020120 DH1: .ASCIZ <200>/TRAP PC DZ11 REG/
030173 200 054105 042520 DH2: .ASCIZ <200>/EXPECTED FOUND REGISTER/
030226 046200 047111 020105 DH3: .ASCIZ <200>/LINE NO./
030240 042600 050130 041505 DH4: .ASCIZ <200>/EXPECTED FOUND LINE/
030267 200 054124 046040 DH5: .ASCIZ <200>/TX LINE PREVIOUS TIME ACTUAL TIME PARAMETER/
    
```

.EVEN

:DATA TABLES FOR ERROR MESSAGES

```

030346 000002 / DT1: 2
030350 006 003 .BYTE 6,3
030352 001204 $REG1
030354 006 001 .BYTE 6,1
030356 001202 $REG0

030360 000003 DT2: 3
030362 006 004 .BYTE 6,4
030364 001214 $REG5
030366 006 001 .BYTE 6,1
030370 001212 $REG4
030372 006 001 .BYTE 6,1
030374 001202 $REG0

030376 000001 DT3: 1
030400 003 001 .BYTE 3,1
030402 001372 SAVLIN

030404 000003 DT4: 3
030406 006 004 .BYTE 6,4
030410 001214 $REG5
030412 006 001 .BYTE 6,1
030414 001212 $REG4
    
```

030416	003	001	.BYTE	3,1
030420	001372		SAVLIN	
030422	000004		DTS:	4
030424	003	005	.BYTE	3,5
030426	001372		SAVLIN	
030430	006	011	.BYTE	6,9.
030432	001214		SREGS	
030434	006	007	.BYTE	6,7
030436	001220		STMP1	
030440	006	001	.BYTE	6,1
030442	001400		REGIST	

;TABLE OF DELAY TIMES FOR INDIVIDUAL BAUD RATES

030444	002450	DLYTBL:	2450	: TIME FOR	50 BAUD
030446	001560		1560	: TIME FOR	75 BAUD
030450	001120		1120	: TIME FOR	110 BAUD
030452	000750		750	: TIME FOR	134 BAUD
030454	000660		660	: TIME FOR	150 BAUD
030456	000330		330	: TIME FOR	300 BAUD
030460	000150		150	: TIME FOR	600 BAUD
030462	000060		60	: TIME FOR	1200 BAUD
030464	000040		40	: TIME FOR	1800 BAUD
030466	000030		30	: TIME FOR	2000 BAUD
030470	000020		20	: TIME FOR	2400 BAUD
030472	000010		10	: TIME FOR	3600 BAUD
030474	000001		1	: TIME FOR	4800 BAUD
030476	000001		1	: TIME FOR	7200 BAUD
030500	000001		1	: TIME FOR	9600 BAUD
030502	000001		1	: TIME OF DELAY FOR	19200 BAUD

; DELAYS WERE COMPUTED TO ALLOW MAXIMUM TIME AT EACH BAUD RATE
; FOR ALL TESTS TO FUNCTION CORRECTLY ON A PDP11/45 WITH BIPOLAR
; MEMORY. THE TIMES WERE ALSO TESTED ON AN 11/40 AND 11/10.

030504 000001 CORMAX:
.END

\$MSGTY	001234	409#	1319	1327*	1339	1343*								
\$MTYP1	001265	430#												
\$MTYP2	001271	438#												
\$MTYP3	001275	441#												
\$MTYP4	001301	444#												
\$MXCNT	005234	1193	1205#	1826										
\$N =	000034	1#	2052	2056	2061#	2095	2099	2104#	2125	2131	2136#	2157	2163	2168#
		2189	2195	2200#	2221	2227	2232#	2253	2259	2264#	2285	2290	2295#	2327
		2333	2338#	2380	2387	2392#	2417	2424	2429#	2446	2450	2455#	2472	2476
		2481#	2498	2509	2515#	2557	2567	2573#	2604	2609	2614#	2640	2649	2655#
		2724	2729	2735#	2805	2813	2818#	2872	2877	2882#	2941	2946	2951#	3014
		3022	3027#	3124	3135	3140#	3259	3265	3270#	3344	3349	3354#	3488	3504
		3510#	3587	3598	3603#	3686	3696	3701#	4490#					
\$NULL	001174	383#	1272	1301										
\$NWTST=	000000	2057#	2100#	2132#	2164#	2196#	2228#	2260#	2291#	2334#	2388#	2425#	2451#	2477#
		2510#	2568#	2610#	2650#	2730#	2814#	2878#	2947#	3023#	3136#	3266#	3350#	3505#
		3599#	3697#											
\$OVER	005176	1165	1168	1179	1191	1197#								
\$PASS	001242	412#	782*	1101*	1104*	1111*	1112*	1129	1138	1187	1206	3924*		
\$PASTM	001470	558#												
\$PWRAD	010006	1746#												
\$PWRON	007646	322	780	1713#	1741									
\$PWRNG	010002	1744#												
\$PWRUP	007720	1723	1729#											
\$QUES	001230	400#	979	1301	1385	1882								
\$REGAD	001200	387#												
\$REGO	001202	389#	1462*	1467	4490									
\$REG1	001204	390#	1461*	1468	4490									
\$REG2	001206	391#	1460*	1469										
\$REG3	001210	392#	1459*	1470										
\$REG4	001212	393#	1458*	1471	4490									
\$REG5	001214	394#	1457*	1472	4490									
\$RTNAD	004740	1128#												
\$SAVR6	010016	1722*	1730	1731*	1732*	1750#								
\$SCOPE	004772	1158#												
\$SETUP=	000000	1110	1159											
\$SVLAD	005160	1176	1194#											
\$SVPC =	000040	332#	337											
\$SWR =	164000	1#	10	399	400	1089	1110	1121	1127	1129	1152	1153	1154	1155
		1167	1179	1181	1182	1183	1184	1185	1197	1205	1747	2059	2102	2134
		2166	2198	2230	2262	2293	2336	2390	2427	2453	2479	2512	2570	2612
		2652	2732	2816	2880	2949	3025	3138	3268	3352	3507	3601	3699	
\$SWREG	001256	420#	1926											
\$SWRMK=	000000	1155												
\$TESTN	001240	411#	1195*											
\$TIMES	001226	399#	1110*	1184*	1190	1193*	1205	1826*	3508*					
\$TKB	001166	380#	880	1166	1372	1378	1659	1672	1693	4047	4134			
\$TKS	001164	379#	878	1164	1370	1663	1670	1691	4045	4132				
\$TMP0	001216	395#	3247*	3252*	3514*	3516	3527*	3563						
\$TMP1	001220	396#	877*	906	918	919	920*	1024*	1753	3556*	3568*	3818*	3858	3862
		4490												
\$TMP2	001222	397#	925*	931*	932*	1025*	1026	1755	3816*	3819*				
\$TMP3	001224	398#	3557*	3570*	3817*									
\$TN =	000035	10#	2057	2059#	2100	2102#	2132	2134#	2164	2166#	2196	2198#	2228	2230#
		2260	2262#	2291	2293#	2334	2336#	2388	2390#	2425	2427#	2451	2453#	2477
		2479#	2510	2512#	2568	2570#	2610	2612#	2650	2652#	2730	2732#	2814	2816#

.SCOP1	005236	690	1210#	
.SETFL	010500	700	1762#	1781
.START	002150	343	776#	789
.TRPSR	006630	326	1532#	
.TRPTA	002002	686#	1537	
.TYPE	005262	692	1217#	
.\$ASTA=	***** U	1305	1308	
.\$X =	001462	544#	549	

COMMEN	1#	130#													
ENDCOM	1#	130#													
ERROR	24#	2092	2110	2123	2142	2148	2155	2174	2180	2187	2206	2212	2219	2238	2244
	2251	2270	2276	2283	2303	2311	2321	2349	2357	2365	2378	2398	2402	2415	2436
	2444	2463	2470	2488	2495	2547	2553	2594	2600	2629	2633	2672	2679	2702	2706
	2758	2765	2788	2792	2860	2864	2909	2911	2914	2932	2934	2937	2974	2979	2985
	2999	3004	3006	3054	3057	3065	3073	3076	3084	3085	3107	3111	3116	3166	3174
	3181	3190	3218	3221	3232	3306	3313	3326	3331	3334	3389	3390	3397	3410	3422
	3430	3433	3436	3444	3454	3473	3572	3644	3659	3668	3674	3742	3757	3766	3772
	3793	3828	3831	3835	3847	3867	4024	4055	4058	4061	4070	4083	4111	4139	4145
	4148	4151	4154	4157	4164	4169	4180								
ESCAPE	1#	130#													
GETPRI	1#	130#													
GETSWR	1#	130#													
MULT	1#	130#													
NEWTST	1#	130#	2057	2100	2132	2164	2196	2228	2260	2291	2334	2388	2425	2451	2477
	2510	2568	2610	2650	2730	2814	2878	2947	3023	3136	3266	3350	3505	3599	3697
PASEND	1#	1093													
POP	1#	130#	1347	1348	1734	1735									
PRGEND	1#	1080													
PRGFRT	1#														
PUSH	1#	130#	1308	1310	1331	1715	1721								
REPORT	1#	130#													
SC	1#	1159													
SCOPE	25#	1093	2058	2101	2133	2165	2197	2229	2261	2292	2335	2389	2426	2452	2478
	2511	2569	2611	2651	2731	2815	2879	2948	3024	3137	3267	3351	3506	3600	3698
SC1	1#	1139													
SETPRI	1#	130#													
SETUP	1#	130#													
SKIP	1#	130#													
SLASH	1#	130#													
SPACE	130#														
STARS	1#	130#	330	352	403	406	541	543	550	1087	1148	1224	1303	1711	1727
	2057	2100	2132	2164	2196	2228	2260	2291	2334	2388	2425	2451	2477	2510	2568
	2610	2650	2730	2814	2878	2947	3023	3136	3266	3350	3505	3599	3697		
SWRSU	1#	130#													
TYPBIN	1#	130#													
TYPDEC	1#	130#													
TYPNAM	1#	130#													
TYPNUM	1#	130#													
TYPOCS	1#	130#													
TYPOCT	1#	130#													
TYPTXT	1#	130#													
SAPTYP	1#														
SBUFE	1#	1785													
SCYCLE	1#	1812													
SEOP	1#	1080													
SGETFL	1#	894													
SGETPA	1#	833	846	859	901	957	990	1048	1860						
SHEADE	1#	11													
SINTSE	1#	2894	2917	2963	2988	3629	3727								
SJUNK	1#	566	573	580	587	594	601	608	615	622	629	636	643	650	657
	664	671													
SLINEU	1#	2655	2735	2882	2951	3027	3141	3271							
SLVLT	1#	2872	2941												
SMRESE	1#	2655	2735	2821	2882	2939	2951	3011	3027	3141	3271	3354	3530	3605	3703

.SSIZE 1#
.SSUPR 1#
.STRAP 1#
.STYPB 1#
.STYPD 1#
.STYPE 1# 1221
.STYPO 1#
.S4OCA 1#
.1170 1#

. ABS. 030504 000

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

DZDZAD, DZDZAD/CRF/SOL/NL: TOC+SYSMAC.SML(400,1066), DZDZAD.P11(400,1523)
RUN-TIME: 17 23 1 SECONDS
RUN-TIME RATIO: 421/43=9.7
CORE USED: 51K (101 PAGES)