

LSI11

FIS DIAGNOSTIC MD-11-DVKAC-A

EP-DVKAC-A-DL-A
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FICHE 1 OF 1

OCT 1976
digital
MADE IN USA

The microfiche contains 128 frames of diagnostic data, organized in an 8x16 grid. Each frame displays a different screen or data set from the FIS DIAGNOSTIC program. The data is presented in a high-contrast, monochrome format typical of early computer diagnostics. The frames contain various types of information, including:

- System status reports and error logs.
- Configuration parameters and hardware identification data.
- Test results and diagnostic procedures.
- Memory dump or data dump screens.
- Flowcharts and diagnostic decision trees.

128

5280	SWITCH OPTIONS AND ASSIGNMENTS
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7445	HLT ROUTINE (ERROR TYPEOUT)
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7467	ASCIZ TYPE OUT ROUTINE

CO1

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DVKACA.SRC

SEQ 0002

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ABS
.MCALL SEOP HEADER, SACT11, .SAPTBL5, .SAPTHDR, .SETUP, STARS
.MCALL PUSH POP, .SPOWER
.TITLE DVKACA
;#COPYRIGHT (C) AUGUST 1975
;#DIGITAL EQUIPMENT CORP.
;#MAYNARD, MASS. 01754
;#
;#PROGRAM BY PERVEZ ZAKI
;#
;#THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
;#PACKAGE (MAINDEC-11-DZQAC-B), JULY 11, 1975.
;#
$TN=1
$SWR=160000      ;;HALT ON ERROR, LOOP ON TEST, INHIBIT ERROR TYP0UT

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SWITCH	USE
8	LOOP ON TEST IN SW<7:0>
9	LOOP ON ERROR
10	1 - BELL ON ERROR
11	INHIBIT ITERATIONS
12	INHIBIT TRACE TRAP
13	INHIBIT ERROR TYPEOUTS
14	LOOP ON TEST
15	HALT ON ERROR

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;ERROR MESSAGE FORMAT:
;ERRNM ADR PSM SP ANS1 ANS2 ANS3 ANS4 ANS5 ANS6
;WHERE ERRNM= ERROR NUMBER
;ADR = ADDRESS OF "HLT" INSTRUCTION + 2
;PSM = PROCESSOR STATUS WORD
;SP = STACK POINTER
;ANS1 THRU ANS6 = DATA OFF THE STACK(S)
;NOTE: ANS1 THRU ANS6 ARE NOT ALWAYS TYPED, DEPENDING ON THE
;NUMBER ADDED TO THE "HLT". "HLT" ALONE TYPES NONE.
;"HLT+1" TYPES ANS1, "HLT+2" TYPES ANS1 AND ANS2, ETC.

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5326 104000 HLT= EMT
5327 000000 R0= X0
5328 000001 R1= X1
5329 000002 R2= X2
5330 000003 R3= X3
5331 000004 R4= X4
5332 000005 R5= X5
5333 000005 TTY= X5
5334 000006 SP= X6
5335 000007 PC= X7
5336 000024 PWRVEC= 24
5337 104400 SCOPE= TRAP
5338 100000 SW15= 100000
5339 040000 SW14= 40000
5340 020000 SW13= 20000
5341 010000 SW12= 10000
5342 004000 SW11= 4000
5343 002000 SW10= 2000
5344 001000 SW09= 1000
5345 000400 SW08= 400
5346 000004 TYPE= IOT
5347 000001 N= 1
5348 000001 SF= 1
5349 *****
5350
5351 000000 .= 0 ;TRAP CATCHER FROM 0 - 776
5352 *****
5353
5354 (1) .SBTTL ACT11 HOOKS
5355 (1) ;HOOKS REQUIRED BY ACT11
5356 (1) $SVPC=. ;SAVE PC
5357 (1) 000046 000046 .=46
5358 (1) 000046 015042 SENDAD ;;1)SET LOC.46 TO ADDRESS OF SENDAD IN .SEOP
5359 (1) 000052 000052 .=52
5360 (1) 000052 000000 .WORD 0 ;;2)SET LOC.52 TO ZERO
5361 (1) 001000 .=SSVPC ;; RESTORE PC
5362

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5343      000432      000432      SPSW:      .=      HLTADS+2      ;PROCESSOR STATUS WORD
5344      000432      000434      SSP:      .=      SPSW+2      ;STACK POINTER
5345      000434      000436      ANS1:     .=      SSP+2      ;FIRST ANSWER (SEE CODE)
5346      000434      000436      ANS2:     .=      ANS1+2
5347      000436      000440      ANS3:     .=      ANS2+2
5348      000436      000442      ANS4:     .=      ANS3+2
5349      000440      000444      ANS5:     0
5350      000440      000446      ANS6:     0
5351      000442      000444      ANS7:     0
5352      000442      000444      ANS8:     0
5353      000444      000000      ANS9:     0,0,0,0      ;NON-%6 STACK BUFFER
5354      000444      000000      ERRORS:   0
5355      000446      000000      FISVEC:   244      ;FIS TRAP VECTOR ADDRESS
5356      000450      000000      FISLVL:   246
5357      000452      000000      LADS:     0
5358      000460      000000      RETURN:   .ASCIZ <12><15>      ;RETURN AND LINEFEED
5359      000462      000000      SPACE:    .ASCIZ <15><12>"      ;RETURN AND 3 SPACES
5360      000464      000244
5361      000466      000246
5362      000470      000000
5363      000472      006412      000      020012      020040
5364      000475      015
5365      000502      000
5366      000503      000      SICNT:    .BYTE 0
5367      000504      000007      SBELL:    .EVEN
5368      000506      000000      SAVTPS:   .WORD 7      ;RING A BELL
5369      000510      000000      STACK0:   0      ;LOC TO SAVE TELEPRINTER STATUS
5370      000512      000000      STACK2:   0      ;NON-%6 STACK NORMAL LIMIT
5371      000514      000000
5372      000516      000000
5373      000520      000000      STACK4:   0
5374      000522      000000      STACK6:   0
5375      000524      000000      STACK8:   0,0,0,0,0      ;NON-%6 STACK BUFFER
5376      000526      000000
5377      000528      000000
5378      000530      000000      STAK10:   0
5379      000532      000511      STACK1 = STACK0+1
5380      000534      000000      TEMP:     0
5381      000536      000000      TIMES:    0
5382      000540      000000      TYPCNT:   0
5383      000542      000006      YESRT:    RTT      ;RETURN FROM TRACE TRAP
5384      000544      000000      PR:       0      ;COUNT AND SWITCH
5385      000546      000064      TTYOUT:   64
5386      000550      177564      STPS:     177564      ;TTY PRINTER STATUS REG.
5387      000552      177566      STPB:     177566      ;TTY PRINTER BUFFER REG.
    
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7104 ;*****
7105
7106 .SBTTL STARTING OF THE PROGRAM
7107
7108
7109      000200      200
7110 000200 012767 000001 000330      = MOV      #1,TIMES      ;NUMBER OF ITERATIONS IN THE FIRST PASS=1
7111 000206 012700 000410      MOV      #SDEVCT,RO      ;PREPARE TO INITIALIZE THE PROGRAM
7112 000212 005040      2S: CLR      -(RO)
7113 000214 022700 000400      CMP      #SMAIL,RO
7114 000220 001374      BNE      2S
7115
7116 000222 000167 000352      RESTRT: JMP      BEGIN      ;JUMP TO STARTING ADDRESS OF PROGRAM
7117
7118
7119
7120      000600      600
7121      =
7122 000600 012706 000600      BEGIN: MOV      #BEGIN, SP      ;INITIALIZE STACK POINTER
7123 000604 012737 000542 000014      MOV      #YESRT, @#14      ;SET TRACE TRAP VECTOR
7124 000612 012737 016346 000020      MOV      #STYPE, @#20      ;SET UP VECTOR 20
7125 000620 012737 016206 000024      MOV      #SPWRDN, @#24      ;SERVICE POWER DOWN ROUTINE FOR ANY FUTURE
7126                                     ;POWER DOWN
7127 000626 012700 000030      MOV      #30, RO      ;SET RO TO VECTOR 30
7128 000632 012720 015642      MOV      #HLTS, (0)+      ;SET ENT VECTOR
7129 000636 012720 000340      MOV      #340, (0)+
7130 000642 012720 015074      MOV      #SCOPES, (0)+      ;SET TRAP VECTOR
7131 000646 012710 000340      MOV      #340, (0)
7132 000652 012737 000006 000004 1S: MOV      #6, @#4      ;RESTORE TIME-OUT VECTOR
7133 000660 132737 000001 000420      BITB    #1,@#SENV      ;ARE WE UNDER APT ?
7134 000666 001410      BEQ      2S      ;IF NOT THEN GO TO 2S
7135 000670 012700 000554      MOV      #STPB+2,RO      ;OTHERWISE SET FOR THE OTHER SLU
7136 000674 012740 176566      MOV      #176566,-(RO)
7137 000700 012740 176564      MOV      #176564,-(RO)
7138 000704 012740 000074      MOV      #74,-(RO)
7139 000710 005067 177470      2S: CLR      $TESTN
7140 000714 005067 177550      CLR      LADS      ;CLEAR LOOP ADDRESS
7141
7142
    
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(1)          :TEST 16:      FADD (LSI-11 FLOATING ADD INSTRUCTION)
(1)          :          100200,000000 + 000377,177777 ==> UNDERFLOW
(1)          :          PS(ON STACK) = 012,      STACK POINTER = R3
(1)          :*****
(1)          :
(1) 003224 104400          SCOPE
(1) 003226 004567 012132  TST16: JSR      R5,      PUSHR   ; PUSH 4 WORDS ONTO R3 STACK, SET PRIORITY
(1) 003232 000377 177777      .WORD    000377,177777 ; SECOND OPERAND ON TOP
(1) 003236 100200 000000      .WORD    100200,000000 ; FIRST OPERAND ON BOTTOM
(1) 003242 000157          .WORD    157             ; PROCESSOR PRIORITY LEVEL
(1) 003244 003276 000000      .WORD    ISR16, 000      ; FIS TRAP VECTOR
(1) 003250 012703 000510      MOV      #STACK0,R3    ; SET UP R3 AS STACK POINTER
(1)
(1) 003254 000240          NOP
(1) 003256 075003          FADD     R3             ; FLOATING ADD ON THE R3 STACK
(1)
(1) 003260 004767 012132  RTA16: JSR      PC,      POPR    ; POP THE "ANSWER"
(1) 003264 010367 175144      MOV      R3,      SSP     ; SAVE STACK POINTER (R3)
(1) 003270 104002          HLT+2   ; FIS TRAP DIDN'T OCCURE!
(3) 003272 000103          103     ; THE ERROR NUMBER IS 103
(1) 003274 000462          BR       END16
(1)
(1) 003276 004767 012144  ISR16: JSR      PC,      POPER   ; POP ALL DATA OFF THE STACKS
(1) 003302 010367 175126      MOV      R3,      SSP     ; SAVE STACK POINTER (R3)
(1) 003306 105767 175120      TSTB    SPSW           ; CHECK PS AFTER FIS TRAP
(1) 003312 001402          BEQ     .+6             ; BRANCH IF OK
(1) 003314 104000          HLT     ; PS AFTER FIS TRAP NOT EQUAL TO 000
(3) 003316 000104          104     ; THE ERROR NUMBER IS 104
(1)
(1) 003320 022767 000510 175106  CMP     #STACK0,SSP    ; CHECK THE STACK POINTER (R3)
(1) 003326 001402          BEQ     .+6             ; BRANCH IF OK
(1) 003330 104000          HLT     ; STACK POINTER (R3) NOT EQUAL TO #STACK0
(3) 003332 000105          105     ; THE ERROR NUMBER IS 105
(1)
(1) 003334 022767 003260 175074  CMP     #RTA16,ANS1    ; CHECK FIS TRAP RETURN ADDRESS
(1) 003342 001402          BEQ     .+6             ; BRANCH IF OK
(1) 003344 104001          HLT+1   ; FIS TRAP AT WRONG ADDRESS
(3) 003346 000106          106     ; THE ERROR NUMBER IS 106
(1)
(1) 003350 022767 000012 175062  CMP     #012,ANS2     ; CHECK PS BEFORE FIS TRAP
(1) 003356 001402          BEQ     .+6             ; BRANCH IF OK
(1) 003360 104002          HLT+2   ; PS AT FIS TRAP TIME NOT 012
(3) 003362 000107          107     ; THE ERROR NUMBER IS 107
(1)
(1) 003364 022767 000377 175050  CMP     #000377,ANS3   ; CHECK DATA FROM THE STACK
(1) 003372 001402          BEQ     .+6             ; BRANCH IF OK
(1) 003374 104004          HLT+4   ; DATA ON STACK (000377) CHANGED
(3) 003376 000110          110     ; THE ERROR NUMBER IS 110
(1)
(1) 003400 022767 177777 175036  CMP     #177777,ANS4   ; CHECK DATA FROM STACK
(1) 003406 001402          BEQ     .+6             ; BRANCH IF OK
(1) 003410 104004          HLT+4   ; DATA ON STACK (177777) CHANGED
(3) 003412 000111          111     ; THE ERROR NUMBER IS 111
(1)
(1) 003414 022767 100200 175024  CMP     #100200,ANS5   ; CHECK DATA FROM STACK
(1) 003422 001402          BEQ     .+6             ; BRANCH IF OK
    
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M02

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(1) 003706 104400
(1) 003710 004567 011450
(1) 003714 177652 125252
(1) 003720 177452 125253
(1) 003724 000105
(1) 003726 003760 000252
(1) 003732 012701 000510
(1)
(1) 003736 000240
(1) 003740 075001
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(1) 003742 004767 011450
(1) 003746 010167 174462
(1) 003752 104002
(3) 003754 000127
(1) 003756 000464
(1)
(1) 003760 004767 011462
(1) 003764 010167 174444
(1) 003770 122767 000252 174434
(1) 003776 001402
(1) 004000 104000
(3) 004002 000130
(1)
(1) 004004 022767 000510 174422
(1) 004012 001402
(1) 004014 104000
(3) 004016 000131
(1)
(1) 004020 022767 003742 174410
(1) 004026 001402
(1) 004030 104001
(3) 004032 000132
(1)
(1) 004034 022767 000002 174376
(1) 004042 001402
(1) 004044 104002
(3) 004046 000133
(1)
(1) 004050 022767 177652 174364
(1) 004056 001402
(1) 004060 104004
(3) 004062 000134
(1)
(1) 004064 022767 125252 174352
(1) 004072 001402
(1) 004074 104004
(3) 004076 000135
(1)
;*****
;TEST 20: FADD (LSI-11 FLOATING ADD INSTRUCTION)
;      177452,125253 + 177652,125252 ==> OVERFLOW
;      PS(ON STACK) = 002,  STACK POINTER = R1
;*****
SCOPE
TST20: JSR    R5,   PUSH 4 WORDS ONTO R1 STACK, SET PRIORITY
        .WORD  177652,125252 ;SECOND OPERAND ON TOP
        .WORD  177452,125253 ;FIRST OPERAND ON BOTTOM
        .WORD  105           ;PROCESSOR PRIORITY LEVEL
        .WORD  ISR20, 252    ;FIS TRAP VECTOR
        MOV    #STACK0,R1   ;SET UP R1 AS STACK POINTER
NOP
FADD   R1                   ;FLOATING ADD ON THE R1 STACK
RTA20: JSR    PC,   POPR    ;POP THE "ANSWER"
        MOV    R1,   SSP    ;SAVE STACK POINTER (R1)
        HLT+2 ;FIS TRAP DIDN'T OCCURE!
        127   ;THE ERROR NUMBER IS 127
BR     END20
ISR20: JSR    PC,   POPER    ;POP ALL DATA OFF THE STACKS
        MOV    R1,   SSP    ;SAVE STACK POINTER (R1)
        CMPB  #252, SPSW   ;CHECK PS AFTER FIS TRAP
        BEQ   .+6          ;BRANCH IF OK
        HLT   ;PS AFTER FIS TRAP NOT EQUAL TO 252
        130   ;THE ERROR NUMBER IS 130
CMP    #STACK0,SSP          ;CHECK THE STACK POINTER (R1)
BEQ    .+6                 ;BRANCH IF OK
HLT    ;STACK POINTER (R1) NOT EQUAL TO #STACK0
131   ;THE ERROR NUMBER IS 131
CMP    #RTA20,ANS1         ;CHECK FIS TRAP RETURN ADDRESS
BEQ    .+6                 ;BRANCH IF OK
HLT+1 ;FIS TRAP AT WRONG ADDRESS
132   ;THE ERROR NUMBER IS 132
CMP    #002,ANS2          ;CHECK PS BEFORE FIS TRAP
BEQ    .+6                 ;BRANCH IF OK
HLT+2 ;PS AT FIS TRAP TIME NOT 002
133   ;THE ERROR NUMBER IS 133
CMP    #177652,ANS3       ;CHECK DATA FROM THE STACK
BEQ    .+6                 ;BRANCH IF OK
HLT+4 ;DATA ON STACK (177652) CHANGED
134   ;THE ERROR NUMBER IS 134
CMP    #125252,ANS4       ;CHECK DATA FROM STACK
BEQ    .+6                 ;BRANCH IF OK
HLT+4 ;DATA ON STACK (125252) CHANGED
135   ;THE ERROR NUMBER IS 135

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:*****
:TEST 21:      FADD (LSI-11 FLOATING ADD INSTRUCTION)
:              077452,125253 + 077452,125252 ==> OVERFLOW
:              PS(ON STACK) = 002,   STACK POINTER = SP
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004144  104400           SCOPE
004146  004567  011040   TST21: JSR      RS,      PUSH5 ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
004152  077452  125252       .WORD    077452,125252 ;SECOND OPERAND ON TOP
004156  077652  125253       .WORD    077652,125253 ;FIRST OPERAND ON BOTTOM
004162  000003       .WORD    003          ;PROCESSOR PRIORITY LEVEL
004164  004212  000344       .WORD    ISR21,   344   ;FIS TRAP VECTOR

004170  000240           NOP
004172  075006           FADD     SP            ;FLOATING ADD ON THE STACK

004174  004767  011052           RTA21: JSR      PC,      POPS   ;POP THE "ANSWER"
004200  104002           HLT+2        ;FIS TRAP DIDN'T OCCURE!
004202  000141           141          ;THE ERROR NUMBER IS 141
004204  012706  000600           MOV      #BEGIN, SP   ;RESTORE THE STACK POINTER
004210  000464           BR

004212  004767  011066           ISR21: JSR      PC,      POPES  ;POP ALL DATA OFF THE STACK
004216  022706  000600           CMP      #BEGIN, SP   ;CHECK THE STACK POINTER
004222  001405           BEQ      ISA21        ;BRANCH IF OK
004224  012706  000600           MOV      #BEGIN, SP   ;RESTORE THE STACK POINTER
004230  104000           HLT       ;STACK POINTER FOULED UP
004232  000142           142        ;THE ERROR NUMBER IS 142
004234  000452           BR      END21        ;SKIP REST OF TEST

004236  122767  000344  174166           ISA21: CMPB     #344,   SPSW   ;CHECK PS AFTER FIS TRAP
004244  001402           BEQ      .+6         ;BRANCH IF OK
004246  104000           HLT       ;PS AFTER FIS TRAP NOT EQUAL TO 344
004250  000143           143        ;THE ERROR NUMBER IS 143

004252  022767  004174  174156           CMP      #RTA21, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
004260  001402           BEQ      .+6         ;BRANCH IF OK
004262  104001           HLT+1     ;FIS TRAP AT WRONG ADDRESS
004264  000144           144        ;THE ERROR NUMBER IS 144

004266  022767  000002  174144           CMP      #002,   ANS2  ;CHECK PS BEFORE FIS TRAP
004274  001402           BEQ      .+6         ;BRANCH IF OK
004276  104002           HLT+2     ;PS AT FIS TRAP TIME NOT 002
004300  000145           145        ;THE ERROR NUMBER IS 145

004302  022767  077452  174132           CMP      #077452,ANS3 ;CHECK DATA FROM THE STACK
004310  001402           BEQ      .+6         ;BRANCH IF OK
004312  104004           HLT+4     ;DATA ON STACK (077452) CHANGED
004314  000146           146        ;THE ERROR NUMBER IS 146

004316  022767  125252  174120           CMP      #125252,ANS4 ;CHECK DATA FROM STACK
004324  001402           BEQ      .+6         ;BRANCH IF OK
004326  104004           HLT+4     ;DATA ON STACK (125252) CHANGED
004330  000147           147        ;THE ERROR NUMBER IS 147

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(1) 006304 022767 000425 172134      CMP      #000425,ANS5 ;CHECK DATA FROM STACK
(1) 006312 001402                      BEQ      .+6          ;BRANCH IF OK
(1) 006314 104006                      HLT+6     ;DATA ON STACK (000425) CHANGED
(3) 006316 000237                      237        ;THE ERROR NUMBER IS 237
(1)
(1) 006320 022767 052525 172122      CMP      #052525,ANS6 ;CHECK DATA FROM STACK
(1) 006326 001402                      BEQ      .+6          ;BRANCH IF OK
(1) 006330 104006                      HLT+6     ;DATA ON STACK (052525) CHANGED
(3) 006332 000240                      240        ;THE ERROR NUMBER IS 240
(1)
(1) 006334 122767 000033 172042 END33: CMPB     #33,      STSTN ;CHECK THE TEST NUMBER
(1) 006342 001402                      BEQ      .+6          ;BRANCH IF OK
(1) 006344 104000                      HLT      ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 006346 000241                      241        ;THE ERROR NUMBER IS 241
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E04

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*****
TEST 36:   FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
          140200,000000 * 052345,123456 = 152345,123456
          PS = 210,         STACK POINTER = R2
*****

TST36:    SCOPE
          JSR      R5,      PUSH4      ; PUSH 4 WORDS ONTO R2 STACK, SET PRIORITY
          .WORD   052345,123456      ; SECOND OPERAND ON TOP
          .WORD   140200,000000      ; FIRST OPERAND ON BOTOM
          .WORD   343                ; PROCESSOR PRIORITY LEVEL
          .WORD   TRAPER, 340        ; FIS TRAP VECTOR
          MOV      #STACK0,R2        ; SET UP STACK POINTER

          NOP
          FMUL    R2                ; FLOATING MULTIPLY ON THE R2 STACK

          JSR      PC,      POPR       ; POP THE ANSWER
          MOV      R2,      $SP       ; SAVE "STACK POINTER"
          CMPB    #210,     $PSW      ; CHECK PS (EXCEPT T BIT)
          BEQ     .+6              ; BRANCH IF OK
          HLT     .+6              ; PS NOT EQUAL TO 210
          .+6                        ; THE ERROR NUMBER IS 261

          CMP     #STACK4,$SP        ; CHECK THE STACK POINTER (R2)
          BEQ     .+6              ; BRANCH IF OK
          HLT     .+6              ; STACK POINTER (R2) NOT EQUAL TO #STACK4
          .+6                        ; THE ERROR NUMBER IS 262

          CMP     #152345,ANS1      ; CHECK FIRST HALF OF ANSWER
          BEQ     .+6              ; BRANCH IF OK
          HLT+2  .+6              ; ANS1 NOT EQUAL TO 152345
          .+6                        ; THE ERROR NUMBER IS 263

          CMP     #123456,ANS2      ; CHECK SECOND HALF OF ANSWER
          BEQ     .+6              ; BRANCH IF OK
          HLT+2  .+6              ; ANS2 NOT EQUAL TO 123456
          .+6                        ; THE ERROR NUMBER IS 264

          CMPB    #36,      $TESTN    ; CHECK THE TEST NUMBER
          BEQ     .+6              ; BRANCH IF OK
          HLT     .+6              ; WRONG TEST! PC MUST HAVE FOULED UP.
          .+6                        ; THE ERROR NUMBER IS 265
    
```

006742 104400
 006744 004567 006414
 006750 052345 .23456
 006754 140200 000000
 006760 000343
 006762 015634 000340
 006766 012702 000510
 006772 000240
 006774 075022
 006776 004767 006414
 007002 010267 171426
 007006 122767 000210 171416
 007014 001402
 007016 104000
 007020 000261
 007022 022767 000514 171404
 007030 001402
 007032 104000
 007034 000262
 007036 022767 152345 171372
 007044 001402
 007046 104002
 007050 000263
 007052 022767 123456 171360
 007060 001402
 007062 104002
 007064 000264
 007066 122767 000036 171310 END36:
 007074 001402
 007076 104000
 007100 000265

F04

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 52-42
 DVKACA.SRC FMUL TEST SECTION

SEQ 0044

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*****
TEST 37:      FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
              100125,052525 * 135753,024642 = 000000,000000
              PS = 004,          STACK POINTER = RS
*****

```

```

007102 104400
007104 004567 006254
007110 135753 024642
007114 100125 052525
007120 000117
007122 015634 000340
007126 012705 000510
007132 000240
007134 075025
007136 004767 006254
007142 010567 171266
007146 122767 000004 171256
007154 001402
007156 104000
007160 000266
007162 022767 000514 171244
007170 001402
007172 104000
007174 000267
007176 005767 171234
007202 001402
007204 104002
007206 000270
007210 005767 171224
007214 001402
007216 104002
007220 000271
007222 122767 000037 171154 END37:
007230 001402
007232 104000
007234 000272

```

```

TST37: SCOPE
JSR     RS,     PUSHR   ;PUSH 4 WORDS ONTO RS STACK, SET PRIORITY
        .WORD   135753,024642 ;SECOND OPERAND ON TOP
        .WORD   100125,052525 ;FIRST OPERAND ON BOTOM
        .WORD   117        ;PROCESSOR PRIORITY LEVEL
        .WORD   TRAPER, 340  ;FIS TRAP VECTOR
MOV     #STACK0,RS      ;SET UP STACK POINTER
NOP
FMUL   RS              ;FLOATING MULTIPLY ON THE RS STACK
JSR     PC,     POPR    ;POP THE ANSWER
MOV     RS,     SSP     ;SAVE "STACK POINTER"
CMPB   #004,    SPSW    ;CHECK PS (EXCEPT T BIT)
BEQ    .+6         ;BRANCH IF OK
HLT    ;PS NOT EQUAL TO 004
        266        ;THE ERROR NUMBER IS 266
CMP     #STACK4,SSP    ;CHECK THE STACK POINTER (RS)
BEQ    .+6         ;BRANCH IF OK
HLT    ;STACK POINTER (RS) NOT EQUAL TO #STACK4
        267        ;THE ERROR NUMBER IS 267
TST     ANS1
BEQ    .+6         ;CHECK FIRST HALF OF ANSWER
        .+6        ;BRANCH IF OK
HLT+2  ;ANS1 NOT EQUAL TO 000000
        270        ;THE ERROR NUMBER IS 270
TST     ANS2
BEQ    .+6         ;CHECK SECOND HALF OF ANSWER
        .+6        ;BRANCH IF OK
HLT+2  ;ANS2 NOT EQUAL TO 000000
        271        ;THE ERROR NUMBER IS 271
CMPB   #37,     STSTN   ;CHECK THE TEST NUMBER
BEQ    .+6         ;BRANCH IF OK
HLT    ;WRONG TEST! PC MUST HAVE FOULED UP.
        272        ;THE ERROR NUMBER IS 272

```



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7198
(1)
(1) :*****
(1) :TEST 42: FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
(1) :124252,125252 * 114100,000001 = 000200,000000
(1) :PS = 200, STACK POINTER = R1
(1) :*****

```

(1) 007526	104400		SCOPE			
(1) 007530	004567	005630	TST42: JSR	RS,	PUSHR	; PUSH 4 WORDS ONTO R1 STACK, SET PRIORITY
(1) 007534	114100	000001	.WORD	114100,000001		; SECOND OPERAND ON TOP
(1) 007540	124252	125252	.WORD	124252,125252		; FIRST OPERAND ON BOTOM
(1) 007544	000200		.WORD	200		; PROCESSOR PRIORITY LEVEL
(1) 007546	015634	000340	.WORD	TRAPER, 340		; FIS TRAP VECTOR
(1) 007552	012701	000510	MOV	#STACK0,R1		; SET UP STACK POINTER
(1) 007556	000240		NOP			
(1) 007560	075021		FMUL	R1		; FLOATING MULTIPLY ON THE R1 STACK
(1) 007562	004767	005630	JSR	PC,	POPR	; POP THE ANSWER
(1) 007566	010167	170642	MOV	R1,	\$SP	; SAVE "STACK POINTER"
(1) 007572	122767	000200	CMPB	#200,	\$PSW	; CHECK PS (EXCEPT T BIT)
(1) 007600	001402		BEQ	+.6		; BRANCH IF OK
(1) 007602	104000		HLT			; PS NOT EQUAL TO 200
(3) 007604	000305		305			; THE ERROR NUMBER IS 305
(1) 007606	022767	000514	CMP	#STACK4,\$SP		; CHECK THE STACK POINTER (R1)
(1) 007614	001402		BEQ	+.6		; BRANCH IF OK
(1) 007616	104000		HLT			; STACK POINTER (R1) NOT EQUAL TO #STACK4
(3) 007620	000306		306			; THE ERROR NUMBER IS 306
(1) 007622	022767	000200	CMP	#000200,ANS1		; CHECK FIRST HALF OF ANSWER
(1) 007630	001402		BEQ	+.6		; BRANCH IF OK
(1) 007632	104002		HLT+2			; ANS1 NOT EQUAL TO 000200
(3) 007634	000307		307			; THE ERROR NUMBER IS 307
(1) 007636	005767	170576	TST	ANS2		; CHECK SECOND HALF OF ANSWER
(1) 007642	001402		BEQ	+.6		; BRANCH IF OK
(1) 007644	104002		HLT+2			; ANS2 NOT EQUAL TO 000000
(3) 007646	000310		310			; THE ERROR NUMBER IS 310
(1) 007650	122767	000042	END42: CMPB	#42,	\$TESTN	; CHECK THE TEST NUMBER
(1) 007656	001402		BEQ	+.6		; BRANCH IF OK
(1) 007660	104000		HLT			; WRONG TEST! PC MUST HAVE FOULED UP.
(3) 007662	000311		311			; THE ERROR NUMBER IS 311
(1)						
(1)						


```

(1) ;TEST 45: FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
(1) ; 024252,125252 * 114100,000000 ==> UNDERFLOW
(1) ; PS(ON STACK) = 212, STACK POINTER = RO
(1) ;*****
(1)
(1) 010214 104400 SCOPE
(1) 010216 004567 005142 TST45: JSR R5, PUSHR ;PUSH 4 WORDS ONTO RO STACK, SET PRIORITY
(1) 010222 114100 000000 .WORD 114100,000000 ;SECOND OPERAND ON TOP
(1) 010226 024252 125252 .WORD 024252,125252 ;FIRST OPERAND ON BOTTOM
(1) 010232 000305 .WORD 305 ;PROCESSOR PRIORITY LEVEL
(1) 010234 010266 000057 .WORD ISR45, 057 ;FIS TRAP VECTOR
(1) 010240 012700 000510 MOV #STACK0,RO ;SET UP RO AS STACK POINTER
(1)
(1) 010244 000240 NOP
(1) 010246 075020 FMUL RO ;FLOATING MULTIPLY ON THE RO STACK
(1)
(1) 010250 004767 005142 RTA45: JSR PC, POPR ;POP THE "ANSWER"
(1) 010254 010067 170154 MOV RO, SSP ;SAVE STACK POINTER (RO)
(1) 010260 104002 HLT+2 ;FIS TRAP DIDN'T OCCURE!
(1) 010262 000326 326 ;THE ERROR NUMBER IS 326
(1) 010264 000463 BR END45
(1)
(1) 010266 004767 005154 ISR45: JSR PC, POPER ;POP ALL DATA OFF THE STACKS
(1) 010272 010067 170136 MOV RO, SSP ;SAVE STACK POINTER (RO)
(1) 010276 122767 000057 170126 CMPB #057, SPSW ;CHECK PS AFTER FIS TRAP
(1) 010304 001402 BEQ .+6 ;BRANCH IF OK
(1) 010306 104000 HLT ;PS AFTER FIS TRAP NOT EQUAL TO 057
(3) 010310 000327 327 ;THE ERROR NUMBER IS 327
(1)
(1) 010312 022767 000510 170114 CMP #STACK0,SSP ;CHECK THE STACK POINTER (RO)
(1) 010320 001402 BEQ .+6 ;BRANCH IF OK
(1) 010322 104000 HLT ;STACK POINTER (RO) NOT EQUAL TO #STACK0
(3) 010324 000330 330 ;THE ERROR NUMBER IS 330
(1)
(1) 010326 022767 010250 170102 CMP #RTA45, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
(1) 010334 001402 BEQ .+6 ;BRANCH IF OK
(1) 010336 104001 HLT+1 ;FIS TRAP AT WRONG ADDRESS
(3) 010340 000331 331 ;THE ERROR NUMBER IS 331
(1)
(1) 010342 022767 000212 170070 CMP #212, ANS2 ;CHECK PS BEFORE FIS TRAP
(1) 010350 001402 BEQ .+6 ;BRANCH IF OK
(1) 010352 104002 HLT+2 ;PS AT FIS TRAP TIME NOT 212
(3) 010354 000332 332 ;THE ERROR NUMBER IS 332
(1)
(1) 010356 022767 114100 170056 CMP #114100,ANS3 ;CHECK DATA FROM THE STACK
(1) 010364 001402 BEQ .+6 ;BRANCH IF OK
(1) 010366 104004 HLT+4 ;DATA ON STACK (114100) CHANGED
(3) 010370 000333 333 ;THE ERROR NUMBER IS 333
(1)
(1) 010372 005767 170046 TST ANS4 ;CHECK DATA FROM STACK
(1) 010376 001402 BEQ .+6 ;BRANCH IF OK
(1) 010400 104004 HLT+4 ;DATA ON STACK (000000) CHANGED
(3) 010402 000334 334 ;THE ERROR NUMBER IS 334
(1)
(1) 010404 022767 024252 170034 CMP #024252,ANS5 ;CHECK DATA FROM STACK
(1) 010412 001402 BEQ .+6 ;BRANCH IF OK
    
```

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DVKACA.SRC TEST FLOATING MUL. INSTRUCTION WITH UNDERFLOW

```
(1) 010414 104006           HLT+6           ;DATA ON STACK (024252) CHANGED
(3) 010416 000335           335            ;THE ERROR NUMBER IS 335
(1)
(1) 010420 022767 125252 170022  CMP #125252,ANS6 ;CHECK DATA FROM STACK
(1) 010426 001402           BEQ .+6         ;BRANCH IF OK
(1) 010430 104006           HLT+6           ;DATA ON STACK (125252) CHANGED
(3) 010432 000336           336            ;THE ERROR NUMBER IS 336
(1)
(1) 010434 122767 000045 167742 END45: CMPB #45, STESTN ;CHECK THE TEST NUMBER
(1) 010442 001402           BEQ .+6         ;BRANCH IF OK
(1) 010444 104000           HLT             ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 010446 000337           337            ;THE ERROR NUMBER IS 337
(1)
(1)
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7210

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(1) ;*****
(1) :TEST 46:    FMUL (LSI-11 FLOATING MULTIPLY INSTRUCTION)
(1) :          076452,125252 * 041500,000001 ==> OVERFLOW
(1) :          PS(ON STACK) = 002,    STACK POINTER = SP
(1) ;*****
(1)
(1) 010450  104400        SCOPE
(1) 010452  004567  004534  TST46:  JSR    R5,    PUSH5 ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
(1) 010456  041500  000001        .WORD  041500,000001 ;SECOND OPERAND ON TOP
(1) 010462  076452  125252        .WORD  076452,125252 ;FIRST OPERAND ON BOTTOM
(1) 010466  000105        .WORD  105          ;PROCESSOR PRIORITY LEVEL
(1) 010470  010516  000357        .WORD  ISR46,   357 ;FIS TRAP VECTOR
(1)
(1) 010474  000240        NOP
(1) 010476  075026        FMUL   SP          ;FLOATING MULTIPLY ON THE STACK
(1)
(1) 010500  004767  004546        RTA46: JSR    PC,    POPS ;POP THE "ANSWER"
(1) 010504  104002        HLT+2 ;FIS TRAP DIDN'T OCCURE!
(3) 010506  000340        340    ;THE ERROR NUMBER IS 340
(1) 010510  012706  000600        MOV    #BEGIN, SP ;RESTORE THE STACK POINTER
(1) 010514  000464        BR     END46
(1)
(1) 010516  004767  004562        ISR46: JSR    PC,    POPES ;POP ALL DATA OFF THE STACK
(1) 010522  022706  000600        CMP    #BEGIN, SP ;CHECK THE STACK POINTER
(1) 010526  001405        BEQ   ISA46        ;BRANCH IF OK
(1) 010530  012706  000600        MOV    #BEGIN, SP ;RESTORE THE STACK POINTER
(1) 010534  104000        HLT           ;STACK POINTER FOULED UP
(3) 010536  000341        341    ;THE ERROR NUMBER IS 341
(1) 010540  000452        BR     END46 ;SKIP REST OF TEST
(1)
(1) 010542  122767  000357  167662  ISA46: CMPB   #357,   SPSW ;CHECK PS AFTER FIS TRAP
(1) 010550  001402        BEQ   .+6         ;BRANCH IF OK
(1) 010552  104000        HLT           ;PS AFTER FIS TRAP NOT EQUAL TO 357
(3) 010554  000342        342    ;THE ERROR NUMBER IS 342
(1)
(1) 010556  022767  010500  167652        CMP    #RTA46, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
(1) 010564  001402        BEQ   .+6         ;BRANCH IF OK
(1) 010566  104001        HLT+1 ;FIS TRAP AT WRONG ADDRESS
(3) 010570  000343        343    ;THE ERROR NUMBER IS 343
(1)
(1) 010572  022767  000002  167640        CMP    #002,   ANS2 ;CHECK PS BEFORE FIS TRAP
(1) 010600  001402        BEQ   .+6         ;BRANCH IF OK
(1) 010602  104002        HLT+2 ;PS AT FIS TRAP TIME NOT 002
(3) 010604  000344        344    ;THE ERROR NUMBER IS 344
(1)
(1) 010606  022767  041500  167626        CMP    #041500,ANS3 ;CHECK DATA FROM THE STACK
(1) 010614  001402        BEQ   .+6         ;BRANCH IF OK
(1) 010616  104004        HLT+4 ;DATA ON STACK (041500) CHANGED
(3) 010620  000345        345    ;THE ERROR NUMBER IS 345
(1)
(1) 010622  022767  000001  167614        CMP    #000001,ANS4 ;CHECK DATA FROM STACK
(1) 010630  001402        BEQ   .+6         ;BRANCH IF OK
(1) 010632  104004        HLT+4 ;DATA ON STACK (000001) CHANGED
(3) 010634  000346        346    ;THE ERROR NUMBER IS 346
(1)

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(1) 010636 022767 076452 167602      CMP      #076452,ANS5    ;CHECK DATA FROM STACK
(1) 010644 001402                      BEQ      .+6           ;BRANCH IF OK
(1) 010646 104006                      HLT+6        ;DATA ON STACK (076452) CHANGED
(3) 010650 000347                      347          ;THE ERROR NUMBER IS 347
(1)
(1) 010652 022767 125252 167570      CMP      #125252,ANS6    ;CHECK DATA FROM STACK
(1) 010660 001402                      BEQ      .+6           ;BRANCH IF OK
(1) 010662 104006                      HLT+6        ;DATA ON STACK (125252) CHANGED
(3) 010664 000350                      350          ;THE ERROR NUMBER IS 350
(1)
(1) 010666 122767 000046 167510 END46: CMPB     #46,      STSTN    ;CHECK THE TEST NUMBER
(1) 010674 001402                      BEQ      .+6           ;BRANCH IF OK
(1) 010676 104000                      HLT          ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 010700 000351                      351          ;THE ERROR NUMBER IS 351
(1)
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7215

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(1) 011040 104400
(1) 011042 004567 004144
(1) 011046 027652 125253
(1) 011052 167452 125252
(1) 011056 000300
(1) 011060 015634 000340
(1)
(1) 011064 000240
(1) 011066 075036
(1)
(1) 011070 004767 004156
(1) 011074 022706 000600
(1) 011100 001405
(1) 011102 012706 000600
(1) 011106 104000
(3) 011110 000357
(1) 011112 000422
(1)
(1) 011114 122767 000210 167310
(1) 011122 001402
(1) 011124 104000
(3) 011126 000360
(1)
(1) 011130 022767 177777 167300
(1) 011136 001402
(1) 011140 104002
(3) 011142 000361
(1)
(1) 011144 022767 177777 167266
(1) 011152 001402
(1) 011154 104002
(3) 011156 000362
(1)
(1) 011160 122767 000050 167216
(1) 011166 001402
(1) 011170 104000
(3) 011172 000363
(1)
(1)

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*****
TEST 50:          FDIV (LSI-1) FLOATING DIVIDE INSTRUCTION)
          167452,125252 / 027652,125253 = 177777,177777
          PS = 210,          STACK POINTER = SP
*****

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```

TST50:  SCOPE          RS,          PUSH5          ;PUSH 4 WORDS ONTO STACK, SET PRIORITY
        JSR           027652,125253  ;SECOND OPERAND ON TOP
        .WORD        167452,125252  ;FIRST OPERAND ON BOTTOM
        .WORD        300            ;PROCESSOR PRIORITY LEVEL
        .WORD        TRAPER, 340    ;FIS TRAP VECTOR

        NOP
        FDIV          SP            ;FLOATING DIVIDE ON THE STACK

        JSR           PC,          POPS          ;POP THE ANSWER
        CMP           #BEGIN, SP     ;CHECK THE STACK POINTER
        BEQ          TSA50          ;BRANCH IF OK
        MOV          #BEGIN, SP     ;RESTORE STACK POINTER
        HLT          ;STACK POINTER FOULED UP
        357          ;THE ERROR NUMBER IS 357
        BR           END50          ;SKIP REST OF TEST

        CMPB         #210,          SPSW        ;CHECK PS (EXCEPT T BIT)
        BEQ          .+6            ;BRANCH IF OK
        HLT          ;PS NOT EQUAL TO 210
        360          ;THE ERROR NUMBER IS 360

        CMP           #177777,ANS1   ;CHECK FIRST HALF OF ANSWER
        BEQ          .+6            ;BRANCH IF OK
        HLT+2       ;ANS1 NOT EQUAL TO 177777
        361          ;THE ERROR NUMBER IS 361

        CMP           #177777,ANS2   ;CHECK SECOND HALF OF ANSWER
        BEQ          .+6            ;BRANCH IF OK
        HLT+2       ;ANS2 NOT EQUAL TO 177777
        362          ;THE ERROR NUMBER IS 362

        CMPB         #50,          $TESTN     ;CHECK THE TEST NUMBER
        BEQ          .+6            ;BRANCH IF OK
        HLT          ;WRONG TEST! PC MUST HAVE FOULED UP.
        363          ;THE ERROR NUMBER IS 363

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7217

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(1)
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(1)
(1) 011332 104400
(1) 011334 004567 004024
(1) 011340 140670 123456
(1) 011344 000000 000000
(1) 011350 000105
(1) 011352 015634 000340
(1) 011356 012703 000510
(1)
(1) 011362 000240
(1) 011364 075033
(1)
(1) 011366 004767 004024
(1) 011372 010367 167036
(1) 011376 122767 000004 167026
(1) 011404 001402
(1) 011406 104000
(3) 011410 000371
(1)
(1) 011412 022767 000514 167014
(1) 011420 001402
(1) 011422 104000
(3) 011424 000372
(1)
(1) 011426 005767 167004
(1) 011432 001402
(1) 011434 104002
(3) 011436 000373
(1)
(1) 011440 005767 166774
(1) 011444 001402
(1) 011446 104002
(3) 011450 000374
(1)
(1) 011452 122767 000052 166724 ENDS2:
(1) 011460 001402
(1) 011462 104000
(3) 011464 000375
(1)
(1)

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*****
TEST 52:      FDIV (LSI-11 FLOATING DIVIDE INSTRUCTION)
              000000,000000 / 140670,123456 = 000000,000000
              PS = 004,      STACK POINTER = R3
*****
TST52:  SCOPE
        JSR      RS,      PUSHR ; PUSH 4 WORDS ONTO R3 STACK, SET PRIORITY
        .WORD    140670,123456 ; SECOND OPERAND ON TOP
        .WORD    000000,000000 ; FIRST OPERAND ON BOTTOM
        .WORD    105 ; PROCESSOR PRIORITY LEVEL
        .WORD    TRAPER, 340 ; FIS TRAP VECTOR
        MOV      #STACK0,R3 ; CHECK STACK POINTER

        NOP
        FDIV    R3 ; FLOATING DIVIDE ON THE R3 STACK

        JSR      PC,      POPR ; POP THE ANSWER
        MOV      R3,      SSP ; SAVE "STACK POINTER"
        CMPB    #004,    SPSW ; CHECK PS (EXCEPT T BIT)
        BEQ     .+6 ; BRANCH IF OK
        HLT ; PS NOT EQUAL TO 004
        371 ; THE ERROR NUMBER IS 371

        CMP      #STACK4,SSP ; CHECK THE STACK POINTER (R3)
        BEQ     .+6 ; BRANCH IF OK
        HLT ; STACK POINTER (R3) NOT EQUAL TO #STACK4
        372 ; THE ERROR NUMBER IS 372

        TST     ANS1 ; CHECK FIRST HALF OF ANSWER
        BEQ     .+6 ; BRANCH IF OK
        HLT+2 ; ANS1 NOT EQUAL TO 000000
        373 ; THE ERROR NUMBER IS 373

        TST     ANS2 ; CHECK SECOND HALF OF ANSWER
        BEQ     .+6 ; BRANCH IF OK
        HLT+2 ; ANS2 NOT EQUAL TO 000000
        374 ; THE ERROR NUMBER IS 374

        CMPB    #52,    $TESTN ; CHECK THE TEST NUMBER
        BEQ     .+6 ; BRANCH IF OK
        HLT ; WRONG TEST! PC MUST HAVE FOULED UP.
        375 ; THE ERROR NUMBER IS 375

```



```

(1)          ;TEST 54:          FDIV (LSI-11 FLOATING DIVIDE INSTRUCTION)
(1)          ;                025252,125251 / 065252,125252 ==> UNDERFLOW
(1)          ;                PS(ON STACK) = 012,          STACK POINTER = R1
(1)          ;                *****
(1)          ;
(1) 011642 104400          SCOPE
(1) 011644 004567 003514  TST54: JSR      R5,      PUSHR   ;PUSH 4 WORDS ONTO R1 STACK, SET PRIORITY
(1) 011650 065252 125252      .WORD   065252,125252 ;SECOND OPERAND ON TOP
(1) 011654 025252 125251      .WORD   025252,125251 ;FIRST OPERAND ON BOTTOM
(1) 011660 000015          .WORD   015           ;PROCESSOR PRIORITY LEVEL
(1) 011662 011714 000300      .WORD   ISR54, 300    ;FIS TRAP VECTOR
(1) 011666 012701 000510      MOV     #STACK0,R1   ;SET UP R1 AS STACK POINTER
(1)
(1) 011672 000240          NOP
(1) 011674 075031          FDIV    R1           ;FLOATING DIVIDE ON THE R1 STACK
(1)
(1) 011676 004767 003514  RTA54: JSR      PC,      POPR    ;POP THE "ANSWER"
(1) 011702 010167 166526      MOV     R1,      SSP    ;SAVE STACK POINTER (R1)
(1) 011706 104002          HLT+2  ;FIS TRAP DIDN'T OCCURE!
(3) 011710 000404          404    ;THE ERROR NUMBER IS 404
(1) 011712 000464          BR      END54
(1)
(1) 011714 004767 003526  ISR54: JSR      PC,      POPER   ;POP ALL DATA OFF THE STACKS
(1) 011720 010167 166510      MOV     R1,      SSP    ;SAVE STACK POINTER (R1)
(1) 011724 122767 000300 166500  CMPB   #300,     SPSW    ;CHECK PS AFTER FIS TRAP
(1) 011732 001402          BEQ     .+6           ;BRANCH IF OK
(1) 011734 104000          HLT     ;PS AFTER FIS TRAP NOT EQUAL TO 300
(3) 011736 000405          405    ;THE ERROR NUMBER IS 405
(1)
(1) 011740 022767 000510 166466  CMP     #STACK0,SSP   ;CHECK THE STACK POINTER (R1)
(1) 011746 001402          BEQ     .+6           ;BRANCH IF OK
(1) 011750 104000          HLT     ;STACK POINTER (R1) NOT EQUAL TO #STACK0
(3) 011752 000406          406    ;THE ERROR NUMBER IS 406
(1)
(1) 011754 022767 011676 166454  CMP     #RTA54, ANS1   ;CHECK FIS TRAP RETURN ADDRESS
(1) 011762 001402          BEQ     .+6           ;BRANCH IF OK
(1) 011764 104001          HLT+1  ;FIS TRAP AT WRONG ADDRESS
(3) 011766 000407          407    ;THE ERROR NUMBER IS 407
(1)
(1) 011770 022767 000012 166442  CMP     #012,     ANS2   ;CHECK PS BEFORE FIS TRAP
(1) 011776 001402          BEQ     .+6           ;BRANCH IF OK
(1) 012000 104002          HLT+2  ;PS AT FIS TRAP TIME NOT 012
(3) 012002 000410          410    ;THE ERROR NUMBER IS 410
(1)
(1) 012004 022767 065252 166430  CMP     #065252,ANS3   ;CHECK DATA FROM THE STACK
(1) 012012 001402          BEQ     .+6           ;BRANCH IF OK
(1) 012014 104004          HLT+4  ;DATA ON STACK (065252) CHANGED
(3) 012016 000411          411    ;THE ERROR NUMBER IS 411
(1)
(1) 012020 022767 125252 166416  CMP     #125252,ANS4   ;CHECK DATA FROM STACK
(1) 012026 001402          BEQ     .+6           ;BRANCH IF OK
(1) 012030 104004          HLT+4  ;DATA ON STACK (125252) CHANGED
(3) 012032 000412          412    ;THE ERROR NUMBER IS 412
(1)
(1) 012034 022767 025252 166404  CMP     #025252,ANS5   ;CHECK DATA FROM STACK
(1) 012042 001402          BEQ     .+6           ;BRANCH IF OK
    
```

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DVKACA.SRC TEST FLOATING DIV. INSTRUCTION WITH UNDERFLOW

SEQ 0060

```
(1) 012044 104006          HLT+6          ;DATA ON STACK (025252) CHANGED
(3) 012046 000413          413           ;THE ERROR NUMBER IS 413
(1)
(1) 012050 022767 125251 166372  CMP      #125251,ANS6 ;CHECK DATA FROM STACK
(1) 012056 001402          BEQ      .+6      ;BRANCH IF OK
(1) 012060 104006          HLT+6          ;DATA ON STACK (125251) CHANGED
(3) 012062 000414          414           ;THE ERROR NUMBER IS 414
(1)
(1) 012064 122767 000054 166312  ENDS4:  CMPB     #54,      $TESTN ;CHECK THE TEST NUMBER
(1) 012072 001402          BEQ      .+6      ;BRANCH IF OK
(1) 012074 104000          HLT           ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 012076 000415          415           ;THE ERROR NUMBER IS 415
(1)
(1)
```



```

(1) 012762 022767 100052 165456      CMP      #100052,ANS5      ;CHECK DATA FROMONG TK
(1) 012770 001402                      BEQ      .+6              ;BRANCH IF OK
(1) 012772 104006                      HLT+6    ;DATA ON STACK (100052) CHANGED
(3) 012774 000451                      451                    ;THE ERROR NUMBER IS 451
(1)
(1) 012776 022767 052525 165444      CMP      #052525,ANS6      ;CHECK DATA FROM STACK
(1) 013004 001402                      BEQ      .+6              ;BRANCH IF OK
(1) 013006 104006                      HLT+6    ;DATA ON STACK (052525) CHANGED
(3) 013010 000452                      452                    ;THE ERROR NUMBER IS 452
(1)
(1) 013012 122767 000057 165364  END57:  CMPB     #57,    $TESTN      ;CHECK THE TEST NUMBER
(1) 013020 001402                      BEQ      .+6              ;BRANCH IF OK
(1) 013022 104000                      HLT      ;WREST! PC MUST HAVE FOULED UP.
(3) 013024 000453                      453                    ;THE ERROR NUMBER IS 453
(1)
(1)

```

7237

(1)
(1)
(1)
(1)
(1)
(1)
(1)
(1)
(1)
(1)

```
*****
TEST 60:      TEST ALL INSTRUCTION TOGETHER
              032107,065432 * 045670,123456
              134343,107070 + ----- = 137201,115230
              (135252,125252 - 040616,016161)
              PS=010, STACK POINTER=R4
*****
```

(1) 013026 104400
 (1) 013030 012704 000532
 (1) 013034 012744 107070
 (1) 013040 012744 134343
 (1) 013044 012744 065432
 (1) 013050 012744 032107
 (1) 013054 012744 123456
 (1) 013060 012744 045670
 (1) 013064 012744 125252
 (1) 013070 012744 135252
 (1) 013074 012744 016161
 (1) 013100 012744 040616
 (2) 013104
 (2) 013104 106427

```
TST60:  SCOPE
        MOV      #STAK10,R4      ;SET STACK POINTER
        MOV      #107070,-(R4)   ;LOAD DATA ONTO STACK
        MOV      #134343,-(R4)
        MOV      #065432,-(R4)
        MOV      #032107,-(R4)
        MOV      #123456,-(R4)
        MOV      #045670,-(R4)
        MOV      #125252,-(R4)
        MOV      #135252,-(R4)
        MOV      #016161,-(R4)
        MOV      #040616,-(R4)
        MTPS     #144             ;SET PROCESSOR STATUS
        .WORD    106400!..C
```

(1) 013110 000240
 (1) 013112 075014
 7238 013114 075034
 (1) 013116 075024
 (1) 013120 075004

```
NOP
FSUB    R4      :135252,125252-040616,016161=140616,017434
FDIV    R4      :045670,123456/140616,017434=145246,047065
FMUL    R4      :032107,065432*145246,047065=137201,106137
FADD    R4      :134343,107070+137201,106137=137201,115230
```

(1) 013122
 (2) 013122 106767
 (1) 013126 042767 000020 165276
 (1) 013134 012467 165276
 (1) 013140 012467 165274
 (1) 013144 010467 165264
 (1) 013150 122767 000010 165254
 (1) 013156 001402
 (1) 013160 104000
 (3) 013162 000454

```
MFPS    SPSW           ;SAVE FINAL PS
        .WORD    106700!..C
        BIC     #20,SPSW      ;CLR T-BIT
        MOV     (R4)+,ANS1    ;SAVE FIRST HALF OF ANSWER
        MOV     (R4)+,ANS2    ;SAVE SECOND HALF OF ANSWER
        MOV     R4,SSP        ;SAVE STACK POINTER
        CMPB   #010,SPSW     ;CHECK PS (EXCEPT T BIT)
        BEQ    .+6           ;BRANCH IF OK
        HLT    .+6           ;PS NOT EQUAL TO 010
        454                ;THE ERROR NUMBER IS 454
```

(1) 013164 022767 000532 165242
 (1) 013172 001402
 (1) 013174 104000
 (3) 013176 000455

```
CMP     #STAK10,SSP       ;CHECK THE STACK POINTER (R4)
        BEQ    .+6           ;BRANCH IF OK
        HLT    .+6           ;STACK POINTER (R4) NOT EQUAL TO THE
        455                ;THE ERROR NUMBER IS 455
        455                ;ADDRESS OF STAK10
```

(1) 013200 022767 137201 165230
 (1) 013206 001402
 (1) 013210 104002
 (3) 013212 000456

```
CMP     #137201,ANS1      ;CHECK FIRST HALF OF ANSWER
        BEQ    .+6           ;BRANCH IF OK
        HLT+2 .+6           ;ANS1 NOT EQUAL TO 137201
        456                ;THE ERROR NUMBER IS 456
```

(1) 013214 022767 115230 165216
 (1) 013222 001402
 (1) 013224 104002
 (3) 013226 000457

```
CMP     #115230,ANS2      ;CHECK SECOND HALF OF ANSWER
        BEQ    .+6           ;BRANCH IF OK
        HLT+2 .+6           ;ANS2 NOT EQUAL TO 115230
        457                ;THE ERROR NUMBER IS 457
```



```

7244
(1)
(1)
(1)
(1)
(1)
(1) 013522 104400
(1) 013524 012737 013614 000004 TST62: MOV #ISR62, #4 ;SET UP ADDRESS TRAP VECTOR
(1) 013532 012737 000340 000006 MOV #340, #6 ;
(1) 013540 004567 001620 JSR R5, PUSHR ;PUSH 4 WORDS ONTO R5 STACK, SET PRIORITY
(1) 013544 065432 123456 .WORD 065432, 123456 ;SECOND OPERAND ON TOP
(1) 013550 037654 032107 .WORD 037654, 032107 ;FIRST OPERAND ON BOTTOM
(1) 013554 000202 .WORD 202 ;PROCESSOR PRIORITY LEVEL
(1) 013556 015634 000340 .WORD TRAPER, 340 ;FIS TRAP VECTOR
(2) 013562 MTPS #202 ;SET PROCESSOR STATUS
(2) 013562 106427 .WORD 106400!..C
(1) 013566 012705 160000 MOV #160000, R5 ;SET UP R5 AS STACK POINTER
(1)
(1) 013572 000240 NOP
(1) 013574 075025 FMUL R5 ;FLOATING MULTIPLY ON THE R5 STACK
(1)
(2) 013576 RTA62: MFPS $PSW ;SAVE THE PSW
(2) 013576 106767 .WORD 106700!..C
(1) 013602 010567 164626 MOV R5, $SP ;SAVE STACK POINTER (R5)
(1) 013606 104000 HLT ;FIS TRAP DIDN'T OCCURE!
(3) 013610 000473 473 ;THE ERROR NUMBER IS 473
(1) 013612 000434 BR END62
(1)
(1) 013614 004767 001626 ISR62: JSR PC, POPER ;POP ALL DATA OFF THE STACKS
(1) 013620 010567 164610 MOV R5, $SP ;SAVE STACK POINTER (R5)
(1) 013624 122767 000340 164600 CMPB #340, $PSW ;CHECK PS AFTER ADR. ERR. TRAP
(1) 013632 001402 BEQ .+6 ;BRANCH IF OK
(1) 013634 104000 HLT ;PS AFTER TRAP NOT EQUAL TO 340
(3) 013636 000474 474 ;THE ERROR NUMBER IS 474
(1)
(1) 013640 022767 160000 164566 CMP #160000, $SP ;CHECK THE STACK POINTER (R5)
(1) 013646 001402 BEQ .+6 ;BRANCH IF OK
(1) 013650 104000 HLT ;STACK POINTER (R5) NOT EQUAL TO #160000
(3) 013652 000475 475 ;THE ERROR NUMBER IS 475
(1)
(1) 013654 022767 013576 164554 CMP #RTA62, ANS1 ;CHECK FIS TRAP RETURN ADDRESS
(1) 013662 001402 BEQ .+6 ;BRANCH IF OK
(1) 013664 104001 HLT+1 ;FIS TRAP AT WRONG ADDRESS
(3) 013666 000476 476 ;THE ERROR NUMBER IS 476
(1)
(1) 013670 022767 000210 164542 CMP #210, ANS2 ;CHECK PS BEFORE FIS TRAP
(1) 013676 001402 BEQ .+6 ;BRANCH IF OK
(1) 013700 104002 HLT+2 ;PS AT FIS TRAP TIME NOT 210
(3) 013702 000477 477 ;THE ERROR NUMBER IS 477
(1)
(1) 013704 122767 000062 164472 END62: CMPB #62, $TESTN ;CHECK THE TEST NUMBER
(1) 013712 001402 BEQ .+6 ;BRANCH IF OK
(1) 013714 104000 HLT ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 013716 000500 500 ;THE ERROR NUMBER IS 500

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7245 013720 012737 000006 000004      MOV    #6,    2#4      ;RESTORE TIME-OUT VECTOR
7246 013726 005037 000006              CLR    2#6
7250 013732 012767 000003 164576      MOV    #3,    TIMES   ;REDUCE NUMBER OF ITERATIONS
7251
(1)                                     ;*****
(1)                                     ;TEST 63:      TEST THAT FIS ABORTS PROPERLY WHEN INTERRUPTED
(1)                                     ;035700,143235 + 000177,134543 = 035700,143235
(1)                                     ;PS = .PS,    STACK POINTER = R1
(1)                                     ;*****
(1)
(1) 013740 104400              SCOPE
(1) 013742 132737 000040 000421 TST63: BITB    #40, 2#SENVM
(1) 013750 001170              BNE    END63+2        ;EXIT THIS TEST IF BIT 5 OF SENVM IS HIGH
(1) 013752 013704 000546      MOV    2#TTYOUT, R4
(1) 013756 012724 014050      MOV    #ISR63, (R4)+ ;SET UP TELEPRINTER INTERUPT VECTOR
(1) 013762 012714 000340      MOV    #340, (R4)
(1) 013766 000004 000473      TYPE,  RETURN+1      ;RETURN+1 CAN BE REPLACED WITH THE ADDRESS OF RETURN
(1)                                     ;TO TYPE CARRIAGE RETURN, LINE FEED
(1) 013772 012767 014000 164470      MOV    #.+6,  LADS    ;RESET LOOP ADDRESS
(1) 014000 004567 001360      JSR    R5,  PUSHR    ;PUSH 4 WORDS ONTO R1 STACK, SET PRIORITY
(1) 014004 000177 134543      .WORD 000177,134543 ;SECOND OPERAND ON TOP
(1) 014010 035700 143235      .WORD 035700,143235 ;FIRST OPERAND ON BOTTOM
(1) 014014 000143              .WORD 143            ;PROCESSOR PRIORITY LEVEL
(1) 014016 015634 000340      .WORD TRAPER, 340    ;FIS TRAP VECTOR
(1) 014022 012701 000510      MOV    #STACK0, R1   ;SET UP STACK POINTER
(1) 014026 012767 000030 164500      MOV    #30, TEMP
(1) 014034 112777 000100 164506      MOVB   #100, 2#STPS ;SET TTY INTERRUPT ENABLE
(1)
(1) 014042 075001              RTA63: FADD   R1      ;FLOATING ADD ON THE STACK
(1) 014044 024141              CMP    -(R1), -(R1) ;RESET THE STACK POINTER FOR NEXT PASS
(1) 014046 000775              BR     RTA63        ;REPEAT UNTIL INTERRUPTED
(1)
(1) 014050 105077 164474              ISR63: CLRB   2#STPS ;CLEAR THE INTERRUPT ENABLE
(1) 014054 022716 014042              CMP    #RTA63, (SP) ;CHECK IF INTERRUPT AT FIS INSTR.
(1) 014060 001421              BEQ   3$           ;BRANCH IF IT DID
(1) 014062 022766 014042 000004      CMP    #RTA63, 4(SP) ;CHECK FOR INTERRUPT WITH T-BIT SET
(1) 014070 001420              BEQ   4$           ;BRANCH IF IT DID
(1) 014072 112777 000015 164452 1$:  MOVB   #15, 2#STPB ;CONTINUE TO TYPE "CR"
(1) 014100 105777 164444      2$:  TSTB   2#STPS   ;LOOP HERE UNTILL DONE BIT COMES ON
(1) 014104 100375              BPL   2$
(1) 014106 112777 000015 164436      MOVB   #15, 2#STPB ;TYPE ANOTHER "CR"
(1) 014114 012777 000100 164426      MOV    #100, 2#STPS ;SET TTY INTERRUPT ENABLE
(1) 014122 000002              RTI
(1)
(1) 014124 004767 001316      3$:  JSR    PC,  POPER  ;SAVE ALL THE STUFF ON THE STACK
(1) 014130 000403              BR
(1)
(1) 014132 022626              4$:  CMP    (SP)+, (SP)+ ;RESET THE STACK TO IGNORE THE TRACE TRAP
(1) 014134 004767 001312      JSR    PC,  POPER1 ;POP ALL THE STUFF OFF THE STACK
(1) 014140 005746              5$:  TST    -(SP)      ;SAVE PSW FOR FUTURE RTI
(1) 014142 012746 014042      MOV    #RTA63, -(SP) ;PLACE THE RTI ADDRESS BACK IN SP
(1) 014146 022706 000574      CMP    #BEGIN-4, SP ;CHECK THE STACK POINTER
(1) 014152 001407              BEQ   6$           ;BRANCH IF OK
(1) 014154 010667 164254      MOV    SP,  $SP    ;SAVE FOR TYPING
(1) 014160 012706 000574      MOV    #BEGIN-4, SP ;RESTORE THE STACK POINTER
(1) 014164 104000              HLT              ;STACK POINTER FOULED UP
    
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(3) 014166 000501          501          ;THE ERROR NUMBER IS 501
(1) 014170 000457          BR          END63      ;SKIP REST OF TEST
(1)
(1) 014172 010167 164236 6S:  MOV      R1,      SSP      ;SAVE STACK POINTER
(1) 014176 122767 000344 164226  CMPB    #344,    SPSW     ;CHECK PS AFTER INTERUPT
(1) 014204 001402          BEQ     .+6          ;BRANCH IF OK
(1) 014206 104000          HLT     ;PS AFTER INTERUPT NOT EQUAL TO LVLA
(3) 014210 000502          502          ;THE ERROR NUMBER IS 502
(1)
(1) 014212 022767 000510 164214  CMP     #STACK0, SSP    ;CHECK THE STACK POINTER (R1)
(1) 014220 001402          BEQ     .+6          ;BRANCH IF OK
(1) 014222 104000          HLT     ;STACK POINTER (R1) NOT EQUAL TO #STACK0
(3) 014224 000503          503          ;THE ERROR NUMBER IS 503
(1)
(1) 014226 022767 014042 164202  CMP     #RTA63, ANS1    ;CHECK FIS TRAP RETURN ADDRESS
(1) 014234 001402          BEQ     .+6          ;BRANCH IF OK
(1) 014236 104001          HLT+1  ;FIS TRAP AT WRONG ADDRESS
(3) 014240 000504          504          ;THE ERROR NUMBER IS 504
(1)
(1)
(1) 014242 022767 000177 164172  CMP     #000177, ANS3   ;CHECK DATA FROM THE STACK
(1) 014250 001402          BEQ     .+6          ;BRANCH IF OK
(1) 014252 104004          HLT+4  ;DATA ON STACK (000177) CHANGED
(3) 014254 000505          505          ;THE ERROR NUMBER IS 505
(1)
(1) 014256 022767 134543 164160  CMP     #134543, ANS4   ;CHECK DATA FROM STACK
(1) 014264 001402          BEQ     .+6          ;BRANCH IF OK
(1) 014266 104004          HLT+4  ;DATA ON STACK (134543) CHANGED
(3) 014270 000506          506          ;THE ERROR NUMBER IS 506
(1)
(1) 014272 022767 035700 164146  CMP     #035700, ANS5   ;CHECK DATA FROM STACK
(1) 014300 001402          BEQ     .+6          ;BRANCH IF OK
(1) 014302 104006          HLT+6  ;DATA ON STACK (035700) CHANGED
(3) 014304 000507          507          ;THE ERROR NUMBER IS 507
(1)
(1) 014306 022767 143235 164134  CMP     #143235, ANS6   ;CHECK DATA FROM STACK
(1) 014314 001402          BEQ     .+6          ;BRANCH IF OK
(1) 014316 104006          HLT+6  ;DATA ON STACK (143235) CHANGED
(3) 014320 000510          510          ;THE ERROR NUMBER IS 510
(1)
(1) 014322 005367 164206          DEC     TEMP          ;STAY IN THE LOOP FOR 30 TIMES
(1) 014326 001261          BNE    IS
(1)
(1) 014330 022626          END63:  CMP     (SP)+, (SP)+  ;RESTORE STACK POINTER TO 500
(1) 014332 122767 000063 164044  CMPB    #63,      STESTN ;CHECK THE TEST NUMBER
(1) 014340 001402          BEQ     .+6          ;BRANCH IF OK
(1) 014342 104000          HLT     ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 014344 000511          511          ;THE ERROR NUMBER IS 511
(2) 014346          MTPS   #340
(2) 014346 106427          .WORD  106400!..C
(1)

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(1) 014604 010067 163624      6$:  MOV      RO,      SSP      ;SAVE STACK POINTER
(1) 014610 122767 000344 163614  CMPB     #344,    SPSW     ;CHECK PS AFTER INTERUPT
(1) 014616 001402                BEQ      .+6                ;BRANCH IF OK
(1) 014620 104000                HLT                        ;PS AFTER INTERUPT NOT EQUAL TO LVLA
(3) 014622 000513                513                        ;THE ERROR NUMBER IS 513
(1)
(1) 014624 022767 000510 163602  CMP      #STACK0, SSP    ;CHECK THE STACK POINTER (RO)
(1) 014632 001402                BEQ      .+6                ;BRANCH IF OK
(1) 014634 104000                HLT                        ;STACK POINTER (RO) NOT EQUAL TO #STACK0
(3) 014636 000514                514                        ;THE ERROR NUMBER IS 514
(1)
(1) 014640 022767 014454 163570  CMP      #RTA64, ANS1    ;CHECK FIS TRAP RETURN ADDRESS
(1) 014646 001402                BEQ      .+6                ;BRANCH IF OK
(1) 014650 104001                HLT+1                      ;FIS TRAP AT WRONG ADDRESS
(3) 014652 000515                515                        ;THE ERROR NUMBER IS 515
(1)
(1)
(1) 014654 022767 040200 163560  CMP      #040200, ANS3   ;CHECK DATA FROM THE STACK
(1) 014662 001402                BEQ      .+6                ;BRANCH IF OK
(1) 014664 104004                HLT+4                      ;DATA ON STACK (040200) CHANGED
(3) 014666 000516                516                        ;THE ERROR NUMBER IS 516
(1)
(1) 014670 005767 163550      TST      ANS4            ;CHECK DATA FROM STACK
(1) 014674 001402                BEQ      .+6                ;BRANCH IF OK
(1) 014676 104004                HLT+4                      ;DATA ON STACK (000000) CHANGED
(3) 014700 000517                517                        ;THE ERROR NUMBER IS 517
(1)
(1) 014702 022767 107070 163536  CMP      #107070, ANS5   ;CHECK DATA FROM STACK
(1) 014710 001402                BEQ      .+6                ;BRANCH IF OK
(1) 014712 104006                HLT+6                      ;DATA ON STACK (107070) CHANGED
(3) 014714 000520                520                        ;THE ERROR NUMBER IS 520
(1)
(1) 014716 022767 070707 163524  CMP      #070707, ANS6   ;CHECK DATA FROM STACK
(1) 014724 001402                BEQ      .+6                ;BRANCH IF OK
(1) 014726 104006                HLT+6                      ;DATA ON STACK (070707) CHANGED
(3) 014730 000521                521                        ;THE ERROR NUMBER IS 521
(1)
(1) 014732 005367 163576      DEC      TEMP            ;STAY IN THE LOOP FOR 30 TIMES
(1) 014736 001262                BNE     IS
(1)
(1) 014740 022626                END64: CMP      (SP)+, (SP)+    ;RESTORE STACK POINTER TO 500
(1) 014742 122767 000064 163434  CMPB     #64,    $TESTN   ;CHECK THE TEST NUMBER
(1) 014750 001402                BEQ      .+6                ;BRANCH IF OK
(1) 014752 104000                HLT                        ;WRONG TEST! PC MUST HAVE FOULED UP.
(3) 014754 000522                522                        ;THE ERROR NUMBER IS 522
(2) 014756                MTPS   #340
(2) 014756 106427                .WORD  106400!..C
(1)

```

L06

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 52-74
DVKACA.SRC INTERUPT ABORT TEST SECTION

SEQ 0076

7253	014762	012767	000377	163546	MOV	#377,	TIMES ;SET NUMBER OF ITERATIONS TO 377
7254	014770	010477	163552		MOV	R4,	BTTYOUT ;RESTORE TTY INTERUPT VECTOR
7255	014774	005014			CLR	(R4)	
7256							

```

7258 ;*****
(1) ;
(1) .SBTTL END OF PASS ROUTINE
(1) ;*INCREMENT THE PASS NUMBER ($PASS)
(1) ;*TYPE "END PASS"
(1) ;*IF THERES A MONITOR GO TO IT
(1) ;*IF THERE ISN'T JUMP TO BEGIN
(1) ;*IF IT IS DESIRED TO HAVE A BELL INDICATE THE "END OF PASS" LOCATION
(1) ;*SENDMG CAN BE CHANGED TO 7.
(1)
(1) 014776 SEOP:
(1) 014776 104400 SCOPE
(1) 015000 005267 163402 INC $PASS ;; INCREMENT THE PASS NUMBER
(1) 015004 042767 100000 163374 BIC #100000,$PASS ;; DON'T ALLOW A NEG. NUMBER
(1) 015012 005327 DEC (PC)+ ;; LOOP?
(1) 015014 000001 SEOPCT: .WORD 1
(1) 015016 003015 BGT $DOAGN ;; YES
(1) 015020 012737 MOV (PC)+,2(PC)+ ;; RESTORE COUNTER
(1) 015022 000001 SENDCT: .WORD 1
(1) 015024 015014 SEOPCT
(1) 015026 000004 015056 TYPE ,SENDMG ;; TYPE "END PASS"
(1) 015032 SGET42:
(1) 015032 013700 000042 MOV 2#42,R0 ;; GET MONITOR ADDRESS
(1) 015036 001405 BEQ $DOAGN ;; BRANCH IF NO MONITOR
(1) 015040 000005 RESET ;; CLEAR THE WORLD
(1) 015042 004710 SENDAD: JSR PC,(R0) ;; GO TO MONITOR
(1) 015044 000240 NOP ;; SAVE ROOM
(1) 015046 000240 NOP ;; FOR
(1) 015050 000240 NOP ;; ACT11
(1) 015052 $DOAGN:
(1) 015052 000137 000600 JMP 2#BEGIN ;; RETURN
(1) 015056 005015 047105 020104 SENDMG: .ASCII <15><12>/END PASS/
(1) 015064 040520 051523 SENULL: .BYTE -1,-1,0 ;; NULL CHARACTER STRING
(1) 015070 377 377 000 .EVEN
(1) 015074
7259
7264 015022 000001 ENDCT: 1
    
```

```

7268 ;*****
(1) .SBTTL SCOPE ROUTINE
(1) (1) 015074 032737 000400 000422 SCOPES: BIT #SW08,2#SSWREG ;KILL LDUB OR LOOP ON SPEC. TEST
(1) (1) 015102 001404 BEQ 1$
(1) (1) 015104 123767 000422 163272 CMPB 2#SSWREG,$TESTN ;ON RIGHT TEST? *SW7-0*
(1) (1) 015112 001431 BEQ OVERS
(1) (1) 015114 032737 040000 000422 1$: BIT #SW14,2#SSWREG ;LOOP ON TEST
(1) (1) 015122 001023 BNE KITS
(1) (1) 015124 032737 004000 000422 BIT #SW11,2#SSWREG ;KILL ITERATIONS
(1) (1) 015132 001412 BEQ SVLADS$
(1) (1) 015134 105767 163343 TSTB $ICNT
(1) (1) 015140 001404 BEQ 2$ ;BRANCH IF FIRST
(1) (1) 015142 126767 163370 163333 CMPB TIMES,$ICNT ;DONE?
(1) (1) 015150 001010 BNE KITS ;BRANCH IF NOT
(1) (1) 015152 112767 000001 163323 2$: MOVB #1,$ICNT ;FIRST ITERATION
(1) (1) 015160 105267 163220 SVLADS: INCB $TESTN ;COUNT TEST NUMBERS
(1) (1) 015164 011667 163300 MOV (6),LADS ;SAVE LOOP ADDRESS
(1) (1) 015170 000002 RTI ;RETURN
(1) (1) 015172 105267 163305 KITS: INCB $ICNT
(1) (1) 015176 005767 163266 OVERS: TST LADS ;FIRST ONE?
(1) (1) 015202 001766 BEQ SVLADS$
(1) (1) 015204 016716 163260 MOV LADS,(6) ;FUDGE RETURN ADDRESS
(1) (1) 015210 000002 RTI ;FIXES PS

```

```

7273
7274 ;SUBROUTINE TO PUSH 4 WORDS ONTO THE STACK
7275
7276 015212 005726
7277 015214 062705 000010
7278 015220 014546
7279 015222 014546
7280 015224 014546
7281 015226 014546
7282 015230 062705 000010
7286 015234
(1) 015234 106425
7290 015236 005205
7291 015240 012577 163220
7292 015244 012577 163216
7293 015250 000115
7294
7295
7296 ;SUBROUTINE TO POP 2 WORDS OFF THE STACK
7297 ;ALSO SAVES THE PROCESSOR STATUS WORD (EXCEPT T BIT)
7298
7302 015252
(1) 015252 106767
7306 015256 042767 000020 163146
7307 015264 012604
7308 015266 012667 163144
7309 015272 012667 163142
7310 015276 010667 163132
7311 015302 000114
7312
7313
7314 ;SUBROUTINE TO POP 6 WORDS OFF THE STACK.
7315 ;THE FIRST TWO WERE PUT ON BY THE ERROR TRAP,
7316 ;THE LAST FOUR WERE THE ORIGINAL INPUT DATA.
7317 ;ALSO SAVES THE PS AND STACK POINTER.
7318
7322 015304
(1) 015304 106767
7326 015310 012604
7327 015312 012667 163120
7328 015316 011667 163116
7329 015322 042767 000020 163110
7330 015330 012746 015336
7331 015334 000002
7332 015336 012667 163100
7333 015342 012667 163076
7334 015346 012667 163074
7335 015352 012667 163072
7336 015356 010667 163052
7337 015362 000114
7338
7339 ;SUBROUTINE TO PUSH 4 WORDS ONTO THE STACK
7340
7341 015364 012704 000510
7342 015370 012524
7343 015372 012524

```

```

;SUBROUTINE TO PUSH 4 WORDS ONTO THE STACK

```

```

PUSHS: TST (SP)+ ;POP STACK BY 1
        ADD #10, R5 ;POINT TO END OF DATA
        MOV -(R5), -(SP) ;PUSH DATA ONTO THE STACK
        MOV -(R5), -(SP) ;PUSH DATA ONTO THE STACK
        MOV -(R5), -(SP) ;PUSH DATA ONTO THE STACK
        MOV -(R5), -(SP) ;PUSH DATA ONTO THE STACK
        ADD #10, R5 ;POINT TO END OF DATA
        MTPS (R5)+ ;SET THE PROCESSOR STATUS
        .WORD 106400!..C
        INC R5
        MOV (R5)+, @FISVEC ;SET UP FIS ERROR TRAP VECTOR
        MOV (R5)+, @FISLVL ;TRAP STATUS
        JMP (R5) ;RETURN

```

```

;SUBROUTINE TO POP 2 WORDS OFF THE STACK
;ALSO SAVES THE PROCESSOR STATUS WORD (EXCEPT T BIT)

```

```

POPS: MFPS $PSW ;SAVE PROCESSOR STATUS WORD
        .WORD 106700!..C
        BIC #20, $PSW ;CLEAR T-BIT
        MOV (SP)+, R4 ;SAVE RTS ADDRESS
        MOV (SP)+, ANS1 ;SAVE THE ANSWER
        MOV (SP)+, ANS2
        MOV SP, $SP ;SAVE THE STACK POINTER
        JMP (R4) ;RETURN

```

```

;SUBROUTINE TO POP 6 WORDS OFF THE STACK.
;THE FIRST TWO WERE PUT ON BY THE ERROR TRAP,
;THE LAST FOUR WERE THE ORIGINAL INPUT DATA.
;ALSO SAVES THE PS AND STACK POINTER.

```

```

POPES: MFPS $PSW ;SAVE PROCESSOR STATUS WORD
        .WORD 106700!..C
        MOV (SP)+, R4 ;SAVE RTS ADDRESS
        MOV (SP)+, ANS1 ;SAVE RTI ADDRESS
        MOV (SP), ANS2 ;SAVE RTI STATUS
        BIC #20, ANS2 ;CLEAR THE T-BIT
        MOV #18, -(SP)
        RTI ;RESTORE THE PROCESSOR STATUS
18: MOV (SP)+, ANS3 ;SAVE DATA
        MOV (SP)+, ANS4
        MOV (SP)+, ANS5
        MOV (SP)+, ANS6
        MOV SP, $SP ;SAVE SP
        JMP (R4) ;RTS

```

```

;SUBROUTINE TO PUSH 4 WORDS ONTO THE STACK

```

```

PUSHR: MOV #STACK0, R4 ;SET R4 TO STACK
        MOV (R5)+, (R4)+ ;PUT DATA ON STACK
        MOV (R5)+, (R4)+

```



```

7344 015374 012524      MOV      (R5)+, (R4)+ ;
7345 015376 012524      MOV      (R5)+, (R4)+ ;
7349 015400              MTPS     (R5)+      ;SET THE PROCESSOR STATUS
(1) 015400 106425      .WORD   106400!..C
7353 015402 005205      INC      RS
7354 015404 012577 163054      MOV      (R5)+, @FISVEC ;SET UP FIS ERROR TRAP VECTOR
7355 015410 012577 163052      MOV      (R5)+, @FISLVL ;TRAP STATUS
7356 015414 000205      RTS      RS        ;RETURN
7357
7358
7359 ;SUBROUTINE TO POP 2 WORDS OFF THE STACK
7360 ;ALSO SAVES THE PROCESSOR STATUS WORD (EXCEPT T BIT)
7361
7365 015416              POPR:  MFPS     SPSW              ;SAVE PROCESSOR STATUS WORD
(1) 015416 106767      .WORD   106700!..C
7369 015422 042767 000020 163002      BIC      #20, SPSW      ;CLEAR T-BIT
7370 015430 016767 163060 163000      MOV      STACK4, ANS1   ;SAVE THE ANSWER
7371 015436 016767 163054 162774      MOV      STACK6, ANS2   ;
7372 015444 000207      RTS      PC
7373
7374
7375 ;SUBROUTINE TO POP 6 WORDS OFF THE STACKS.
7376 ;THE TWO OFF THE R6 STACK WERE PUT ON BY THE ERROR TRAP.
7377 ;THE FOUR OFF THE SOFTWARE STACK WERE THE ORIGINAL INPUT DATA.
7378 ;ALSO SAVES THE PS AND STACK POINTER AFTER THE FIS TRAP.
7379
7383 015446              POPER:  MFPS     SPSW              ;SAVE PROCESSOR STATUS WORD
(1) 015446 106767      .WORD   106700!..C
7387 015452 012667 000056      POPER1: MOV      (SP)+, SAVRTS ;SAVE RTS ADDRESS
7388 015456 012667 162754      MOV      (SP)+, ANS1    ;SAVE RTI ADDRESS
7389 015462 011667 162752      MOV      (SP), ANS2     ;SAVE RTI STATUS
7390 015466 042767 000020 162744      BIC      #20, ANS2     ;CLEAR THE T-BIT
7391 015474 012746 015502      MOV      #15, -(SP)
7392 015500 000002      RTI
7393 015502 016767 163002 162732 1S:  MOV      STACK0, ANS3   ;RESTORE PROCESSOR STATUS
7394 015510 016767 162776 162726      MOV      STACK2, ANS4   ;SAVE DATA
7395 015516 016767 162772 162722      MOV      STACK4, ANS5
7396 015524 016767 162766 162716      MOV      STACK6, ANS6
7397 015532 000137      JMP      @PC)+        ;SIMULATED RTS
7398 015534 000000      SAVRTS: 0
7399
7400 ;SUBROUTINE TO PUSH 4 WORDS ONTO THE PC STACK
7401
7402 015536 012504      PUSH7: MOV      (R5)+, R4    ;SET R4 TO STACK
7403 015540 012524      MOV      (R5)+, (R4)+  ;PUT DATA ON STACK
7404 015542 012524      MOV      (R5)+, (R4)+
7405 015544 012524      MOV      (R5)+, (R4)+
7406 015546 012524      MOV      (R5)+, (R4)+
7410 015550              MTPS     (R5)+      ;SET THE PROCESSOR STATUS
(1) 015550 106425      .WORD   106400!..C
7414 015552 005205      INC      RS
7415 015554 012577 162704      MOV      (R5)+, @FISVEC ;SET UP FIS ERROR TRAP VECTOR
7416 015560 012577 162702      MOV      (R5)+, @FISLVL ;TRAP STATUS
7417 015564 000205      RTS      RS        ;RETURN
7418
7419 ;SUBROUTINE TO POP 4 WORDS OFF THE PC "STACK"

```

```

7420                                     ;ALSO SAVES THE PROCESSOR STATUS WORD (EXCEPT T BIT)
7421
7425 015566                               POP7: MFPS   SPSW   ;SAVE PROCESSOR STATUS WORD
      (1) 015566 106767                     WORD 106700!..C
7429 015572 042767 000020 162632          BIC     820   SPSW   ;CLEAR T-BIT
7430 015600 011600                               MOV     (SP), RO   ;GET RETURN ADDRESS
7431 015602 162700 000014                     SUB     814   RO   ;POINT TO TOP OF "PC STACK"
7432 015606 012067 162624                     MOV     (RO)+, ANS1 ;SAVE 1ST HALF INPUT DATA
7433 015612 012067 162622                     MOV     (RO)+, ANS2 ;SAVE 2ND HALF INPUT DATA
7434 015616 010067 162612                     MOV     RO,   SSP  ;SAVE ASSUMED END PC "STACK POINTER"
7435 015622 012067 162614                     MOV     (RO)+, ANS3 ;SAVE 1ST HALF OF ANSWER
7436 015626 012067 162612                     MOV     (RO)+, ANS4 ;SAVE 2ND HALF OF ANSWER
7437 015632 000207                               RTS     PC
7438
7439                                     ;ERRONIOUS TRAP SERVICE ROUTINE
7440
7441 015634 104000          TRAPER: HLT           ;FIS SHOULDN'T HAVE TRAPED
7442 015636 000523          523                 ;THE ERROR NUMBER IS 523
7443 015640 000002          RTI
7444

```

```

7445 ;*****
(1) .SBTTL HLT ROUTINE (ERROR TIMEOUT)
(1) 015642 032737 002000 000422 HLTS: BIT #SW10,2#SSWREG ;SHOULD IT RING THE BELL ON ERROR?
(1) 015650 001402 BEQ 1$ ;NO - SKIP
(1) 015652 000004 000504 TYPE $BELL ;RING BELL
(1) 015656 005267 162600 1S: INC ERRORS ;COUNT THE NUMBER OF ERRORS
(1) 015662 032737 020000 000422 BIT #SW13,2#SSWREG ;SKIP TYPEOUT IF SET
(1) 015670 001023 BNE 2$ ;SKIP TYPEOUTS
(1) 015672 000004 000472 TYPE RETURN
(1) 015676 013637 000402 MOV 2(6)+,2#SFATAL ;PLACE THE ERROR NUMBER IN LOCATION SFATAL
(1) 015702 014667 162522 MOV -(6),HLTADS ;PUT ADDRESS OF INSTRUCTION ON STACK
(1) 015706 162767 000002 162514 SUB #2,HLTADS
(2) 015714 017605 000000 MOV 2(6),TTY ;TYPE 2(6) IN OCTAL
(2) 015720 004767 000124 JSR %7,PRINTR ;TYPE LEADING ZERO'S
(1) 015724 062716 000002 ADD #2,(6) ;ADJUST THE RETURN ADDRESS
(1) 015730 000004 000500 TYPE $SPACE+3
(1) 015734 004767 000046 JSR PC,ERRORS ;GO TO USER ERROR ROUTINE
(1) 015740 105767 162454 2$: TSTB SEIV ;ARE WE RUNNING UNDER APT?
(1) 015744 001403 BEQ 4$ ;IF NOT THEN GO TO 4$
(1) 015746 005237 000400 INC 2#SMSGTY ;OTHERWISE INFORM APT
(1) 015752 000777 BR ;AND LOOP
(1) 015754 005737 000422 4$: TST 2#SSWREG ;HALT ON ERROR
(1) 015760 100001 BPL .+4 ;SKIP IF CONTINUE
(1) 015762 000000 HALT ;HALT ON ERROR!
(1) 015764 032737 001000 000422 BIT #SW09,2#SSWREG ;CHECK FOR INHIBIT LOOP ON ERROR
(1) 015772 001001 BNE .+4 ;SKIP IF LOOP ON ERROR
(1) 015774 000002 RTI
(1) 015776 105067 162501 CLRB $ICNT
(1) 016002 000167 177164 JMP KITS ;LOOP ON TEST UNTIL NO ERRORS
(1)
7446

```

```

7448
7449 ;*****
7450
7451 .SBTTL USER ERROR ROUTINE
7452
7453 016006 117767 162416 162524 ERRORS: MOV B @HLTADS, TYPCNT ; TYPE COUNT IS LOW BYTE OF HLT
7454 016014 062767 000002 162516 ADD R2, TYPCNT ; TYPE COUNT = X+2
7455 016022 012703 000430 MOV @HLTADS, R3 ; TOP OF DATA TO BE TYPED
7456 016026
(1) 016026 012305 ERRIS: MOV (R3)+, TTY ; TYPE (R3)+ IN OCTAL
(1) 016030 004767 000014 JSR %7, PRINTR ; TYPE LEADING ZERO'S
7457 016034 000004 000501 TYPE, SPACE+4 ; SPACE
7458 016040 105367 162474 DECB TYPCNT ; CHECK FOR DONE
7459 016044 100370 BPL ERRIS ; BRANCH IF NOT DONE
7460 016046 000207 RTS PC
7461

```

7463	016050	112767	000001	162466	PRINTR:	MOV B	#1, .PR	; SET ZERO FILL SWITCH
(1)	016056	000402				BR	+.6	; SKIP
(1)	016060	005067	162460		PRINTS:	CLR	.PR	; SUPPRESS LEADING ZERO'S
(1)	016064	112767	177772	162453		MOV B	#-6, .PR+1	; SET COUNT
(1)	016072	010446				MOV	R4, -(6)	; SAVE R4
(1)	016074	012704	016176			MOV	#.PRBUF, R4	; SET POINTER TO FIRST ASCII CHAR.
(1)	016100	105014				CLRB	(4)	; CLEAR FIRST BYTE
(1)	016102	000405				BR	.PRF	; ROTATE FIRST BIT
(1)	016104	105014			.PRL:	CLRB	(4)	; CLEAR BYTE OF CHARACTER
(1)	016106	006105				ROL	TTY	; ROTATE BIT INTO C
(1)	016110	106114				ROLB	(4)	; PACK IT
(1)	016112	006105				ROL	TTY	; ROTATE BIT INTO C
(1)	016114	106114				ROLB	(4)	; PACK IT
(1)	016116	006105			.PRF:	ROL	TTY	; ROTATE BIT INTO C
(1)	016120	106114				ROLB	(4)	; PACK IT
(1)	016122	105714				TSTB	(4)	; IS IT ZERO?
(1)	016124	001402				BEQ	+.6	; SKIP INC
(1)	016126	105267	162412			INCB	.PR	; SET FILL SWITCH
(1)	016132	105767	162406			TSTB	.PR	; CHECK FILL SWITCH
(1)	016136	001402				BEQ	+.6	; SKIP BITSET
(1)	016140	152724	000060			BISB	#'0, (4)+	; MAKE INTO ASCII CHAR
(1)	016144	105267	162375			INCB	.PR+1	; INC COUNT
(1)	016150	001355				BNE	.PRL	; REPEAT
(1)	016152	022704	016176			CMP	#.PRBUF, R4	; EMPTY BUFFER?
(1)	016156	001002				BNE	+.6	; SKIP IF NOT
(1)	016160	112724	000060			MOV B	#'0, (4)+	; LOAD 1 ZERO
(1)	016164	105014				CLRB	(4)	; NULL TERMINATOR
(1)	016166	000004	016176			TYPE	.PRBUF	; TYPE IT
(1)	016172	012604				MOV	(6)+, R4	; RESTORE R4
(1)	016174	000207				RTS	PC	; RETURN
(1)	016176	000004			.PRBUF:	.BLKW	4	; OUTPUT BUFFER

```

7465 ;*****
(1)
(1) .SBTTL POWER DOWN AND UP ROUTINES
(1)
(1) :POWER DOWN ROUTINE
(1) 016206 012737 016330 000024 $PWRDN: MOV $SILLUP,2#PWRVEC ;;SET FOR FAST UP
(1) 016214 012737 000340 000026 MOV #340,2#PWRVEC+2 ;;PRIO:7
(3) 016222 010046 MOV R0,-(SP) ;;PUSH R0 ON STACK
(3) 016224 010146 MOV R1,-(SP) ;;PUSH R1 ON STACK
(3) 016226 010246 MOV R2,-(SP) ;;PUSH R2 ON STACK
(3) 016230 010346 MOV R3,-(SP) ;;PUSH R3 ON STACK
(3) 016232 010446 MOV R4,-(SP) ;;PUSH R4 ON STACK
(3) 016234 010546 MOV R5,-(SP) ;;PUSH R5 ON STACK
(1) 016236 010667 000072 MOV SP,$SAVR6 ;;SAVE SP
(1) 016242 012737 016254 000024 MOV $SPWRUP,2#PWRVEC ;;SET UP VECTOR
(1) 016250 000000 HALT
(1) 016252 000776 BR .-2 ;;HANG UP
(1)
(1) :POWER UP ROUTINE
(1) 016254 016706 000054 $PWRUP: MOV $SAVR6,SP ;;GET SP
(1) 016260 005067 000050 CLR $SAVR6 ;;WAIT LOOP FOR THE TTY
(1) 016264 005267 000044 IS: INC $SAVR6 ;;WAIT FOR THE INC
(1) 016270 001375 BNE IS ;;OF WORD
(3) 016272 012605 MOV (SP)+,R5 ;;POP STACK INTO R5
(3) 016274 012604 MOV (SP)+,R4 ;;POP STACK INTO R4
(3) 016276 012603 MOV (SP)+,R3 ;;POP STACK INTO R3
(3) 016300 012602 MOV (SP)+,R2 ;;POP STACK INTO R2
(3) 016302 012601 MOV (SP)+,R1 ;;POP STACK INTO R1
(3) 016304 012600 MOV (SP)+,R0 ;;POP STACK INTO R0
(1) 016306 012737 016206 000024 MOV $SPWRDN,2#PWRVEC ;;SET UP THE POWER DOWN VECTOR
(1) 016314 012737 000340 000026 MOV #340,2#PWRVEC+2 ;;PRIO:7
(1) 016322 000004 TYPE ;;REPORT THE POWER FAILURE
(1) 016324 016336 $PWRMG: .WORD $POWER ;;POWER FAIL MESSAGE POINTER
(1) 016326 000002 RTI
(1) 016330 000000 $SILLUP: HALT ;;THE POWER UP SEQUENCE WAS STARTED
(1) 016332 000776 BR .-2 ;;BEFORE THE POWER DOWN WAS COMPLETE
(1) 016334 000000 $SAVR6: 0 ;;PUT THE SP HERE
(1) 016336 005015 047520 042527 $POWER: .ASCIZ <15><12>"POWER"
(1) 016344 000122 .EVEN

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```

7470
7471
7472
7473
7474
7475
7476
7477
7478 016346 132737 000040 000421 $TYPE: BITB #40,@#SENVH ;HAS THE CONSOLE OUTPUTS BEEN SUPPRESSED?
7479 016354 001007 BNE 3$ ;IF SO THEN RETURN FROM THE SUBROUTINE VIA 3$
7480 016356 010046 MOV RO,-(SP) ;OTHERWISE SAVE RO
7481 016360 017600 000002 MOV @2(SP),RO ;GET THE ADDRESS OF THE ASSCIZ STRING
7482 016364 112046 2$: MOVB (RO)+,-(SP) ;PUSH THE CHARACTER TO BE TYPED ONTO STACK
7483 016366 001005 BNE 4$ ;BRANCH IF IT IS NOT THE TERMINATOR
7484 016370 005726 TST (SP)+
7485 016372 012600 MOV (SP)+,RO ;OTHERWISE RESTORE THE STACK AND RO
7486 016374 062716 000002 3$: ADD #2,(SP) ;ADJUST THE RETURN PC
7487 016400 000002 RTI ;AND RETURN
7488
7489 016402 105777 162142 4$: TSTB @STPS ;IS THE PRINTER AVAILABLE?
7490 016406 100375 BPL 4$ ;IF NOT THEN LOOP HERE
7491 016410 112677 162136 MOVB (SP)+,@STPB ;OUT PUT THE CHARACTER
7492 016414 000763 BR 2$ ;AND GO BACK
7493 000001 .END
    
```

A	= 015074	7261#	7266											
ABASE	= 000000	5339												
ACDW1	= 000000	5339												
ACDW2	= 000000	5339												
ACPUOP	= 000000	5339												
ADDW0	= 000000	5339												
ADDW1	= 000000	5339												
ADDW10	= 000000	5339												
ADDW11	= 000000	5339												
ADDW12	= 000000	5339												
ADDW13	= 000000	5339												
ADDW14	= 000000	5339												
ADDW15	= 000000	5339												
ADDW2	= 000000	5339												
ADDW3	= 000000	5339												
ADDW4	= 000000	5339												
ADDW5	= 000000	5339												
ADDW6	= 000000	5339												
ADDW7	= 000000	5339												
ADDW8	= 000000	5339												
ADDW9	= 000000	5339												
ADEVCT	= 000000	5339												
ADEVN	= 000000	5339												
RENV	= 000000	5339												
RENVN	= 000000	5339												
AFATAL	= 000000	5339												
AMADR1	= 000000	5339												
AMADR2	= 000000	5339												
AMADR3	= 000000	5339												
AMADR4	= 000000	5339												
AMAMS1	= 000000	5339												
AMAMS2	= 000000	5339												
AMAMS3	= 000000	5339												
AMAMS4	= 000000	5339												
AMSGAD	= 000000	5339												
AMSGLG	= 000000	5339												
AMSGTY	= 000000	5339												
AMTYP1	= 000000	5339												
AMTYP2	= 000000	5339												
AMTYP3	= 000000	5339												
AMTYP4	= 000000	5339												
ANS1	000436	5348#	5349	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156	7157
		7158	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179
		7180	7181	7185	7189	7193	7194	7195	7196	7197	7198	7200	7202	7206
		7210	7214	7215	7216	7217	7219	7223	7227	7231	7232	7238*	7243	7244
		7251	7252	7308*	7327*	7370*	7388*	7432*						
ANS2	000440	5350#	5351	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156	7157
		7158	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179
		7180	7181	7185	7189	7193	7194	7195	7196	7197	7198	7200	7202	7206
		7210	7214	7215	7216	7217	7219	7223	7227	7231	7232	7238*	7243	7244
		7309*	7328*	7329*	7371*	7389*	7390*	7433*						
ANS3	000442	5352#	5353	7159	7163	7164	7168	7169	7185	7189	7200	7202	7206	7210
		7219	7223	7227	7231	7232	7243	7251	7252	7332*	7393*	7435*		
ANS4	000444	5354#	7159	7163	7164	7168	7169	7185	7189	7200	7202	7206	7210	7219
		7223	7227	7231	7232	7243	7251	7252	7333*	7394*	7436*			
ANS5	000446	5355#	7163	7164	7168	7169	7185	7189	7206	7210	7223	7227	7231	7232

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 53-5
 DVKACA.SRC CROSS REFERENCE TABLE -- USER SYMBOLS

TST20	003710	7168#							
TST21	004146	7169#							
TST22	004400	7173#							
TST23	004540	7174#							
TST24	004674	7175#							
TST25	005030	7176#							
TST26	005166	7177#							
TST27	005322	7178#							
TST3	001212	7149#							
TST30	005462	7179#							
TST31	005622	7180#							
TST32	005762	7181#							
TST33	006120	7185#							
TST34	006352	7189#							
TST35	006610	7193#							
TST36	006744	7194#							
TST37	007104	7195#							
TST4	001346	7150#							
TST40	007240	7196#							
TST41	007374	7197#							
TST42	007530	7198#							
TST43	007666	7200#							
TST44	010042	7202#							
TST45	010216	7206#							
TST46	010452	7210#							
TST47	010704	7214#							
TST5	001500	7151#							
TST50	011042	7215#							
TST51	011176	7216#							
TST52	011334	7217#							
TST53	011470	7219#							
TST54	011644	7223#							
TST55	012102	7227#							
TST56	012340	7231#							
TST57	012576	7232#							
TST6	001632	7152#							
TST60	013030	7237#							
TST61	013246	7243#							
TST62	013524	7244#							
TST63	013742	7251#							
TST64	014354	7252#							
TST7	001772	7153#							
TTY	=%000005	5303#	7445#	7456#	7463#				
TTYOUT	000546	5380#	7251	7252	7254#				
TYPCNT	000540	5377#	7453#	7454#	7458#				
TYPE	= 000004	5316#	7251	7252	7258	7445	7457	7463	7465
YESRT	000542	5378#	7123						
\$APTHD	000430	5340#	5341						
\$BELL	000504	5366#	7445						
\$CPUOP	000426	5339#							
\$DEVCT	000410	5339#	7111						
\$DOAGN	015052	7258#							
\$ENDAD	015042	5330	7258#						
\$ENDCT	015022	7258#	7262						
\$ENDMG	015056	7258#							
\$ENULL	015070	7258#							

DVKACA MACY11 27(732) 24-AUG-76 15:37 PAGE 54
 DVKACA.SRC CROSS REFERENCE TABLE -- MACRO NAMES

COMMEN	974#																	
DUMP	5132#	7445	7456															
ENDCOM	986#																	
ESCAPE	1097#																	
MFPS	5070#	7238	7244	7302	7322	7365	7383	7425										
MTPS	5085#	7237	7243	7244	7251	7252	7286	7349	7410									
MULT	3466#																	
NEWTST	1030#																	
POP	1483#	5056#	7465															
PRINT	5142#																	
PUSH	1475#	5056#	7465															
REPORT	4437#																	
SDUMP	5137#																	
SETUP	801#																	
SKIP	1131#																	
SLASH	926#																	
STARS	895#	5055#	5319	5330	5339	5340	7104	7258	7268	7445	7449	7465						
TYPBIN	1419#																	
TYPDEC	1389#																	
TYPNAM	1177#																	
TYPNUM	1356#																	
TYPCS	1309#																	
TYPOCT	1272#																	
TYPTXT	1226#																	
SADDER	5605#	7163	7168															
SADDES	5507#	7164	7169															
SADDR	5443#	7147	7148	7149	7152	7153	7154	7155	7158									
SADDS	5383#	7150	7151	7156	7157													
SADRER	6110#	7243	7244															
SALL	6881#	7237																
SALL2	6914#	7238																
SDIVER	6779#	7223	7227	7231														
SDIVES	6681#	7232																
SDIVR	6617#	7214	7216	7217														
SDIVS	6553#	7215																
SFATL	5059#	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156	7157	7158	7159	7163			
	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179	7180	7181	7185	7189	7193			
	7194	7195	7196	7197	7198	7200	7202	7206	7210	7214	7215	7216	7217	7219	7223			
	7227	7231	7232	7238	7243	7244	7251	7252	7442									
SFINT	6972#	7251	7252															
SFIS7	6031#	7159	7200	7202	7219													
SFLT	5181#	7445																
SMULR	6451#	7206																
SMULES	6353#	7210																
SMULR	6289#	7193	7194	7195	7196	7198												
SMULS	6233#	7197																
SOCTAL	5226#	7463																
SCOPE	5150#	7268																
SSUBR	5929#	7189																
SSUBES	5831#	7185																
SSUBR	5767#	7173	7174	7176	7178	7179	7180	7181										
SSUBS	5707#	7175	7177															
SSMDOC	5099#	5283																
SSESCA	1110#																	
SSFATL	5066#	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156	7157	7158	7159	7163			
	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179	7180	7181	7185	7189	7193			

RESET	7258														
ROL	7463														
ROLB	7463														
RTI	7251	7252	7268	7331	7392	7443	7445	7465	7487						
RTS	7356	7372	7417	7437	7460	7463									
RTT	5378														
SUB	7431	7445													
TRAP	5307														
TST	7147	7148	7149	7150	7151	7154	7155	7156	7159	7163	7164	7174	7176	7193	7195
	7196	7198	7200	7202	7206	7216	7217	7219	7251	7252	7268	7276	7445	7484	
TSTB	7148	7159	7163	7174	7181	7214	7251	7252	7268	7445	7463	7489			
.ABS	5049														
.ASCII	7258														
.ASCIZ	5362	5363	7465												
.BLKW	7463														
.BYTE	5339	5364	7258												
.ENABL	4														
.END	7493														
.ENDC	5057	5283	5319	5328	5330	5339	5340	7104	7147	7148	7149	7150	7151	7152	7153
	7154	7155	7156	7157	7158	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177
	7178	7179	7180	7181	7185	7189	7193	7194	7195	7196	7197	7198	7200	7202	7206
	7210	7214	7215	7216	7217	7219	7223	7227	7231	7232	7238	7243	7244	7251	7252
	7258	7268	7445	7449	7463	7465									
.EVEN	5339	5365	7258	7465											
.IF	5057	5283	5319	5328	5330	5339	5340	7104	7147	7148	7149	7150	7151	7152	7153
	7154	7155	7156	7157	7158	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177
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	7258	7268	7445	7449	7463	7465									
.IFF	5319	5330	5339	5340	7104	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156
	7157	7158	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179	7180
	7181	7185	7189	7193	7194	7195	7196	7197	7198	7200	7202	7206	7210	7214	7215
	7216	7217	7219	7223	7227	7231	7232	7238	7243	7244	7251	7252	7258	7268	7445
	7449	7465													
.IFNE	7243	7244													
.IIF	5057	5339	7258	7463											
.IRP	5328	7465													
.LIST	2	4623	5053	5054	5281	5327	5328	5335	5339	7145	7147	7148	7149	7150	7151
	7152	7153	7154	7155	7156	7157	7158	7159	7162	7163	7164	7167	7168	7169	7172
	7173	7174	7175	7176	7177	7178	7179	7180	7181	7184	7185	7188	7189	7192	7193
	7194	7195	7196	7197	7198	7200	7202	7205	7206	7209	7210	7213	7214	7215	7216
	7217	7219	7222	7223	7226	7227	7230	7231	7232	7236	7237	7238	7241	7243	7244
	7249	7251	7252	7258	7263	7267	7271	7284	7285	7286	7289	7300	7301	7302	7305
	7320	7321	7322	7325	7347	7348	7349	7352	7363	7364	7365	7368	7381	7382	7383
	7386	7408	7409	7410	7413	7423	7424	7425	7428	7442	7463	7468			
.MACRO	39	81	166	308	485	515	586	747	801	895	926	974	986	1030	1064
	1097	1110	1131	1144	1177	1226	1272	1309	1356	1389	1419	1475	1483	1535	1739
	1947	2140	2228	2353	2450	2528	2613	2827	2919	2995	3095	3223	3286	3348	3466
	3504	3568	3666	3751	3790	3853	3891	3934	4032	4080	4350	4397	4437	4513	5059
	5066	5070	5085	5099	5132	5137	5142	5150	5181	5226	5383	5443	5507	5605	5707
	5767	5831	5929	6031	6110	6233	6289	6353	6451	6553	6617	6681	6779	6881	6914
	6972														
.MCALL	5055	5056													
.MEXIT	5339														
.NLIST	1	3	5050	5052	5279	5322	5328	5333	5339	7143	7147	7148	7149	7150	7151
	7152	7153	7154	7155	7156	7157	7158	7159	7160	7163	7164	7165	7168	7169	7170

	7173	7174	7175	7176	7177	7178	7179	7180	7181	7182	7185	7186	7189	7190	7193
	7194	7195	7196	7197	7198	7200	7202	7203	7206	7207	7210	7211	7214	7215	7216
	7217	7219	7220	7223	7224	7227	7228	7231	7232	7234	7237	7238	7239	7243	7244
	7247	7251	7252	7258	7260	7265	7269	7283	7286	7287	7288	7299	7302	7303	7304
	7319	7322	7323	7324	7346	7349	7350	7351	7362	7365	7366	7367	7380	7383	7384
	7385	7407	7410	7411	7412	7422	7425	7426	7427	7442	7463	7466			
.NTYPE	7237	7238	7243	7244	7251	7252	7286	7302	7322	7349	7365	7383	7410	7425	
.PAGE	5045	5047	5336	6232	7103	7146	7147	7148	7149	7150	7151	7152	7153	7154	7155
	7156	7157	7158	7163	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179	7180
	7181	7185	7189	7193	7194	7195	7196	7197	7198	7199	7201	7206	7210	7214	7215
	7216	7217	7218	7223	7227	7231	7232	7233	7242	7243	7244	7251	7252	7257	7268
	7272	7445	7447	7462	7464	7469									
.REPT	4624	5323													
.SBTTL	5280	5330	5334	5339	5340	7106	7144	7161	7166	7171	7183	7187	7191	7204	7208
	7212	7221	7225	7229	7235	7240	7248	7258	7268	7270	7445	7451	7463	7465	7467
.TITLE	5057														
.WORD	5330	5339	5340	5366	7147	7148	7149	7150	7151	7152	7153	7154	7155	7156	7157
	7158	7159	7163	7164	7168	7169	7173	7174	7175	7176	7177	7178	7179	7180	7181
	7185	7189	7193	7194	7195	7196	7197	7198	7200	7202	7206	7210	7214	7215	7216
	7217	7219	7223	7227	7231	7232	7237	7238	7243	7244	7251	7252	7258	7286	7302
	7322	7349	7365	7383	7410	7425	7465								

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

* DVKACA/CRF=DVKACA.SML DVKACA.SRC
 RUN-TIME: 44 56 5 SECONDS
 RUN-TIME RATIO: 568/106=5.3
 CORE USED: 41K (81 PAGES)

