

# LSI

MACRO INSTRUCTION EXERCISER  
MD-11-DVKAA-A

EP-DVKAA-A-DL-A

OCT 1976

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FICHE 1 OF 1

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4918	ACT11 HOOKS	
5031	APT MAILBOX-ETABLE	
5032	APT PARAMETER BLOCK	
5065	STARTING OF THE PROGRAM	
5093	T0	CHECK BRANCH INSTRUCTIONS WITH ZERO CONDITION CODES
5116	T1	CHECK BRANCH INSTRUCTIONS WITH N BIT SET
5133	T2	CHECK BRANCH INSTRUCTIONS WITH N&V BITS SET
5150	T3	CHECK BRANCH INSTRUCTIONS WITH N,V&C BITS SET
5167	T4	CHECK BRANCH INSTRUCTIONS WITH N,Z,V&C BITS SET
5190	T5	CHECK BRANCH INSTRUCTIONS WITH ALL THE CONDITION CODES SET
5207	T6	CLEAR THE CONDITION CODES
5227	T7	CHECK FORWARD AND BACKWARD BRANCHES.
5242	T10	CHECK JMP INSTRUCTIONS FOR MODE 1
5259	T11	CHECK JMP INSTRUCTIONS FOR MODES 2 AND 3
5276	T12	TEST JUMP INSTRUCTION FOR MODE 4, 5
5293	T13	TEST JMP INSTRUCTION FOR MODE 6 AND 7
5310	T14	CHECK JSR AND MARK INSTRUCTIONS
5327	T15	CHECK REGISTER SELECTION
5344		CHECK BYTE INSTRUCTIONS, DESTINATION MODE 0 ONLY
5361	T16	NEW INSTRUCTIONS USED IN THIS SECTION ARE TSTB, CLPB, MOVB
5378	T17	NEW INSTRUCTIONS USED IN THIS SECTION ARE CMPB, BICB
5395	T20	NEW INSTRUCTIONS USED IN THIS SECTION ARE BICB, BIYB
5412	T21	NEW INSTRUCTIONS USED IN THIS SECTION ARE INCB, DECB
5429	T22	NEW INSTRUCTION IN THIS SECTION IS COMB
5446	T23	NEW INSTRUCTION IN THIS SECTION IS NEGB
5463	T24	NEW INSTRUCTION IN THIS SECTION IS ROLB
5480	T25	NEW INSTRUCTION IN THIS SECTION IS RORB
5497	T26	NEW INSTRUCTION IN THIS SECTION IS ASLB
5514	T27	NEW INSTRUCTION IN THIS SECTION IS ASRB
5531	T30	NEW INSTRUCTION IN THIS SECTION IS ADCB
5548	T31	NEW INSTRUCTION IN THIS SECTION IS SBCB
5565		CHECK WORD INSTRUCTIONS, DESTINATION MODE 0 ONLY
5582	T32	NEW INSTRUCTIONS USED IN THIS SECTION ARE TST, CLR, MOV
5599	T33	NEW INSTRUCTIONS USED IN THIS SECTION ARE CMP, BIS
5616	T34	NEW INSTRUCTIONS USED IN THIS SECTION ARE BIC, BIT
5633	T35	NEW INSTRUCTIONS USED IN THIS SECTION ARE INC, DEC
5650	T36	NEW INSTRUCTION IN THIS SECTION IS COM
5667	T37	NEW INSTRUCTION IN THIS SECTION IS NEG
5684	T40	NEW INSTRUCTION IN THIS SECTION IS ROL
5701	T41	NEW INSTRUCTION IN THIS SECTION IS ROR
5718	T42	NEW INSTRUCTION IN THIS SECTION IS ASL
5735	T43	NEW INSTRUCTION IN THIS SECTION IS ASR
5752	T44	NEW INSTRUCTION IN THIS SECTION IS ADC
5769	T45	NEW INSTRUCTION IN THIS SECTION IS SBC
5786	T46	NEW INSTRUCTION IN THIS SECTION IS SXT
5803	T47	NEW INSTRUCTION IN THIS SECTION IS SWAB
5820	T50	NEW INSTRUCTION IN THIS SECTION IS XOR
5837	T51	NEW INSTRUCTION IN THIS SECTION IS ADD
5854	T52	NEW INSTRUCTION IN THIS SECTION IS SUB
5871	T53	NEW INSTRUCTIONS IN THARE SECTION IS MTPS & MFPS
5888		LSI-11 INSTRUCTIONS NOT MODE 0
5905	T54	CHECK MODES 0 & 1 USING THE MOVB AND MOV INSTRUCTIONS
5922	T55	CHECK MODE 2 USING THE MOVB AND MOV INSTRUCTIONS
5939	T56	CHECK MODE 3 USING THE MOVB AND MOV INSTRUCTIONS
5956	T57	CHECK MODE 4 USING THE MOVB AND MOV INSTRUCTIONS
5973	T60	CHECK MODE 5 USING THE MOVB AND MOV INSTRUCTIONS



6423	T61	CHECK MODE 6 USING THE MOVB AND MOV INSTRUCTIONS
6448	T62	CHECK MODE 7 USING THE MOVB AND MOV INSTRUCTIONS
6472		CHECK BYTE INSTRUCTIONS, NOT DESTINATION MODE 0
6481	T63	NEW INSTRUCTIONS USED IN THIS SECTION ARE TSTB, CLRB, MOVB
6509	T64	NEW INSTRUCTIONS USED IN THIS SECTION ARE CMPB, BISB
6532	T65	NEW INSTRUCTIONS USED IN THIS SECTION ARE BICB, BITB
6575	T66	NEW INSTRUCTIONS USED IN THIS SECTION ARE INCB, DECB
6624	T67	NEW INSTRUCTION IN THIS SECTION IS COMB
6652	T70	NEW INSTRUCTION IN THIS SECTION IS NEGB
6672	T71	NEW INSTRUCTION IN THIS SECTION IS ROLB
6693	T72	NEW INSTRUCTION IN THIS SECTION IS RORB
6716	T73	NEW INSTRUCTION IN THIS SECTION IS ASLB
6728	T74	NEW INSTRUCTION IN THIS SECTION IS ASRB
6762	T75	NEW INSTRUCTION IN THIS SECTION IS AOCB
6792	T76	NEW INSTRUCTION IN THIS SECTION IS SBCB
6829		CHECK WORD INSTRUCTIONS, NOT DESTINATION MODE 0
6841	T77	NEW INSTRUCTIONS USED IN THIS SECTION ARE TST, CLR, MOV
6862	T100	NEW INSTRUCTIONS USED IN THIS SECTION ARE CMP, BIS
6911	T101	NEW INSTRUCTIONS USED IN THIS SECTION ARE BIC, BIT
6962	T102	NEW INSTRUCTIONS USED IN THIS SECTION ARE INC, DEC
6999	T103	NEW INSTRUCTION IN THIS SECTION IS COM
7024	T104	NEW INSTRUCTION IN THIS SECTION IS NEG
7047	T105	NEW INSTRUCTION IN THIS SECTION IS ROL
7068	T106	NEW INSTRUCTION IN THIS SECTION IS ROR
7091	T107	NEW INSTRUCTION IN THIS SECTION IS ASL
7110	T110	NEW INSTRUCTION IN THIS SECTION IS ASR
7138	T111	NEW INSTRUCTION IN THIS SECTION IS ADC
7168	T112	NEW INSTRUCTION IN THIS SECTION IS SBC
7206	T113	NEW INSTRUCTION IN THIS SECTION IS SXT
7226	T114	NEW INSTRUCTION IN THIS SECTION IS SWAB
7251	T115	NEW INSTRUCTION IN THIS SECTION IS XOR
7275	T116	NEW INSTRUCTION IN THIS SECTION IS ADD
7328	T117	NEW INSTRUCTION IN THIS SECTION IS SUB
7362	T120	NEW INSTRUCTION IN THIS SECTION IS SOB
7390	T121	NEW INSTRUCTIONS IN THIS SECTION ARE MTPS & MFPS
7429	T122	BYTE INSTRUCTIONS REQUIRING WORD INST. TO CHECK
7481		END OF PASS ROUTINE
7512		POWER FAIL ROUTINE
7525		TYPE ROUTINE
7544		ROUTINES TO CHECK CONDITION CODES



001

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54  
DVKAAA.P11

\*\*\* SEQ 0003

4888



E01

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-1  
DVKAAA.P11

\*\*\* SEQ 0004

4890







G01

```

4906          000000          . = 0
4907
4908          ;*****
4909
4910          ;      TRAP CATCHERS OF .+2 AND HALT IN LOCATIONS 0 THRU 776 [IT IS NLISTED]
4911
4912          ;*****
(1)
(1)
(1)
(1)
(1) 000046 001000          .SBTTL ACT11 HOOKS
(1) 000046 000046          ;HOOKS REQUIRED BY ACT11
(1) 000046 016750          $SVPC=.          ;SAVE PC
(1) 000052 000052          . = 46          ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .SEOP
(1) 000052 000000          $ENDAD          ;;2)SET LOC.52 TO ZERO
(1) 000052 001000          . = 52          ;; RESTORE PC
(1) 001000          .WORD 0
(1) 001000          .=$SVPC

5004          000400          . = 400
5005          000000          R0          =%0
5006          000001          R1          =%1
5007          000002          R2          =%2
5008          000003          R3          =%3
5009          000004          R4          =%4
5010          000005          R5          =%5
5011          000006          R6          =%6
5012          000006          SP          =%6
5013          000007          PC          =%7
5014          000254          CLNZ          =254
5015          000001          ERRNM          =1
5016          000260          NOP1          =260
5017          000263          SEVC          =263
5018          000273          SENVC          =273
5019          000000          $TN          =0
5020          000004          .TYPE          =IOT

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5068
5069
5070          ;STARTING OF THE PROGRAM
5071          ;-----
5072
5073          . =200
5074 000200 012737 016770 000024      MOV    #PWRDN, @#24      ; SERVICE POWER DOWN ROUTINE ON ANY FUTURE POWER DOWN
5075 000206 012700 000420              MOV    #SETABLE, R0
5076 000212 005040              2$: CLR    -(R0)              ; START CLEANING THE STACK
5077 000214 020027 000400              CMP    R0, #SMAIL      ; FOR INITIALIZATION
5078 000220 101374              BHI   2$
5079 000222 000167 000302              JMP   START
5080          ;
5081
5082
5083          . =530
5084
5085 000530 012706 000530      START: MOV    #START, SP      ; SET THE STACK POINTER
5086 000534 012705 000404      MOV    #STESTN, R5     ; PLACE THE ADDRESS OF LOCATION $TESTN IN R5
5087 000540 005715              TST   (R5)             ; CHECK THE SEQUENCE COUNTER
5088 000542 001401              BEQ   NOBIT            ; IF THIS IS THE STARTING OF THE TEST THEN
5089                                ; GO TO NOBIT TEST
5090 000544 000000              HALT                    ; OTHERWISE HALT AND WAIT FOR THE OPERATOR
5091                                ; TO START AT THE PROPER TEST NUMBER
5092
    
```



# K01

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-7  
DVKAAA.P11 TO CHECK BRANCH INSTRUCTIONS WITH ZERO CONDITION CODES

\*\*\* SEQ 0010

```
5093      ;*****
(2)      ;*TEST: 0      CHECK BRANCH INSTRUCTIONS WITH ZERO CONDITION CODES
(3)      ;*****
5094
5095 000546      NOBIT:
(2) 000546 021527 000000      CMP      (R5),#0
5096 000552 001017      BNE      CCO      ; IF IN WRONG SEQUENCE GO TO HALT AT END OF THE TEST
5097 000554 005215      IS:      INC      (R5)      ; ZERO CONDITION CODES, NZVC=0000
5098 000556 000257      CCC
5099 000560 103414      BCS      CCO
5100 000562 102413      BVS      CCO
5101 000564 001412      BEQ      CCO
5102 000566 100411      BMI      CCO
5103 000570 000260      NOP1      ; CHECK NOP1 INSTRUCTION I.E. OP-CODE 260
5104 000572 103407      BCS      CCO
5105 000574 102406      BVS      CCO
5106 000576 001405      BEQ      CCO
5107 000600 100404      BMI      CCO
5108 000602 002403      BLT      CCO
5109 000604 003402      BLE      CCO
5110 000606 101401      BLOS     CCO
5111 000610 101004      BHI      ENDCCO
5112 000612      CCO:
(2) 000612 012745 000001      MOV      #1, -(R5)
(2) 000616 005245      INC      -(R5)
(2) 000620 000000      HALT
5113 000622 102000      ENDCCO: BVC      NBIT      ; ONE OF THE ABOVE BRANCHES FAILED OR WRONG SEQUENCE
5114
5115
5116      ;*****
(2)      ;*TEST: 1      CHECK BRANCH INSTRUCTIONS WITH N BIT SET
(3)      ;*****
5117
5118 000624      NBIT:
(2) 000624 021527 000001      CMP      (R5),#1
5119 000630 001012      BNE      CC1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5120 000632 005215      IS:      INC      (R5)      ; NBIT IS SET, NZVC=1000
5121 000634 000270      SEN
5122 000636 100007      BPL      CC1
5123 000640 001406      BEQ      CC1
5124 000642 002005      BGE      CC1
5125 000644 003004      BGT      CC1
5126 000646 103403      BCS      CC1
5127 000650 101402      BLOS     CC1
5128 000652 103401      BLO      CC1
5129 000654 003404      BLE      ENDCC1
5130 000656      CC1:
(2) 000656 012745 000002      MOV      #2, -(R5)
(2) 000662 005245      INC      -(R5)
(2) 000664 000000      HALT
5131 000666 001000      ENDCC1: BNE      VBIT      ; ONE OF THE ABOVE BRANCHES FAILED OR WRONG SEQUENCE
```



# L01

DVKAAA MACY11 27(732)  
DVKAAA.P11 T2

25-AUG-76 13:25 PAGE 54-8  
CHECK BRANCH INSTRUCTIONS WITH N&V BITS SET

\*\*\* SEQ 0011

```
5132      ;:*****
(2)      ;*TEST: 2      CHECK BRANCH INSTRUCTIONS WITH N&V BITS SET
(3)      ;:*****
5133
5134 000670      VBIT:      CMP      (R5),#2
(2) 000670 021527 000002      BNE      CC2      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5135 000674 001014      IS:      INC      (R5)
5136 000676 005215      SEN
5137 000700 000270      SEV      ; V AND N BIT SET, NZVC = 1010
5138 000702 000262      BVC      CC2
5139 000704 102010      BEQ      CC2
5140 000706 001407      BPL      CC2
5141 000710 100006      BCS      CC2
5142 000712 103405      BLT      CC2
5143 000714 002404      BLE      CC2
5144 000716 003403      BLOS     CC2
5145 000720 101402      BLO      CC2
5146 000722 103401      BGT      ENDCC2
5147 000724 003004      CC2:      MOV      #3, -(R5)
5148 000726      INC      -(R5)
(2) 000726 012745 000003      HALT
(2) 000732 005245      ENDCC2: BGE      CBIT      ; ONE OF THE ABOVE BRANCHES FAILED OR WRONG SEQUENCE
(2) 000734 000000
5149 000736 002000
5150
5151
5152      ;:*****
(2)      ;*TEST: 3      CHECK BRANCH INSTRUCTIONS WITH N,V&C BITS SET
(3)      ;:*****
5153
5154 000740      CBIT:      CMP      (R5),#3
(2) 000740 021527 000003      BNE      CC3      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5155 000744 001013      IS:      INC      (R5)
5156 000746 005215      SEN
5157 000750 000270      SEV
5158 000752 000262      SEC      ; C, V, AND N BITS ARE SET, NZVC=1011
5159 000754 000261      BEQ      CC3
5160 000756 001406      BPL      CC3
5161 000760 100005      BVC      CC3
5162 000762 102004      BLT      CC3
5163 000764 002403      BLE      CC3
5164 000766 003402      BHI      CC3
5165 000770 101001      BGE      ZBIT
5166 000772 002004      CC3:      MOV      #4, -(R5)
5167 000774      INC      -(R5)
(2) 000774 012745 000004      HALT
(2) 001000 005245      ; ONE OF THE ABOVE BRANCHES FAILED
(2) 001002 000000      ; OR WRONG SEQUENCE
5168
```



# MO1

DVKAAA MACY11 27(732)  
DVKAAA.P11 T4

25-AUG-76 13:25 PAGE 54-9  
CHECK BRANCH INSTRUCTIONS WITH N,Z,V&C BITS SET

\*\*\* SEQ 0012

```
5169          ;:*****
(2)          ;*TEST: 4      CHECK BRANCH INSTRUCTIONS WITH N,Z,V&C BITS SET
(3)          ;:*****
5170
5171 001004    ZBIT:      CMP      (R5),#4
(2) 001004    021527    000004    BNE      CC4      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5172 001010    001015    INC      (R5)
5173 001012    005215    SEN
5174 001014    000270    SEV
5175 001016    000262    SEC
5176 001020    000261    SEZ      ; ALL BITS SET, NZVC=1111
5177 001022    000264    BNE      CC4
5178 001024    001007    BPL      CC4
5179 001026    100006    BVC      CC4
5180 001030    102005    BCC      CC4
5181 001032    103004    BLT      CC4
5182 001034    002403    BGT      CC4
5183 001036    003002    BHI      CC4
5184 001040    101001    BEQ      YESCC
5185 001042    001404    CC4:      MOV      #5, -(R5)
5186 001044    012745    000005    INC      -(R5)
(2) 001044    012745    000005    HALT      ; ONE OF THE ABOVE BRANCHES FAILED
(2) 001050    005245    ; OR WRONG SEQUENCE
(2) 001052    000000
5187
5188
5189
5190          ;:*****
(2)          ;*TEST: 5      CHECK BRANCH INSTRUCTIONS WITH ALL THE CONDITION CODES SET
(3)          ;:*****
5191
5192 001054    YESCC:     CMP      (R5),#5
(2) 001054    021527    000005    BNE      CC6      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5193 001060    001014    INC      (R5)
5194 001062    005215    SCC      ; NZVC=1111
5195 001064    000277    BPL      CC6
5196 001066    100011    BNE      CC6
5197 001070    001010    BVC      CC6
5198 001072    102007    BCC      CC6
5199 001074    103006    NOP      ; CHECK NOP INSTRUCTION
5200 001076    000240    BPL      CC6
5201 001100    100004    BNE      CC6
5202 001102    001003    BVC      CC6
5203 001104    102002    BCC      CC6
5204 001106    103001    BLOS     NOTCC
5205 001110    101404    CC6:      MOV      #6, -(R5)
5206 001112    012745    000006    INC      -(R5)
(2) 001112    012745    000006    HALT      ; SCC OR A BRANCH FAILED, OR WRONG SEQUENCE
(2) 001116    005245
(2) 001120    000000
```



# NO1

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-10  
 DVKAAA.P11 T6 CLEAR THE CONDITION CODES

\*\*\* SEQ 0013

```

5207          ;:*****
(2)          ;*TEST: 6      CLEAR THE CONDITION CODES
(3)          ;:*****
5208
5209 001122   NOTCC:      CMP      (R5),#6
(2) 001122   021527 000006      BNE      CC5      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5210 001126   001013      INC      (R5)
5211 001130   005215      SCC
5212 001132   000277      CLC      ; NZVC=1111
5213 001134   000241      CLC      ; NZVC=1110
5214 001136   103407      BCS      CC5
5215 001140   000242      CLV      ; NZVC=1100
5216 001142   102405      BVS      CC5
5217 001144   000244      CLZ      ; NZVC=1000
5218 001146   001403      BEQ      CC5
5219 001150   000250      CLN      ; NZVC=0000
5220 001152   100401      BMI      CC5
5221 001154   101004      BHI      ENDCC5
5222 001156   CC5:      MOV      #7, -(R5)
(2) 001156   012745 000007      INC      -(R5)
(2) 001162   005245      HALT
(2) 001164   000000      ENDCC5: BPL      BRANCH      ; ONE OF THE ABOVE CLEARS FAILED OR WRONG SEQUENCE
5223 001166   100000
5224
5225
5226
5227          ;:*****
(2)          ;*TEST: 7      CHECK FORWARD AND BACKWARD BRANCHES.
(3)          ;:*****
5228
5229 001170   BRANCH:      CMP      (R5),#7
(2) 001170   021527 000007      BEQ      1$      ; IF IN WRONG SEQUENCE GO TO HLT
5230 001174   001404      MOV      #10, -(R5)
5231 001176   012745 000010      INC      -(R5)
(2) 001202   005245      HALT
(2) 001204   000000      1$:      INC      (R5)      ; CHECK BRANCH FORWARD AND BACKWARD
5232 001206   005215      BR      4$
5233 001210   000416      MOV      #11, -(R5)      ; CHECK BRANCH FORWARD AND BACKWARD
5234 001212   012745 000011      INC      -(R5)
(2) 001216   005245      HALT      ; FORWARD BRANCH FAILED
(2) 001220   000000      2$:      BR      3$
5235 001222   000404      MOV      #12, -(R5)
5236 001224   012745 000012      INC      -(R5)
(2) 001230   005245      HALT      ; FORWARD BRANCH FAILED
(2) 001232   000000      3$:      BR      5$
5237 001234   000411      MOV      #13, -(R5)
5238 001236   012745 000013      INC      -(R5)
(2) 001242   005245      HALT      ; FORWARD BRANCH FAILED
(2) 001244   000000      4$:      BR      2$
5239 001246   000765      MOV      #14, -(R5)
5240 001250   012745 000014      INC      -(R5)
(2) 001254   005245      HALT      ; BACKWARD BRANCH FAILED
(2) 001256   000000      5$:      BR      JMP1
5241 001260   000400
  
```

```

5242          ::*****
(2)          ::*TEST: 10    CHECK JMP INSTRUCTIONS FOR MODE 1
(3)          ::*****
5243
5244 001262    JMP1:
(2) 001262    021527 000010    CMP      (R5),#10
5245 001266    001033    BNE     ENDJP1      ; IF IN WRONG SEQUENCE GO TO HALT AT THE END OF THE TEST
5246 001270    005215    1$:    INC     (R5)
5247 001272    012700    001312    MOV     #2$,R0      ; TEST JUMP INSTRUCTION MODE 1
5248 001276    000277    SCC
5249 001300    000110    JMP     (R0)
5250 001302    012745    000015    MOV     #15,-(R5)
(2) 001306    005245    INC     -(R5)
(2) 001310    000000    HALT    ; JUMP INSTRUCTION FAILED
5251 001312    2$:
(1) 001312    100003    BPL     3$
(1) 001314    001002    BNE     3$
(1) 001316    102001    BVC     3$
(1) 001320    103404    BCS     4$
(2) 001322    3$:
(3) 001322    012745    000016    MOV     #16,-(R5)
(3) 001326    005245    INC     -(R5)
(3) 001330    000000    HALT    ; WRONG CC
5252 001332    020027    001312    4$:    CMP     R0,#2$
5253 001336    001404    BEQ     5$      ; CONTINUE IF R0 IS OK
5254 001340    012745    000017    MOV     #17,-(R5)
(2) 001344    005245    INC     -(R5)
(2) 001346    000000    HALT
5255 001350    012700    001366    5$:    MOV     #JMP2,R0  ; TEST JUMP INSTRUCTION MODE 1
5256 001354    000110    JMP     (R0)
5257 001356    ENDJP1:
(2) 001356    012745    000020    MOV     #20,-(R5)
(2) 001362    005245    INC     -(R5)
(2) 001364    000000    HALT    ; JUMP INSTRUCTION FAILED OR WRONG SEQUENCE
5258
5259
5260          ::*****
(2)          ::*TEST: 11    CHECK JMP INSTRUCTIONS FOR MODES 2 AND 3
(3)          ::*****
5261
5262 001366    JMP2:
(2) 001366    021527 000011    CMP     (R5),#11
5263 001372    001073    BNE     ENDJP3      ; IF IN WRONG SEQUENCE GO TO HALT AT THE END OF TEST
5264 001374    005215    INC     (R5)
5265 001376    012700    001416    MOV     #3$,R0      ; TEST JUMP INSTRUCTION MODE 2
5266 001402    000277    SCC
5267 001404    000120    JMP     (R0)+
5268 001406    012745    000021    MOV     #21,-(R5)
(2) 001412    005245    INC     -(R5)
(2) 001414    000000    HALT    ; JUMP INSTRUCTION FAILED
5269 001416    3$:
(1) 001416    100003    BPL     4$
(1) 001420    001002    BNE     4$
(1) 001422    102001    BVC     4$
(1) 001424    103404    BCS     5$
(2) 001426    4$:
    
```



DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-12  
 DVKAAA.P11 T11 CHECK JMP INSTRUCTIONS FOR MODES 2 AND 3

\*\*\* SEQ 0015

(3)	001426	012745	000022			MOV	#22, -(R5)		
(3)	001432	005245				INC	-(R5)		
(3)	001434	000000				HALT			; WRONG CC
5270	001436	020027	001420		5S:	CMP	RO, #3S+2		; IS THERE AUTO INC.?
5271	001442	001404				BEQ	6S		
5272	001444	012745	000023			MOV	#23, -(R5)		
(2)	001450	005245				INC	-(R5)		
(2)	001452	000000				HALT			; MODE 2 FAILED FOR JMP INSTRUCTION
5273	001454	012700	001472		6S:	MOV	#JMP3, RO		; TEST JUMP INSTRUCTION MODE 2
5274	001460	000120				JMP	(RO)+		
5275	001462	012745	000024			MOV	#24, -(R5)		
(2)	001466	005245				INC	-(R5)		
(2)	001470	000000				HALT			; JUMP INSTRUCTION FAILED
5276									
5277	001472	012767	001526	176740	JMP3:	MOV	#3S, TEMP		; TEST JUMP INSTRUCTION MODE 3
5278	001500	012767	001546	176734		MOV	#4S, TEMP+2		
5279	001506	012700	000440			MOV	#TEMP, RO		
5280	001512	000277				SCC			
5281	001514	000130				JMP	2(RO)+		
5282	001516	012745	000025			MOV	#25, -(R5)		
(2)	001522	005245				INC	-(R5)		
(2)	001524	000000				HALT			; JUMP INSTRUCTION FAILED
5283	001526	027067	000000	000012	3S:	CMP	2(RO), 4S		; IS THERE AUTO INC.?
5284	001534	001404				BEQ	4S		
5285	001536	012745	000026			MOV	#26, -(R5)		
(2)	001542	005245				INC	-(R5)		
(2)	001544	000000				HALT			; JMP INSTRUCTION FAILED IN MODE 2
5286	001546	012767	001572	176664	4S:	MOV	#JMP4, TEMP		; TEST JUMP INSTRUCTION MODE 3
5287	001554	012700	000440			MOV	#TEMP, RO		
5288	001560	000130				JMP	2(RO)+		
5289	001562				ENDJP3:				
(2)	001562	012745	000027			MOV	#27, -(R5)		
(2)	001566	005245				INC	-(R5)		
(2)	001570	000000				HALT			; JUMP ERROR OR WRONG SEQUENCE

```

5290          ;*****
(2)          ;*TEST: 12      TEST JUMP INSTRUCTION FOR MODE 4, 5
(3)          ;*****
5291
5292 001572      JMP4:      CMP      (R5),#12
(2) 001572 021527 000012      BNE     ENDJPS      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5293 001576 001075      INC      (R5)
5294 001600 005215      MOV     #3$,R0      ; TEST JUMP INSTRUCTION MODE 4
5295 001602 012700 001624      SCC
5296 001606 000277      JMP     -(R0)
5297 001610 000140      MOV     #30, -(R5)
5298 001612 012745 000030      INC     -(R5)
(2) 001616 005245      HALT
(2) 001620 000000      BR      4$          ; JUMP INSTRUCTION FAILED
5299 001622 000404      3$:      MOV     #31, -(R5)      ; JUMP SHOULD LAND HERE
5300 001624      INC     -(R5)
(2) 001630 005245      HALT          ; NO AUTO DECREMENT FROM JMP4
(2) 001632 000000      4$:      CMP     #3$-2,R0      ; CHECK R0
5301 001634 022700 001622      BEQ     5$
5302 001640 001404      MOV     #32, -(R5)
5303 001642 012745 000032      INC     -(R5)
(2) 001646 005245      HALT
(2) 001650 000000      5$:      MOV     #JMP5+2,R0      ; TEST JUMP INSTRUCTION MODE 4
5304 001652 012700 001672      JMP     -(R0)
5305 001656 000140      MOV     #33, -(R5)
5306 001660 012745 000033      INC     -(R5)
(2) 001664 005245      HALT          ; JUMP INSTRUCTION FAILED
(2) 001666 000000      5307      MOV     #3$,TEMP1      ; TEST JUMP INSTRUCTION MODE 5
5308 001670 012767 001722 176544      MOV     #TEMP1,R0
5309 001676 012700 000442      MOV     #4$,TEMP1-2
5310 001702 012767 001732 176530      JMP     2-(R0)
5311 001710 000150      MOV     #34, -(R5)
5312 001712 012745 000034      INC     -(R5)
(2) 001716 005245      HALT          ; JUMP INSTRUCTION FAILED
(2) 001720 000000      3$:      MOV     #35, -(R5)
5313 001722      INC     -(R5)
(2) 001726 005245      HALT          ; ERROR, NO AUTO DECREMENT
(2) 001730 000000      4$:      CMP     #TEMP1-2,R0      ; CHECK R0
5314 001732 022700 000440      BEQ     5$
5315 001736 001404      MOV     #36, -(R5)
5316 001740 012745 000036      INC     -(R5)
(2) 001744 005245      HALT          ; JUMP ONSTRUCTION FAILED IN MODE 5
(2) 001746 000000      5$:      MOV     #3$,TEMP1      ; TEST JUMP INSTRUCTION MODE 5
5317 001750 012767 001722 176464      MOV     #TEMP1,R0
5318 001756 012700 000442      MOV     #JMP6,TEMP1-2
5319 001762 012767 002002 176450      JMP     2-(R0)
5320 001770 000150      ENDJPS:
5321 001772      MOV     #37, -(R5)
(2) 001772 012745 000037      INC     -(R5)
(2) 001776 005245      HALT          ; JUMP ERROR OR WRONG SEQUENCE
(2) 002000 000000
5322
5323          ;*****

```



# E02

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-14  
 DVKAAA.P11 T13 TEST JMP INSTRUCTION FOR MODE 6 AND 7

\*\*\* SEQ 0017

```

(2) ;*TEST: 13 TEST JMP INSTRUCTION FOR MODE 6 AND 7
(3) ;*****
5324 002002 021527 000013 JMP6: CMP (R5),#13
5325 002002 001071 BNE ENDJP7 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5326 002006 005215 INC (R5)
5327 002010 012703 002040 MOV #15+6,R3
5328 002016 000163 177772 JMP -6(R3)
5330 002022 012745 000040 MOV #40,-(R5)
(2) 002026 005245 INC -(R5)
(2) 002030 000000 HALT ; JUMP INSTRUCTION FAILED
5331 002032 020327 002040 1$: CMP R3,#15+6 ; CHECK R3
5332 002036 001404 BEQ 2$
5333 002040 012745 000041 MOV #41,-(R5)
(2) 002044 005245 INC -(R5)
(2) 002046 000000 HALT ; WRONG VALUE IN REGISTER AFTER JUMP MODE 6
5334 ; OR JUMP INSTRUCTION FAILED IN MODE 6
5335 002050 000167 000010 2$: JMP 3$-4(PC) ; TEST JUMP INSTRUCTION MODE 6
5336 002054 012745 000042 MOV #42,-(R5)
(2) 002060 005245 INC -(R5)
(2) 002062 000000 HALT ; JUMP INSTRUCTION FAILED
5337 002064 012703 002104 3$: MOV #JMP7,R3 ; JUMP SHOULD LAND HERE
5338 002070 000163 000000 JMP 0(R3)
5339 002074 012745 000043 MOV #43,-(R5)
(2) 002100 005245 INC -(R5)
(2) 002102 000000 HALT ; JUMP INSTRUCTION FAILED
5340
5341 002104 012703 000440 JMP7: MOV #TEMP,R3
5342 002110 012713 002130 MOV #15,(R3)
5343 002114 000173 000000 JMP 2(R3)
5344 002120 012745 000044 MOV #44,-(R5)
(2) 002124 005245 INC -(R5)
(2) 002126 000000 HALT ; JUMP INSTRUCTION FAILED
5345 002130 012713 002154 1$: MOV #35,(R3) ; TEST JUMP INSTRUCTION MODE 7
5346 002134 012700 000434 MOV #TEMP-4,R0
5347 002140 000170 000004 JMP 24(R0)
5348 002144 012745 000045 MOV #45,-(R5)
(2) 002150 005245 INC -(R5)
(2) 002152 000000 HALT ; JUMP INSTRUCTION FAILED
5349 002154 012767 002202 176256 3$: MOV #JSRTST,TEMP ; CONTINUE
5350 002162 012700 000440 MOV #TEMP,R0
5351 002166 000170 000000 JMP 20(R0)
5352 002172
(2) 002172 012745 000046 ENDJP7: MOV #46,-(R5)
(2) 002176 005245 INC -(R5)
(2) 002200 000000 HALT ; JUMP ERROR OR SEQUENCE ERROR
  
```

# F02

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-15  
 DVKAAA.P11 T14 CHECK JSR AND MARK INSTRUCTIONS

\*\*\* SEQ 0018

```

5353          ;:*****
(2)          ;*TEST: 14   CHECK JSR AND MARK INSTRUCTIONS
(3)          ;:*****
5354
5355 002202   JSRTST:
(2) 002202 021527 000014      CMP      (R5),#14
5356 002206 001177          BNE     ENDJSR      ; IF IN WRONG SEQUENCE GO TO HALT AT THE END OF THE TEST
5357 002210 005215          INC     (R5)
5358 002212 012706 000530      MOV     #START,SP   ; SET UP STACK POINTER.
5359 002216 000277          SCC
5360 002220 004767 000026      JSR     PC,3$
5361 002224          1$:
(2) 002224 012745 000047      MOV     #47,-(R5)
(2) 002230 005245          INC     -(R5)
(2) 002232 000000          HALT
5362 002234 022706 000530      2$:    CMP     #START,SP   ; JSR INSTRUCTION FAILED
5363 002240 001441          BEQ     JSRM        ; HAS SP BEEN RESTORED?
5364 002242 012745 000050      MOV     #50,-(R5)
(2) 002246 005245          INC     -(R5)
(2) 002250 000000          HALT
5365 002252          3$:
(1) 002252 100003          BPL     4$
(1) 002254 001002          BNE     4$
(1) 002256 102001          BVC     4$
(1) 002260 103404          BCS     5$
(2) 002262          4$:
(3) 002262 012745 000051      MOV     #51,-(R5)
(3) 002266 005245          INC     -(R5)
(3) 002270 000000          HALT
5366 002272 022706 000526      5$:    CMP     #START-2,SP  ; WRONG CC
5367 002276 001404          BEQ     6$          ; WAS THE SP EFFECTED?
5368 002300 012745 000052      MOV     #52,-(R5)
(2) 002304 005245          INC     -(R5)
(2) 002306 000000          HALT
5369 002310 022716 002224      6$:    CMP     #1$, (SP)   ; WRONG SP AFTER EXECUTION OF JSR INSTRUCTION
5370 002314 001404          BEQ     7$          ; IS THE RETURN ADDRESS =1$
5371 002316 012745 000053      MOV     #53,-(R5)
(2) 002322 005245          INC     -(R5)
(2) 002324 000000          HALT
5372          7$:
5373 002326 012716 002234      MOV     #2$, (SP)   ; SP DID NOT HAVE CORRECT RETURN ADDRESS
5374 002332 000207          RTS     PC          ; AFTER EXECUTION OF JSR INSTRUCTION
5375 002334 012745 000054      MOV     #54,-(R5)   ; SET 2$ AS THE RETURN ADDRESS
(2) 002340 005245          INC     -(R5)
(2) 002342 000000          HALT
5376 002344 010546          JSRM:  MOV     R5,-(SP) ; RTS INSTRUCTION FAILED
5377 002346 016746 176064      MOV     DUMMY,-(SP) ; MOV R5 TO STACK
5378 002352 016746 176060      MOV     DUMMY,-(SP)
5379 002356 016746 176070      MOV     MARK2,-(SP) ; STORE MARK 2 ON THE STACK.
5380 002362 010503          MOV     R5,R3      ; SAVE R5 IN R3
5381 002364 004467 000130      JSR     R4,10$
5382 002370          1$:
(2) 002370 012745 000055      MOV     #55,-(R5)
(2) 002374 005245          INC     -(R5)
(2) 002376 000000          HALT
5383 002400          2$:

```



(1)	002400	100003		BPL	3\$	
(1)	002402	001002		BNE	3\$	
(1)	002404	102001		BVC	3\$	
(1)	002406	103404		BCS	4\$	
(2)	002410		3\$:			
(3)	002410	012743	000056	MOV	#56, -(R3)	
(3)	002414	005243		INC	-(R3)	
(3)	002416	000000		HALT		; WRONG CC
5384	002420	022705	000404	4\$:	CMP	#\$TESTN, R5
5385	002424	001404		BEQ	5\$	
5386	002426	012743	000057	MOV	#57, -(R3)	
(2)	002432	005243		INC	-(R3)	
(2)	002434	000000		HALT		; MARK INSTRUCTION FAILED
5387	002436	022706	000530	5\$:	CMP	#START, SP
5388	002442	001404		BEQ	6\$	
5389	002444	012745	000060	MOV	#60, -(R5)	
(2)	002450	005245		INC	-(R5)	
(2)	002452	000000		HALT		; MARK INSTRUCTION FAILED
5390	002454	012701	002562	6\$:	MOV	#12\$, R1
5391	002460	004011		JSR	RO, (R1)	; PLACE THE ADDRESS OF 12\$ IN R1
5392	002462			7\$:		; GO TO TAG 12\$
(2)	002462	012745	000061	MOV	#61, -(R5)	
(2)	002466	005245		INC	-(R5)	
(2)	002470	000000		HALT		; JSR INSTRUCTION FAILED
5393	002472	012745	000062	MOV	#62, -(R5)	
(2)	002476	005245		INC	-(R5)	
(2)	002500	000000		HALT		; RTS BROUGHT THE PROGRAM BACK IN WRONG
5394						; PLACE
5395	002502	022706	000530	8\$:	CMP	#START, SP
5396	002506	001443		BEQ	REGS	
5397	002510	012745	000063	MOV	#63, -(R5)	
(2)	002514	005245		INC	-(R5)	
(2)	002516	000000		HALT		; STACK POINTER WAS NOT RESET
5398						
5399	002520	020427	002370	10\$:	CMP	R4, #1\$
5400	002524	001404		BEQ	11\$	; IS THE RETURN ADDRESS =1\$ ?
5401	002526	012745	000064	MOV	#64, -(R5)	
(2)	002532	005245		INC	-(R5)	
(2)	002534	000000		HALT		; WRONG RETURN ADDRESS IN UNKAGE REGISTER R4
5402	002536	010605		11\$:	MOV	SP, R5
5403	002540	005725		TST	(R5)+	; SET UP ADDRESS IN R5 AT MARK 2 INSTRUCTION
5404	002542	012716	002400	MOV	#2\$, (SP)	; SET RETURN ADDRESS =2\$
5405	002546	000277		SCC		
5406	002550	000205		RTS	R5	; RETURN USING R5 AND IN-TURN USING MARK INSTRUCTION
5407	002552	012745	000065	MOV	#65, -(R5)	
(2)	002556	005245		INC	-(R5)	
(2)	002560	000000		HALT		; RTS INSTRUCTION FAILED
5408						
5409	002562	020027	002462	12\$:	CMP	RO, #7\$
5410						; DOES RO CONTAIN THE RETURN ADDRESS?
5411	002566	001404		BEQ	13\$	
5412	002570	012745	000066	MOV	#66, -(R5)	
(2)	002574	005245		INC	-(R5)	
(2)	002576	000000		HALT		; WRONG RETURN ADDRESS IN LINKAGE REGISTER RO
5413	002600	012700	002502	13\$:	MOV	#8\$, RO
5414	002604	000200		RTS	RO	; SET RETURN ADDRESS AT 8\$

```

5415 002606
(2) 002606 012745 000067
(2) 002612 005245
(2) 002614 000000
5416
5417
(2)
(3)
5418
5419 002616
(2) 002616 021527 000015
5420 002622 001034
5421 002624 005215
5422 002626 010667 175606
5423 002632 012700 000001
5424 002636 012701 000004
5425 002642 012702 000020
5426 002646 012703 000100
5427 002652 012704 000400
5428 002656 005006
5429 002660 060006
5430 002662 060106
5431 002664 060206
5432 002666 060306
5433 002670 060406
5434 002672 060506
5435 002674 022706 001131
5436 002700 001003
5437 002702 016706 175532
5438 002706 000406
5439 002710 016706 175524
5440 002714
(2) 002714 012745 000070
(2) 002720 005245
(2) 002722 000000

```

```

ENDJSR:
MOV #67, -(R5)
INC -(R5)
HALT ; RTS INSTRUCTION FAILED OR SEQUENCE ERROR

*****
*TEST: 15 CHECK REGISTER SELECTION
*****

REGS:
CMP (R5), #15
BNE EREGS ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOV R6, TEMP ; SAVE THE STACK POINTER
MOV #1, R0 ; LOAD THE REGISTERS
MOV #4, R1
MOV #20, R2
MOV #100, R3
MOV #400, R4
CLR R6
ADD R0, R6 ; ADD UP THE REGISTERS
ADD R1, R6
ADD R2, R6
ADD R3, R6
ADD R4, R6
ADD R5, R6
CMP #TESTN+525, R6 ; CHECK IT
BNE 1$ ; FAILED
MOV TEMP, R6 ; RESTORE STACK POINTER
BR TSTB0 ; CONTINUE
MOV TEMP, R6 ; RESTORE STACK POINTER

1$:
EREGS:
MOV #70, -(R5)
INC -(R5)
HALT ; REGISTER SELECTION FAILURE OR SEQUENCE ERROR

```



```

5445
5446
5447
5448
5449
(2)
(3)
5450
5451 002724
(2) 002724 021527 000016
5452 002730 001404
5453 002732 012745 000071
(2) 002736 005245
(2) 002740 000000
5454 002742 005215
5455 002744 000277
5456 002746 105000
5457 002750 004737 017164
5458 002754 105700
5459 002756 004737 017164
5460 002762 112701 000377
5461 002766 004737 017252
5462 002772 105701
5463 002774 004737 017252
5464
5465
5466
5467
(2)
(3)
5468
5469 003000
(2) 003000 021527 000017
5470 003004 001027
5471 003006 005215
5472 003010 000277
5473 003012 152702 000377
5474 003016 004737 017272
5475 003022 122702 000377
5476 003026 001404
5477 003030 012745 000072
(2) 003034 005245
(2) 003036 000000
5478 003040 112700 000077
5479 003044 120002
5480 003046 100004
5481 003050 012745 000073
(2) 003054 005245
(2) 003056 000000
5482 003060 120200
5483 003062 100404
5484 003064
(2) 003064 012745 000074
(2) 003070 005245
(2) 003072 000000
    
```

```

: CHECK BYTE INSTRUCTIONS, DESTINATION MODE 0 ONLY
:
: *****
: *TEST: 16 NEW INSTRUCTIONS USED IN THIS SECTION ARE TSTB, CLRB, MOVB
: *****
    
```

```

TSTB0:
      CMP      (R5), #16
      BEQ     2$ ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV     #71, -(R5)
      INC     -(R5)
      HALT
2$:   INC     (R5) ; PROGRAM IS IN WRONG SEQUENCE
      SCC
      CLRB   R0 ; CLEAR THE REGISTER
      JSR   PC, @#SCC4 ; CHECK FOR CC = 4
      TSTB  R0 ; CHECK IT
      JSR   PC, @#SCC4 ; CHECK FOR CC = 4
      MOVB  #377, R1 ; LOAD THE REGISTER
      JSR   PC, @#SCC10 ; CHECK FOR CC = 10
      TSTB  R1 ; CHECK IT
      JSR   PC, @#SCC10 ; CHECK FOR CC = 10
    
```

```

: *****
: *TEST: 17 NEW INSTRUCTIONS USED IN THIS SECTION ARE CMPB, BISB
: *****
    
```

```

CMPB0:
1$:  CMP      (R5), #17
      BNE     ECMPB0 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
      INC     (R5)
      SCC
      BISB   #377, R2 ; LOAD REGISTER
      JSR   PC, @#SCC11 ; CHECK FOR CC = 11
      CMPB  #377, R2 ; CHECK COMPARE
      BEQ     2$ ; CONTINUE IF OK
      MOV     #72, -(R5)
      INC     -(R5)
      HALT
2$:  MOVB   #77, R0 ; BISB OR CMPB INSTRUCTION FAILED
      CMPB  R0, R2 ; CHECK IT AGAIN
      BPL   3$ ; CONTINUE IF OK
      MOV     #73, -(R5)
      INC     -(R5)
      HALT
3$:  CMPB   R2, R0 ; CMPB INSTRUCTION FAILED [WRONG CC]
      BMI   ECMPB0 ; ONCE MORE
      BICB0
ECMPB0:
      MOV     #74, -(R5)
      INC     -(R5)
      HALT ; WRONG CC OR WRONG SEQUENCE
    
```

```

5485 (2) (3)
5486
5487 003074 021527 000020
5488 003100 001404
5489 003102 012745 000075
5490 003112 005245
5491 003114 112703 000377
5492 003120 112700 000252
5493 003124 000277
5494 003126 140003
5495 003130 004737 017102
5496 003134 130003
5497 003136 001404
5498 003140 012745 000076
5499 003150 132703 000125
5500 003154 004737 017102
5501 003160 150003
5502 003162 100404
5503 003164 012745 000077
5504 003174 142703 000177
5505 003200 004737 017272
5506 003204 132703 000377
5507 003210 004737 017272
5508
5509
5510
5511 (2) (3)
5512
5513 003214 021527 000021
5514 003220 001404
5515 003222 012745 000100
5516 003232 005245
5517 003234 112704 000177
5518 003240 000261
5519 003242 105204
5520 003244 004737 017334
5521 003250 112704 000376
5522 003254 105204
5523 003256 004737 017272
5524 003262 105204
5525 003264 004737 017206
5526 003270 105204
    
```

```

*****
*TEST: 20 NEW INSTRUCTIONS USED IN THIS SECTION ARE BICB, BITB
*****
BICB0:
CMP (R5), #20
BEQ 2$ ; IF IN WRONG SEQUENCE GO TO HLT BELOW
MOV #75, -(R5)
INC -(R5)
HALT ; PROGRAM IS IN WRONG SEQUENCE
2$: INC (R5)
MOVB #377, R3 ; LOAD REGISTER
MOVB #252, R0 ; PLACE #252 IN R0
SCC
BICB R0, R3 ; CLEAR EVERY OTHER BIT
JSR PC, @#SCC1 ; CHECK FOR CC = 1
BITB R0, R3 ; CHECK IT
BEQ 4$ ; CONTINUE IF OK
MOV #76, -(R5)
INC -(R5)
HALT ; BICB OR BITB INSTRUCTION FAILED
4$: BITB #125, R3 ; CHECK IT
JSR PC, @#SCC1 ; CHECK FOR CC = 1
BISB R0, R3 ; SET THE BITS THAT WERE CLEARED
BMI 6$
MOV #77, -(R5)
INC -(R5)
HALT ; BISB INSTRUCTION FAILED
6$: BICB #177, R3 ; CLEAR ALL THE BITS EXCEPT FOR SIGN
JSR PC, @#SCC11 ; CHECK FOR CC = 11
BITB #377, R3 ; CHECK IT
JSR PC, @#SCC11 ; CHECK FOR CC = 11
    
```

```

*****
*TEST: 21 NEW INSTRUCTIONS USED IN THIS SECTION ARE INCB, DECB
*****
INCBO:
CMP (R5), #21
BEQ 1$ ; IF IN WRONG SEQUENCE GO TO HLT
MOV #100, -(R5)
INC -(R5)
HALT ; PROGRAM IS IN WRONG SEQUENCE
1$: INC (R5)
MOVB #177, R4 ; R4 = 177
SEC
INCB R4 ; ADD ONES INTO REG. 4
JSR PC, @#SCC13 ; CHECK FOR CC = 13
MOVB #376, R4
INCB R4
JSR PC, @#SCC11 ; CHECK FOR CC = 11
INCB R4
JSR PC, @#SCC5 ; CHECK FOR CC = 5
INCB R4
    
```



# K02

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-20  
 DVKAAA.P11 T21 NEW INSTRUCTIONS USED IN THIS SECTION ARE INCB, DECB

\*\*\* SEQ 0023

5527	003272	004737	017102		JSR	PC, @#SCC1	; CHECK FOR CC = 1
5528	003276	122704	000001		CMPB	#1, R4	; CHECK IT
5529	003302	001404			BEG	2\$	; CONTINUE IF OK
5530	003304	012745	000101		MOV	#101, -(R5)	
(2)	003310	005245			INC	-(R5)	
(2)	003312	000000			HALT		; INCB INSTRUCTION FAILED
5531	003314	000261		2\$:	SEC		
5532	003316	105304			DECB	R4	; SUBTRACT ONES FROM REG. 4
5533	003320	004737	017206		JSR	PC, @#SCC5	; CHECK FOR CC = 5
5534	003324	105304			DECB	R4	
5535	003326	004737	017272		JSR	PC, @#SCC11	; CHECK FOR CC = 11
5536	003332	012704	000200		MOV	#200, R4	
5537	003336	105304			DECB	R4	
5538	003340	004737	017142		JSR	PC, @#SCC3	; CHECK FOR CC = 3
5539	003344	105304			DECB	R4	
5540	003346	004737	017102		JSR	PC, @#SCC1	; CHECK FOR CC = 1

5541 (2) (3) ;\*\*\*\*\*  
; \*TEST: 22 NEW INSTRUCTION IN THIS SECTION IS COMB  
;\*\*\*\*\*

5542 5543 003352 COMBO: CMP (R5), #22  
(2) 003352 021527 000022 BEQ 1\$ ; IF IN WRONG SEQUENCE GO TO HLT  
5544 003356 001404 MOV #102, -(R5)  
5545 003360 012745 000102 INC -(R5)  
(2) 003364 005245 ; PROGRAM IS IN WRONG SEQUENCE  
(2) 003366 000000 HALT  
5546 003370 005215 1\$: INC (R5) ; LOAD EVERY OTHER BIT  
5547 003372 112703 000252 MOVB #252, R3  
5548 003376 000277 SCC ; 1'S COMPLEMENT  
5549 003400 105103 COMB R3 ; CHECK FOR CC = 1  
5550 003402 004737 017102 JSR PC, @#SCC1 ; CHECK IT  
5551 003406 122703 000125 CMPB #125, R3 ; CONTINUE IF OK  
5552 003412 001404 BEQ 2\$  
5553 003414 012745 000103 MOV #103, -(R5)  
(2) 003420 005245 INC -(R5)  
(2) 003422 000000 HALT ; COMB INSTRUCTION FAILED  
5554 003424 000277 2\$: SCC ; COMPLEMENT BACK  
5555 003426 105103 COMB R3 ; CHECK FOR CC = 11  
5556 003430 004737 017272 JSR PC, @#SCC11 ; CHECK IT  
5557 003434 122703 000252 CMPB #252, R3 ; CONTINUE IF OK  
5558 003440 001404 BEQ 3\$  
5559 003442 012745 000104 MOV #104, -(R5)  
(2) 003446 005245 INC -(R5)  
(2) 003450 000000 HALT ; COMB INSTRUCTION FAILED  
5560 003452 012703 000377 3\$: MOV #377, R3  
5561 003456 000277 SCC  
5562 003460 105103 COMB R3  
5563 003462 004737 017206 JSR PC, @#SCC5 ; CHECK FOR CC = 5

5564 5565 5566 5567 ;\*\*\*\*\*

(2) ; \*TEST: 23 NEW INSTRUCTION IN THIS SECTION IS NEGB  
(3) ;\*\*\*\*\*

5568 5569 003466 NEGBO: CMP (R5), #23  
(2) 003466 021527 000023 BNE ENEGBO ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST  
5570 003472 001025 1\$: INC (R5)  
5571 003474 005215 MOVB #1, R0 ; LOAD THE REGISTER  
5572 003476 112700 000001 NEGB R0 ; 2'S COMPLEMENT  
5573 003502 105400 JSR PC, @#SCC11 ; CHECK FOR CC = 11  
5574 003504 004737 017272 CMPB #377, R0 ; CHECK IT  
5575 003510 122700 000377 BEQ 2\$ ; CONTINUE IF OK  
5576 003514 001404 MOV #105, -(R5)  
5577 003516 012745 000105 INC -(R5)  
(2) 003522 005245 HALT ; NEGB INSTRUCTION FAILED  
(2) 003524 000000 2\$: MOV #200, R0  
5578 003526 012700 000200 NEGB R0 ; 2'S COMPLEMENT  
5579 003532 105400 JSR PC, @#SCC13 ; CHECK FOR CC = 13  
5580 003534 004737 017334 CMPB #200, R0 ; CHECK IT  
5581 003540 122700 000200 BEQ ROLBO ; CONTINUE IF OK  
5582 003544 001404



M02

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-22  
DVKAAA.P11 T23 NEW INSTRUCTION IN THIS SECTION IS NEGB

\*\*\* SEQ 0025

5583 003546  
(2) 003546 012745 000106  
(2) 003552 005245  
(2) 003554 000000

ENEGBO: MOV #106, -(R5)  
INC -(R5)  
HALT

; WRONG RESULT IN R0 OR WRONG SEQUENCE

```

5584
(2)
(3)
5585
5586 003556 021527 000024
(2) 003556 001026
5587 003562 005215
5588 003564 112701 000040
5589 003566 000257
5590 003572 106101
5591 003574 106101
5592 003576 004737 017314
5593 003600 122701 000200
5594 003604 001404
5595 003610 012745 000107
5596 003612 005245
(2) 003616 000000
(2) 003620 106101
5597 003622 004737 017230
5598 003624 106101
5599 003630 122701 000001
5600 003632 001404
5601 003636 012745 000110
5602 003640 005245
(2) 003644 000000
(2) 003646
5603
5604
5605
(2)
(3)
5606
5607 003650 021527 000025
(2) 003650 001026
5608 003654 005215
5609 003656 112702 000004
5610 003660 000257
5611 003664 106002
5612 003666 106002
5613 003670 122702 000001
5614 003672 001404
5615 003676 012745 000111
5616 003700 005245
(2) 003704 000000
(2) 003706 106002
5617 003710 004737 017230
5618 003712 106002
5619 003716 004737 017314
5620 003720 122702 000200
5621 003724 001404
5622 003730 012745 000112
5623 003732 005245
(2) 003736 000000
(2) 003740

```

```

*****
*TEST: 24 NEW INSTRUCTION IN THIS SECTION IS ROLB
*****

```

```

ROLBO:
CMP (R5), #24
BNE EROLBO ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOVB #40, R1 ; LOAD REGISTER
CCC ; CLEAR FLAGS
ROLB R1 ; SHIFT
ROLB R1
JSR PC, @#SCC12 ; CHECK FOR CC = 12
CMPB #200, R1 ; CHECK IT
BEQ 1$ ; CONTINUE IF OK
MOV #107, -(R5)
INC -(R5)
HALT ; ROLB INSTRUCTION FAILED

1$:
ROLB R1 ; SHIFT
JSR PC, @#SCC7 ; CHECK FOR CC = 7
ROLB R1 ; SHIFT
CMPB #1, R1 ; CHECK IT
BEQ RORBO ; CONTINUE IF OK

EROLBO:
MOV #110, -(R5)
INC -(R5)
HALT ; WRONG RESULT IN R1 OR WRONG SEQUENCE

```

```

*****
*TEST: 25 NEW INSTRUCTION IN THIS SECTION IS RORB
*****

```

```

RORBO:
CMP (R5), #25
BNE ERORBO ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOVB #4, R2 ; LOAD REGISTER
CCC ; CLEAR FLAGS
RORB R2 ; SHIFT
RORB R2
CMPB #1, R2 ; CHECK IT
BEQ 1$ ; CONTINUE IF OK
MOV #111, -(R5)
INC -(R5)
HALT ; RORB INSTRUCTION FAILED

1$:
RORB R2 ; SHIFT
JSR PC, @#SCC7 ; CHECK FOR CC = 7
RORB R2 ; SHIFT
JSR PC, @#SCC12 ; CHECK FOR CC = 12
CMPB #200, R2 ; CHECK IT
BEQ ASLBO ; CONTINUE IF OK

ERORBO:
MOV #112, -(R5)
INC -(R5)
HALT

```



```

5624
(2)
(3)
5625
5626 003742
(2) 003742 021527 000026
5627 003746 001404
5628 003750 012745 000113
(2) 003754 005245
(2) 003756 000000
5629 003760 005215
5630 003762 112703 000040
5631 003766 000257
5632 003770 106303
5633 003772 106303
5634 003774 004737 017314
5635 004000 122703 000200
5636 004004 001404
5637 004006 012745 000114
(2) 004012 005245
(2) 004014 000000
5638 004016 106303
5639 004020 004737 017230
5640 004024 106303
5641 004026 004737 017164
5642
5643
(2)
(3)
5644
5645 004032
(2) 004032 021527 000027
5646 004036 001034
5647 004040 005215
5648 004042 112704 000004
5649 004046 000257
5650 004050 106204
5651 004052 106204
5652 004054 122704 000001
5653 004060 001404
5654 004062 012745 000115
(2) 004066 005245
(2) 004070 000000
5655 004072 106204
5656 004074 004737 017230
5657 004100 106204
5658 004102 004737 017164
5659 004106 112703 000202
5660 004112 106203
5661 004114 106203
5662 004116 004737 017272
5663 004122 122703 000340
5664 004126 001404
5665 004130
(2) 004130 012745 000116
(2) 004134 005245

```

```

*****
:TEST: 26 NEW INSTRUCTION IN THIS SECTION IS ASLB
*****

```

```

ASLBO:
      CMP      (R5),#26
      BEQ      2$          ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV      #113,-(R5)
      INC      -(R5)
      HALT     ; PROGRAM IS IN WRONG SEQUENCE
2$:   INC      (R5)
      MOV      #40,R3      ; LOAD REGISTER
      CCC      ; CLEAR FLAGS
      ASLB     R3          ; SHIFT
      ASLB     R3
      JSR      PC,@#SCC12 ; CHECK FOR CC = 12
      CMP      #200,R3    ; CHECK IT
      BEQ      4$          ; CONTINUE IF OK
      MOV      #114,-(R5)
      INC      -(R5)
      HALT     ; ASLB INSTRUCTION FAILED
4$:   ASLB     R3          ; SHIFT
      JSR      PC,@#SCC7  ; CHECK FOR CC = 7
      ASLB     R3          ; SHIFT
      JSR      PC,@#SCC4  ; CHECK FOR CC = 4

```

```

*****
:TEST: 27 NEW INSTRUCTION IN THIS SECTION IS ASRB
*****

```

```

ASRBO:
      CMP      (R5),#27
      BNE     EASRBO      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
1$:   INC      (R5)
      MOV      #4,R4      ; LOAD REGISTER
      CCC      ; CLEAR FLAGS
      ASRB     R4          ; SHIFT
      ASRB     R4
      CMP      #1,R4      ; CHECK IT
      BEQ      2$          ; CONTINUE IF OK
      MOV      #115,-(R5)
      INC      -(R5)
      HALT     ; ASRB INSTRUCTION FAILED
2$:   ASRB     R4          ; SHIFT
      JSR      PC,@#SCC7  ; CHECK FOR CC = 7
      ASRB     R4          ; SHIFT
      JSR      PC,@#SCC4  ; CHECK FOR CC = 4
      MOV      #202,R3    ; LOAD REGISTER
      ASRB     R3          ; SHIFT
      ASRB     R3
      JSR      PC,@#SCC11 ; CHECK FOR CC = 11
      CMP      #340,R3    ; CHECK IT
      BEQ      ADCB0      ; CONTINUE IF OK
EASRBO:
      MOV      #116,-(R5)
      INC      -(R5)

```

C03

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-25  
DVKAAA.P11 T27 NEW INSTRUCTION IN THIS SECTION IS ASRB

\*\*\* SEQ 0028

(2) 004136 000000

HALT



5666 (2) (3) \*\*\*\*\*  
: \*TEST: 30 NEW INSTRUCTION IN THIS SECTION IS ADCB  
: \*\*\*\*\*

5667				ADCB0:		
5668	004140	021527	000030		CMP	(R5), #30
5669	(2) 004140	001404			BEG	2\$ ; IF IN WRONG SEQUENCE GO TO HLT BELOW
5670	004146	012745	000117		MOV	#117, -(R5)
(2)	004152	005245			INC	-(R5)
(2)	004154	000000			HALT	; PROGRAM IS IN WRONG SEQUENCE
5671	004156	005215		2\$:	INC	(R5)
5672	004160	105000			CLRB	RO ; CLEAR THE REGISTER
5673	004162	000257			CCC	; CLEAR FLAGS
5674	004164	105500			ADCB	RO ; ADD C BIT = 0
5675	004166	004737	017164		JSR	PC, @#SCC4 ; CHECK FOR CC = 4
5676	004172	000261			SEC	C=1
5677	004174	105500			ADCB	RO ; ADD C BIT=1
5678	004176	000261			SEC	C=1
5679	004200	105500			ADCB	RO ; AGAIN
5680	004202	004737	017062		JSR	PC, @#SCC0 ; CHECK FOR CC = 0
5681	004206	122700	000002		CMPB	#2, RO ; CHECK IT
5682	004212	001404			BEG	4\$ ; CONTINUE IF OK
5683	004214	012745	000120		MOV	#120, -(R5)
(2)	004220	005245			INC	-(R5)
(2)	004222	000000			HALT	; ADCB INSTRUCTION FAILED
5684	004224	112700	000177	4\$:	MOVB	#177, RO ; LOAD LARGEST POSITIVE NUMBER
5685	004230	000261			SEC	C=1
5686	004232	105500			ADCB	RO ; ADD C BIT=1
5687	004234	004737	017314		JSR	PC, @#SCC12 ; CHECK FOR CC = 12
5688	004240	122700	000200		CMPB	#200, RO ; CHECK IT
5689	004244	001404			BEG	6\$ ; CONTINUE IF OK
5690	004246	012745	000121		MOV	#121, -(R5)
(2)	004252	005245			INC	-(R5)
(2)	004254	000000			HALT	; ADCB INSTRUCTION FAILED
5691	004256	112700	000377	6\$:	MOVB	#377, RO ; LOAD -1
5692	004262	000261			SEC	C=1
5693	004264	105500			ADCB	RO ; ADD C BIT=1
5694	004266	004737	017206		JSR	PC, @#SCC5 ; CHECK FOR CC = 5

5695  
5696  
5697  
5698 (2) (3) \*\*\*\*\*  
: \*TEST: 31 NEW INSTRUCTION IN THIS SECTION IS SBCB  
: \*\*\*\*\*

5699				SBCB0:		
5700	004272	021527	000031		CMP	(R5), #31
(2)	004272	001404			BEG	1\$ ; IF IN WRONG SEQUENCE GO TO HLT BELOW
5701	004276	001404			MOV	#122, -(R5)
5702	004300	012745	000122		INC	-(R5)
(2)	004304	005245			HALT	; PROGRAM IS IN WRONG SEQUENCE
(2)	004306	000000			INC	(R5) ; TEST IS IN WRONG SEQUENCE
5703	004310	005215		1\$:	MOVB	#3, R1 ; LOAD REGISTER
5704	004312	112701	000003		CCC	; CLEAR FLAGS
5705	004316	000257			SBCB	R1 ; SUBTRACT C BIT=0
5706	004320	105601			JSR	PC, @#SCC0 ; CHECK FOR CC = 0
5707	004322	004737	017062			

## E03

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-27  
 DVKAAA.P11 T31 NEW INSTRUCTION IN THIS SECTION IS SBCB

\*\*\* SEQ 0030

5708	004326	122701	000003	CMPB	#3,R1	:	CHECK IT
5709	004332	001404		BEQ	2\$	:	CONTINUE IF OK
5710	004334	012745	000123	MOV	#123,-(R5)		
(2)	004340	005245		INC	-(R5)		
(2)	004342	000000		HALT		:	SBCB INSTRUCTION FAILED
5711	004344	000261		SEC		:	C=1
5712	004346	105601		SBCB	R1	:	SUBTRACT C BIT=1
5713	004350	000261		SEC		:	C=1
5714	004352	105601		SBCB	R1	:	
5715	004354	004737	017062	JSR	PC,@#SCC0	:	CHECK FOR CC = 0
5716	004360	122701	000001	CMPB	#1,R1	:	CHECK IT
5717	004364	001404		BEQ	3\$	:	CONTINUE IF OK
5718	004366	012745	000124	MOV	#124,-(R5)		
(2)	004372	005245		INC	-(R5)		
(2)	004374	000000		HALT		:	SBCB INSTRUCTION FAILED
5719	004376	000261		SEC		:	C=1
5720	004400	105601		SBCB	R1	:	SUBTRACT C BIT=1
5721	004402	004737	017164	JSR	PC,@#SCC4	:	CHECK FOR CC = 4
5722	004406	000261		SEC		:	C=1
5723	004410	105601		SBCB	R1	:	SUBTRACT C BIT = 1
5724	004412	004737	017272	JSR	PC,@#SCC11	:	CHECK FOR CC = 11
5725	004416	122701	000377	CMPB	#377,R1	:	CHECK IT
5726	004422	001404		BEQ	4\$	:	CONTINUE IF OK
5727	004424	012745	000125	MOV	#125,-(R5)		
(2)	004430	005245		INC	-(R5)		
(2)	004432	000000		HALT		:	SBCB INSTRUCTION FAILED
5728	004434	112701	000200	MOVB	#200,R1	:	LOAD R1
5729	004440	000261		SEC		:	C=1
5730	004442	105601		SBCB	R1	:	SUBTRACT C BIT = 1
5731	004444	004737	017122	JSR	PC,@#SCC2	:	CHECK FOR CC = 2



# F03

DVKAAR MACY11 27(732) 25-AUG-76 13:25 PAGE 54-28  
 DVKAAR.P11 CHECK WORD INSTRUCTIONS, DESTINATION MODE 0 ONLY

\*\*\* SEQ 0031

```

5736
5737
5738
5739
5740
5741
5742
5743
5744
(2)
(3)
5745
5746 004450 021527 000032
(2) 004450 001404
5747 004454 012745 000126
5748 004456 005245
(2) 004462 000000
(2) 004464 005215
5749 004466 000277
5750 004470 005000
5751 004472 004737 017164
5752 004474 005700
5753 004500 004737 017164
5754 004502 012704 177777
5755 004506 010401
5756 004512 004737 017252
5757 004514 005701
5758 004520 004737 017252
5759 004522 020401
5760 004526 001404
5761 004530 012745 000127
5762 004532 005245
(2) 004536 000000
(2) 004540 000263
5763 004542 010000
5764 004544 004767 012434
5765 004546
5766
5767
5768
5769
(2)
(3)
5770
5771 004552 021527 000033
(2) 004552 001026
5772 004556 005215
5773 004560 012700 177777
5774 004562 050002
5775 004566 004737 017252
5776 004570 020002
5777 004574 001404
5778 004576 012745 000130
(2) 004600 005245
(2) 004604 000000
  
```

: CHECK WORD INSTRUCTIONS, DESTINATION MODE 0 ONLY  
 -----

```

:*****
: *TEST: 32 NEW INSTRUCTIONS USED IN THIS SECTION ARE TST, CLR, MOV
:*****
  
```

```

TST0:
      CMP      (R5), #32
      BEQ      1$ ; IF IN WRONG SEQUENCE GO TO HLT
      MOV      #126, -(R5)
      INC      -(R5)
      HALT
1$:   INC      (R5) ; TEST IS IN WRONG SEQUENCE
      SCC
      CLR      R0 ; CLEAR THE REGISTER
      JSR      PC, @#SCC4 ; CHECK FOR CC = 4
      TST      R0 ; CHECK IT
      JSR      PC, @#SCC4 ; CHECK FOR CC = 4
      MOV      #177777, R4 ; LOAD THE REGISTER
      MOV      R4, R1
      JSR      PC, @#SCC10 ; CHECK FOR CC = 10
      TST      R1 ; CHECK IT
      JSR      PC, @#SCC10 ; CHECK FOR CC = 10
      CMP      R4, R1 ; CHECK R1 TO CONTAIN PROPER DATA
      BEQ      2$
      MOV      #127, -(R5)
      INC      -(R5)
      HALT
2$:   SEVC
      MOV      R0, R0 ; SET V & C BITS
      JSR      PC, SCC5
  
```

```

:*****
: *TEST: 33 NEW INSTRUCTIONS USED IN THIS SECTION ARE CMP, BIS
:*****
  
```

```

CMPO:
1$:   CMP      (R5), #33
      BNE      ECMP0 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
      INC      (R5)
      MOV      #177777, R0 ; LOAD REGISTER
      BIS      R0, R2 ; CHECK THE BIS INSTRUCTION
      JSR      PC, @#SCC10 ; CHECK FOR CC = 10
      CMP      R0, R2 ; CHECK COMPARE
      BEQ      2$ ; CONTINUE IF OK
      MOV      #130, -(R5)
      INC      -(R5)
      HALT
      ; BIS OR CMP INSTRUCTION FAILED
  
```

# G03

DVKAAA MACY11 27(732)  
DVKAAA.P11 T33

25-AUG-76 13:25 PAGE 54-29  
NEW INSTRUCTIONS USED IN THIS SECTION ARE CMP, BIS

\*\*\* SEQ 0032

5780	004610	022702	000077	2\$:	CMP	#77,R2	:	CHECK IT AGAIN
5781	004614	100004			BPL	3\$	:	CONTINUE IF OK
5782	004616	012745	000131		MOV	#131, -(R5)		
(2)	004622	005245			INC	-(R5)		
(2)	004624	000000			HALT			
5783	004626	020227	000077	3\$:	CMP	R2, #77	:	CMP INSTRUCTION FAILED [WRONG CC]
5784	004632	100404			BMI	BIC0	:	ONCE MORE
5785	004634			ECMPO:			:	CONTINUE IF OK
(2)	004634	012745	000132		MOV	#132, -(R5)		
(2)	004640	005245			INC	-(R5)		
(2)	004642	000000			HALT		:	WRONG CC OR WRONG SEQUENCE



# H03

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-30  
DVKAAA.P11 T34 NEW INSTRUCTIONS USED IN THIS SECTION ARE BIC, BIT

\*\*\* SEQ 0033

```
5786          ;*****  
(2)          ;*TEST: 34   NEW INSTRUCTIONS USED IN THIS SECTION ARE BIC, BIT  
(3)          ;*****  
5787  
5788 004644    BICO:      CMP      (R5), #34  
(2) 004644    021527    000034    BNE      EBICO      ; IF IN WRONG SEQUENCE GO TO HLT ABOVE  
5789 004650    001053    INC      (R5)  
5790 004652    005215    MOV     #177777, R3    ; LOAD REGISTER  
5791 004654    012703    177777    MOV     #TEMP, R0     ; PLACE THE ADDRESS OF LOCATION TEMP IN R0  
5792 004660    012700    000440    MOV     #125252, (R0) ; SET (R0)  
5793 004664    012710    125252    SCC  
5794 004670    000277    BIC     (R0), R3     ; CLEAR EVERY OTHER BIT  
5795 004672    041003    JSR     PC, @#SCC1   ; CHECK FOR CC = 1  
5796 004674    004737    017102    BIT     (R0), R3     ; CHECK IT  
5797 004700    031003    BEQ     1$           ; CONTINUE IF OK  
5798 004702    001404    MOV     #133, -(R5)  
5799 004704    012745    000133    INC     -(R5)  
(2) 004710    005245    HALT  
(2) 004712    000000    ; BIC OR BIT INSTRUCTION FAILED  
5800 004714    032703    052525    1$:      BIT     #52525, R3    ; CHECK IT  
5801 004720    004737    017102    JSR     PC, @#SCC1   ; CHECK FOR CC = 1  
5802 004724    052703    125252    BIS     #125252, R3  ; SET THE BITS THAT WERE CLEARED  
5803 004730    100404    BMI     2$           ; CONTINUE IF OK  
5804 004732    012745    000134    MOV     #134, -(R5)  
(2) 004736    005245    INC     -(R5)  
(2) 004740    000000    HALT  
5805 004742    042703    077777    2$:      BIC     #77777, R3   ; BIT OR BIS INSTRUCTION FAILED  
5806 004746    004737    017272    JSR     PC, @#SCC11  ; CLEAR ALL THE BITS EXCEPT FOR SIGN  
5807 004752    012700    177777    MOV     #177777, R0  ; CHECK FOR CC = 11  
5808 004756    030003    BIT     R0, R3       ; CHECK IT  
5809 004760    004737    017272    JSR     PC, @#SCC11  ; CHECK FOR CC = 11  
5810 004764    000263    SEVC  
5811 004766    040000    BIC     R0, R0       ; SET V & C BITS  
5812 004770    004737    017206    JSR     PC, @#SCC5   ; CHECK CC = 5  
5813 004774    005700    TST     R0           ; CHECK R0 TO CONTAIN 0  
5814 004776    001404    BEQ     INCO  
5815 005000    EBICO:     MOV     #135, -(R5)  
(2) 005000    012745    000135    INC     -(R5)  
(2) 005004    005245    HALT  
(2) 005006    000000    ; BIC FAILED OR SEQUENCE ERROR  
5816  
5817  
5818  
5819          ;*****  
(2)          ;*TEST: 35   NEW INSTRUCTIONS USED IN THIS SECTION ARE INC, DEC  
(3)          ;*****  
5820  
5821 005010    INCO:     CMP     (R5), #35  
(2) 005010    021527    000035    BEQ     2$           ; IF IN WRONG SEQUENCE GO TO HLT BELOW  
5822 005014    001404    MOV     #136, -(R5)  
5823 005016    012745    000136    INC     -(R5)  
(2) 005022    005245    HALT  
(2) 005024    000000    ; PROGRAM IS IN WRONG SEQUENCE  
5824 005026    005215    2$:      INC     (R5)  
5825 005030    012704    077777    MOV     #77777, R4   ; R4=77777  
5826 005034    000261    SEC
```

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-31  
 DVKAAA.P11 T35 NEW INSTRUCTIONS USED IN THIS SECTION ARE INC, DEC

\*\*\* SEQ 0034

5827	005036	005204		INC	R4	; ADD ONES INTO REG. 4
5828	005040	004737	017334	JSR	PC,@#5CC13	; CHECK FOR CC = 13
5829	005044	012704	177776	MOV	#177776,R4	
5830	005050	005204		INC	R4	
5831	005052	004737	017272	JSR	PC,@#5CC11	; CHECK FOR CC = 11
5832	005056	005204		INC	R4	
5833	005060	004737	017206	JSR	PC,@#5CC5	; CHECK FOR CC = 5
5834	005064	005204		INC	R4	
5835	005066	004737	017102	JSR	PC,@#5CC1	; CHECK FOR CC = 1
5836	005072	022704	000001	CMP	#1,R4	; CHECK IT
5837	005076	001404		BEQ	4\$	; FAILED
5838	005100	012745	000137	MOV	#137,-(R5)	
(2)	005104	005245		INC	-(R5)	
(2)	005106	000000		HALT		; INC INSTRUCTION FAILED
5839	005110	000261		SEC		
5840	005112	005304		DEC	R4	; SUBTRACT ONES FROM REG. 4
5841	005114	004737	017206	JSR	PC,@#5CC5	; CHECK FOR CC = 5
5842	005120	005304		DEC	R4	
5843	005122	004737	017272	JSR	PC,@#5CC11	; CHECK FOR CC = 11
5844	005126	012704	100000	MOV	#100000,R4	
5845	005132	005304		DEC	R4	
5846	005134	004737	017142	JSR	PC,@#5CC3	; CHECK FOR CC = 3
5847	005140	005304		DEC	R4	
5848	005142	004737	017102	JSR	PC,@#5CC1	; CHECK FOR CC = 1

4\$:



# J03

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-32  
 DVKAAA.P11 T36 NEW INSTRUCTION IN THIS SECTION IS COM

\*\*\* SEQ 0035

```

5849          ;*****
(2)          ;*TEST: 36      NEW INSTRUCTION IN THIS SECTION IS COM
(3)          ;*****
5850
  
```

```

5851 005146 021527 000036      COMD:      CMP      (R5), #36
(2) 005146 001404 000036      1$:      BEQ      1$          ; IF IN WRONG SEQUENCE GO TO HLT BELOW
5852 005152 012745 000140      MOV      #140, -(R5)
5853 005160 005245 000140      INC      -(R5)
(2) 005162 000000 000140      HALT     ; TEST IS IN WRONG SEQUENCE
5854 005164 005215 125252      1$:      INC      (R5)          ; LOAD EVERY OTHER BIT
5855 005166 012703 125252      MOV      #125252, R3
5856 005172 000277 000140      SCC
5857 005174 005103 000140      COM      R3          ; 1'S COMPLEMENT
5858 005176 004737 017102      JSR      PC, @#SCC1  ; CHECK FOR CC = 1
5859 005202 022703 052525      CMP      #52525, R3 ; CHECK IT
5860 005206 001404 000141      BEQ      2$          ; CONTINUE IF OK
5861 005210 012745 000141      MOV      #141, -(R5)
(2) 005214 005245 000141      INC      -(R5)
(2) 005216 000000 000141      HALT     ; COM INSTRUCTION FAILED
5862 005220 000277 000141      2$:      SCC
5863 005222 005103 000141      COM      R3          ; COMPLEMENT BACK
5864 005224 004737 017272      JSR      PC, @#SCC11 ; CHECK FOR CC = 11
5865 005230 022703 125252      CMP      #125252, R3 ; CHECK IT
5866 005234 001404 000142      BEQ      3$          ; CONTINUE IF OK
5867 005236 012745 000142      MOV      #142, -(R5)
(2) 005242 005245 000142      INC      -(R5)
(2) 005244 000000 000142      HALT     ; COM INSTRUCTION FAILED
5868 005246 012703 177777      3$:      MOV      #177777, R3
5869 005252 000277 000142      SCC
5870 005254 005103 017206      COM      R3
5871 005256 004737 017206      JSR      PC, @#SCC5  ; CHECK FOR CC = 5
5872
5873
5874
5875
  
```

```

(2)          ;*****
(3)          ;*TEST: 37      NEW INSTRUCTION IN THIS SECTION IS NEG
5876          ;*****
  
```

```

5877 005262 021527 000037      NEGO:     CMP      (R5), #37
(2) 005262 001025 000037      1$:      BNE      ENEG0      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5878 005266 005215 000001      INC      (R5)
5879 005270 012700 000001      MOV      #1, R0      ; LOAD THE REGISTER
5880 005272 005400 000001      NEG      R0          ; 2'S COMPLEMENT
5881 005276 004737 017272      JSR      PC, @#SCC11 ; CHECK FOR CC = 11
5882 005300 022700 177777      CMP      #177777, R0 ; CHECK IT
5883 005304 001404 000143      BEQ      2$          ; CONTINUE IF OK
5884 005310 012745 000143      MOV      #143, -(R5)
(2) 005316 005245 000143      INC      -(R5)
(2) 005320 000000 000143      HALT     ; NEG INSTRUCTION FAILED
5886 005322 012700 100000      2$:      MOV      #100000, R0
5887 005326 005400 000143      NEG      R0          ; 2'S COMPLEMENT
5888 005330 004737 017334      JSR      PC, @#SCC13 ; CHECK FOR CC = 13
5889 005334 022700 100000      CMP      #100000, R0 ; CHECK IT
5890 005340 001404 000143      BEQ      ROLO        ; CONTINUE IF OK
  
```

K03

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-33  
DVKAAA.P11 T37 NEW INSTRUCTION IN THIS SECTION IS NEG

\*\*\* SEQ 0036

5891	005342			ENEGO:		
(2)	005342	012745	000144		MOV	#144, -(R5)
(2)	005346	005245			INC	-(R5)
(2)	005350	000000			HALT	

; WRONG RESULT IN R0 OR WRONG SEQUENCE



```

5892
(2)
(3)
5893
5894 005352
(2) 005352 021527 000040
5895 005356 001026
5896 005360 005215
5897 005362 012701 020000
5899 005366 000257
5899 005370 006101
5900 005372 006101
5901 005374 004737 017314
5902 005400 022701 100000
5903 005404 001404
5904 005406 012745 000145
(2) 005412 005245
(2) 005414 000000
5905 005416 006101
5906 005420 004737 017230
5907 005424 006101
5908 005426 022701 000001
5909 005432 001404
5910 005434
(2) 005434 012745 000146
(2) 005440 005245
(2) 005442 000000
5911
5912
5913
5914
(2)
(3)
5915
5916 005444
(2) 005444 021527 000041
5917 005450 001026
5918 005452 005215
5919 005454 012702 000004
5920 005460 000257
5921 005462 006002
5922 005464 006002
5923 005466 022702 000001
5924 005472 001404
5925 005474 012745 000147
(2) 005500 005245
(2) 005502 000000
5926 005504 006002
5927 005506 004737 017230
5928 005512 006002
5929 005514 004737 017314
5930 005520 022702 100000
5931 005524 001404
5932 005526
(2) 005526 012745 000150
(2) 005532 005245

```

```

*****
*TEST: 40 NEW INSTRUCTION IN THIS SECTION IS ROL
*****

```

```

ROLO:
CMP (R5),#40
BNE EROLO ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOV #20000,R1 ; LOAD REGISTER
CCC ; CLEAR FLAGS
ROL R1 ; SHIFT
ROL R1
JSR PC,@#SCC12 ; CHECK FOR CC = 12
CMP #100000,R1 ; CHECK IT
BEQ 1$ ; CONTINUE IF OK
MOV #145,-(R5)
INC -(R5)
HALT ; ROL INSTRUCTION FAILED
1$: ROL R1 ; SHIFT
JSR PC,@#SCC7 ; CHECK FOR CC = 7
ROL R1 ; SHIFT
CMP #1,R1 ; CHECK IT
BEQ RORO ; CONTINUE IF OK
EROLO:
MOV #146,-(R5)
INC -(R5)
HALT ; WRONG RESULT IN R1 OR WRONG SEQUENCE

```

```

*****
*TEST: 41 NEW INSTRUCTION IN THIS SECTION IS ROR
*****

```

```

RORO:
CMP (R5),#41
BNE ERORO ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOV #4,R2 ; LOAD REGISTER
CCC ; CLEAR FLAGS
ROR R2 ; SHIFT
ROR R2
CMP #1,R2 ; CHECK IT
BEQ 1$ ; CONTINUE IF OK
MOV #147,-(R5)
INC -(R5)
HALT ; ROR INSTRUCTION FAILED
1$: ROR R2 ; SHIFT
JSR PC,@#SCC7 ; CHECK FOR CC = 7
ROR R2 ; SHIFT
JSR PC,@#SCC12 ; CHECK FOR CC = 12
CMP #100000,R2 ; CHECK IT
BEQ ASLO ; CONTINUE IF OK
ERORO:
MOV #150,-(R5)
INC -(R5)

```

M03

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-35  
DVKAAA.P11 T41 NEW INSTRUCTION IN THIS SECTION IS ROR

\*\*\* SEQ 0038

(2) 005534 000000

HALT

; WRONG RESULT IN R2 OR WRONG SEQUENCE



# N03

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-36  
 DVKAAA.P11 T42 NEW INSTRUCTION IN THIS SECTION IS ASL

\*\*\* SEQ 0039

```

5933      ;*****
(2)      ;*TEST: 42      NEW INSTRUCTION IN THIS SECTION IS ASL
(3)      ;*****
5934
5935
  
```

```

ASLO:
(2) 005536 021527 000042      CMP      (R5), #42
5936 005542 001404      BEQ      2$      ; IF IN WRONG SEQUENCE GO TO HLT BELOW
5937 005544 012745 000151      MOV      #151, -(R5)
(2) 005550 005245      INC      -(R5)
(2) 005552 000000      HALT     ; PROGRAM IS IN WRONG SEQUENCE
5938 005554 005215      2$: INC      (R5)
5939 005556 012703 020000      MOV      #20000, R3      ; LOAD REGISTER
5940 005562 000257      CCC     ; CLEAR FLAGS
5941 005564 006303      ASL     ; SHIFT
5942 005566 006303      ASL     ;
5943 005570 004737 017314      JSR     PC, @#$CC12      ; CHECK FOR CC = 12
5944 005574 022703 100000      CMP      #100000, R3      ; CHECK IT
5945 005600 001404      BEQ     ; CONTINUE IF OK
5946 005602 012745 000152      MOV      #152, -(R5)
(2) 005606 005245      INC      -(R5)
(2) 005610 000000      HALT     ; ASL INSTRUCTION FAILED
5947 005612 006303      4$: ASL     R3      ; SHIFT
5948 005614 004737 017230      JSR     PC, @#$CC7      ; CHECK FOR CC = 7
5949 005620 006303      ASL     R3      ; SHIFT
5950 005622 004737 017164      JSR     PC, @#$CC4      ; CHECK FOR CC = 4
5951
5952
  
```

```

(2)      ;*****
(3)      ;*TEST: 43      NEW INSTRUCTION IN THIS SECTION IS ASR
5953      ;*****
  
```

```

ASRO:
(2) 005626 021527 000043      CMP      (R5), #43
5955 005632 001034      BNE     EASRO      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
5956 005634 005215      1$: INC      (R5)
5957 005636 012704 000004      MOV      #4, R4      ; LOAD REGISTER
5958 005642 000257      CCC     ; CLEAR FLAGS
5959 005644 006204      ASR     ; SHIFT
5960 005646 006204      ASR     R4
5961 005650 022704 000001      CMP      #1, R4      ; CHECK IT
5962 005654 001404      BEQ     ; CONTINUE IF OK
5963 005656 012745 000153      MOV      #153, -(R5)
(2) 005662 005245      INC      -(R5)
(2) 005664 000000      HALT     ; ASR INSTRUCTION FAILED
5964 005666 006204      2$: ASR     R4      ; SHIFT
5965 005670 004737 017230      JSR     PC, @#$CC7      ; CHECK FOR CC = 7
5966 005674 006204      ASR     R4      ; SHIFT
5967 005676 004737 017164      JSR     PC, @#$CC4      ; CHECK FOR CC = 4
5968 005702 012703 100002      MOV      #100002, R3      ; LOAD REGISTER
5969 005706 006203      ASR     R3      ; SHIFT
5970 005710 006203      ASR     R3
5971 005712 004737 017272      JSR     PC, @#$CC11      ; CHECK FOR CC = 11
5972 005716 022703 160000      CMP      #160000, R3      ; CHECK IT
5973 005722 001404      BEQ     ; CONTINUE IF OK
5974 005724      EASRO: MOV      #154, -(R5)
(2) 005724 012745 000154      INC      -(R5)
(2) 005730 005245
  
```

B04

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-37  
DVKAAA.P11 T43 NEW INSTRUCTION IN THIS SECTION IS ASR

\*\*\* SEQ 0040

(2) 005732 000000

HALT

; WRONG RESULT IN R3 OR WRONG SEQUENCE



5975 (2) (3) \*\*\*\*\*  
: \*TEST: 44 NEW INSTRUCTION IN THIS SECTION IS ADC  
: \*\*\*\*\*

```

5976
5977 005734 ADCO: CMP (R5),#44
(2) 005734 021527 000044 BEQ 2$ ; IF IN WRONG SEQUENCE GO TO HLT BELOW
5978 005740 001404 MOV #155,-(R5)
5979 005742 012745 000155 INC -(R5)
(2) 005746 005245 HALT ; PROGRAM IS IN WRONG SEQUENCE
(2) 005750 000000
5980 005752 005215 2$: INC (R5)
5981 005754 005000 CLR R0 ; CLEAR THE REGISTER
5982 005756 000257 CCC ; CLEAR FLAGS
5983 005760 005500 ADC R0 ; ADD C BIT = 0
5984 005762 004737 017164 JSR PC,@#SCC4 ; CHECK FOR CC = 4
5985 005766 000261 SEC ; C=1
5986 005770 005500 ADC R0 ; ADD C BIT=1
5987 005772 000261 SEC ; C=1
5988 005774 005500 ADC R0 ; AGAIN
5989 005776 004737 017062 JSR PC,@#SCC0 ; CHECK FOR CC = 0
5990 006002 022700 000002 CMP #2,R0 ; CHECK IT
5991 006006 001404 BEQ 4$ ; CONTINUE IF OK
5992 006010 012745 000156 MOV #156,-(R5)
(2) 006014 005245 INC -(R5)
(2) 006016 000000 HALT ; ADC INSTRUCTION FAILED
5993 006020 012700 077777 4$: MOV #77777,R0 ; LOAD LARGEST POSITIVE NUMBER
5994 006024 000261 SEC ; C=1
5995 006026 005500 ADC R0 ; ADD C BIT=1
5996 006030 004737 017314 JSR PC,@#SCC12 ; CHECK FOR CC = 12
5997 006034 022700 100000 CMP #100000,R0 ; CHECK IT
5998 006040 001404 BEQ 6$ ; FAILED
5999 006042 012745 000157 MOV #157,-(R5)
(2) 006046 005245 INC -(R5)
(2) 006050 000000 HALT ; ADC INSTRUCTION FAILED
6000 006052 012700 177777 6$: MOV #-1,R0 ; LOAD -1
6001 006056 000261 SEC ; C=1
6002 006060 005500 ADC R0 ; ADD C BIT=1
6003 006062 004737 017206 JSR PC,@#SCC5 ; CHECK FOR CC = 5
6004
6005
6006
6007

```

6008 (2) (3) \*\*\*\*\*  
: \*TEST: 45 NEW INSTRUCTION IN THIS SECTION IS SBC  
: \*\*\*\*\*

```

6009 006066 SBCO: CMP (R5),#45
(2) 006066 021527 000045 BEQ 1$ ; IF IN WRONG SEQUENCE GO TO HLT
6010 006072 001404 MOV #160,-(R5)
6011 006074 012745 000160 INC -(R5)
(2) 006100 005245 HALT ; TEST IS IN WRONG SEQUENCE
(2) 006102 000000
6012 006104 005215 1$: INC (R5)
6013 006106 012701 000003 MOV #3,R1 ; LOAD REGISTER
6014 006112 000257 CCC ; CLEAR FLAGS
6015 006114 005601 SBC R1 ; SUBTRACT C BIT=0
6016 006116 004737 017062 JSR PC,@#SCC0 ; CHECK FOR CC = 0

```

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-39  
 DVKAAA.P11 T45 NEW INSTRUCTION IN THIS SECTION IS SBC

\*\*\* SEQ 0042

6017	006122	022701	000003		CMP	#3,R1	:	CHECK IT
6018	006126	001404			BEQ	2\$	:	CONTINUE IF OK
6019	006130	012745	000161		MOV	#161,-(R5)		
(2)	006134	005245			INC	-(R5)		
(2)	006136	000000			HALT		:	SBC INSTRUCTION FAILED
6020	006140	000261		2\$:	SEC		:	C=1
6021	006142	005601			SBC	R1	:	SUBTRACT C BIT=1
6022	006144	000261			SEC		:	C=1
6023	006146	005601			SBC	R1		
6024	006150	004737	017062		JSR	PC,@#SCC0	:	CHECK FOR CC = 0
6025	006154	022701	000001		CMP	#1,R1	:	CHECK IT
6026	006160	001404			BEQ	3\$	:	CONTINUE IF OK
6027	006162	012745	000162		MOV	#162,-(R5)		
(2)	006166	005245			INC	-(R5)		
(2)	006170	000000			HALT		:	SBC INSTRUCTION FAILED
6028	006172	000261		3\$:	SEC		:	C=1
6029	006174	005601			SBC	R1	:	SUBTRACT C BIT=1
6030	006176	004737	017164		JSR	PC,@#SCC4	:	CHECK FOR CC = 4
6031	006202	000261			SEC		:	C=1
6032	006204	005601			SBC	R1	:	SUBTRACT C BIT = 1
6033	006206	004737	017272		JSR	PC,@#SCC11	:	CHECK FOR CC = 11
6034	006212	022701	177777		CMP	#-1,R1	:	CHECK IT
6035	006216	001404			BEQ	4\$	:	CONTINUE IF F OK
6036	006220	012745	000163		MOV	#163,-(R5)		
(2)	006224	005245			INC	-(R5)		
(2)	006226	000000			HALT		:	SBC INSTRUCTION FAILED
6037	006230	012701	100000	4\$:	MOV	#100000,R1	:	LOAD R1
6038	006234	000261			SEC		:	C=1
6039	006236	005601			SBC	R1	:	SUBTRACT C BIT = 1
6040	006240	004737	017122		JSR	PC,@#SCC2	:	CHECK FOR CC = 2



# E04

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-40  
 DVKAAA.P11 T46 NEW INSTRUCTION IN THIS SECTION IS SXT

\*\*\* SEQ 0043

```

6041
(2)
(3)
6042
6043 006244
(2) 006244 021527 000046
6044 006250 001024
6045 006252 005215
6046 006254 005002
6047 006256 000277
6048 006260 000254
6049 006262 006702
6050 006264 004737 017206
6051 006270 005702
6052 006272 001404
6053 006274 012745 000164
(2) 006300 005245
(2) 006302 000000
6054 006304 000273
6055 006306 006702
6056 006310 004737 017272
6057 006314 022702 177777
6058 006320 001404
6059 006322
(2) 006322 012745 000165
(2) 006326 005245
(2) 006330 000000
6060
6061
6062
6063
(2)
(3)
6064
6065 006332
(2) 006332 021527 000047
6066 006336 001031
6067 006340 005215
6068 006342 012703 125125
6069 006346 000277
6070 006350 000250
6071 006352 000303
6072 006354 004737 017252
6073 006360 022703 052652
6074 006364 001404
6075 006366 012745 000166
(2) 006372 005245
(2) 006374 000000
6076 006376 012703 000377
6077 006402 000277
6078 006404 000244
6079 006406 000303
6080 006410 004737 017164
6081 006414 022703 177400
6082 006420 001404
6083 006422
  
```

```

:*****
:*TEST: 46 NEW INSTRUCTION IN THIS SECTION IS SXT
:*****
SXT0:
      CMP      (R5),#46
      BNE     ESXT0      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
1$:   INC      (R5)
      CLR     R2         ; CLEAR REGISTER
      SCC
      CLNZ
      SXT     R2         ; SIGN EXTEND
      JSR    PC,@#SCC5   ; CHECK FOR CC = 5
      TST    R2         ; REG. 2 SHOULD STILL BE 0
      BEQ    2$         ; CONTINUE IF OK
      MOV    #164,-(R5)
      INC    -(R5)
2$:   HALT
      SENVC
      SXT     R2         ; SXT INSTRUCTION FAILED
      JSR    PC,@#SCC11 ; SET N, V & C BITS
      CMP    #-1,R2     ; SIGN EXTEND
      BEQ    SWAB0      ; CHECK FOR CC = 11
      MOV    #165,-(R5) ; REG. 2 SHOULD NOW HAVE -1
      INC    -(R5)     ; CONTINUE IF OK
      HALT
      ESXT0:
      MOV    #165,-(R5)
      INC    -(R5)
      HALT
      ; WRONG RESULT IN R2 OR WRONG SEQUENCE
  
```

```

:*****
:*TEST: 47 NEW INSTRUCTION IN THIS SECTION IS SWAB
:*****
  
```

```

SWAB0:
      CMP      (R5),#47
      BNE     ESWAB0    ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
      INC      (R5)
      MOV      #125125,R3 ; LOAD BIT PATTERN INTO REGISTER
      SCC
      CLN
      SWAB    R3         ; SWAP BYTES OF REGISTER
      JSR    PC,@#SCC10 ; CHECK FOR CC = 10
      CMP      #52652,R3 ; CHECK IT
      BEQ     1$         ; CONTINUE IF OK
      MOV      #166,-(R5)
      INC      -(R5)
      HALT
      1$:   MOV      #377,R3 ; SWAB INSTRUCTION FAILED
      SCC
      CLZ
      SWAB    R3
      JSR    PC,@#SCC4   ; CHECK FOR CC = 4
      CMP      #177400,R3
      BEQ     XORO
      ESWAB0:
      XORO
  
```

# F04

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-41  
DVKAAA.P11 T47 NEW INSTRUCTION IN THIS SECTION IS SWAB

\*\*\* SEQ 0044

(2)	006422	012745	000167	MOV	#167, -(R5)
(2)	006426	005245		INC	-(R5)
(2)	006430	000000		HALT	

; WRONG RESULT IN R3 OR WRONG SEQUENCE



```

6084
(2)
(3)
6085
6086 006432
(2) 006432 021527 000050
6087 006436 001034
6088 006440 005215
6089 006442 012704 177777
6090 006446 012703 177777
6091 006452 000277
6092 006454 074403
6093 006456 004737 017206
6094 006462 012703 077777
6095 006466 010400
6096 006470 000263
6097 006472 000244
6098 006474 074003
6099 006476 004737 017272
6100 006502 012702 125252
6101 006506 012704 052525
6102 006512 000277
6103 006514 074204
6104 006516 004737 017272
6105 006522 022704 177777
6106 006526 001404
6107 006530
(2) 006530 012745 000170
(2) 006534 005245
(2) 006536 000000
6108
6109
6110
6111
(2)
(3)
6112
6113 006540
(2) 006540 021527 000051
6114 006544 001055
6115 006546 005215
6116 006550 012701 021421
6117 006554 060101
6118 006556 004737 017062
6119 006562 022701 043042
6120 006566 001404
6121 006570 012745 000171
(2) 006574 005245
(2) 006576 000000
6122 006600 012700 156357
6123 006604 060000
6124 006606 004737 017272
6125 006612 022700 134736
6126 006616 001404
6127 006620 012745 000172
(2) 006624 005245

```

```

*****
: *TEST: 50 NEW INSTRUCTION IN THIS SECTION IS XOR
*****

```

```

XORO:
      CMP      (R5),#50
      BNE     EXORO      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
      INC     (R5)
      MOV     #-1,R4      ; LOAD REGISTERS
      MOV     #-1,R3
      SCC
      XOR     R4,R3      ; SHOULD PRODUCE 0'S IN REG. 3
      JSR    PC,@#SCC5   ; CHECK FOR CC = 5
      MOV     #77777,R3
      MOV     R4,R0      ; PLACE A -1 IN R0
      SEVC
      CLZ
      XOR     R0,R3
      JSR    PC,@#SCC11  ; CHECK FOR CC = 11
      MOV     #125252,R2 ; LOAD REGISTERS
      MOV     #52525,R4
      SCC
      XOR     R2,R4      ; SHOULD PRODUCE ALL 1'S IN REG. 4
      JSR    PC,@#SCC11  ; CHECK FOR CC = 11
      CMP     #-1,R4     ; CHECK IT
      BEQ
      ADDO
      EXORO:
      MOV     #170,-(R5) ; WRONG RESULT IN R4 OR WRONG SEQUENCE
      INC     -(R5)
      HALT

```

```

*****
: *TEST: 51 NEW INSTRUCTION IN THIS SECTION IS ADD
*****

```

```

ADDO:
      CMP     (R5),#51
      BNE     EADDO     ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
      INC     (R5)
      MOV     #21421,R1 ; LOAD REGISTERS
      ADD     R1,R1
      JSR    PC,@#SCC0  ; CHECK FOR CC = 0
      CMP     #43042,R1 ; CHECK IT
      BEQ     1$        ; CONTINUE IF OK
      MOV     #171,-(R5)
      INC     -(R5)
      HALT
      1$:
      MOV     #-21421,R0 ; ADD INSTRUCTION FAILED
      ADD     R0,R0      ; LOAD REGISTERS
      ADD
      JSR    PC,@#SCC11  ; CHECK FOR CC = 11
      CMP     #-43042,R0 ; CHECK IT
      BEQ     2$        ; CONTINUE IF OK
      MOV     #172,-(R5)
      INC     -(R5)

```

H04

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-43  
DVKAAA.P11 T51 NEW INSTRUCTION IN THIS SECTION IS ADD

\*\*\* SEQ 0046

(2)	006626	000000			HALT						
6128	006630	012702	100000	2\$:	MOV	#100000,R2				:	ADD INSTRUCTION FAILED
6129	006634	060202			ADD	R2,R2				:	LOAD REGISTERS
6130	006636	004737	017230		JSR	PC,3#SCC7				:	ADD SHOULD RESULT AS 0'S
6131	006642	012704	021421		MOV	#21421,R4				:	CHECK FOR CC = 7
6132	006646	012701	156357		MOV	#-21421,R1				:	LOAD REGISTERS
6133	006652	060401			ADD	R4,R1				:	ADD SHOULD RESULT AS 0'S
6134	006654	001404			BEQ	3\$				:	CONTINUE IF OK
6135	006656	012745	000173		MOV	#173,-(R5)					
(2)	006662	005245			INC	-(R5)					
(2)	006664	000000			HALT					:	ADD INSTRUCTION FAILED
6136	006666	005404		3\$:	NEG	R4				:	SWITCH SOURCE AND DESTINATION
6137	006670	012701	021421		MOV	#21421,R1				:	
6138	006674	060104			ADD	R1,R4				:	SHOULD RESULT AS 0'S
6139	006676	001404			BEQ	SUB0				:	CONTINUE IF OK
6140	006700			EADD0:							
(2)	006700	012745	000174		MOV	#174,-(R5)					
(2)	006704	005245			INC	-(R5)					
(2)	006706	000000			HALT					:	WRONG RESULT IN R1 OR WRONG SEQUENCE



```

6141          ;*****
        (2)    ;*TEST: 52      NEW INSTRUCTION IN THIS SECTION IS SUB
        (3)    ;*****
    
```

```

6142
6143 006710      SUBO:
        (2) 006710 021527 000052      CMP      (R5),#52
6144 006714 001404      BEQ      2$          ; IF IN WRONG SEQUENCE GO TO HLT BELOW
6145 006716 012745 000175      MOV      #175,-(R5)
        (2) 006722 005245      INC      -(R5)
        (2) 006724 000000      HALT     ; PROGRAM IS IN WRONG SEQUENCE
6146 006726 005215      2$:      INC      (R5)
6147 006730 012702 021421      MOV      #21421,R2      ; LOAD REGISTERS
6148 006734 012703 156357      MOV      #-21421,R3
6149 006740 160203      SUB      R2,R3          ; RESULT SHOULD=-43042
6150 006742 004737 017252      JSR      PC,@#SCC10     ; CHECK FOR CC = 10
6151 006746 022703 134736      CMP      #-43042,R3    ; CHECK IT
6152 006752 001404      BEQ      4$          ; CONTINUE IF OK
6153 006754 012745 000176      MOV      #176,-(R5)
        (2) 006760 005245      INC      -(R5)
        (2) 006762 000000      HALT     ; SUB INSTRUCTION FAILED
6154 006764 012703 021421      4$:      MOV      #21421,R3      ; LOAD REGISTER
6155 006770 010204      MOV      R2,R4          ; NOW R4 = #21421
6156 006772 160403      SUB      R4,R3          ; RESULT SHOULD=0
6157 006774 001404      BEQ      6$
6158 006776 012745 000177      MOV      #177,-(R5)
        (2) 007002 005245      INC      -(R5)
        (2) 007004 000000      HALT     ; SUB NSTRUCTION FAILED
6159 007006 012703 177777      6$:      MOV      #-1,R3          ; LOAD REGISTERS
6160 007012 012702 077777      MOV      #77777,R2     ; LOAD REGISTERS
6161 007016 160302      SUB      R3,R2          ; RESULT SHOULD BE 100000 AND OVERFLOW
6162 007020 004737 017334      JSR      PC,@#SCC13     ; CHECK FOR CC = 13
6163 007024 022702 100000      CMP      #100000,R2    ; CHECK IT
6164 007030 001404      BEQ      8$          ; CONTINUE IF OK
6165 007032 012745 000200      MOV      #200,-(R5)
        (2) 007036 005245      INC      -(R5)
        (2) 007040 000000      HALT     ; SUB INSTRUCTION FAILED
6166 007042 012704 177777      8$:      MOV      #-1,R4
6167 007046 160304      SUB      R3,R4
6168 007050 004737 017164      JSR      PC,@#SCC4      ; CHECK FOR CC = 4
6169
6170
    
```

```

6171          ;*****
        (2)    ;*TEST: 53      NEW INSTRUCTIONS IN THARE SECTION IS MTPS & MFPS
        (3)    ;*****
    
```

```

6172
6173 007054      PSW:
        (2) 007054 021527 000053      CMP      (R5),#53
6174 007060 001032      BNE     EPSW          ; IF IN WRONG SEQUENCE THEN GO TO HLT AT THE END OF THE
6175 007062 005215      1$:      INC      (R5)
6176 007064 012701 177777      MOV      #177777,R1
6177 007070 005000      CLR     R0
6181 007072      MTPS   R0          ; SET PSW TO 0
        (1) 007072 106400      .WORD   106400!...C
6182 007074 004737 017062      JSR      PC,@#SCC0     ; CHECK FOR CC = 0
6183 007100      MFPS   R1          ; MOVE PSW TO R1
        (1) 007100 106701      .WORD   106700!...C
    
```

# J04

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-45  
 DVKAAA.P11 T53 NEW INSTRUCTIONS IN THARE SECTION IS MTPS & MFPS

\*\*\* SEQ 0048

6187	007102	001404			BEQ	25		; CONTINUE IF BIT 8 OF PSW WAS EXTENDED IN R1
6188	007104	012745	000201		MOV	#201, -(R5)		
(2)	007110	005245			INC	-(R5)		
(2)	007112	000000			HALT			; MTPS OR MFPS INSTRUCTION FAILED
6189	007114	004737	017164	25:	JSR	PC, @#SCC4		; CHECK FOR CC = 4
6190	007120	012700	000377		MOV	#377, R0		
6194	007124				MTPS	R0		; SET PSW TO 357 SINCE MTPS DOES NOT SET T BIT
(1)	007124	106400			.WORD	106400!..C		
6195	007126	004737	017354		JSR	PC, @#SCC17		; CHECK FOR CC = 17
6196	007132				MFPS	R1		; MOVE PSW TO R1
(1)	007132	106701			.WORD	106700!..C		
6200	007134	004737	017272		JSR	PC, @#SCC11		; CHECK FOR CC = 11 IC BIT SHOULD NOT BE EFFECTED BY MFP
6201	007140	022701	177757		CMP	#177757, R1		; CHECK TO SEE IF BIT 8 OF PSW WAS EXTENDED THRU R1
6202	007144	001404			BEQ	MODE0		
6203	007146			EPSW:				
(2)	007146	012745	000202		MOV	#202, -(R5)		
(2)	007152	005245			INC	-(R5)		
(2)	007154	000000			HALT			; MTPS OR MFPS INSTRUCTION FAILED OR WRONG SEQUENCE



6208  
6209  
6210  
6211  
6212  
6213  
(2)  
(3)  
6214  
6215  
(2)  
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6217  
6218  
6219  
6220  
6221  
6222  
6223  
(2)  
(2)  
6224  
6225  
6226  
6227  
6228  
6229  
(2)  
(2)  
6230  
6231  
6232  
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6240  
(2)  
(2)  
6241  
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6245  
6246  
(2)  
(2)  
(2)  
6247  
6248  
6249  
6250  
6251

LSI-11 INSTRUCTIONS NOT MODE 0

\*\*\*\*\*  
\*TEST: 54 CHECK MODES 0 & 1 USING THE MOVB AND MOV INSTRUCTIONS  
\*\*\*\*\*

MODE0:

CMP (R5), #54  
BNE EMODE0 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST  
INC (R5)  
MOVB #252, R0 ; LOAD REGISTERS  
MOV R0, R1  
MOVB R1, R2  
CMPB #252, R2 ; CHECK IT  
BEQ 1\$ ; OK, CONTINUE  
MOV #203, -(R5)  
INC -(R5)  
HALT ; MOV INSTRUCTION FAILED IN MODE 0  
1\$: MOV #125252, R0 ; LOAD REGISTERS  
MOV R0, R1  
MOV R1, R2  
CMPB #125252, R2 ; CHECK IT  
BEQ MODE1 ; OK, CONTINUE  
MOV #204, -(R5)  
INC -(R5)  
HALT ; MOV INSTRUCTION FAILED IN MODE 0

MODE1:

MOV #TEMP, R0 ; LOAD ADDRESSES INTO REGS.  
MOV #TEMP1, R1  
MOV #TEMP2, R2  
CLR TEMP2  
MOVB #125, (R0) ; START CLEAN  
MOVB (R0), (R1) ; LOAD THE LOCATIONS  
MOVB (R1), (R2) ; TEMP ---> TEMP1  
CMPB #125, TEMP2 ; TEMP1 ---> TEMP2  
BEQ 1\$ ; CHECK IT  
MOV #205, -(R5) ; OK, CONTINUE  
INC -(R5)  
HALT ; MOV INSTRUCTION FAILED IN MODE 1  
1\$: MOV #52525, (R0) ; LOAD THE LOCATIONS  
MOV (R0), (R1) ; TEMP ---> TEMP1  
MOV (R1), (R2) ; TEMP1 ---> TEMP2  
CMPB #52525, TEMP2 ; CHECK IT  
BEQ MODE2 ; OK, CONTINUE

EMODE0:

MOV #206, -(R5)  
INC -(R5)  
HALT ; MOV INSTRUCTION FAILED IN MODE 1  
; OR WRONG SEQUENCE

\*\*\*\*\*

# L04

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-47  
DVKAAA.P11 T55 CHECK MODE 2 USING THE MOVB AND MOV INSTRUCTIONS

\*\*\* SEQ 0050

```
(2) ;*TEST: 55 CHECK MODE 2 USING THE MOVB AND MOV INSTRUCTIONS
(3) ;*****
6252
6253 007342 MODE2: CMP (R5),#55
(2) 007342 021527 000055 BNE EMODE2 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
6254 007346 001050 INC (R5) ;
6255 007350 005215 MOV #TEMP,R0 ; LOAD ADDRESSES
6256 007352 012700 000440 MOV #TEMP1,R1 ;
6257 007356 012701 000442 MOV #TEMP2,R2 ;
6258 007362 012702 000444 CLRB (R2)+ ; START CLEAN
6259 007366 105022 MOVB #252,(R0) ; LOAD THE LOCATIONS
6260 007370 112710 000252 MOVB (R0)+,(R1)+ ; TEMP ---> TEMP1
6261 007374 112021 INCB R1 ; MAKE IT EVEN
6262 007376 105201 MOVB (R1),TEMP ; MORE 0'S INTO TEMP
6263 007400 111167 171034 INCB RO ; MAKE IT EVEN
6264 007404 105200 MOVB (R0)+,(R1)+ ; TEMP1 ---> TEMP2
6265 007406 112021 CMPB -(R2),#252 ; CHECK IT
6266 007410 124227 000252 BNE 1$ ; FAILED
6267 007414 001003 TSTB TEMP ; CHECK IT
6268 007416 105767 171016 BEQ 2$ ; OK, CONTINUE
6269 007422 001404
6270 007424 1$: MOV #207,-(R5)
(2) 007424 012745 000207 INC -(R5)
(2) 007430 005245 HALT ; INSTRUCTIONS FAILED IN MODE 2
(2) 007432 000000
6271
6272 007434 005741 2$: TST -(R1)
6273 007436 005022 CLR (R2)+ ; START CLEAN
6274 007440 012740 125252 MOV #125252,-(R0) ; LOAD LOCATIONS
6275 007444 012020 MOV (R0)+,(R0)+ ; TEMP ---> TEMP1
6276 007446 011067 170766 MOV (R0),TEMP ; 0 ---> TEMP
6277 007452 012121 MOV (R1)+,(R1)+ ; 125252 ---> TEMP2
6278 007454 024227 125252 CMPB -(R2),#125252 ; CHECK IT
6279 007460 001003 BNE EMODE2 ; FAILED
6280 007462 005767 170752 TST TEMP ; CHECK IT
6281 007466 001404 BEQ MODE3 ; OK, CONTINUE
6282 007470 EMODE2:
(2) 007470 012745 000210 MOV #210,-(R5)
(2) 007474 005245 INC -(R5)
(2) 007476 000000 HALT ; INSTRUCTIONS FAILED IN MODE 2
6283 ; OR WRONG SEQUENCE
```



# M04

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-48  
DVKAAA.P11 T56 CHECK MODE 3 USING THE MOV B AND MOV INSTRUCTIONS

\*\*\* SEQ 0051

6284 (2) (3) \*\*\*\*\*  
\*TEST: 56 CHECK MODE 3 USING THE MOV B AND MOV INSTRUCTIONS  
\*\*\*\*\*

6285	007500			MODE3:		
6286	007500	021527	000056		CMP	(R5), #56
6287	007504	001066			BNE	EMODE3 ; IF IN WRONG SEQUENCE GO TO HLT ABOVE
6288	007506	005215			INC	(R5)
6289	007510	012767	000440 170712		MOV	#TEMP, ADR ; LOAD ADDRESSES
6290	007516	012767	000442 170706		MOV	#TEMP1, ADR1 ;
6291	007524	012767	000444 170702		MOV	#TEMP2, ADR2 ;
6292	007532	012700	000430		MOV	#ADR, R0 ; LOAD ADDRESSES OF ADDRESSES
6293	007536	012701	000432		MOV	#ADR1, R1 ;
6294	007542	105067	170676		CLRB	TEMP2 ; START CLEAN
6295	007546	112767	000125 170664		MOVB	#125, TEMP ;
6296	007554	113031			MOVB	@(R0)+, @(R1)+ ; TEMP ---> TEMP1
6297	007556	113167	170656		MOVB	@(R1)+, TEMP ; TEMP2 ---> TEMP
6298	007562	113030			MOVB	@(R0)+, @(R0)+ ; TEMP1 ---> TEMP2
6299	007564	122767	000125 170652		CMPB	#125, TEMP2 ; CHECK IT
6300	007572	001003			BNE	1\$ ; FAILED
6301	007574	105767	170640		TSTB	TEMP ; CHECK IT
6302	007600	001404			BEQ	2\$ ; OK, CONTINUE
6303	007602			1\$:		
(2)	007602	012745	000211		MOV	#211, -(R5)
(2)	007606	005245			INC	-(R5)
(2)	007610	000000			HALT	;
6304	007612	005067	170626		CLR	TEMP2 ; INSTRUCTIONS FAILED IN MODE 3
6305	007616	012767	052525 170614	2\$:	MOV	#52525, TEMP ; START CLEAN
6306	007624	012700	000430		MOV	#ADR, R0 ; LOAD LOCATIONS
6307	007630	012701	000432		MOV	#ADR1, R1 ; LOAD ADDRESSES OF ADDRESSES
6308	007634	013030			MOV	@(R0)+, @(R0)+ ; TEMP ---> TEMP1
6309	007636	013067	170576		MOV	@(R0)+, TEMP ; TEMP2 ---> TEMP
6310	007642	013131			MOV	@(R1)+, @(R1)+ ; TEMP1 ---> TEMP2
6311	007644	022767	052525 170572		CMP	#52525, TEMP2 ; CHECK IT
6312	007652	001003			BNE	EMODE3 ; FAILED
6313	007654	005767	170560		TST	TEMP ; CHECK IT
6314	007660	001404			BEQ	MODE4 ; OK, CONTINUE
6315	007662			EMODE3:		
(2)	007662	012745	000212		MOV	#212, -(R5)
(2)	007666	005245			INC	-(R5)
(2)	007670	000000			HALT	;
6316						;
6317						;
6318						;
6319						;
(2)						*****
(3)						*TEST: 57 CHECK MODE 4 USING THE MOV B AND MOV INSTRUCTIONS
						*****

6320						
6321	007672			MODE4:		
(2)	007672	021527	000057		CMP	(R5), #57
6322	007676	001120			BNE	EMODE4 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
6323	007700	005215			INC	(R5)
6324	007702	105067	170532		CLRB	TEMP ; START CLEAN
6325	007706	012700	000440		MOV	#TEMP, R0 ; LOAD ADDRESSES
6326	007712	012701	000442		MOV	#TEMP1, R1 ;
6327	007716	012702	000444		MOV	#TEMP2, R2 ;

6328	007722	005202			INC	R2		; ADJUST THE POINTER
6329	007724	021267	170515		CMP	(R2),TEMP2+1		
6330	007730	001404			BEQ	1\$		
6331	007732	012745	000213		MOV	#213,-(R5)		
(2)	007736	005245			INC	-(R5)		
(2)	007740	000000			HALT			; INSTRUCTIONS FAILED IN MODE 4
6332	007742	112742	000252	1\$:	MOVB	#252,-(R2)		LOAD TEMP2
6333	007746	005201			INC	R1		ADJUST THE POINTERS
6334	007750	005202			INC	R2		
6335	007752	114241			MOVB	-(R2),-(R1)		TEMP2 ---> TEMP1
6336	007754	005200			INC	RO		ADJUST THE POINTERS
6337	007756	005202			INC	R2		
6338	007760	114042			MOVB	-(RO),-(R2)		TEMP ---> TEMP2
6339	007762	105200			INCB	RO		ADJUST THE POINTERS
6340	007764	021067	170451		CMP	(RO),TEMP+1		
6341	007770	001404			BEQ	2\$		
6342	007772	012745	000214		MOV	#214,-(R5)		
(2)	007776	005245			INC	-(R5)		
(2)	010000	000000			HALT			; INSTRUCTIONS FAILED IN MODE 4
6343	010002	105201		2\$:	INCB	R1		
6344	010004	114140			MOVB	-(R1),-(RO)		TEMP1 ---> TEMP
6345	010006	122767	000252	170424	CMPB	#252,TEMP		CHECK IT
6346	010014	001003			BNE	3\$		FAILED
6347	010016	105767	170422		TSTB	TEMP2		CHECK IT
6348	010022	001404			BEQ	4\$		OK, CONTINUE
6349	010024			3\$:				
(2)	010024	012745	000215		MOV	#215,-(R5)		
(2)	010030	005245			INC	-(R5)		
(2)	010032	000000			HALT			; INSTRUCTIONS FAILED IN MODE 4
6350	010034	005067	170400	4\$:	CLR	TEMP		START CLEAN
6351	010040	012700	000440		MOV	#TEMP,RO		LOAD ADDRESSES
6352	010044	012701	000442		MOV	#TEMP1,R1		
6353	010050	012702	000444		MOV	#TEMP2,R2		
6354	010054	005722			TST	(R2)+		ADJUST THE POINTER
6355	010056	021267	170364		CMP	(R2),TEMP2+2		
6356	010062	001404			BEQ	5\$		
6357	010064	012745	000216		MOV	#216,-(R5)		
(2)	010070	005245			INC	-(R5)		
(2)	010072	000000			HALT			; INSTRUCTIONS FAILED IN MODE 4
6358	010074	012742	125252	5\$:	MOV	#125252,-(R2)		LOAD TEMP2
6359	010100	005721			TST	(R1)+		ADJUST THE POINTERS
6360	010102	005722			TST	(R2)+		
6361	010104	014241			MOV	-(R2),-(R1)		TEMP2 ---> TEMP1
6362	010106	005720			TST	(RO)+		ADJUST POINTERS
6363	010110	005722			TST	(R2)+		
6364	010112	014042			MOV	-(RO),-(R2)		TEMP ---> TEMP2
6365	010114	005720			TST	(RO)+		ADJUST THE POINTERS
6366	010116	005721			TST	(R1)+		
6367	010120	014140			MOV	-(R1),-(RO)		TEMP1 ---> TEMP
6368	010122	022767	125252	170310	CMP	#125252,TEMP		CHECK IT
6369	010130	001003			BNE	EMODE4		FAILED
6370	010132	005767	170306		TST	TEMP2		CHECK IT
6371	010136	001404			BEQ	MODE5		OK, CONTINUE
6372	010140			EMODE4:				
(2)	010140	012745	000217		MOV	#217,-(R5)		
(2)	010144	005245			INC	-(R5)		



# B05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-50  
DVKAAA.P11 T57 CHECK MODE 4 USING THE MOVB AND MOV INSTRUCTIONS

\*\*\* SEQ 0053

(2) 010146 000000  
6373

HALT

: INSTRUCTIONS FAILED IN MODE 4  
: OR WRONG SEQUENCE

# C05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-51  
 DVKAAA.P11 T60 CHECK MODE 5 USING THE MOVB AND MOV INSTRUCTIONS

\*\*\* SEQ 0054

```

6374 (2)
6375 (3)
6376 010150 021527 000060
6377 010154 001105
6378 010156 005215
6379 010160 105067 170254
6380 010164 012767 000440 170236
6381 010172 012767 000442 170232
6382 010200 012767 000444 170226
6383 010206 012700 000430
6384 010212 012701 000432
6385 010216 012702 000434
6386 010222 005722
6387 010224 112752 000125
6388 010230 022122
6389 010232 115251
6390 010234 022022
6391 010236 115052
6392 010240 022022
6393 010242 125052
6394 010244 001404
6395 010246 012745 000220
6396 (2) 010252 005245
6397 (2) 010254 000000
6398 010256 022120
6399 010260 115150
6400 010262 122767 000125 170150
6401 010270 001003
6402 010272 105767 170146
6403 010276 001404
6404 010300
6405 (2) 010300 012745 000221
6406 (2) 010304 005245
6407 (2) 010306 000000
6408 010310 005067 170124
6409 010314 012700 000430
6410 010320 012701 000432
6411 010324 012702 000434
6412 010330 005722
6413 010332 012752 052525
6414 010336 022122
6415 010340 015251
6416 010342 022022
6417 010344 015052
6418 010346 022021
6419 010350 015150
6420 010352 022767 052525 170060
6421 010360 001003
6422 010362 005767 170056
6423 010366 001404
6424 010370
6425 (2) 010370 012745 000222
6426 (2) 010374 005245
  
```

```

*****
: *TEST: 60 CHECK MODE 5 USING THE MOVB AND MOV INSTRUCTIONS
*****
  
```

```

MODE5:
CMP (R5), #60
BNE EMODE5 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
CLRB TEMP ; START CLEAN
MOV #TEMP, ADR ; LOAD ADDRESSES
MOV #TEMP1, ADR1
MOV #TEMP2, ADR2
MOV #ADR, R0 ; LOAD ADDRESSES OF ADDRESSES
MOV #ADR1, R1
MOV #ADR2, R2
TST (R2)+ ; ADJUST THE POINTER
MOVB #125, 2-(R2) ; LOAD TEMP2
CMP (R1)+, (R2)+ ; ADJUST THE POINTERS
MOVB 2-(R2), 2-(R1) ; TEMP2 ---> TEMP1
CMP (R0)+, (R2)+ ; ADJUST THE POINTERS
MOVB 2-(R0), 2-(R2) ; TEMP ---> TEMP2
CMP (R0)+, (R2)+ ; ADJUST THE POINTERS
CMPB 2-(R0), 2-(R2) ; CHECK IT
BEQ 1$
MOV #220, -(R5)
INC -(R5)
1$:
CMP (R1)+, (R0)+ ; ADJUST THE POINTERS
MOVB 2-(R1), 2-(R0) ; TEMP1 ---> TEMP
CMPB #125, TEMP ; CHECK IT
BNE 2$ ; FAILED
TSTB TEMP2 ; CHECK IT
BEQ 3$ ; OK, CONTINUE
2$:
MOV #221, -(R5)
INC -(R5)
3$:
CLR TEMP ; INSTRUCTIONS FAILED IN MODE 5
MOV #ADR, R0 ; START CLEAN
MOV #ADR1, R1 ; LOAD ADDRESSES OF ADDRESSES
MOV #ADR2, R2
TST (R2)+ ; ADJUST THE POINTER
MOVB #52525, 2-(R2) ; LOAD TEMP2
CMP (R1)+, (R2)+ ; ADJUST THE POINTERS
MOVB 2-(R2), 2-(R1) ; TEMP2 ---> TEMP1
CMP (R0)+, (R2)+ ; ADJUST THE POINTERS
MOVB 2-(R0), 2-(R2) ; TEMP ---> TEMP2
CMP (R0)+, (R1)+ ; ADJUST THE POINTERS
MOVB 2-(R1), 2-(R0) ; TEMP1 ---> TEMP
CMP #52525, TEMP ; CHECK IT
BNE EMODE5 ; FAILED
TST TEMP2 ; CHECK IT
BEQ MODE6 ; OK, CONTINUE
EMODE5:
MOV #222, -(R5)
INC -(R5)
  
```



(2) 010376 000000  
6420  
6421  
6422  
6423  
6424  
6425  
(2) 010400  
6426 010400 021527 000061  
6427 010404 001055  
6428 010406 005215  
6429 010410 005067 170030  
6430 010414 012700 000440  
6431 010420 012701 000442  
6432 010424 012702 000444  
6433 010430 112760 000252 000000  
6434 010436 112760 000252 000001  
6435 010444 022767 125252 167766  
6436 010452 001012  
6437 010454 116062 000001 000000  
6438 010462 116160 000002 000005  
6439 010470 022767 125252 167746  
6440 010476 001404  
6441 010500  
(2) 010500 012745 000223  
(2) 010504 005245  
(2) 010506 000000  
6442 010510 005067 167726  
6443 010514 012760 052525 000000  
6444 010522 016260 177774 000002  
6445 010530 022767 052525 167704  
6446 010536 001404  
6447 (2) 010540  
(2) 010540 012745 000224  
(2) 010544 005245  
(2) 010546 000000

HALT ; INSTRUCTIONS FAILED IN MODE 5  
; OR WRONG SEQUENCE  
  
\*\*\*\*\*  
\*TEST: 61 CHECK MODE 6 USING THE MOVB AND MOV INSTRUCTIONS  
\*\*\*\*\*  
MODE6:  
CMP (R5), #61  
BNE EMODE6 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST  
INC (R5)  
CLR TEMP2 ; START CLEAN  
MOV #TEMP, R0 ; LOAD ADDRESSES  
MOV #TEMP1, R1  
MOV #TEMP2, R2  
MOVB #252, 0(R0) ; LOAD TEMP (LOW BYTE)  
MOVB #252, 1(R0) ; LOAD TEMP (HIGH BYTE)  
CMP #125252, TEMP ; CHECK IT  
BNE IS ; FAILED  
MOVB 1(R0), 0(R2) ; TEMP(H) ----> TEMP2(L)  
MOVB 2(R1), 5(R0) ; TEMP2(L) ----> TEMP2(H)  
CMP #125252, TEMP2 ; CHECK IT  
BEQ 2S ; OK, CONTINUE  
  
1S: MOV #223, -(R5)  
INC -(R5)  
HALT ; INSTRUCTIONS FAILED IN MODE 6  
2S: CLR TEMP1 ; START CLEAN  
MOV #52525, 0(R0) ; LOAD TEMP  
MOV -4(R2), 2(R0) ; TEMP ----> TEMP1  
CMP #52525, TEMP1 ; CHECK IT  
BEQ MODE7 ; OK, CONTINUE  
  
EMODE6: MOV #224, -(R5)  
INC -(R5)  
HALT ; INSTRUCTIONS FAILED IN MODE 6  
; OR WRONG SEQUENCE

# E05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-53  
 DVKAAA.P11 T62 CHECK MODE 7 USING THE MOVB AND MOV INSTRUCTIONS

\*\*\* SEQ 0056

```

6448      ::*****
(2)      ::*TEST: 62   CHECK MODE 7 USING THE MOVB AND MOV INSTRUCTIONS
(3)      ::*****
6449
6450 010550      MODE7:      CMP      (R5),#62
(2) 010550      021527      000062      BNE      EMODE7      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
6451 010554      001052      INC      (R5)
6452 010556      005215      INC      TEMP1      ; START CLEAN
6453 010560      005067      167656      CLR      TEMP1      ; LOAD ADDRESSES
6454 010564      012767      000440      167636      MOV      #TEMP,ADR
6455 010572      012767      000442      167632      MOV      #TEMP1,ADR1
6456 010600      012767      000444      167626      MOV      #TEMP2,ADR2
6457 010606      012700      000430      MOV      #ADR,R0      ; LOAD ADDRESSES OF ADDRESSES
6458 010612      012701      000432      MOV      #ADR1,R1
6459 010616      012702      000434      MOV      #ADR2,R2
6460 010622      112770      000252      000000      MOVB     #252,R0(R0)      ; LOAD TEMP
6461 010630      117270      177774      000002      MOVB     @-4(R2),@2(R0)   ; TEMP ----> TEMP1
6462 010636      122767      000252      167576      CMPB     #252,TEMP1      ; CHECK IT
6463 010644      001404      BEQ      1$          ; OK, CONTINUE
6464 010646      012745      000225      MOV      #225,-(R5)
(2) 010652      005245      INC      -(R5)
(2) 010654      000000      HALT
6465 010656      012770      125252      000000      1$:      MOV      #125252,@0(R0)   ; MODE 7 IS FAILING
6466 010664      017270      177774      000002      MOV      @-4(R2),@2(R0) ; LOAD TEMP
6467 010672      022767      125252      167542      CMP      #125252,TEMP1 ; TEMP ----> TEMP1
6468 010700      001404      BEQ      TSTB1      ; CHECK IT
6469 010702      012745      000226      EMODE7:      MOV      #226,-(R5)      ; OK, CONTINUE
(2) 010706      005245      INC      -(R5)
(2) 010710      000000      HALT      ; INSTRUCTIONS FAILED IN MODE 7
6470      ; OR WRONG SEQUENCE
  
```



# F05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-54  
 DVKAAA.P11 CHECK BYTE INSTRUCTIONS, NOT DESTINATION MODE 0

\*\*\* SEQ 0057

6475  
 6476  
 6477  
 6478  
 6479  
 6480  
 6481  
 (2)  
 (3)  
 6482  
 6483  
 (2)  
 6484  
 6485  
 6486  
 6487  
 6488  
 6489  
 6490  
 6491  
 6492  
 6493  
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 6495  
 6496  
 6497  
 6498  
 6499  
 6500  
 6501  
 6502  
 (2)  
 (2)  
 6503  
 6504  
 6505  
 (2)  
 (2)  
 (2)  
 6506  
 6507  
 6508

```

:      CHECK BYTE INSTRUCTIONS, NOT DESTINATION MODE 0
:-----
:*****
:*TEST: 63      NEW INSTRUCTIONS USED IN THIS SECTION ARE TSTB, CLRB, MOVB
:*****
TSTB1:
      CMP      (R5), #63
      BNE     ETSTB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF TEST
2$:   INC      (R5)
      MOV     #TEMP, R0   ; LOAD ADDRESSES
      MOV     #TEMP1, R1
      SCC
      CLRB   (R0)        ; CLEAR THE LOCATION
      JSR    PC, @#SCC4   ; CHECK FOR CC = 4
      TSTB  (R0)        ; CHECK IT
      JSR    PC, @#SCC4   ; CHECK FOR CC = 4
      MOVB  #377, (R1)   ; LOAD THE LOCATION
      JSR    PC, @#SCC10  ; CHECK FOR CC = 10
      TSTB  (R1)        ; CHECK IT
      JSR    PC, @#SCC10  ; CHECK FOR CC = 10
      MOV   R0, R2      ; R2 IS NOW POINTING TO LOCATION TEMP
      MOVB #200, 0(R2)  ; PLACE #200 IN LOCATION TEMP
      MOVB (R2)+, -(R1) ; MOVE #200 TO LOCATION TEMP+1
      CMP  -1(R1), #100200 ; CHECK THE DATA IN LOCATION TEMP
      BEQ  4$
      MOV  #227, -(R5)
      INC -(R5)
      HALT
4$:   CMP     R1, R2
      BEQ    CMPB1
ETSTB1:
      MOV   #230, -(R5)
      INC  -(R5)
      HALT
      ; MOVB INSTRUCTION FAILED
      ; CHECK THE REGISTERS FOR PROPER VALUE
      ; MOVB INSTRUCTION FAILED OR WRONG SEQUENCE
  
```

# G05

DVKAAA MACY11 27(732)  
DVKAAA.P11 T64

25-AUG-76 13:25 PAGE 54-55  
NEW INSTRUCTIONS USED IN THIS SECTION ARE CMPB, BISB

\*\*\* SEQ 0058

```

6509          ::*****
(2)          ::*TEST: 64      NEW INSTRUCTIONS USED IN THIS SECTION ARE CMPB, BISB
(3)          ::*****
6510
6511 011034   CMPB1:  CMP      (R5), #64
(2) 011034   021527   000064      BNE      ECMPB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
6512 011040   001032      INC      (R5)
6513 011042   005215      MOV     #TEMP2, R1
6514 011044   012701   000444      MOV     #TEMP, R2      ; LOAD ADDRESS
6515 011050   012702   000440      MOV     #77, (R1)      ; PLACE 77 IN LOCATION TEMP2
6516 011054   012711   000077      MOV     #377, R4      ; R4 SHOULD CONTAIN #177777
6517 011060   112704   000377      MOV     R4, (R2)      ; LOAD LOCATION
6518 011064   150412      BISB   R4, (R2)      ; CHECK FOR CC = 10
6519 011066   004737   017252      JSR    PC, @#SCC10   ; CHECK COMPARE
6520 011072   120412      CMPB   R4, (R2)      ; CONTINUE IF OK
6521 011074   001404      BEQ    2$
6522 011076   012745   000231      MOV     #231, -(R5)
(2) 011102   005245      INC     -(R5)
(2) 011104   000000      HALT
6523 011106   121112      2$:  CMPB   (R1), (R2)      ; BISB OR CMPB INSTRUCTION FAILED
6524 011110   100004      BPL    3$      ; CHECK IT AGAIN
6525 011112   012745   000232      MOV     #232, -(R5)      ; CONTINUE IF OK
(2) 011116   005245      INC     -(R5)
(2) 011120   000000      HALT
6526 011122   121211      3$:  CMPB   (R2), (R1)      ; CMPB INSTRUCTION FAILED [WRONG CC]
6527 011124   100404      BMI    BICB1      ; ONCE MORE
6528 011126      ECMPB1:  MOV     #233, -(R5)      ; CONTINUE IF OK
(2) 011126   012745   000233      INC     -(R5)
(2) 011132   005245      HALT
(2) 011134   000000

6529
6530
6531
6532          ::*****
(2)          ::*TEST: 65      NEW INSTRUCTIONS USED IN THIS SECTION ARE BICB, BITB
(3)          ::*****
6533
6534 011136   BICB1:  CMP     (R5), #65
(2) 011136   021527   000065      BEQ     2$      ; IF IN WRONG SEQUENCE GO TO HLT BELOW
6535 011142   001404      MOV     #234, -(R5)
6536 011144   012745   000234      INC     -(R5)
(2) 011150   005245      HALT      ; PROGRAM IS IN WRONG SEQUENCE
(2) 011152   000000
6537 011154   005215      2$:  INC     (R5)
6538 011156   012703   000440      MOV     #TEMP, R3      ; LOAD ADDRESS
6539 011162   112713   000377      MOV     #377, (R3)      ; LOAD LOCATION
6540 011166   012700   000442      MOV     #TEMP1, R0      ; PLACE THE ADDRESS OF LOCATION TEMP1 IN R0
6541 011172   010001      MOV     R0, R1      ; AND R1
6542 011174   112721   000252      MOV     #252, (R1)+      ; PLACE #252 IN TEMP1
6543 011200   000277      SCC
6544 011202   146013   000000      BICB   0(R0), (R3)      ; CLEAR EVERY OTHER BIT
6545 011206   004737   017102      JSR    PC, @#SCC1      ; CHECK FOR CC = 1
6546 011212   136113   177777      BITB   -1(R1), (R3)      ; CHECK IT
6547 011216   001404      BEQ    4$      ; CONTINUE IF OK
6548 011220   012745   000235      MOV     #235, -(R5)
(2) 011224   005245      INC     -(R5)

```



## H05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-56  
 DVKAAA.P11 T65 NEW INSTRUCTIONS USED IN THIS SECTION ARE BICB, BITB

\*\*\* SEQ 0059

(2)	011226	000000		HALT			: BICB OR BITB INSTRUCTION FAILED
6549	011230	132713	000125	4\$: BITB	#125,(R3)		: CHECK IT
6550	011234	004737	017102	JSR	PC, @#SCC1		: CHECK FOR CC = 1
6551	011240	154113		BISB	-(R1),(R3)		: SET THE BITS THAT WERE CLEARED
6552	011242	100404		BMI	6\$		: CONTINUE IF OK
6553	011244	012745	000236	MOV	#236,-(R5)		
(2)	011250	005245		INC	-(R5)		
(2)	011252	000000		HALT			: BITB OR BISB INSTRUCTION FAILED
6554	011254	012746	000177	6\$: MOV	#177,-(SP)		: STORE #177 ON THE STACK
6555	011260	142613		BICB	(SP)+,(R3)		: CLEAR ALL THE BITS EXCEPT SIGN BIT
6556	011262	004737	017272	JSR	PC, @#SCC11		: CHECK FOR CC = 11
6557	011266	132713	000377	BITB	#377,(R3)		: CHECK IT
6558	011272	004737	017272	JSR	PC, @#SCC11		: CHECK FOR CC = 11
6559	011276	010300		MOV	R3,R0		: PLACE THE ADDRESS OF LOCATION TEMP IN R0
6560	011300	012710	000442	MOV	#TEMP1,(R0)		: PLACE THE ADDRESS OF LOCATION TEMP1 IN TEMP
6561	011304	012730	000377	MOV	#377,@(R0)+		: WRITE A 377 IN LOCATION TEMP1
6562	011310	000263		SEVC			: SET V & C BITS
6563	011312	145070	000000	BICB	@-(R0),@(R0)		: BIT CLEAR THE CONTENTS
6564							: OF TEMP1 TO THE CONTENTS OF TEMP1
6565	011316	004737	017206	JSR	PC, @#SCC5		: CHECK FOR CC = 5
6566	011322	022027	000442	CMP	(R0)+,#TEMP1		: MAKE SURE THAT (R0) IS POINTING TO LOCATION TEMP1
6567	011326	001404		BEQ	8\$		
6568	011330	012745	000237	MOV	#237,-(R5)		
(2)	011334	005245		INC	-(R5)		
(2)	011336	000000		HALT			: BICB OR CMP INSTRUCTION FAILED IN THE SPECIFIC MODE
6569	011340	005750		8\$: TST	@-(R0)		: TEST LOCATION TEMP1
6570	011342	001404		BEQ	10\$		
6571	011344	012745	000240	MOV	#240,-(R5)		
(2)	011350	005245		INC	-(R5)		
(2)	011352	000000		HALT			: BICB INSTRUCTION FAILED
6572	011354	000257		10\$: CCC			
6573	011356	141010		BICB	(R0),(R0)		: CLEAR THE LOCATION TEMP
6574	011360	004737	017164	JSR	PC, @#SCC4		: CHECK FOR CC = 4

```

6575          ::*****
(2)          ::*TEST: 66   NEW INSTRUCTIONS USED IN THIS SECTION ARE INCB, DECB
(3)          ::*****
6576
6577 011364    021527 000066      INCB1:  CMP      (R5),#66
(2) 011364    001067              BNE      EINCB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
6578 011370    005215              INC      (R5)
6579 011372    012704 000440      1$:  MOV      #TEMP,R4      ; LOAD ADDRESS
6580 011374    112714 000177      MOVVB   #177,(R4)      ; TEMP LOCATION=177
6581 011400    000261              SEC
6582 011404    105214              INCB    (R4)          ; ADD ONES INTO LOCATION
6583 011410    004737 017334      JSR     PC,@#SCC13     ; CHECK FOR CC = 13
6584 011414    012714 000376      MOV     #376,(R4)
6585 011420    012700 017272      MOV     #SCC11,RO      ; MAKE RO POINT TO CHECKING ROUTINE FOR CC = 11
6586 011424    105224              INCB    (R4)+
6587 011426    004720              JSR     PC,(RO)+
6588 011430    105744              TSTB   -(R4)          ; CHECK FOR CC = 11
6589 011432    010426              TST    -(SP)          ; DECREMENT R4 BY 1
6590 011434    000241              MOV     R4,(SP)+      ; AND SP BY 2
6591 011436    105256              CLC
6592 011440    004737 017164      INCB    @-(SP)         ; PLACE THE ADDRESS OF TEMP ON THE STACK
6593 011442    123634              JSR     PC,@#SCC4     ; CLEAR C BIT
6594 011446    000261              CMPB   @-(SP)+,@(R4)+ ; INCREMENT THE CONTENTS OF LOCATION TEMP
6595 011450    105264 177777      SEC
6596 011452    004737 017102      INCB    -1(R4)
6597 011456    124427 000001      JSR     PC,@#SCC1     ; CHECK FOR CC = 1
6598 011462    001404              CMPB   -(R4),#1      ; CHECK IT
6599 011470    005245              BEQ    2$            ; CONTINUE IF OK
6600 011474    000000              MOV     #241,-(R5)
6601 011476    000000              INC    -(R5)
(2) 011500    000261              HALT
(2) 011502    105314              SEC          ; INCB INSTRUCTION FAILED
6602 011504    004737 017206      2$:  DECB    (R4)          ; SUBTRACT ONES FROM LOCATION
6603 011510    004737 000200 177777      JSR     PC,@#SCC5     ; CHECK FOR CC = 5
6604 011512    112764 000200 177777      DECB    (R4)+
6605 011514    105344              JSR     PC,-(RO)      ; CHECK FOR CC = 11
6606 011516    004760 177650      MOVVB   #200,-1(R4)
6607 011518    105364 000000      DECB    -(R4)
6608 011520    004737 017102      JSR     PC,@#SCC3-SCC11(RO) ; CHECK FOR CC = 3
6609 011522    126427 000000 000176      DECB    0(R4)
6610 011524    001404              JSR     PC,@#SCC1     ; CHECK FOR CC = 1
6611 011526    000000              CMPB   0(R4),#176
6612 011528    001404              BEQ    COMB1
6613 011530    012745 000242      EINCB1: MOV     #242,-(R5)
(2) 011532    005245              INC    -(R5)
(2) 011534    000000              HALT
(2) 011536    000000              ; DECB INSTRUCTION FAILED OR SEQUENCE ERROR
6615
6616
6617
6618
6619
6620
6621
6622

```



# J05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-58  
 DVKAAA.P11 T66 NEW INSTRUCTIONS USED IN THIS SECTION ARE INCB, DECB

\*\*\* SEQ 0061

```

6623
6624
(2)
(3)
6625
6626 011560 021527 000067
(2) 011560 001404
6627 011564 012745
6628 011566 012745 000243
(2) 011572 005245
(2) 011574 000000
6629 011576 005215
6630 011600 012703 000440
6631 011604 012704 000442
6632 011610 012714 000252
6633 011614 112413
6634 011616 000277
6635 011620 105113
6636 011622 004737 017102
6637 011626 122713 000125
6638 011632 001404
6639 011634 012745 000244
(2) 011640 005245
(2) 011642 000000
6640 011644 000277
6641 011646 105113
6642 011650 004737 017272
6643 011654 010400
6644 011656 126013 177777
6645 011662 001404
6646 011664 012745 000245
(2) 011670 005245
(2) 011672 000000
6647 011674 112724 000377
6648 011700 114413
6649 011702 000277
6650 011704 105113
6651 011706 004737 017206
  
```

```

*****
;TEST: 67 NEW INSTRUCTION IN THIS SECTION IS COMB
*****
COMB1:
      CMP      (R5),#67
      BEQ      1$ ; IF IN WRONG SEQUENCE GO TO HLT
      MOV      #243,-(R5)
      INC      -(R5)
      HALT     ; TEST IS IN WRONG SEQUENCE
1$:   INC      (R5) ; LOAD ADDRESS
      MOV      #TEMP,R3
      MOV      #TEMP1,R4
      MOV      #252,(R4)
      MOV      (R4)+,(R3) ; LOAD EVERY OTHER BIT
      SCC
      COMB     (R3) ; 1'S COMPLEMENT
      JSR     PC,@#SCC1 ; CHECK FOR CC = 1
      CMPB    #125,(R3) ; CHECK IT
      BEQ     2$ ; CONTINUE IF OK
      MOV      #244,-(R5)
      INC      -(R5)
      HALT     ; COMB INSTRUCTION FAILED
2$:   SCC
      COMB     (R3) ; COMPLEMENT BACK
      JSR     PC,@#SCC11 ; CHECK FOR CC = 11
      MOV      R4,R0
      CMPB    -1(R0),(R3) ; CHECK IT
      BEQ     3$ ; CONTINUE IF OK
      MOV      #245,-(R5)
      INC      -(R5)
      HALT     ; COMB INSTRUCTION FAILED
3$:   MOV      #377,(R4)+
      MOV      -(R4),(R3) ; PLACE #377 IN (R3)
      SCC
      COMB     (R3)
      JSR     PC,@#SCC5 ; CHECK FOR CC = 5
  
```

# K05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-59  
 DVKAAA.P11 T70 NEW INSTRUCTION IN THIS SECTION IS NEGB

\*\*\* SEQ 0062

```

6652
(2)
(3)
6653
6654 011712
(2) 011712 021527 000070
6655 011716 001027
6656 011720 005215
6657 011722 012700 000440
6658 011726 112710 000001
6659 011732 105410
6660 011734 004737 017272
6661 011740 122710 000377
6662 011744 001404
6663 011746 012745 000246
(2) 011752 005245
(2) 011754 000000
6664 011756 012710 000200
6665 011762 105410
6666 011764 004737 017334
6667 011770 122710 000200
6668 011774 001404
6669 011776
(2) 011776 012745 000247
(2) 012002 005245
(2) 012004 000000
6670
6671
6672
6673
(2)
(3)
6674
6675 012006
(2) 012006 021527 000071
6676 012012 001030
6677 012014 005215
6678 012016 012701 000442
6679 012022 112711 000040
6680 012026 000257
6681 012030 106111
6682 012032 106111
6683 012034 004737 017314
6684 012040 122711 000200
6685 012044 001404
6686 012046 012745 000250
(2) 012052 005245
(2) 012054 000000
6687 012056 106111
6688 012060 004737 017230
6689 012064 106111
6690 012066 122711 000001
6691 012072 001404
6692 012074
(2) 012074 012745 000251
(2) 012100 005245
  
```

```

*****
*TEST: 70 NEW INSTRUCTION IN THIS SECTION IS NEGB
*****
NEGB1:
      CMP      (R5),#70
      BNE     ENEGB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
1$:   INC      (R5)
      MOV     #TEMP, R0   ; LOAD ADDRESS
      MOVB    #1, (R0)    ; LOAD THE LOCATION
      NEGB   (R0)        ; 2'S COMPLEMENT
      JSR    PC, @#SCC11  ; CHECK FOR CC = 11
      CMPB   #377, (R0)   ; CHECK IT
      BEQ    2$          ; CONTINUE IF OK
      MOV     #246, -(R5)
      INC    -(R5)
      HALT
2$:   MOV     #200, (R0)   ; NEGB INSTRUCTION FAILED
      NEGB   (R0)        ; 2'S COMPLEMENT
      JSR    PC, @#SCC13  ; CHECK FOR CC = 13
      CMPB   #200, (R0)   ; CHECK IT
      BEQ    ROLB1       ; CONTINUE IF OK
ENEGB1:
      MOV     #247, -(R5)
      INC    -(R5)
      HALT
      ; WRONG RESULT AT TEMP OR WRONG SEQUENCE
  
```

```

*****
*TEST: 71 NEW INSTRUCTION IN THIS SECTION IS ROLB
*****
ROLB1:
      CMP      (R5),#71
      BNE     EROLB1      ; IF IN WRONG SEQUENCE GO TO HLT ABOVE
      INC     (R5)
      MOV     #TEMP1, R1  ; LOAD ADDRESS
      MOVB    #40, (R1)   ; LOAD LOCATION
      CCC
      ROLB   (R1)        ; CLEAR FLAGS
      ROLB   (R1)        ; SHIFT
      JSR    PC, @#SCC12  ; CHECK FOR CC = 12
      CMPB   #200, (R1)   ; CHECK IT
      BEQ    1$          ; CONTINUE IF OK
      MOV     #250, -(R5)
      INC    -(R5)
      HALT
1$:   ROLB   (R1)        ; ROLB INSTRUCTION FAILED
      JSR    PC, @#SCC7   ; SHIFT
      ROLB   (R1)        ; CHECK FOR CC = 7
      CMPB   #1, (R1)     ; SHIFT
      BEQ    RORB1       ; CHECK IT
      ; CONTINUE IF OK
EROLB1:
      MOV     #251, -(R5)
      INC    -(R5)
  
```



L05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-60  
DVKAAA.P11 T71 NEW INSTRUCTION IN THIS SECTION IS ROLB

\*\*\* SEQ 0063

(2) 012102 000000

HALT

; WRONG RESULT AT TEMP1 OR WRONG SEQUENCE

# M05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-61  
 DVKAAA.P11 T72 NEW INSTRUCTION IN THIS SECTION IS RORB

\*\*\* SEQ 0064

```

6693
(2)
(3)
6694
6695 012104
(2) 012104 021527 000072
6696 012110 001030
6697 012112 005215
6698 012114 012702 000442
6699 012120 112712 000004
6700 012124 000257
6701 012126 106012
6702 012130 106012
6703 012132 122712 000001
6704 012136 001404
6705 012140 012745 000252
(2) 012144 005245
(2) 012146 000000
6706 012150 106012
6707 012152 004737 017230
6708 012156 106012
6709 012160 004737 017314
6710 012164 122712 000200
6711 012170 001404
6712 012172
(2) 012172 012745 000253
(2) 012176 005245
(2) 012200 000000
6713
6714
6715
6716
(2)
(3)
6717
6718 012202
(2) 012202 021527 000073
6719 012206 001404
6720 012210 012745 000254
(2) 012214 005245
(2) 012216 000000
6721 012220 005215
6722 012222 012703 000442
6723 012226 112713 000040
6724 012232 000257
6725 012234 106313
6726 012236 106313
6727 012240 004737 017314
6728 012244 122713 000200
6729 012250 001404
6730 012252 012745 000255
(2) 012256 005245
(2) 012260 000000
6731 012262 106313
6732 012264 004737 017230
6733 012270 106313
    
```

```

;*****
;*TEST: 72 NEW INSTRUCTION IN THIS SECTION IS RORB
;*****
RORB1:
    CMP      (R5), #72
    BNE     ERORB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
    INC     (R5)
    MOV     #TEMP1, R2  ; LOAD ADDRESS
    MOVB   #4, (R2)    ; LOAD LOCATION
    CCC
    RORB   (R2)        ; CLEAR FLAGS
    RORB   (R2)        ; SHIFT
    CMPB   #1, (R2)    ; CHECK IT
    BEQ    1$          ; CONTINUE IF OK
    MOV     #252, -(R5)
    INC    -(R5)
    HALT
1$:
    RORB   (R2)        ; RORB INSTRUCTION FAILED
    JSR    PC, @#SCC7  ; SHIFT
    RORB   (R2)        ; CHECK FOR CC = 7
    JSR    PC, @#SCC12 ; SHIFT
    CMPB   #200, (R2) ; CHECK FOR CC = 12
    BEQ    ASLB1      ; CHECK IT
    MOV     #253, -(R5) ; CONTINUE IF OK
    INC    -(R5)
    HALT
    ; WRONG RESULT AT TEMP1 OR WRONG SEQUENCE
    
```

```

;*****
;*TEST: 73 NEW INSTRUCTION IN THIS SECTION IS ASLB
;*****
ASLB1:
    CMP     (R5), #73
    BEQ    2$          ; IF IN WRONG SEQUENCE GO TO HLT BELOW
    MOV     #254, -(R5)
    INC    -(R5)
    HALT
    ; PROGRAM IS IN WRONG SEQUENCE
2$:
    INC     (R5)
    MOV     #TEMP1, R3 ; LOAD ADDRESS
    MOVB   #40, (R3)  ; LOAD LOCATION
    CCC
    ASLB   (R3)        ; CLEAR FLAGS
    ASLB   (R3)        ; SHIFT
    JSR    PC, @#SCC12 ; CHECK FOR CC = 12
    CMPB   #200, (R3) ; CHECK IT
    BEQ    4$          ; CONTINUE IF OK
    MOV     #255, -(R5)
    INC    -(R5)
    HALT
    ; ASLB INSTRUCTION FAILED
4$:
    ASLB   (R3)
    JSR    PC, @#SCC7  ; SHIFT
    ASLB   (R3)        ; CHECK FOR CC = 7
    ASLB   (R3)        ; SHIFT
    
```



N05

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-62  
DVKAAA.P11 T73 NEW INSTRUCTION IN THIS SECTION IS ASLB

\*\*\* SEQ 0065

6734 012272 004737 017164 JSR PC, @#SCC4 ; CHECK FOR CC = 4

```

6735
(2)
(3)
6736
6737 012276 021527 000074
(2) 012276 001040
6738 012302 005215
6739 012304 012704 000442
6740 012306 012703 000444
6741 012312 112714 000004
6742 012316 000257
6743 012322 106214
6744 012324 106214
6745 012326 122714 000001
6746 012330 001404
6747 012334 012745 000256
(2) 012342 005245
(2) 012344 000000
6749 012346 106214
6750 012350 004737 017230
6751 012354 106214
6752 012356 004737 017164
6753 012362 112713 000202
6754 012366 106213
6755 012370 106213
6756 012372 004737 017272
6757 012376 122713 000340
6758 012402 001404
6759 012404 012745 000257
(2) 012404 005245
(2) 012410 000000
(2) 012412

```

```

*****
:TEST: 74 NEW INSTRUCTION IN THIS SECTION IS ASRB
*****

```

```

ASRB1:
      CMP      (R5),#74
      BNE     EASRB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
1$:   INC      (R5)
      MOV     #TEMP1,R4   ; LOAD ADDRESSES
      MOV     #TEMP2,R3   ; LOAD LOCATION
      MOVB   #4,(R4)      ; CLEAR FLAGS
      CCC
      ASRB   (R4)         ; SHIFT
      ASRB   (R4)
      CMPB   #1,(R4)     ; CHECK IT
      BEQ    2$          ; CONTINUE IF OK
      MOV     #256,-(R5)
      INC    -(R5)
2$:   HALT
      ASRB   (R4)         ; ASRB INSTRUCTION FAILED
      JSR    PC,#$CC7    ; SHIFT
                        ; CHECK FOR CC = 7
      ASRB   (R4)         ; SHIFT
      JSR    PC,#$CC4    ; CHECK FOR CC = 4
      MOVB   #202,(R3)   ; LOAD LOCATION
      ASRB   (R3)         ; SHIFT
      ASRB   (R3)
      JSR    PC,#$CC11   ; CHECK FOR CC = 11
      CMPB   #340,(R3)   ; CHECK IT
      BEQ    ADCB1      ; CONTINUE IF OK
EASRB1:
      MOV     #257,-(R5)
      INC    -(R5)
      HALT                ; WRONG RESULT AT TEMP2 OR WRONG SEQUENCE

```

```

6760
6761
6762
6763
(2)
(3)
6764
6765 012414 021527 000075
(2) 012414 001404
6766 012420 012745 000260
(2) 012422 005245
(2) 012426 000000
6768 012432 005215
6769 012434 012700 000444
6770 012440 105010
6771 012442 000257
6772 012444 105510
6773 012446 004737 017164
6774 012452 000261
6775 012454 105510
6776 012456 000261
6777 012460 105510

```

```

*****
:TEST: 75 NEW INSTRUCTION IN THIS SECTION IS ADCB
*****

```

```

ADCB1:
      CMP      (R5),#75
      BEQ     2$          ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV     #260,-(R5)
      INC    -(R5)
      HALT                ; PROGRAM IS IN WRONG SEQUENCE
2$:   INC      (R5)
      MOV     #TEMP2,R0   ; LOAD ADDRESS
      CLRB   (R0)         ; CLEAR THE LOCATION
      CCC
      ADCB   (R0)         ; CLEAR FLAGS
      JSR    PC,#$CC4    ; ADD C BIT = 0
                        ; CHECK FOR CC = 4
      SEC
      ADCB   (R0)         ; C=1
                        ; ADD C BIT=1
      SEC
      ADCB   (R0)         ; C=1
                        ; AGAIN

```



# C06

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-64  
 DVKAAA.P11 T75 NEW INSTRUCTION IN THIS SECTION IS ADCB

\*\*\* SEQ 0067

6778	012462	004737	017062		JSR	PC,2#SCCD	: CHECK FOR CC = 0
6779	012466	122710	000002		CMPB	#2,(R0)	: CHECK IT
6780	012472	001404			BEQ	4\$	: CONTINUE IF OK
6781	012474	012745	000261		MOV	#261,-(R5)	
(2)	012500	005245			INC	-(R5)	
(2)	012502	000000			HALT		: ADCB INSTRUCTION FAILED
6782	012504	112710	000177	4\$:	MOVB	#177,(R0)	: LOAD LARGEST POSITVE BYTE
6783	012510	000261			SEC		: C=1
6784	012512	105510			ADCB	(R0)	: ADD C BIT=1
6785	012514	004737	017314		JSR	PC,2#SCC12	: CHECK FOR CC = 12
6786	012520	122710	000200		CMPB	#200,(R0)	: CHECK IT
6787	012524	001404			BEQ	6\$	: CONTINUE IF OK
6788	012526	012745	000262		MOV	#262,-(R5)	
(2)	012532	005245			INC	-(R5)	
(2)	012534	000000			HALT		: ADCB INSTRUCTION FAILED
6789	012536	112710	000377	6\$:	MOVB	#377,(R0)	: LOAD -1
6790	012542	000261			SEC		: C=1
6791	012544	105510			ADCB	(R0)	: ADD C BIT=1
6792	012546	004737	017206		JSR	PC,2#SCC5	: CHECK FOR CC = 5

```

6793          ;*****
(2)          ;*TEST: 76      NEW INSTRUCTION IN THIS SECTION IS SBCB
(3)          ;*****
6794
6795 012552    SBCB1:  CMP      (R5),#76
(2) 012552    021527 000076  BEQ      1$          ; IF IN WRONG SEQUENCE GO TO HLT
6796 012556    001404  BEQ      1$          ; IF IN WRONG SEQUENCE GO TO HLT
6797 012560    012745 000263  MOV      #263,-(R5)
(2) 012564    005245  INC      -(R5)
(2) 012566    000000  HALT
6798 012570    005215  1$:      INC      (R5)          ; TEST IS IN WRONG SEQUENCE
6799 012572    012701 000444  MOV      #TEMP2,R1    ; LOAD ADDRESS
6800 012576    112711 000003  MOV      #3,(R1)      ; LOAD LOCATION
6801 012602    000257  CCC      ; CLEAR FLAGS
6802 012604    105611  SBCB     (R1)          ; SUBTRACT C BIT=0
6803 012606    004737 017062  JSR      PC,#$CC0     ; CHECK FOR CC = 0
6804 012612    122711 000003  CMP      #3,(R1)      ; CHECK IT
6805 012616    001404  BEQ      2$          ; CONTINUE IF OK
6806 012620    012745 000264  MOV      #264,-(R5)
(2) 012624    005245  INC      -(R5)
(2) 012626    000000  HALT
6807 012630    000261  2$:      SEC          ; SBCB INSTRUCTION FAILED
6808 012632    105611  SBCB     (R1)          ; C=1
6809 012634    000261  SEC      ; SUBTRACT C BIT=1
6810 012636    105611  SBCB     (R1)          ; C=1
6811 012640    004737 017062  JSR      PC,#$CC0     ; CHECK FOR CC = 0
6812 012644    122711 000001  CMP      #1,(R1)      ; CHECK IT
6813 012650    001404  BEQ      3$          ; CONTINUE IF OK
6814 012652    012745 000265  MOV      #265,-(R5)
(2) 012656    005245  INC      -(R5)
(2) 012660    000000  HALT
6815 012662    000261  3$:      SEC          ; SBCB INSTRUCTION FAILED
6816 012664    105611  SBCB     (R1)          ; C=1
6817 012666    004737 017164  JSR      PC,#$CC4     ; SUBTRACT C BIT=1
6818 012672    000261  SEC      ; CHECK FOR CC = 4
6819 012674    105611  SBCB     (R1)          ; C=1
6820 012676    004737 017272  JSR      PC,#$CC11    ; SUBTRACT C BIT = 1
6821 012702    122711 000377  CMP      #377,(R1)    ; CHECK FOR CC = 11
6822 012706    001404  BEQ      4$          ; CHECK IT
6823 012710    012745 000266  MOV      #266,-(R5)
(2) 012714    005245  INC      -(R5)
(2) 012716    000000  HALT
6824 012720    112711 000200  4$:      MOV      #200,(R1)    ; SBCB INSTRUCTION FAILED
6825 012724    000261  SEC      ; LOAD R1
6826 012726    105611  SBCB     (R1)          ; C=1
6827 012730    004737 017122  JSR      PC,#$CC2     ; SUBTRACT C BIT = 1
                          ; CHECK FOR CC = 2

```



6832  
6833  
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(2)  
(3)  
6842  
6843  
(2)  
6844  
6845  
(2)  
(2)  
6846  
6847  
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6849  
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6851  
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6862

⋮

CHECK WORD INSTRUCTIONS, NOT DESTINATION MODE 0  
-----

⋮\*\*\*\*\*  
⋮\*TEST: 77 NEW INSTRUCTIONS USED IN THIS SECTION ARE TST, CLR, MOV  
⋮\*\*\*\*\*

```

TST1:  CMP      (R5), #77
        BEQ      1$          ; IF IN WRONG SEQUENCE GO TO HLT
        MOV      #267, -(R5)
        INC      -(R5)
        HALT
1$:     INC      (R5)          ; TEST IS IN A WRONG SEQUENCE
        MOV      #TEMP, R1    ; LOAD ADDRESSES
        MOV      #TEMP1, R0
        SCC
        CLR      (R0)         ; CLEAR THE LOCATION
        JSR      PC, @#SCC4   ; CHECK FOR CC = 4
        TST      (R0)+        ; CHECK IT
        JSR      PC, @#SCC4   ; CHECK FOR CC = 4
        MOV      R0, -(R0)
        MOV      #177777, @ (R0)+
        MOV      @-2(R0), (R1) ; LOAD THE LOCATION
        JSR      PC, @#SCC10  ; CHECK FOR CC = 10
        TST      (R1)         ; CHECK IT
        JSR      PC, @#SCC10  ; CHECK FOR CC = 10

```

# F06

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-67  
 DVKAAA.P11 T100 NEW INSTRUCTIONS USED IN THIS SECTION ARE CMP, BIS

\*\*\* SEQ 0070

```

6863          ;*****
(2)          ;*TEST: 100   NEW INSTRUCTIONS USED IN THIS SECTION ARE CMP, BIS
(3)          ;*****
6864
6865 013026    CMP1:      CMP      (R5), #100
(2) 013026    021527    000100    BNE     ECMP1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE EST
6866 013032    001113    1$:      INC      (R5)
6867 013034    005215    MOV     #TEMP1, R2 ; LOAD ADDRESS
6868 013036    012702    000442    MOV     #TEMP, R0  ; PLACE THE ADDRESS OF TEMP IN R0
6869 013042    012700    000440    MOV     #177777, (R0)+ ; PLACE #177777 IN LOCATION TEMP AND INC. R0 BY 2
6870 013046    012720    177777    BIS     -(R0), (R2) ; LOAD LOCATION
6871 013052    054012    6872:    JSR     PC, #1000  ; CHECK FOR CC = 10
6872 013054    004737    017252    CMP     (R2)+, #177777 ; CHECK COMPARE
6873 013060    022227    177777    BEQ     2$        ; CONTINUE IF OK
6874 013064    001404
6875 013066    012745    000270    MOV     #270, -(R5)
(2) 013072    005245    INC     -(R5)
(2) 013074    000000    HALT
6876 013076    020227    000444    2$:      CMP     R2, #TEMP1+2 ; CMP OR BIS INSTRUCTION FAILED
6877 013102    001404    BEQ     3$        ; CHECK R2 TO CONTAIN ADDRESS OF TEMP1+2
6878 013104    012745    000271    MOV     #271, -(R5)
(2) 013110    005245    INC     -(R5)
(2) 013112    000000    HALT
6879 013114    022742    000077    3$:      CMP     #77, -(R2) ; NO AUTO INCREMENT
6880 013120    004737    017102    JSR     PC, #1000  ; CHECK IT AGAIN
6881 013124    022722    077777    CMP     #77777, (R2)+ ; CHECK FOR CC = 1
6882 013130    004737    017334    JSR     PC, #1000  ; CHECK FOR CC = 13
6883 013134    024227    077777    CMP     -(R2), #77777 ; ONCE MORE
6884 013140    004737    017252    JSR     PC, #1000  ; CHECK FOR CC = 10
6885 013144    012767    052525    165272  MOV     #52525, TEMP2 ; SET EVERY OTHER BIT IN TEMP2
6886 013152    012767    000444    165262  MOV     #TEMP2, TEMP1 ; PLACE THE ADDRESS OF TEMP2 IN LOCATION TEMP1
6887 013160    012704    000430    MOV     #ADR, R4
6888 013164    012714    000432    MOV     #ADR1, (R4) ; PLACE THE ADDRESS OF ADR1 IN ADR POINTED BY R4
6889 013170    012734    125252    MOV     #125252, a(R4)+ ; PLACE THE #125252 IN LOCATION ADR1
6890 013174    057432    177776    BIS     a-2(R4), a(R2)+ ; SET EVERY OTHER BIT AT LOCATION TEMP2
6891
6892 013200    010200    MOV     R2, R0 ; AND INCREMENT R2 BY 2
6893 013202    025027    177777    CMP     a-(R0), #177777 ; PLACE ADDRESS OF TEMP2 IN R0
6894 013206    001404    BEQ     4$        ; TEMP2 SHOULD CONTAIN ALL 1'S
6895 013210    012745    000272    MOV     #272, -(R5)
(2) 013214    005245    INC     -(R5)
(2) 013216    000000    HALT
6896 013220    020227    000444    4$:      CMP     R2, #TEMP1+2 ; CMP OR BIS INSTUCTIONS FAILED IN MODES OTHER THAN 0
6897
6898 013224    001404    BEQ     5$        ; R2 SHOULD CONTAIN THE ADDRESS FOR TEMP2
6899 013226    012745    000273    MOV     #273, -(R5) ; I.E. TEMP1+2
(2) 013232    005245    INC     -(R5)
(2) 013234    000000    HALT
6900 013236    005040    5$:      CLR     -(R0) ; MODE 5 IS FAILING
6901 013240    010067    165200    MOV     R0, TEMP2 ; PLACE A 0 IN LOCATION TEMP
6902 013244    022020    CMP     (R0)+, (R0)+ ; PLACE ADDRESS OF TEMP IN LOCATION TEMP2
6903 013246    055070    000002    BIS     a-(R0), a(R0)+ ; BUMP R0 BY 4
6904 013252    022767    000440    165160  CMP     #TEMP, TEMP ; PLACE THE CONTENTS OF LOCATION TEMP2 AT TEMP
6905 013260    001404    BEG     BIC1      ; LOCATION TEMP SHOULD CONTAIN ITS OWN ADDRESS
6906 013262
(2) 013262    012745    000274    ECMP1:  MOV     #274, -(R5)
    
```



# G06

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-68  
 DVKAAA.P11 T100 NEW INSTRUCTIONS USED IN THIS SECTION ARE CMP, BIS

\*\*\* SEQ 0071

```

(2) 013266 005245
(2) 013270 000000
6907
6908
6909
6910
6911
(2)
(3)
6912
6913 013272
(2) 013272 021527 000101
6914 013276 001122
6915 013300 005215
6916 013302 012703 000440
6917 013306 012713 177777
6918 013312 012704 000430
6919 013316 012714 000432
6920 013322 011334
6921 013324 012700 000442
6922 013330 012710 125252
6923 013334 000277
6924 013336 042013
6925 013340 004737 017102
6926 013344 034013
6927 013346 001404
6928 013350 012745 000275
(2) 013354 005245
(2) 013356 000000
6929 013360 032713 052525
6930 013364 004737 017102
6931 013370 056013 000000
6932 013374 100404
6933 013376 012745 000276
(2) 013402 005245
(2) 013404 000000
6934 013406 012720 077777
6935 013412 010002
6936 013414 046213 177776
6937 013420 004737 017272
6938 013424 020027 000444
6939 013430 001404
6940 013432 012745 000277
(2) 013436 005245
(2) 013440 000000
6941 013442 010020
6942 013444 000263
6943 013446 045000
6944 013450 004737 017206
6945 013454 037413 177776
6946 013460 004737 017272
6947 013464 012746 125252
6948 013470 017423 177776
6949 013474 046643 000000
6950 013500 022327 052525
6951 013504 001404
  
```

```

INC -(R5)
HALT ; CMP OR BIS INSTRUCTIONS FAILED OR WRONG
; SEQUENCE COUNTER
  
```

```

*****
*TEST: 101 NEW INSTRUCTIONS USED IN THIS SECTION ARE BIC, BIT
*****
  
```

BIC1:

```

CMP (R5), #101
BNE EBIC1 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
INC (R5)
MOV #TEMP, R3 ; LOAD ADDRESS
MOV #177777, (R3) ; LOAD LOCATION
MOV #ADR, R4 ; PLACE THE ADDRESS OF ADR IN R4
MOV #ADR1, (R4) ; PLACE THE ADDRESS OF ADR1 IN ADR
MOV (R3), (R4)+ ; LOAD LOCATION ADR1 WITH #177777
MOV #TEMP1, R0 ; PLACE THE ADDRESS OF TEMP1 IN R0
MOV #125252, (R0) ; SET EVERY OTHER BIT AT LOCATION TEMP1
SCC
BIC (R0)+, (R3) ; CLEAR EVERY OTHER BIT
JSR PC, @#SCC1 ; CHECK FOR CC = 1
BIT -(R0), (R3) ; CHECK IT
BEQ 1$ ; CONTINUE IF OK
MOV #275, -(R5)
INC -(R5)
HALT ; BIC OR BIT INSTRUCTION FAILED
1$: BIT #52525, (R3) ; CHECK IT
JSR PC, @#SCC1 ; CHECK FOR CC = 1
BIS 0(R0), (R3) ; SET THE BITS THAT WERE CLEARED
BMI 2$ ; CONTINUE IF OK
MOV #276, -(R5)
INC -(R5)
HALT ; BIT OR BIS INSTRUCTION FAILED
2$: MOV #77777, (R0)+ ; SET ALL THE BITS AT LOCATION TEMP1 EXCEPT SIGN BIT
MOV R0, R2
BIC -2(R2), (R3) ; TRY CLEARING THE OTHER BITS
JSR PC, @#SCC1 ; CHECK FOR CC = 11
CMP R0, #TEMP1+2 ; R0 SHOULD CONTAIN THE ADDRESS OF TEMP1+2
BEQ 3$
MOV #277, -(R5)
INC -(R5)
HALT
3$: MOV R0, (R0)+ ; PLACE THE ADDRESS OF LOCATION TEMP2 IN TEMP2
SEVC ; SET V & C BITS
BIC @-(R0), R0 ; CLEAR R0
JSR PC, @#SCC5 ; CHECK FOR CC = 5
BIT @-2(R4), (R3) ; CHECK IT
JSR PC, @#SCC11 ; CHECK FOR CC = 11
MOV #125252, -(SP) ; SET EVERY OTHER BIT ON THE STACK
MOV @-2(R4), (R3)+ ; SET ALL THE BITS AT LOCATION TEMP
BIC 0(SP), -(R3) ; CLEAR EVERY OTHER BIT AT LOCATION TEMP
CMP (R3)+, #52525 ; TEMP SHOULD CONTAIN # 52525
BEQ 4$
  
```

# H06

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-69  
 DVKAAA.P11 T101 NEW INSTRUCTIONS USED IN THIS SECTION ARE BIC, BIT

\*\*\* SEQ 0072

6952	013506	012745	000300		MOV	#300, -(R5)	
(2)	013512	005245			INC	-(R5)	
(2)	013514	000000			HALT		: BIC FAILED IN MODE 6
6953	013516	012700	000446	4S:	MOV	#TEMP2+2, R0	: PLACE THE ADDRESS OF TEMP2+2 IN R0
6954	013522	010340			MOV	R3, -(R0)	: PLACE THE ADDRESS OF TEMP1 IN TEMP2
6955	013524	014330			MOV	-(R3), @ (R0)+	: MOVE # 52525 IN LOCATION TEMP1
6956	013526	000263			SEVC		: SET V & C BITS
6957	013530	035026			BIT	@-(R0), (SP)+	: BIT TEST TEMP1 WITH STACK AND RESTORE STACK POINTER
6958	013532	004737	017206		JSR	PC, @#SCC5	: CHECK FOR CC = 5
6959	013536	020627	000530		CMP	SP, #START	: MAKE SURE THAT THE SP IS OK
6960	013542	001404			BEQ	INC1	
6961	013544			EBIC1:			
(2)	013544	012745	000301		MOV	#301, -(R5)	
(2)	013550	005245			INC	-(R5)	
(2)	013552	000000			HALT		: STACK POINTER FOULED UP OR SEQUENCE ERROR



```

6962          ::*****
(2)          ::*TEST: 102   NEW INSTRUCTIONS USED IN THIS SECTION ARE INC, DEC
(3)          ::*****
6963
6964 013554          INC1:  CMP      (R5),#102
(2) 013554 021527 000102          BEQ      2$          ; IF IN WRONG SEQUENCE GO TO HLT BELOW
6965 013560 001404          MOV      #302,-(R5)
6966 013562 012745 000302          INC      -(R5)
(2) 013566 005245          HALT          ; PROGRAM IS IN WRONG SEQUENCE
(2) 013570 000000          2$:  INC      (R5)
6967 013572 005215          MOV      #TEMP1,R4          ; LOAD ADDRESS
6968 013574 012704 000442          MOV      #77777,(R4)          ; TEMP1 = 77777
6969 013600 012714 077777          SEC
6970 013604 000261          INC      (R4)          ; ADD ONES INTO LOCATION
6971 013606 005214          JSR      PC,@#SCC13          ; CHECK FOR CC = 13
6972 013610 004737 017334          MOV      #177776,(R4)
6973 013614 012714 177776          MOV      #TEMP,R0          ; R0 IS POINTING TO LOCATION TEMP
6974 013620 012700 000440          MOV      #SCC11,(R0)          ; PLACE THE ADDRESS OF SUBROUTINE TO CHECK CC = 11
6975 013624 012710 017272          ; IN LOCATION TEMP
6976
6977 013630 005214          INC      (R4)
6978 013632 004730          JSR      PC,@(R0)+          ; CHECK FOR CC = 11
6979 013634 005214          INC      (R4)
6980 013636 004737 017206          JSR      PC,@#SCC5          ; CHECK FOR CC = 5
6981 013642 005214          INC      (R4)
6982 013644 004737 017102          JSR      PC,@#SCC1          ; CHECK FOR CC = 1
6983 013650 026427 000000 000001          CMP      C(R4),#1          ; CHECK IT
6984 013656 001404          BEQ      4$          ; CONTINUE IF OK
6985 013660 012745 000303          MOV      #303,-(R5)
(2) 013664 005245          INC      -(R5)
(2) 013666 000000          HALT          ; INC INSTRUCTION FAILED
6986 013670 000261          4$:  SEC
6987 013672 005314          DEC      (R4)          ; SUBTRACT ONES FROM LOCATION
6988 013674 004737 017206          JSR      PC,@#SCC5          ; CHECK FOR CC = 5
6989 013700 005314          DEC      (R4)
6990 013702 004770 177776          JSR      PC,@-2(R0)          ; CHECK FOR CC = 11
6991 013706 012714 100000          MOV      #100000,(R4)
6992 013712 005314          DEC      (R4)
6993 013714 004737 017142          JSR      PC,@#SCC3          ; CHECK FOR CC = 3
6994 013720 005314          DEC      (R4)
6995 013722 004737 017102          JSR      PC,@#SCC1          ; CHECK FOR CC = 1
6996
6997
6998
6999          ::*****
(2)          ::*TEST: 103   NEW INSTRUCTION IN THIS SECTION IS COM
(3)          ::*****
7000
7001 013726          COM1:  CMP      (R5),#103
(2) 013726 021527 000103          BEQ      1$          ; IF IN WRONG SEQUENCE GO TO HLT
7002 013732 001404          MOV      #304,-(R5)
7003 013734 012745 000304          INC      -(R5)
(2) 013740 005245          HALT          ; TEST IS IN WRONG SEQUENCE
(2) 013742 000000          1$:  INC      (R5)
7004 013744 005215          MOV      #TEMP1,R3          ; LOAD ADDRESS
7005 013746 012703 000442
    
```

# JOB

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-71  
 DVKAAA.P11 T103 NEW INSTRUCTION IN THIS SECTION IS COM

\*\*\* SEQ 0074

7006	013752	012713	125252		MOV	#125252, (R3)	; LOAD EVERY OTHER BIT
7007	013756	000277			SCC		
7008	013760	005163	000000		COM	0(R3)	; 1'S COMPLEMENT
7009	013764	004737	017102		JSR	PC, @#SCC1	; CHECK FOR CC = 1
7010	013770	022713	052525		CMP	#52525, (R3)	; CHECK IT
7011	013774	001404			BEG	2\$	; CONTINUE IF OK
7012	013776	012745	000305		MOV	#305, -(R5)	
(2)	014002	005245			INC	-(R5)	
(2)	014004	000000			HALT		; COM INSTRUCTION FAILED
7013	014006	000277		2\$:	SCC		
7014	014010	005123			COM	(R3)+	; COMPLEMENT BACK
7015	014012	004737	017272		JSR	PC, @#SCC11	; CHECK FOR CC = 11
7016	014016	022743	125252		CMP	#125252, -(R3)	; CHECK IT
7017	014022	001404			BEG	3\$	; CONTINUE IF OK
7018	014024	012745	000306		MOV	#306, -(R5)	
(2)	014030	005245			INC	-(R5)	
(2)	014032	000000			HALT		; COM INSTRUCTION FAILED
7019	014034	010300		3\$:	MOV	R3, R0	; R0 IS NOW POINTING TO LOCATION TEMP1
7020	014036	012710	177777		MOV	#177777, (R0)	
7021	014042	000277			SCC		
7022	014044	005110			COM	(R0)	
7023	014046	004737	017206		JSR	PC, @#SCC5	; CHECK FOR CC = 5



# K06

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-72  
 DVKAAA.P11 T104 NEW INSTRUCTION IN THIS SECTION IS NEG

\*\*\* SEQ 0075

```

7024      ::*****
(2)      ::*TEST: 104   NEW INSTRUCTION IN THIS SECTION IS NEG
(3)      ::*****
7025
7026 014052 021527 000104      NEG1:  CMP      (R5), #104
(2) 014052 001033      BNE      ENEG1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7027 014056 005215      1$:  INC      (R5)
7028 014060 012704 000442      MOV      #TEMP1,R4      ; LOAD ADDRESS
7029 014062 012704 000001      MOV      #1,(R4)+      ; LOAD THE LOCATION
7030 014066 010402      MOV      R4,R2
7031 014072 012762 100000 000000      MOV      #100000,0(R2)
7032 014102 005444      NEG      -(R4)      ; 2'S COMPLEMENT
7033 014104 004737 017272      JSR      PC,@#SCC11      ; CHECK FOR CC = 11
7034 014110 022724 177777      CMP      #177777,(R4)+      ; CHECK IT
7035 014114 001404      BEQ      2$      ; CONTINUE IF OK
7036 014116 012745 000307      MOV      #307,-(R5)
(2) 014122 005245      INC      -(R5)
(2) 014124 000000      HALT      ; NEG INSTRUCTION FAILED
7038 014126 016444 000000      2$:  MOV      0(R4),-(R4)      ; TEMP1 CONTAINS THE LARGEST NEGATIVE NUMBER
7039 014132 005414      NEG      (R4)      ; 2'S COMPLEMENT
7040 014134 004737 017334      JSR      PC,@#SCC13      ; CHECK FOR CC = 13
7041 014140 026214 000000      CMP      0(R2),(R4)      ; CHECK IT
7042 014144 001404      BEQ      ROL1      ; CONTINUE IF OK
7043 014146      ENEG1:  MOV      #310,-(R5)
(2) 014146 012745 000310      INC      -(R5)
(2) 014152 005245      HALT      ; WRONG RESULT IN TEMP2 OR WRONG SEQUENCE
(2) 014154 000000
7044
7045
7046
7047      ::*****
(2)      ::*TEST: 105   NEW INSTRUCTION IN THIS SECTION IS ROL
(3)      ::*****
7048
7049 014156 021527 000105      ROL1:  CMP      (R5), #105
(2) 014156 001032      BNE      EROL1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7050 014162 005215      INC      (R5)
7051 014164 012701 000444      MOV      #TEMP2,R1      ; LOAD ADDRESS
7052 014166 012711 020000      MOV      #20000,(R1)      ; LOAD LOCATION
7053 014172 000257      CCC      ; CLEAR FLAGS
7054 014176 006121      ROL      (R1)+      ; SHIFT
7055 014200 006141      ROL      -(R1)
7056 014202 004737 017314      JSR      PC,@#SCC12      ; CHECK FOR CC = 12
7057 014210 022711 100000      CMP      #100000,(R1)      ; CHECK IT
7058 014214 001404      BEQ      1$      ; CONTINUE IF OK
7059 014216 012745 000311      MOV      #311,-(R5)
(2) 014222 005245      INC      -(R5)
(2) 014224 000000      HALT      ; ROL INSTRUCTION FAILED
7061 014226 006161 000000      1$:  ROL      0(R1)      ; SHIFT
7062 014232 004737 017230      JSR      PC,@#SCC7      ; CHECK FOR CC = 7
7063 014236 010102      MOV      R1,R2      ; R2 IS NOW POINTING TO LOCATION TEMP2
7064 014240 006112      ROL      (R2)      ; SHIFT
7065 014242 022711 000001      CMP      #1,(R1)      ; CHECK IT
7066 014246 001404      BEQ      ROR1      ; CONTINUE IF OK
  
```

L06

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-73  
DVKAAA.P11 T105 NEW INSTRUCTION IN THIS SECTION IS ROL

\*\*\* SEQ 0076

7067 014250  
(2) 014250 012745 000312  
(2) 014254 005245  
(2) 014256 000000

EROL1: MOV #312, -(R5)  
INC -(R5)  
HALT

; WRONG RESULT AT TEMP2 OR WRONG SEQUENCE



# MO6

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-74  
 DVKAAA.P11 T106 NEW INSTRUCTION IN THIS SECTION IS ROR

\*\*\* SEQ 0077

```

7068          ;*****
(2)          ;*TEST: 106   NEW INSTRUCTION IN THIS SECTION IS ROR
(3)          ;*****
7069
7070 014260   021527 000106   ROR1:      CMP      (R5), #106
(2) 014260   001030          BNE      EROR1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7071 014264   005215          INC      (R5)
7072 014270   012702 000444   MOV      #TEMP2, R2   ; LOAD ADDRESS
7073 014274   012712 000004   MOV      #4, (R2)     ; LOAD LOCATION
7074 014300   000257          CCC
7075 014302   006012          ROR      (R2)         ; CLEAR FLAGS
7076 014304   006012          ROR      (R2)         ; SHIFT
7077 014306   022712 000001   CMP      #1, (R2)     ; CHECK IT
7078 014312   001404          BEQ      1$           ; CONTINUE IF OK
7079 014314   012745 000313   MOV      #313, -(R5)
7080 014320   005245          INC      -(R5)
(2) 014322   000000          HALT
(2) 014324   006012   1$:      ROR      (R2)         ; ROR INSTRUCTION FAILED
7081 014326   004737 017230   JSR      PC, @#SCC7   ; SHIFT
7082 014332   006012          ROR      (R2)         ; CHECK FOR CC = 7
7083 014334   004737 017314   JSR      PC, @#SCC12  ; SHIFT
7084 014340   022712 100000   CMP      #100000, (R2) ; CHECK FOR CC = 12
7085 014344   001404          BEQ      ASL1         ; CHECK IT
7086 014346   012745 000314   EROR1:   MOV      #314, -(R5) ; CONTINUE IF OK
(2) 014352   005245          INC      -(R5)
(2) 014354   000000          HALT                ; WRONG RESULT AT TEMP2 OR WRONG SEQUENCE
7088
7089
7090
7091          ;*****
(2)          ;*TEST: 107   NEW INSTRUCTION IN THIS SECTION IS ASL
(3)          ;*****
7092
7093 014356   021527 000107   ASL1:     CMP      (R5), #107
(2) 014356   001404          BEQ      2$           ; IF IN WRONG SEQUENCE GO TO HLT BELOW
7094 014362   012745 000315   MOV      #315, -(R5)
7095 014370   005245          INC      -(R5)
(2) 014372   000000          HALT                ; PROGRAM IS IN WRONG SEQUENCE
7096 014374   005215   2$:      INC      (R5)
7097 014376   012703 000444   MOV      #TEMP2, R3   ; LOAD ADDRESS
7098 014402   012713 020000   MOV      #20000, (R3) ; LOAD LOCATION
7099 014406   000257          CCC                ; CLEAR FLAGS
7100 014410   006313          ASL      (R3)         ; SHIFT
7101 014412   006313          ASL      (R3)
7102 014414   004737 017314   JSR      PC, @#SCC12  ; CHECK FOR CC = 12
7103 014420   022713 100000   CMP      #100000, (R3) ; CHECK IT
7104 014424   001404          BEQ      4$           ; CONTINUE IF OK
7105 014426   012745 000316   MOV      #316, -(R5)
(2) 014432   005245          INC      -(R5)
(2) 014434   000000          HALT                ; ASL INSTRUCTION FAILED
7106 014436   006313   4$:      ASL      (R3)         ; SHIFT
7107 014440   004737 017230   JSR      PC, @#SCC7   ; CHECK FOR CC = 7
7108 014444   006313          ASL      (R3)         ; SHIFT
  
```

N06

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-75  
DVKAAA.P11 T107 NEW INSTRUCTION IN THIS SECTION IS ASL

\*\*\* SEQ 0078

7109 014446 004737 017164 JSR PC, @SCC4 ; CHECK FOR CC = 4



```

7110
(2)
(3)
7111
7112 014452 021527 000110
(2) 014452
7113 014456 001040
7114 014460 005215
7115 014462 012704 000444
7116 014466 012703 000440
7117 014472 012714 000004
7118 014476 000257
7119 014500 006214
7120 014502 006214
7121 014504 022714 000001
7122 014510 001404
7123 014512 012745 000317
(2) 014516 005245
(2) 014520 000000
7124 014522 006214
7125 014524 004737 017230
7126 014530 006214
7127 014532 004737 017164
7128 014536 012713 100002
7129 014542 006213
7130 014544 006213
7131 014546 004737 017272
7132 014552 022713 160000
7133 014556 001404
7134 014560
(2) 014560 012745 000320
(2) 014564 005245
(2) 014566 000000
7135
7136
7137
7138

```

```

*****
:TEST: 110 NEW INSTRUCTION IN THIS SECTION IS ASR
*****

```

```

ASR1:
      CMP      (R5),#110
      BNE     EASR1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
2$:   INC      (R5)
      MOV     #TEMP2,R4  ; LOAD ADDRESSES
      MOV     #TEMP,R3
      MOV     #4,(R4)    ; LOAD LOCATION
      CCC
      ASR     (R4)       ; CLEAR FLAGS
      ASR     (R4)       ; SHIFT
      CMP     #1,(R4)    ; CHECK IT
      BEQ     2$        ; CONTINUE IF OK
      MOV     #317,-(R5)
      INC     -(R5)
      HALT
2$:   ASR     (R4)       ; ASR INSTRUCTION FAILED
      JSR     PC,#$CC7   ; CHECK FOR CC = 7
      ASR     (R4)       ; SHIFT
      JSR     PC,#$CC4   ; CHECK FOR CC = 4
      MOV     #100002,(R3) ; LOAD LOCATION
      ASR     (R3)       ; SHIFT
      ASR     (R3)
      JSR     PC,#$CC11  ; CHECK FOR CC = 11
      CMP     #160000,(R3) ; CHECK IT
      BEQ     ADC1      ; CONTINUE IF OK
EASR1:
      MOV     #320,-(R5)
      INC     -(R5)
      HALT
      ; WRONG RESULT IN TEMP OR WRONG SEQUENCE

```

```

7139
(2)
(3)
7140 014570 021527 000111
(2) 014570
7141 014574 001404
7142 014576 012745 000321
(2) 014602 005245
(2) 014604 000000
7143 014606 005215
7144 014610 012700 000440
7145 014614 005010
7146 014616 000257
7147 014620 005510
7148 014622 004737 017164
7149 014626 000261
7150 014630 005510
7151 014632 000261
7152 014634 005510

```

```

*****
:TEST: 111 NEW INSTRUCTION IN THIS SECTION IS ADC
*****

```

```

ADC1:
      CMP     (R5),#111
      BEQ     2$        ; IF IN WRONG SEQUENCE GO TO HLT BELOW
      MOV     #321,-(R5)
      INC     -(R5)
      HALT
      ; PROGRAM IS IN WRONG SEQUENCE
2$:   INC     (R5)
      MOV     #TEMP,RO  ; LOAD ADDRESS
      CLR     (RO)      ; CLEAR THE LOCATION
      CCC
      ADC     (RO)      ; CLEAR FLAGS
      JSR     PC,#$CC4   ; ADD C BIT = 0
      SEC
      ; CHECK FOR CC = 4
      ; C=1
      ADC     (RO)      ; ADD C BIT=1
      SEC
      ; C=1
      ADC     (RO)      ; AGAIN

```

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-77  
 DVKAAA.P11 T111 NEW INSTRUCTION IN THIS SECTION IS ADC

\*\*\* SEQ 0080

7153	014636	004737	017062		JSR	PC, @#SCC0	:	CHECK FOR CC = 0
7154	014642	022710	000002		CMP	#2, (R0)	:	CHECK IT
7155	014646	001404			BEQ	4\$	:	CONTINUE IF OK
7156	014650	012745	000322		MOV	#322, -(R5)		
(2)	014654	005245			INC	-(R5)		
(2)	014656	000000			HALT		:	ADC INSTRUCTION FAILED
7157	014660	012710	077777	4\$:	MOV	#77777, (R0)	:	LOAD LARGEST POSITIVE NUMBER
7158	014664	000261			SEC		:	C=1
7159	014666	005510			ADC	(R0)	:	ADD C BIT=1
7160	014670	004737	017314		JSR	PC, @#SCC12	:	CHECK FOR CC = 12
7161	014674	022710	100000		CMP	#100000, (R0)	:	CHECK IT
7162	014700	001404			BEQ	6\$	:	CONTINUE IF OK
7163	014702	012745	000323		MOV	#323, -(R5)		
(2)	014706	005245			INC	-(R5)		
(2)	014710	000000			HALT		:	ADC INSTRUCTION AILED
7164	014712	012710	177777	6\$:	MOV	#-1, (R0)	:	LOAD -1
7165	014716	000261			SEC		:	C=1
7166	014720	005510			ADC	(R0)	:	ADD C BIT=1
7167	014722	004737	017206		JSR	PC, @#SCC5	:	CHECK FOR CC = 5



7168 (2) (3) \*\*\*\*\*  
\*TEST: 112 NEW INSTRUCTION IN THIS SECTION IS SBC  
\*\*\*\*\*

```

7169
7170 014726 021527 000112 SBC1: CMP (R5),#112
(2) 014726 001404 BEQ 1$ ; IF IN WRONG SEQUENCE GO TO HLT
7171 014732 012745 000324 MOV #324,-(R5)
(2) 014740 005245 INC -(R5)
(2) 014742 000000 HALT ; TEST IS IN WRONG SEQUENCE
7173 014744 005215 1$: INC (R5)
7174 014746 012701 000440 MOV #TEMP,R1 ; LOAD ADDRESS
7175 014752 012711 000003 MOV #3,(R1) ; LOAD LOCATION
7176 014756 000257 CCC ; CLEAR FLAGS
7177 014760 005611 SBC (R1) ; SUBTRACT C BIT=0
7178 014762 004737 017062 JSR PC,#$CC0 ; CHECK FOR CC = 0
7179 014766 022711 000003 CMP #3,(R1) ; CHECK IT
7180 014772 001404 BEQ 2$ ; CONTINUE IF OK
7181 014774 012745 000325 MOV #325,-(R5)
(2) 015000 005245 INC -(R5)
(2) 015002 000000 HALT ; SBC INSTRUCTION FAILED
7182 015004 000261 2$: SEC ; C=1
7183 015006 005611 SBC (R1) ; SUBTRACT C BIT=1
7184 015010 000261 SEC ; C=1
7185 015012 005611 SBC (R1)
7186 015014 004737 017062 JSR PC,#$CC0 ; CHECK FOR CC = 0
7187 015020 022711 000001 CMP #1,(R1) ; CHECK IT
7188 015024 001404 BEQ 3$
7189 015026 012745 000326 MOV #326,-(R5)
(2) 015032 005245 INC -(R5)
(2) 015034 000000 HALT ; SBC INSTRUCTION FAILED
7190 015036 000261 3$: SEC ; C=1
7191 015040 005611 SBC (R1) ; SUBTRACT C BIT=1
7192 015042 004737 017164 JSR PC,#$CC4 ; CHECK FOR CC = 4
7193 015046 000261 SEC ; C=1
7194 015050 005611 SBC (R1) ; SUBTRACT C BIT = 1
7195 015052 004737 017272 JSR PC,#$CC11 ; CHECK FOR CC = 11
7196 015056 022711 177777 CMP #-1,(R1) ; CHECK IT
7197 015062 001404 BEQ 4$ ; CONTINUE IF OK
7198 015064 012745 000327 MOV #327,-(R5)
(2) 015070 005245 INC -(R5)
(2) 015072 000000 HALT ; SBC INSTRUCTION FAILED
7199 015074 012711 100000 4$: MOV #100000,(R1) ; LOAD R1
7200 015100 000261 SEC ; C=1
7201 015102 005611 SBC (R1) ; SUBTRACT C BIT = 1
7202 015104 004737 017122 JSR PC,#$CC2 ; CHECK FOR CC = 2
7203
7204
7205
7206 *****
(2) *TEST: 113 NEW INSTRUCTION IN THIS SECTION IS SXT
(3) *****

```

7207 (2) (3) \*\*\*\*\*  
\*TEST: 113 NEW INSTRUCTION IN THIS SECTION IS SXT  
\*\*\*\*\*

```

7208 015110 021527 000113 SXT1: CMP (R5),#113
(2) 015110 001026 BNE ESXT1 ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7209 015114 001026

```

# E07

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-79  
 DVKAAA.P11 T113 NEW INSTRUCTION IN THIS SECTION IS SXT

\*\*\* SEQ 0082

7210	015116	005215			INC	(R5)	
7211	015120	012702	000442	1S:	MOV	#TEMP1,R2	: LOAD ADDRESS
7212	015124	005012			CLR	(R2)	: CLEAR LOCATIONS
7213	015126	000277			SCC		
7214	015130	000254			CLNZ		
7215	015132	006712			SXT	(R2)	: SIGN EXTEND
7216	015134	004737	017206		JSR	PC,2#SCC5	: CHECK FOR CC = 5
7217	015140	005712			TST	(R2)	: LOCATION SHOULD STILL BE 0
7218	015142	001404			BEQ	2S	: CONTINUE IF OK
7219	015144	012745	000330		MOV	#330, -(R5)	
(2)	015150	005245			INC	-(R5)	
(2)	015152	000000			HALT		: SXT INSTRUCTION FAILED
7220	015154	000273		2S:	SENV		: SET N, V & C BITS
7221	015156	006712			SXT	(R2)	: SIGN EXTEND
7222	015160	004737	017272		JSR	PC,2#SCC11	: CHECK FOR CC = 11
7223	015164	022712	177777		CMP	#-1, (R2)	: LOCATION SHOULD NEW HAVE -1
7224	015170	001404			BEQ	SWAB1	: CONTINUE IF OK
7225	015172			ESXT1:			
(2)	015172	012745	000331		MOV	#331, -(R5)	
(2)	015176	005245			INC	-(R5)	
(2)	015200	000000			HALT		: WRONG RESULT IN TEMP1 OR WRONG SEQUENCE



# F07

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-80  
 DVKAAA.P11 T114 NEW INSTRUCTION IN THIS SECTION IS SWAB

\*\*\* SEQ 0083

```

7226          ;*****
(2)          ;*TEST: 114   NEW INSTRUCTION IN THIS SECTION IS SWAB
(3)          ;*****
7227
7228 015202   SWAB1:  CMP      (R5),#114
(2) 015202   021527 000114  BNE     ESWAB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7229 015206   001034  INC     (R5)
7230 015210   005215  MOV     #TEMP2,R3   ; LOAD ADDRESS
7231 015212   012703 000444  MOV     #125125,(R3) ; LOAD BIT PATTERN INTO LOCATION
7232 015216   012713 125125  SCC
7233 015222   000277  CLN
7234 015224   000250  SWAB   (R3)         ; SWAP BYTES OF LOCATIONS
7235 015226   000313  JSR    PC,@#SCC10   ; CHECK FOR CC = 10
7236 015230   004737 017252  CMP     #52652,(R3) ; CHECK IT
7237 015234   022713 052652  BEQ    1$          ; CONTINUE IF OK
7238 015240   001404  MOV     #332,-(R5)
7239 015242   012745 000332  INC    -(R5)
(2) 015246   005245  HALT
(2) 015250   000000  1$:  MOV     #377,(R3) ; SWAB INSTRUCTION FAILED
7240 015252   012713 000377  SCC
7241 015256   000277  CLZ
7242 015260   000244  SWAB   0(R3)
7243 015262   000363 000000  JSR    PC,@#SCC4   ; CHECK FOR CC = 4
7244 015266   004737 017164  CMP     #177400,(R3)
7245 015272   022713 177400  BEQ    XOR1
7246 015276   001404  ESWAB1: MOV     #333,-(R5)
7247 015300   012745 000333  INC    -(R5)
(2) 015304   005245  HALT ; WRONG RESULT IN: TEMP2 OR WRONG SEQUENCE
(2) 015306   000000
7248
7249
7250
7251          ;*****
(2)          ;*TEST: 115   NEW INSTRUCTION IN THIS SECTION IS XOR
(3)          ;*****
7252
7253 015310   XOR1:  CMP     (R5),#115
(2) 015310   021527 000115  BNE     EXOR1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7254 015314   001041  INC     (R5)
7255 015316   005215  MOV     #-1,R4     ; LOAD LOCATIONS
7256 015320   012704 177777  MOV     #-1,TEMP1  ;
7257 015324   012767 177777 163110  SCC
7258 015332   000277  XOR     R4,TEMP1   ; SHOULD PRODUCE 0'S IN TEMP1
7259 015334   074467 163102  JSR    PC,@#SCC5   ; CHECK FOR CC = 5
7260 015340   004737 017206  MOV     #77777,TEMP1
7261 015344   012767 077777 163070  MOV     #TEMP1,RO  ; PLACE THE ADDRESS OF TEMP1 IN RO
7262 015352   012700 000442  SEVC
7263 015356   000263  CLZ
7264 015360   000244  XOR     R4,(RO)
7265 015362   074410  JSR    PC,@#SCC11  ; CHECK FOR CC = 11
7266 015364   004737 017272  MOV     #125252,R1 ; LOAD LOCATIONS
7267 015370   012701 125252  MOV     #52525,(RO)+ ;
7268 015374   012720 052525  SCC
7269 015400   000277  XOR    R1,-(RO)   ; SHOULD PRODUCE ALL 1'S IN TEMP1
7270 015402   074140
  
```

G07

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-81  
DVKAAA.P11 T115 NEW INSTRUCTION IN THIS SECTION IS XOR

\*\*\* SEQ 0084

7271	015404	004737	017272		JSR	PC, @#SCC11	:	CHECK FOR CC = 11
7272	015410	022737	177777	000442	CMP	#-1, @#TEMP1	:	CHECK IT
7273	015416	001404			BEG	ADD1	:	CONTINUE IF OK
7274	015420				EXOR1:			
(2)	015420	012745	000334		MOV	#334, -(R5)		
(2)	015424	005245			INC	-(R5)		
(2)	015426	000000			HALT		:	WRONG RESULT IN TEMP1 OR WRONG SEQUENCE



```

7275          ;*****
(2)          ;*TEST: 116   NEW INSTRUCTION IN THIS SECTION IS ADD
(3)          ;*****
7276
7277 015430   ADD1:  CMP      (R5), #116
(2) 015430   021527 000116  BNE     EADD1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7278 015434   001133  INC     (R5)
7279 015436   005215  MOV     #TEMP2, R0  ; LOAD ADDRESSES
7280 015440   012700 000444  MOV     #TEMP, R1
7281 015444   012701 000440  MOV     #21421, TEMP2 ; LOAD LOCATIONS
7282 015450   012767 021421 162766  MOV     (R0), (R1)
7283 015456   011011  MOV     (R0), (R1)
7284 015460   061011  ADD     (R0), (R1)
7285 015462   004737 017062  JSR     PC, @#SCC0  ; CHECK FOR CC = 0
7286 015466   022767 043042 162744  CMP     #43042, TEMP ; CHECK IT
7287 015474   001404  BEQ     1$
7288 015476   012745 000335  MOV     #335, -(R5)  ; CONTINUE IF OK
(2) 015502   005245  INC     -(R5)
(2) 015504   000000  HALT
7289 015506   005010 1$: CLR     (R0)      ; ADD INSTRUCTION FAILED
7290 015510   060020  ADD     R0, (R0)+  ; CLEAR LOCATION TEMP2
7291 015512   024027 000444  CMP     -(R0), #TEMP2 ; PLACE THE ADDRESS OF TEMP2 IN LOCATION TEMP2
7292 015516   001404  BEQ     2$
7293 015520   012745 000336  MOV     #336, -(R5)  ; CHECK IT
(2) 015524   005245  INC     -(R5)
(2) 015526   000000  HALT
7294 015530   012767 156357 162706 2$: MOV     #-21421, TEMP2 ; ADD INSTRUCTION FAILED IN MODE 2
7295 015536   012011  MOV     (R0)+, (R1) ; LOAD LOCATIONS
7296 015540   064011  ADD     -(R0), (R1)
7297 015542   004737 017272  JSR     PC, @#SCC11 ; ADD
7298 015546   022767 134736 162664  CMP     #-43042, TEMP ; CHECK FOR CC = 11
7299 015554   001404  BEQ     3$
7300 015556   012745 000337  MOV     #337, -(R5)  ; CHECK IT
(2) 015562   005245  INC     -(R5)
(2) 015564   000000  HALT
7301 015566   012767 100000 162650 3$: MOV     #100000, TEMP2 ; ADD INSTRUCTION FAILED
7302 015574   011061 000000  MOV     (R0), 0(R1) ; LOAD LOCATIONS
7303 015600   066011 000000  ADD     0(R0), (R1)
7304 015604   004737 017230  JSR     PC, @#SCC7  ; ADD SHOULD RESULT AS 0'S
7305 015610   012767 021421 162624  MOV     #21421, TEMP1 ; CHECK FOR CC=7
7306 015616   012760 000442 000000  MOV     #TEMP1, 0(R0) ; LOAD LOCATION TEMP1
7307 015624   012711 156357  MOV     #-21421, (R1) ; PLACE THE ADDRESS OF LOCATION TEMP1 IN TEMP2
7308 015630   010004  MOV     R0, R4      ; LOAD LOCATION TEMP
7309 015632   067411 000000  ADD     @0(R4), (R1) ; MAKE R4 POINT TO LOCATION TEMP2
7310 015636   004737 017206  JSR     PC, @#SCC5  ; ADD SHOULD RESULT AS 0'S
7311 015642   005430  NEG     @ (R0)+      ; CHECK FOR CC=5
7312 015644   012746 021421  MOV     #21421, -(SP) ; NEGATE THE CONTENTS OF TEMP1
7313 015650   065066 000000  ADD     @-(R0), 0(SP) ; PLACE # 21421 ON THE STACK
7314 015654   004737 017206  JSR     PC, @#SCC5  ; ADD, SHOULD=0'S
7315 015660   005726  TST     (SP)+
7316
7317 015662   001404  BEQ     4$
7318 015664   012745 000340  MOV     #340, -(R5)
(2) 015670   005245  INC     -(R5)
(2) 015672   000000  HALT
7319 015674   012767 137777 162542 4$: MOV     #137777, TEMP2 ; ADD INSTRUCTION FAILED IN MODE 5

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7320 015702 062767 137777 162534      ADD    #137777,TEMP2
7321 015710 004737 017142                    JSR    PC,#$CC3      ; CHECK CC=3
7322 015714 022767 077776 162522      CMP    #77776,TEMP2
7323 015722 001404                    BEQ    SUB1
7324 015724                    EADD1:
(2) 015724 012745 000341      MOV    #341,-(R5)
(2) 015730 005245      INC    -(R5)
(2) 015732 000000      HALT    ; WRONG RESULT AT TEMP OR WRONG SEQUENCE
    
```

```

:*****
:*TEST: 117 NEW INSTRUCTION IN THIS SECTION IS SUB
:*****
    
```

```

7329
7330 015734      SUB1:
(2) 015734 021527 000117      CMP    (R5),#117
7331 015740 001100      BNE    ESUB1      ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7332 015742 005215      INC    (R5)
7333 015744 012702 000440      MOV    #TEMP,R2      ; LOAD ADDRESSES
7334 015750 012703 000442      MOV    #TEMP1,R3      ;
7335 015754 012767 021421 162456      MOV    #21421,TEMP      ; LOAD LOCATIONS
7336 015762 012767 156357 162452      MOV    #-21421,TEMP1      ;
7337 015770 161213      SUB    (R2),(R3)      ; RESULT SHOULD=-43042
7338 015772 004737 017252      JSR    PC,#$CC10      ; CHECK FOR CC = 10
7339 015776 022767 134736 162436      CMP    #-43042,TEMP1      ; CHECK IT
7340 016004 001404      BEQ    1$      ; CONTINUE IF OK
7341 016006 012745 000342      MOV    #342,-(R5)
(2) 016012 005245      INC    -(R5)
(2) 016014 000000      HALT    ; SUB INSTRUCTION FAILED
7342 016016 012767 021421 162416 1$:      MOV    #21421,TEMP1      ; LOAD LOCATION
7343 016024 161213      SUB    (R2),(R3)      ; RESULT SHOULD=0
7344 016026 001404      BEQ    2$
7345 016030 012745 000343      MOV    #343,-(R5)
(2) 016034 005245      INC    -(R5)
(2) 016036 000000      HALT    ; SUB INSTRUCTION FAILED
7346 016040 012767 177777 162374 2$:      MOV    #-1,TEMP1      ; LOAD LOCATIONS
7347 016046 012767 077777 162364      MOV    #77777,TEMP      ; LOAD LOCATIONS
7348 016054 161312      SUB    (R3),(R2)      ; RESULT SHOULD GIVE 100000 AND OVERFLOW
7349 016056 004737 017334      JSR    PC,#$CC13      ; CHECK FOR CC = 13
7350 016062 022767 100000 162350      CMP    #100000,TEMP      ; CHECK IT
7351 016070 001404      BEQ    3$      ; CONTINUE IF OK
7352 016072 012745 000344      MOV    #344,-(R5)
(2) 016076 005245      INC    -(R5)
(2) 016100 000000      HALT    ; SUB INSTRUCTION FAILED
7353 016102 012712 177777      3$:      MOV    #-1,(R2)
7354 016106 161312      SUB    (R3),(R2)
7355 016110 004737 017164      JSR    PC,#$CC4      ; CHECK FOR CC = 4
7356 016114 012767 077777 162316      MOV    #77777,TEMP
7357 016122 162767 077777 162310      SUB    #77777,TEMP
7358 016130 004737 017164      JSR    PC,#$CC4      ; CHECK FOR CC=4
7359 016134 005767 162300      TST    TEMP
7360 016140 001404      BEQ    SOB      ; TEMP SHOULD BE =0
7361 016142      ESUB1:
(2) 016142 012745 000345      MOV    #345,-(R5)
(2) 016146 005245      INC    -(R5)
    
```



J07

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-84  
DVKAAA.P11 T117 NEW INSTRUCTION IN THIS SECTION IS SUB

\*\*\* SEQ 0087

(2) 016150 000000

HALT

; SUB INSTRUCTION FAILED OR SEQUENCE ERROR

```

7362 (2)
7363 (3)
7364 016152 021527 000120 SOB: CMP (R5),#120
      016152 001042 BNE ESOB ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7365 016156 005215 INC (R5)
7366 016160 012700 000012 MOV #10.,R0 ; LOAD REGISTERS
7367 016162 005001 CLR R1 ;
7368 016166 005201 1$: INC R1 ; KEEP COUNT
7369 016170 020127 000012 CMP R1,#10.
7370 016172 012745 000346 MOV #346,-(R5)
7371 016176 005245 INC -(R5)
7372 016200 000000 HALT ; SOB INSTRUCTION FAILED
      016204 000277 2$: SCC ;
7373 016210 077012 SOB R0,1$ ; SUB. 1 FROM REG. 0, GO BACK TO 1$
7374 016212 004737 017354 JSR PC,2$SCC17 ; CHECK FOR CC = 17
7375 016214 005700 TST R0 ; REG. 0 = 0 ?
7376 016220 001404 BEQ 3$ ; NO, FAILED
7377 016222 012745 000347 MOV #347,-(R5)
      016230 005245 INC -(R5)
      016232 000000 HALT ; SOB INSTRUCTION FAILED
7379 016234 022701 000012 3$: CMP #10.,R1 ; DID IT GO THRU 10 TIMES ?
7380 016240 001404 BEQ 4$ ; CONTINUE IF OK
7381 016242 012745 000350 MOV #350,-(R5)
      016246 005245 INC -(R5)
      016250 000000 HALT ; SOB INSTRUCTION FAILED
7382 016252 012704 000010 4$: MOV #10,R4 ; PLACE #10 IN R4
7383 016256 077401 5$: SOB R4,5$ ; STAY HERE UNTILL R4 = 0
7384 016260 005704 TST R4
7385 016262 001404 BEQ PSWNO ; CONTINUE IF OK
7386 016264 ESOB: MOV #351,-(R5)
      016264 012745 000351 INC -(R5)
      016270 005245 HALT ; SOB FAILED OR WRONG SEQUENCE
      016272 000000
7387
7388
7389
7390 (2)
      (3)
7391
7392
7393 016274 PSWNO: CMP (R5),#121
      016274 021527 000121 BNE EPSWNO ; IF IN WRONG SEQUENCE GO TO HLT AT THE END OF THE TEST
7394 016300 001042 INC (R5)
7395 016302 005215 MOV #TEMP,R0 ; PUT THE ADDRESS OF TEMP IN R0
7396 016304 012700 000440 MOV #TEMP1,R1 ; PUT THE ADDRESS OF TEMP1 IN R1
7397 016310 012701 000442 MOV #177777,(R1) ; TEMP1 = 177777
7398 016314 012711 177777 CLR (R0) ; TEMP = 0
7399 016320 005010 MTPS (R0) ; PSW = 0
7403 016322 106410 .WORD 106400!..C
      (1) 016322 004737 017062 JSR PC,2$SCC0 ; CHECK FOR CC = 0
7404 016324
    
```

```

*****
*TEST: 121 NEW INSTRUCTIONS IN THIS SECTION ARE MTPS & MFPS
*****
    
```





```

7429          ;*****
(2)          ;*TEST: 122   BYTE INSTRUCTIONS REQUIRING WORD INST. TO CHECK
(3)          ;*****
7430
7431 016416   BTWRD:  CMP      (R5),#122
(2) 016416   021527 000122  BNE      EBTWRD      ; IF IN WRONG SEQUENCE GO TO HALT AT THE END OF THE TEST
7432 016422   001124      INC      (R5)
7433 016424   005215      CLR      RO
7434 016426   005000      SCC
7435 016430   000277      MOVB     #200,RO      ; SET THE HIGHEST BIT OF THE
7436 016432   112700 000200  JSR      PC,@#SCC11  ; LOWER BYTE
7437          ;
7438 016436   004737 017272  CMP      #177600,RO  ; CHECK FOR CC=11
7439 016442   022700 177600  BEQ      1$          ; CHECK FOR SIGN EXTENSION IN RO
7440 016446   001404      MOV      #354,-(R5)
7441 016450   012745 000354  INC      -(R5)
(2) 016454   005245      HALT
(2) 016456   000000      1$:  SCC
7442 016460   000277      MOV      #177777,RO  ; SIGN WAS NOT EXTENDED IN RO
7443 016462   012700 177777  MOVB     #0,RO      ; CLEAR THE LOWER BYTE OF RO.
7444 016466   112700 000000  JSR      PC,@#SCC5  ; CHECK FOR CC=5
7445 016472   004737 017206  TST      RO          ; CHECK RO FOR SIGN EXTENTION
7446 016476   005700      BEQ      2$
7447 016500   001404      MOV      #355,-(R5)
7448 016502   012745 000355  INC      -(R5)
(2) 016506   005245      HALT
(2) 016510   000000      2$:  MOV      #TEMP2,R4  ; SIGN WAS NOT EXTENDED IN RO.
7449 016512   012704 000444  MOV      #377,(R4)  ; R4 IS POINTING TO TEMP2
7450 016516   012714 000377  MOV      #START-2,R6 ; PLACE #377 IN LOCATION TEMP2
7451 016522   012706 000526  MOVB     0(R4),(R6)+ ; PUSH # 377 ON STACK
7452 016526   116426 000000  CMP      #START,R6
7453 016532   022706 000530  BEQ      3$
7454 016536   001404      MOV      #356,-(R5)
7455 016540   012745 000356  INC      -(R5)
(2) 016544   005245      HALT
(2) 016546   000000      3$:  CMPB     -(R6),#377 ; R6 DID NOT GET INCREMENTED
7456          ; BY 2 BY A BYTE INSTRUCTION
7457 016550   124627 000377  BEQ      4$          ; CHECK LOCATION START-2 TO
7458          ; CONTAIN PROPER DATA
7459 016554   001404      MOV      #357,-(R5)
7460 016556   012745 000357  INC      -(R5)
(2) 016562   005245      HALT
(2) 016564   000000      4$:  CMP      #START-2,R6 ; BYTE INSTRUCTION IS FAILING WITH R6
7461 016566   022706 000526  BEQ      5$          ; CHECK THAT R6 WAS DECREMENTED
7462          ; BY 2 BY A BYTE INSTRUCTION
7463 016572   001404      MOV      #360,-(R5)
7464 016574   012745 000360  INC      -(R5)
(2) 016600   005245      HALT
(2) 016602   000000      5$:  MOV      0(R4),TEMP ; R6 WAS NOT DECREMENTED
7465 016604   016467 000000 161626  TST      (R6)+      ; SET THE LOWER BYTE OF LOCATION TEMP
7466 016612   005726      SCC          ; RESTORE STACK POINTER
7467 016614   000277      MOVB     -(SP),TEMP+1 ; SET THE HIGHER BYTE OF LOCATION TEMP
7468 016616   114667 161617  JSR      PC,@#SCC11 ; CHECK FOR CC=11
7469 016622   004737 017272  CMP      #177777,TEMP ; CHECK TEMP FOR THE CORRECT VALUE
7470 016626   022767 177777 161604  BEQ      6$
7471 016634   001404
    
```



N07

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-88  
DVKAAA.P11 T122 BYTE INSTRUCTIONS REQUIRING WORD INST. TO CHECK

\*\*\* SEQ 0091

7472	016636	012745	000361		MOV	#361, -(R5)	
(2)	016642	005245			INC	-(R5)	
(2)	016644	000000			HALT		; TEMP FOULED UP
7473	016646	005067	161566	6S:	CLR	TEMP	
7474	016652	000241			CLC		
7475	016654	105167	161561		COMB	TEMP+1	; WRITE 1'S IN THE HIGHER BYTE OF TEMP
7476	016660	004737	017272		JSR	PC, @#5CC11	; CHECK FOR CC=11
7477	016664	022767	177400	161546	CMP	#177400, TEMP	
7478	016672	001404			BEQ	NEXT	
7479	016674			E8TWRD:			
(2)	016674	012745	000362		MOV	#362, -(R5)	
(2)	016700	005245			INC	-(R5)	
(2)	016702	000000			HALT		; WRONG VALUE IN TEMP OR WRONG SEQUENCE





```

7523
7524
7525
7526
7527
7528 017020 132737 000040 000421 TYPE: BITB    #40, @#SENVN    : HAS THE CONSOLE OUTPUTS BEEN SUPPRESSED?
7529 017026 001012          BNE      4$          : IF SO THEN GO TO 4$
7530 017030 017603 000000          MOV      @ (SP), R3   : GET ADDRESS OF MESSAGE
7531
7532 017034 105713          1$:    TSTB   (R3)     : END OF MESSAGE ?
7533 017036 001406          BEQ      4$          : YES, GO WRAP IT UP
7534
7535 017040 105777 161402          3$:    TSTB   @TPS     : READY FOR NEXT CHARACTER ?
7536 017044 100375          BPL      3$          : NO, WAIT
7537 017046 112377 161376          MOVB    (R3)+, @TPB  : LOAD AND TYPE THE CHARACTER
7538 017052 000770          BR       1$          : YES, GET THE NEXT CHARACTER
7539
7540 017054 062716 000002          4$:    ADD     #2, (SP) : ADJUST THE RETURN PC
7541 017060 000006          RTT                      : RETURN
7542

```

7547	017062	003402		\$CC0:	BLE	1\$	
7548	017064	100401			BMI	1\$	
7549	017066	103004			BCC	2\$	
7550	017070			1\$:			
(2)	017070	012745	000364		MOV	#364, -(R5)	
(2)	017074	005245			INC	-(R5)	
(2)	017076	000000			HALT		;WRONG CC, IT SHOULD HAVE BEEN = 0
7551	017100	000207		2\$:	RTS	PC	
7552							
7553	017102	003402		\$CC1:	BLE	1\$	
7554	017104	100401			BMI	1\$	
7555	017106	103404			BCS	2\$	
7556	017110			1\$:			
(2)	017110	012745	000365		MOV	#365, -(R5)	
(2)	017114	005245			INC	-(R5)	
(2)	017116	000000			HALT		;WRONG CC, IT SHOULD HAVE BEEN = 1
7557	017120	000207		2\$:	RTS	PC	
7558							
7559	017122	100402		\$CC2:	BMI	1\$	
7560	017124	101401			BLOS	1\$	
7561	017126	102404			BVS	2\$	
7562	017130			1\$:			
(2)	017130	012745	000366		MOV	#366, -(R5)	
(2)	017134	005245			INC	-(R5)	
(2)	017136	000000			HALT		;WRONG CC, IT SHOULD HAVE BEEN = 2
7563	017140	000207		2\$:	RTS	PC	
7564							
7565	017142	100403		\$CC3:	BMI	1\$	
7566	017144	001402			BEQ	1\$	
7567	017146	102001			BVC	1\$	
7568	017150	103404			BCS	2\$	
7569	017152			1\$:			
(2)	017152	012745	000367		MOV	#367, -(R5)	
(2)	017156	005245			INC	-(R5)	
(2)	017160	000000			HALT		;WRONG CC, IT SHOULD HAVE BEEN = 3
7570	017162	000207		2\$:	RTS	PC	
7571							
7572	017164	100403		\$CC4:	BMI	1\$	
7573	017166	001002			BNE	1\$	
7574	017170	102401			BVS	1\$	
7575	017172	103004			BCC	2\$	
7576	017174			1\$:			
(2)	017174	012745	000370		MOV	#370, -(R5)	
(2)	017200	005245			INC	-(R5)	
(2)	017202	000000			HALT		;WRONG CC, IT SHOULD HAVE BEEN = 4
7577	017204	000207		2\$:	RTS	PC	
7578							
7579	017206	100403		\$CC5:	BMI	1\$	
7580	017210	001002			BNE	1\$	
7581	017212	102401			BVS	1\$	
7582	017214	103404			BCS	2\$	
7583	017216			1\$:			
(2)	017216	012745	000371		MOV	#371, -(R5)	
(2)	017222	005245			INC	-(R5)	
(2)	017224	000000			HALT		;WRONG CC, IT SHOULD HAVE BEEN = 5
7584	017226	000207		2\$:	RTS	PC	



7585						
7586						
7587	017230	100403		SCC7:	BMI	1\$
7588	017232	001002			BNE	1\$
7589	017234	102001			BVC	1\$
7590	017236	103404			BCS	2\$
7591	017240			1\$:		
(2)	017240	012745	000372		MOV	#372, -(R5)
(2)	017244	005245			INC	-(R5)
(2)	017246	000000			HALT	
7592	017250	000207		2\$:	RTS	PC
7593						
7594	017252	100002		SCC10:	BPL	1\$
7595	017254	101401			BLOS	1\$
7596	017256	102004			BVC	2\$
7597	017260			1\$:		
(2)	017260	012745	000373		MOV	#373, -(R5)
(2)	017264	005245			INC	-(R5)
(2)	017266	000000			HALT	
7598	017270	000207		2\$:	RTS	PC
7599						
7600	017272	100003		SCC11:	BPL	1\$
7601	017274	001402			BEQ	1\$
7602	017276	102401			BVS	1\$
7603	017300	103404			BCS	2\$
7604	017302			1\$:		
(2)	017302	012745	000374		MOV	#374, -(R5)
(2)	017306	005245			INC	-(R5)
(2)	017310	000000			HALT	
7605	017312	000207		2\$:	RTS	PC
7606						
7607	017314	100002		SCC12:	BPL	1\$
7608	017316	101401			BLOS	1\$
7609	017320	102404			BVS	2\$
7610	017322			1\$:		
(2)	017322	012745	000375		MOV	#375, -(R5)
(2)	017326	005245			INC	-(R5)
(2)	017330	000000			HALT	
7611	017332	000207		2\$:	RTS	PC
7612						
7613	017334	100002		SCC13:	BPL	1\$
7614	017336	003401			BLE	1\$
7615	017340	103404			BCS	2\$
7616	017342			1\$:		
(2)	017342	012745	000376		MOV	#376, -(R5)
(2)	017346	005245			INC	-(R5)
(2)	017350	000000			HALT	
7617	017352	000207		2\$:	RTS	PC
7618						
7619	017354	100003		SCC17:	BPL	1\$
7620	017356	001002			BNE	1\$
7621	017360	102001			BVC	1\$
7622	017362	103404			BCS	2\$
7623	017364			1\$:		
(2)	017364	012745	000377		MOV	#377, -(R5)
(2)	017370	005245			INC	-(R5)

;WRONG CC, IT SHOULD HAVE BEEN = 7

;WRONG CC, IT SHOULD HAVE BEEN = 10

;WRONG CC, IT SHOULD HAVE BEEN = 11

;WRONG CC, IT SHOULD HAVE BEEN = 12

;WRONG CC, IT SHOULD HAVE BEEN = 13

F08

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 54-93  
DVKAAA.P11 ROUTINES TO CHECK CONDITION CODES

\*\*\* SEQ 0096

(2) 017372 000000  
7624 017374 000207  
7625  
7626 000001

2S: HALT  
RTS PC  
.END

;WRONG CC, IT SHOULD HAVE BEEN = 17























# M08

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 55-6  
 DVKAAA.P11 CROSS REFERENCE TABLE -- USER SYMBOLS

\*\*\* SEQ 0103

R6	=%000006	5020#	5422	5428*	5429*	5430*	5431*	5432*	5433*	5434*	5435	5437*	5439*	7451*
		7452*	7453	7457	7461	7466								
SBCB0	004272	5700#												
SBCB1	012552	6795#												
SBC0	006066	6009#												
SBC1	014726	7170#												
SEVC	= 000273	5027#	6054	7220										
SEVC	= 000263	5026#	5763	5810	6096	6562	6942	6956	7263					
SOB	016152	7360	7364#											
SP	=%000006	5021#	5085*	5358*	5362	5366	5369	5373*	5376*	5377*	5378*	5379*	5387	5395
		5402	5404*	6554*	6555	6590	6591*	6593*	6595	6947*	6949	6957	6959	7312*
		7313*	7315	7468	7518*	7530	7540*							
START	000530	5079	5085#	5358	5362	5366	5387	5395	6959	7451	7453	7461	7507	7518
SUB0	006710	6139	6143#											
SUB1	015734	7323	7330#											
SWAB0	006332	6058	6065#											
SWAB1	015202	7224	7228#											
SXT0	006244	6043#												
SXT1	015110	7208#												
TEMP	000440	5043#	5044	5277*	5278*	5279	5286*	5287	5341	5346	5349*	5350	5422*	5437
		5439	5792	6231	6256	6263*	6268	6276*	6280	6289	6295*	6297*	6301	6305*
		6309*	6313	6324*	6325	6340	6345	6350*	6351	6368	6379*	6380	6398	6403*
		6415	6429	6434	6454	6486	6515	6538	6580	6630	6657	6847	6869	6904
		6916	6974	7116	7144	7174	7281	7286	7298	7333	7335*	7347*	7350	7356*
		7357*	7359	7396	7465*	7468*	7470	7473*	7475*	7477				
TEMP1	000442	5045#	5046	5308*	5309	5310*	5314	5317*	5318	5319*	6232	6257	6290	6326
		6352	6381	6430	6441*	6444	6453*	6455	6462	6467	6487	6540	6560	6566
		6631	6678	6698	6722	6740	6848	6868	6876	6886*	6896	6921	6938	6968
		7005	7029	7211	7257*	7259*	7261*	7262	7272	7305*	7306	7334	7336*	7339
		7342*	7346*	7397	7420*	7425								
TEMP2	000444	5047#	5048	6233	6234*	6238	6244	6258	6291	6294*	6299	6304*	6311	6327
		6329	6347	6353	6355	6370	6382	6400	6417	6428*	6431	6438	6456	6514
		6741	6769	6799	6885*	6886	6901*	6953	7052	7073	7097	7115	7231	7280
		7282*	7291	7294*	7301*	7319*	7320*	7322	7449					
TPB	000450	5050#	7537*											
TPS	000446	5049#	7535											
TSTB0	002724	5438	5451#											
TSTB1	010712	6468	6483#											
TST0	004450	5746#												
TST1	012734	6843#												
TYPE	017020	5059	7528#											
VBIT	000670	5131	5134#											
XORD	006432	6082	6086#											
XOR1	015310	7246	7253#											
YESCC	001054	5185	5192#											
ZBIT	001004	5166	5171#											
\$APTHD	000430	5032#	5034											
\$CC0	017062	5680	5707	5715	5989	6016	6024	6118	6182	6778	6803	6811	7153	7178
		7186	7285	7404	7547#									
\$CC1	017102	5495	5500	5527	5540	5550	5796	5801	5835	5848	5858	6545	6550	6598
		6611	6636	6880	6925	6930	6982	6995	7009	7553#				
\$CC10	017252	5461	5463	5757	5759	5776	6072	6150	6494	6496	6519	6857	6859	6872
		6884	7236	7338	7594#									
\$CC11	017272	5474	5505	5507	5523	5535	5556	5574	5662	5724	5806	5809	5831	5843
		5864	5882	5971	6033	6056	6099	6104	6124	6200	6556	6558	6586	6609
		6642	6660	6756	6820	6937	6946	6975	7015	7034	7131	7195	7222	7266





.SX	=	000430	50320							
..A	=	016366	61810	61830	61940	61960	74030	74050	74180	74200
..B	=	016372	61810	61830	61940	61960	74030	74050	74180	74200
..C	=	000067	61810	61830	61940	61960	74030	74050	74180	74200

COMMEN	974#														
ENDCOM	986#														
ERROR	4976#	5112	5130	5148	5167	5186	5206	5222	5231	5234	5236	5238	5240	5250	5251
	5254	5257	5268	5269	5272	5275	5282	5285	5289	5298	5300	5303	5306	5312	5313
	5316	5321	5330	5333	5336	5339	5344	5348	5352	5361	5364	5365	5368	5371	5375
	5382	5383	5386	5389	5392	5393	5397	5401	5407	5412	5415	5440	5453	5477	5481
	5484	5489	5498	5503	5515	5530	5545	5553	5559	5577	5583	5596	5602	5616	5623
	5628	5637	5654	5665	5670	5683	5690	5702	5710	5718	5727	5748	5762	5779	5782
	5785	5799	5804	5815	5823	5838	5853	5861	5867	5885	5891	5904	5910	5925	5932
	5937	5946	5963	5974	5979	5992	5999	6011	6019	6027	6036	6053	6059	6075	6083
	6107	6121	6127	6135	6140	6145	6153	6158	6165	6188	6203	6223	6229	6240	6246
	6270	6282	6303	6315	6331	6342	6349	6357	6372	6395	6402	6419	6440	6446	6464
	6469	6502	6505	6522	6525	6528	6536	6548	6553	6568	6571	6601	6614	6628	6639
	6646	6663	6669	6686	6692	6705	6712	6720	6730	6748	6759	6767	6781	6788	6797
	6806	6814	6823	6845	6875	6878	6895	6899	6906	6928	6933	6940	6952	6961	6966
	6985	7003	7012	7018	7037	7043	7060	7067	7080	7087	7095	7105	7123	7134	7142
	7156	7163	7172	7181	7189	7198	7219	7225	7239	7247	7274	7288	7293	7300	7318
	7324	7341	7345	7352	7361	7372	7378	7381	7386	7413	7427	7441	7448	7455	7460
	7464	7472	7479	7494	7550	7556	7562	7569	7576	7583	7591	7597	7604	7610	7616
	7623														
ESCAPE	1097#														
HLT	4961#	5112	5130	5148	5167	5186	5206	5222	5231	5234	5236	5238	5240	5250	5251
	5254	5257	5268	5269	5272	5275	5282	5285	5289	5298	5300	5303	5306	5312	5313
	5316	5321	5330	5333	5336	5339	5344	5348	5352	5361	5364	5365	5368	5371	5375
	5382	5389	5392	5393	5397	5401	5407	5412	5415	5440	5453	5477	5481	5484	5489
	5498	5503	5515	5530	5545	5553	5559	5577	5583	5596	5602	5616	5623	5628	5637
	5654	5665	5670	5683	5690	5702	5710	5718	5727	5748	5762	5779	5782	5785	5799
	5804	5815	5823	5838	5853	5861	5867	5885	5891	5904	5910	5925	5932	5937	5946
	5963	5974	5979	5992	5999	6011	6019	6027	6036	6053	6059	6075	6083	6107	6121
	6127	6135	6140	6145	6153	6158	6165	6188	6203	6223	6229	6240	6246	6270	6282
	6303	6315	6331	6342	6349	6357	6372	6395	6402	6419	6440	6446	6464	6469	6502
	6505	6522	6525	6528	6536	6548	6553	6568	6571	6601	6614	6628	6639	6646	6663
	6669	6686	6692	6705	6712	6720	6730	6748	6759	6767	6781	6788	6797	6806	6814
	6823	6845	6875	6878	6895	6899	6906	6928	6933	6940	6952	6961	6966	6985	7003
	7012	7018	7037	7043	7060	7067	7080	7087	7095	7105	7123	7134	7142	7156	7163
	7172	7181	7189	7198	7219	7225	7239	7247	7274	7288	7293	7300	7318	7324	7341
	7345	7352	7361	7372	7378	7381	7386	7413	7427	7441	7448	7455	7460	7464	7472
	7479	7494	7550	7556	7562	7569	7576	7583	7591	7597	7604	7610	7616	7623	
HLT1	4969#	5383	5386												
MFPS	4948#	6183	6196	7405	7420										
MTPS	4933#	6181	6194	7403	7418										
MULT	3466#														
NEWST	1030#														
NWTEST	4982#	5093	5116	5132	5152	5169	5190	5207	5227	5242	5260	5290	5323	5353	5417
	5449	5467	5485	5511	5541	5567	5584	5605	5624	5643	5666	5698	5744	5769	5786
	5819	5849	5875	5892	5914	5933	5952	5975	6007	6041	6063	6084	6111	6141	6171
	6213	6251	6284	6319	6374	6423	6448	6481	6509	6532	6575	6624	6652	6673	6693
	6716	6735	6763	6793	6841	6863	6911	6962	6999	7024	7047	7068	7091	7110	7138
	7168	7206	7226	7251	7275	7328	7362	7390	7429						
POP	1483#														
PUSH	1475#														
REPORT	4437#														
SEQCHK	5005#	5095	5118	5134	5154	5171	5192	5209	5229	5244	5262	5292	5325	5355	5419
	5451	5469	5487	5513	5543	5569	5586	5607	5626	5645	5668	5700	5746	5771	5788
	5821	5851	5877	5894	5916	5935	5954	5977	6009	6043	6065	6086	6113	6143	6173
	6215	6253	6286	6321	6376	6425	6450	6483	6511	6534	6577	6626	6654	6675	6695





.SRDDE	2919#
.SRDOC	2827#
.SREAO	2613#
.SR2AZ	2934#
.SSAVE	2995#
.SSB2D	2751#
.SSB2O	2853#
.SSCOP	1739#
.SSIZF	2348#
.SSUPR	2891#
.STRAP	2095#
.STYPB	2528#
.STYPD	2450#
.STYPE	2228#
.STYPO	2353#
.S40CA	515#



# F09

ADC	5983	5986	5988	5995	6002	7147	7150	7152	7159	7166					
ADCB	5674	5677	5679	5686	5693	6772	6775	6777	6784	6791					
ADD	5429	5430	5431	5432	5433	5434	6117	6123	6129	6133	6138	7284	7290	7296	7303
	7309	7313	7320	7540											
ASL	5941	5942	5947	5949	7100	7101	7106	7108							
ASLB	5632	5633	5638	5640	6725	6726	6731	6733							
ASR	5959	5960	5964	5966	5969	5970	7119	7120	7124	7126	7129	7130			
ASRB	5650	5651	5655	5657	5660	5661	6744	6745	6749	6751	6754	6755			
BCC	5181	5199	5204	7549	7575										
BCCS	5099	5104	5126	5142	5214	5251	5269	5365	5383	7555	7568	7582	7590	7603	7615
	7622														
BEQ	5088	5101	5106	5123	5140	5160	5185	5218	5230	5253	5271	5284	5302	5315	5332
	5363	5367	5370	5385	5388	5396	5400	5411	5452	5476	5488	5497	5514	5529	5544
	5552	5558	5576	5582	5595	5601	5615	5622	5627	5636	5653	5664	5669	5682	5689
	5701	5709	5717	5726	5747	5761	5778	5798	5814	5822	5837	5852	5860	5866	5884
	5890	5903	5909	5924	5931	5936	5945	5962	5973	5978	5991	5998	6010	6018	6026
	6035	6052	6058	6074	6082	6106	6120	6126	6134	6139	6144	6152	6157	6164	6187
	6202	6222	6228	6239	6245	6269	6281	6302	6314	6330	6341	6348	6356	6371	6394
	6401	6418	6439	6445	6463	6468	6501	6504	6521	6535	6547	6567	6570	6600	6613
	6627	6638	6645	6662	6668	6685	6691	6704	6711	6719	6729	6747	6758	6766	6780
	6787	6796	6805	6813	6822	6844	6874	6877	6894	6898	6905	6927	6939	6951	6960
	6965	6984	7002	7011	7017	7036	7042	7059	7066	7079	7086	7094	7104	7122	7133
	7141	7155	7162	7171	7180	7188	7197	7218	7224	7238	7246	7273	7287	7292	7299
	7317	7323	7340	7344	7351	7360	7377	7380	7385	7412	7426	7440	7447	7454	7459
	7463	7471	7478	7493	7501	7533	7566	7601							
BGE	5124	5149	5166												
BGT	5125	5147	5183												
BHI	5078	5111	5165	5184	5221										
BIC	5795	5805	5811	6924	6936	6943	6949								
BICB	5494	5504	6544	6555	6563	6573									
BIS	5775	5802	6871	6890	6903	6931									
BISB	5473	5501	6518	6551											
BIT	5797	5800	5808	6926	6929	6945	6957								
BITB	5496	5499	5506	6546	6549	6557	7528								
BLE	5109	5129	5144	5164	7371	7547	7553	7614							
BLO	5128	5146													
BLOS	5110	5127	5145	5205	7560	7595	7608								
BLT	5108	5143	5163	5182											
BMI	5102	5107	5220	5483	5502	5784	5803	6527	6552	6932	7548	7554	7559	7565	7572
	7579	7587													
BNE	5096	5119	5131	5135	5155	5172	5178	5193	5197	5202	5210	5245	5251	5263	5269
	5293	5326	5356	5365	5383	5420	5436	5470	5570	5587	5608	5646	5772	5789	5878
	5895	5917	5955	6044	6066	6087	6114	6174	6216	6254	6267	6279	6287	6300	6312
	6322	6346	6369	6377	6399	6416	6426	6435	6451	6484	6512	6578	6655	6676	6696
	6738	6866	6914	7027	7050	7071	7113	7209	7229	7254	7278	7331	7365	7394	7432
	7498	7529	7573	7580	7588	7620									
BPL	5122	5141	5161	5179	5196	5201	5223	5251	5269	5365	5383	5480	5781	6524	7536
	7594	7600	7607	7613	7619										
BR	5233	5235	5237	5239	5241	5299	5438	7521	7538						
BVC	5113	5139	5162	5180	5198	5203	5251	5269	5365	5383	7567	7589	7596	7621	
BVS	5100	5105	5216	7561	7574	7581	7602	7609							
CCC	5098	5590	5611	5631	5649	5673	5705	5898	5920	5940	5958	5982	6014	6572	6680
	6700	6724	6743	6771	6801	7054	7075	7099	7118	7146	7176				
CLC	5213	6592	7474												
CLN	5219	6070	7234												
CLR	5076	5428	5751	5981	6046	6177	6181	6183	6194	6196	6234	6273	6304	6350	6403

	6428	6441	6453	6850	6900	7145	7212	7289	7368	7399	7403	7405	7414	7418	7420
CLRB	7434	7473	7506												
CLV	5456	5672	6259	6294	6324	6379	6489	6770							
CLZ	5215														
CMP	5217	6078	6097	7242	7264										
	5077	5095	5118	5134	5154	5171	5192	5209	5229	5244	5252	5262	5270	5283	5292
	5301	5314	5325	5331	5355	5362	5366	5369	5384	5387	5395	5399	5409	5419	5435
	5451	5469	5487	5513	5543	5569	5586	5607	5626	5645	5668	5700	5746	5760	5771
	5777	5780	5783	5788	5821	5836	5851	5859	5865	5877	5883	5889	5894	5902	5908
	5916	5923	5930	5935	5944	5954	5961	5972	5977	5990	5997	6009	6017	6025	6034
	6043	6057	6065	6073	6081	6086	6105	6113	6119	6125	6143	6151	6163	6173	6201
	6215	6227	6244	6253	6278	6286	6311	6321	6329	6340	6355	6368	6376	6388	6390
	6392	6396	6409	6411	6413	6415	6425	6434	6438	6444	6450	6467	6483	6500	6503
	6511	6534	6566	6577	6626	6654	6675	6695	6718	6737	6765	6795	6843	6865	6873
	6876	6879	6881	6883	6893	6896	6902	6904	6913	6938	6950	6959	6964	6983	7001
	7010	7016	7026	7035	7041	7049	7058	7065	7070	7078	7085	7093	7103	7112	7121
	7132	7140	7154	7161	7170	7179	7187	7196	7208	7223	7228	7237	7245	7253	7272
	7277	7286	7291	7298	7322	7330	7339	7350	7364	7370	7379	7393	7410	7425	7431
	7439	7453	7461	7470	7477	7492									
CMPB	5475	5479	5482	5528	5551	5557	5575	5581	5594	5600	5614	5621	5635	5652	5663
	5681	5688	5708	5716	5725	6221	6238	6266	6299	6345	6393	6398	6462	6520	6523
	6526	6595	6599	6612	6637	6644	6661	6667	6684	6690	6703	6710	6728	6746	6757
	6779	6786	6804	6812	6821	7457	7496								
COM	5857	5863	5870	7008	7014	7022									
COMB	5549	5555	5562	6635	6641	6650	7475								
DEC	5840	5842	5845	5847	6987	6989	6992	6994							
DECB	5532	5534	5537	5539	6603	6605	6608	6610							
HALT	4916	5090	5112	5130	5148	5167	5186	5206	5222	5231	5234	5236	5238	5240	5250
	5251	5254	5257	5268	5269	5272	5275	5282	5285	5289	5298	5300	5303	5306	5312
	5313	5316	5321	5330	5333	5336	5339	5344	5348	5352	5361	5364	5365	5368	5371
	5375	5382	5383	5386	5389	5392	5393	5397	5401	5407	5412	5415	5440	5453	5477
	5481	5484	5489	5498	5503	5515	5530	5545	5553	5559	5577	5583	5596	5602	5616
	5623	5628	5637	5654	5665	5670	5683	5690	5702	5710	5718	5727	5748	5762	5779
	5782	5785	5799	5804	5815	5823	5838	5853	5861	5867	5885	5891	5904	5910	5925
	5932	5937	5946	5963	5974	5979	5992	5999	6011	6019	6027	6036	6053	6055	6075
	6083	6107	6121	6127	6135	6140	6145	6153	6158	6165	6188	6203	6223	6229	6240
	6246	6270	6282	6303	6315	6331	6342	6349	6357	6372	6395	6402	6419	6440	6446
	6464	6469	6502	6505	6522	6525	6528	6536	6548	6553	6568	6571	6601	6614	6628
	6639	6646	6663	6669	6686	6692	6705	6712	6720	6730	6748	6759	6767	6781	6788
	6797	6806	6814	6823	6845	6875	6878	6895	6899	6906	6928	6933	6940	6952	6961
	6966	6985	7003	7012	7018	7037	7043	7060	7067	7080	7087	7095	7105	7123	7134
	7142	7156	7163	7172	7181	7189	7198	7219	7225	7239	7247	7274	7288	7293	7300
	7318	7324	7341	7345	7352	7361	7372	7378	7381	7386	7413	7427	7441	7448	7455
	7460	7464	7472	7479	7494	7516	7550	7556	7562	7569	7576	7583	7591	7597	7604
	7610	7616	7623												
INC	5097	5112	5120	5130	5136	5148	5156	5167	5173	5186	5194	5206	5211	5222	5231
	5232	5234	5236	5238	5240	5246	5250	5251	5254	5257	5264	5268	5269	5272	5275
	5282	5285	5289	5294	5298	5300	5303	5306	5312	5313	5316	5321	5327	5330	5333
	5336	5339	5344	5348	5352	5357	5361	5364	5365	5368	5371	5375	5382	5383	5386
	5389	5392	5393	5397	5401	5407	5412	5415	5421	5440	5453	5454	5471	5477	5481
	5484	5489	5490	5498	5503	5515	5516	5530	5545	5546	5553	5559	5571	5577	5583
	5588	5596	5602	5609	5616	5623	5628	5629	5637	5647	5654	5665	5670	5671	5683
	5690	5702	5703	5710	5718	5727	5748	5749	5762	5773	5779	5782	5785	5790	5799
	5804	5815	5823	5824	5827	5830	5832	5834	5838	5853	5854	5861	5867	5879	5885
	5891	5896	5904	5910	5918	5925	5932	5937	5938	5946	5956	5963	5974	5979	5980
	5992	5999	6011	6012	6019	6027	6036	6045	6053	6059	6067	6075	6083	6088	6107



	6115	6121	6127	6135	6140	6145	6146	6153	6158	6165	6175	6188	6203	6217	6223
	6229	6240	6246	6255	6270	6282	6288	6303	6315	6323	6328	6331	6333	6334	6336
	6337	6342	6349	6357	6372	6378	6395	6402	6419	6427	6440	6446	6452	6464	6469
	6485	6502	6505	6513	6522	6525	6528	6536	6537	6548	6553	6568	6571	6579	6601
	6614	6628	6629	6639	6646	6656	6663	6669	6677	6686	6692	6697	6705	6712	6720
	6721	6730	6739	6748	6759	6767	6768	6781	6788	6797	6798	6806	6814	6823	6845
	6846	6867	6875	6878	6895	6899	6906	6915	6928	6933	6940	6952	6961	6966	6967
	6971	6977	6979	6981	6985	7003	7004	7012	7018	7028	7037	7043	7051	7060	7067
	7072	7080	7087	7095	7096	7105	7114	7123	7134	7142	7143	7156	7163	7172	7173
	7181	7189	7198	7210	7219	7225	7230	7239	7247	7255	7274	7279	7288	7293	7300
	7318	7324	7332	7341	7345	7352	7361	7366	7369	7372	7378	7381	7386	7395	7413
	7427	7433	7441	7448	7455	7460	7464	7472	7479	7494	7495	7550	7556	7562	7569
	7576	7583	7591	7597	7604	7610	7616	7623							
INCB	5519	5522	5524	5526	6262	6264	6339	6343	6583	6587	6593	6597			
TOT	5029														
JMP	5079	5249	5256	5267	5274	5281	5288	5297	5305	5311	5320	5329	5335	5338	5343
	5347	5351	7507												
JSR	5360	5381	5391	5457	5459	5461	5463	5474	5495	5500	5505	5507	5520	5523	5525
	5527	5533	5535	5538	5540	5550	5556	5563	5574	5580	5593	5598	5618	5620	5634
	5639	5641	5656	5658	5662	5675	5680	5687	5694	5707	5715	5721	5724	5731	5752
	5754	5757	5759	5765	5776	5796	5801	5806	5809	5812	5828	5831	5833	5835	5841
	5843	5846	5848	5858	5864	5871	5882	5888	5901	5906	5927	5929	5943	5948	5950
	5965	5967	5971	5984	5989	5996	6003	6016	6024	6030	6033	6040	6050	6056	6072
	6080	6093	6099	6104	6118	6124	6130	6150	6162	6168	6182	6189	6195	6200	6490
	6492	6494	6496	6519	6545	6550	6556	6558	6565	6574	6584	6588	6594	6598	6604
	6606	6609	6611	6636	6642	6651	6660	6666	6683	6688	6707	6709	6727	6732	6734
	6750	6752	6756	6773	6778	6785	6792	6803	6811	6817	6820	6827	6851	6853	6857
	6859	6872	6880	6882	6884	6925	6930	6937	6944	6946	6958	6972	6978	6980	6982
	6988	6990	6993	6995	7009	7015	7023	7034	7040	7057	7062	7082	7084	7102	7107
	7109	7125	7127	7131	7148	7153	7160	7167	7178	7186	7192	7195	7202	7216	7222
	7236	7244	7260	7266	7271	7285	7297	7304	7310	7314	7321	7338	7349	7355	7358
	7375	7404	7409	7419	7424	7438	7445	7469	7476	7502					
MARK	5051														
MOV	5074	5075	5085	5086	5112	5130	5148	5167	5186	5206	5222	5231	5234	5236	5238
	5240	5247	5250	5251	5254	5255	5257	5265	5268	5269	5272	5273	5275	5277	5278
	5279	5282	5285	5286	5287	5289	5295	5298	5300	5303	5304	5306	5308	5309	5310
	5312	5313	5316	5317	5318	5319	5321	5328	5330	5333	5336	5337	5339	5341	5342
	5344	5345	5346	5348	5349	5350	5352	5358	5361	5364	5365	5368	5371	5373	5375
	5376	5377	5378	5379	5380	5382	5383	5386	5389	5390	5392	5393	5397	5401	5402
	5404	5407	5412	5413	5415	5422	5423	5424	5425	5426	5427	5437	5439	5440	5453
	5477	5481	5484	5489	5498	5503	5515	5530	5536	5545	5553	5559	5560	5577	5578
	5583	5596	5602	5616	5623	5628	5637	5654	5665	5670	5683	5690	5702	5710	5718
	5727	5748	5755	5756	5762	5764	5774	5779	5782	5785	5791	5792	5793	5799	5804
	5807	5815	5823	5825	5829	5838	5844	5853	5855	5861	5867	5868	5880	5885	5886
	5891	5897	5904	5910	5919	5925	5932	5937	5939	5946	5957	5963	5968	5974	5979
	5992	5993	5999	6000	6011	6013	6019	6027	6036	6037	6053	6059	6068	6075	6076
	6083	6089	6090	6094	6095	6100	6101	6107	6116	6121	6122	6127	6128	6131	6132
	6135	6137	6140	6145	6147	6148	6153	6154	6155	6158	6159	6160	6165	6166	6176
	6188	6190	6203	6223	6224	6225	6226	6229	6231	6232	6233	6240	6241	6242	6243
	6246	6256	6257	6258	6270	6274	6275	6276	6277	6282	6289	6290	6291	6292	6293
	6303	6305	6306	6307	6308	6309	6310	6315	6325	6326	6327	6331	6342	6349	6351
	6352	6353	6357	6358	6361	6364	6367	6372	6380	6381	6382	6383	6384	6385	6395
	6402	6404	6405	6406	6408	6410	6412	6414	6419	6429	6430	6431	6440	6442	6443
	6446	6454	6455	6456	6457	6458	6459	6464	6465	6466	6469	6486	6487	6497	6502
	6505	6514	6515	6516	6522	6525	6528	6536	6538	6540	6541	6548	6553	6554	6559
	6560	6561	6568	6571	6580	6585	6586	6591	6601	6614	6628	6630	6631	6632	6639





.ENDC	4898	4908	4918	5031	5093	5116	5132	5152	5169	5190	5207	5227	5242	5251	5260
	5269	5290	5323	5353	5365	5383	5417	5449	5467	5485	5511	5541	5567	5584	5605
	5624	5643	5666	5698	5744	5769	5786	5819	5849	5875	5892	5914	5933	5952	5975
	6007	6041	6063	6084	6111	6141	6171	6213	6251	6284	6319	6374	6423	6448	6481
	6509	6532	6575	6624	6652	6673	6693	6716	6735	6763	6793	6841	6863	6911	6962
	6999	7024	7047	7068	7091	7110	7138	7168	7206	7226	7251	7275	7328	7362	7390
.EVEN	5031	5053	5055												
.IF	4898	4908	4918	5031	5093	5116	5132	5152	5169	5190	5207	5227	5242	5251	5260
	5269	5290	5323	5353	5365	5383	5417	5449	5467	5485	5511	5541	5567	5584	5605
	5624	5643	5666	5698	5744	5769	5786	5819	5849	5875	5892	5914	5933	5952	5975
	6007	6041	6063	6084	6111	6141	6171	6213	6251	6284	6319	6374	6423	6448	6481
	6509	6532	6575	6624	6652	6673	6693	6716	6735	6763	6793	6841	6863	6911	6962
	6999	7024	7047	7068	7091	7110	7138	7168	7206	7226	7251	7275	7328	7362	7390
.IFF	4908	4918													
.IIF	4898	5031	5093	5116	5132	5152	5169	5190	5207	5227	5242	5260	5290	5323	5353
	5417	5467	5485	5511	5541	5567	5584	5605	5624	5643	5666	5698	5769	5786	5819
	5849	5875	5892	5914	5933	5952	5975	6007	6041	6063	6084	6111	6141	6171	6251
	6284	6319	6374	6423	6448	6509	6532	6575	6624	6652	6673	6693	6716	6735	6763
	6793	6863	6911	6962	6999	7024	7047	7068	7091	7110	7138	7168	7206	7226	7251
.LIST	2	4623	4896	4917	5031	5066	5093	5095	5112	5116	5118	5130	5132	5134	5148
	5152	5154	5167	5169	5171	5186	5190	5192	5206	5207	5209	5222	5227	5229	5231
	5234	5236	5238	5240	5242	5244	5250	5251	5254	5257	5260	5262	5268	5269	5272
	5275	5282	5285	5289	5290	5292	5298	5300	5303	5306	5312	5313	5316	5321	5323
	5325	5330	5333	5336	5339	5344	5348	5352	5353	5355	5361	5364	5365	5368	5371
	5375	5382	5383	5386	5389	5392	5393	5397	5401	5407	5412	5415	5417	5419	5440
	5443	5449	5451	5453	5467	5469	5477	5481	5484	5485	5487	5489	5498	5503	5511
	5513	5515	5530	5541	5543	5545	5553	5559	5567	5569	5577	5583	5584	5586	5596
	5602	5605	5607	5616	5623	5624	5626	5628	5637	5643	5645	5654	5665	5666	5668
	5670	5683	5690	5698	5700	5702	5710	5718	5727	5734	5744	5746	5748	5762	5769
	5771	5779	5782	5785	5786	5788	5799	5804	5815	5819	5821	5823	5838	5849	5851
	5853	5861	5867	5875	5877	5885	5891	5892	5894	5904	5910	5914	5916	5925	5932
	5933	5935	5937	5946	5952	5954	5963	5974	5975	5977	5979	5992	5999	6007	6009
	6011	6019	6027	6036	6041	6043	6053	6059	6063	6065	6075	6083	6084	6086	6107
	6111	6113	6121	6127	6135	6140	6141	6143	6145	6153	6158	6165	6171	6173	6179
	6180	6181	6183	6186	6188	6192	6193	6194	6196	6199	6203	6206	6213	6215	6223
	6229	6240	6246	6251	6253	6270	6282	6284	6286	6303	6315	6319	6321	6331	6342
	6349	6357	6372	6374	6376	6395	6402	6419	6423	6425	6440	6446	6448	6450	6464
	6469	6473	6481	6483	6502	6505	6509	6511	6522	6525	6528	6532	6534	6536	6548
	6553	6568	6571	6575	6577	6601	6614	6624	6626	6628	6639	6646	6652	6654	6663
	6669	6673	6675	6686	6692	6693	6695	6705	6712	6716	6718	6720	6730	6735	6737
	6748	6759	6763	6765	6767	6781	6788	6793	6795	6797	6806	6814	6823	6830	6841
	6843	6845	6863	6865	6875	6878	6895	6899	6906	6911	6913	6928	6933	6940	6952
	6961	6962	6964	6966	6985	6999	7001	7003	7012	7018	7024	7026	7037	7043	7047
	7049	7060	7067	7068	7070	7080	7087	7091	7093	7095	7105	7110	7112	7123	7134
	7138	7140	7142	7156	7163	7168	7170	7172	7181	7189	7198	7206	7208	7219	7225
	7226	7228	7239	7247	7251	7253	7274	7275	7277	7288	7293	7300	7318	7324	7328
	7330	7341	7345	7352	7361	7362	7364	7372	7378	7381	7386	7390	7393	7401	7402
	7403	7405	7408	7413	7416	7417	7418	7420	7423	7427	7429	7431	7441	7448	7455
	7460	7464	7472	7479	7482	7492	7494	7545	7550	7556	7562	7569	7576	7583	7591
	7597	7604	7610	7616	7623										
.MACRO	39	81	168	308	485	515	586	747	801	895	926	974	986	1030	1064
	1097	1110	1131	1144	1177	1226	1272	1309	1356	1389	1419	1475	1483	1535	1739
	1947	2140	2228	2353	2450	2528	2613	2827	2919	2995	3095	3223	3286	3348	3466

	3504	3568	3666	3751	3790	3853	3891	3934	4032	4080	4350	4397	4437	4513	4920
.MCALL	4933	4948	4961	4969	4976	4982	4986	4998	5001	5005					
.MEXIT	4897														
.NLIST	5031														
	1	3	4895	4912	5031	5064	5093	5095	5112	5116	5118	5130	5132	5134	5148
	5152	5154	5167	5169	5171	5186	5190	5192	5206	5207	5209	5222	5227	5229	5231
	5234	5236	5238	5240	5242	5244	5250	5251	5254	5257	5260	5262	5268	5269	5272
	5275	5282	5285	5289	5290	5292	5298	5300	5303	5306	5312	5313	5316	5321	5323
	5325	5330	5333	5336	5339	5344	5348	5352	5353	5355	5361	5364	5365	5368	5371
	5375	5382	5383	5386	5389	5392	5393	5397	5401	5407	5412	5415	5417	5419	5440
	5441	5449	5451	5453	5467	5469	5477	5481	5484	5485	5487	5489	5498	5503	5511
	5513	5515	5530	5541	5543	5545	5553	5559	5567	5569	5577	5583	5584	5586	5596
	5602	5605	5607	5616	5623	5624	5626	5628	5637	5643	5645	5654	5665	5666	5668
	5670	5683	5690	5698	5700	5702	5710	5718	5727	5732	5744	5746	5748	5762	5769
	5771	5779	5782	5785	5786	5788	5799	5804	5815	5819	5821	5823	5838	5849	5851
	5853	5861	5867	5875	5877	5885	5891	5892	5894	5904	5910	5914	5916	5925	5932
	5933	5935	5937	5946	5952	5954	5963	5974	5975	5977	5979	5992	5999	6007	6009
	6011	6019	6027	6036	6041	6043	6053	6059	6063	6065	6075	6083	6084	6086	6107
	6111	6113	6121	6127	6135	6140	6141	6143	6145	6153	6158	6165	6171	6173	6178
	6181	6183	6184	6185	6188	6191	6194	6196	6197	6198	6203	6204	6213	6215	6223
	6229	6240	6246	6251	6253	6270	6282	6284	6286	6303	6315	6319	6321	6331	6342
	6349	6357	6372	6374	6376	6395	6402	6419	6423	6425	6440	6446	6448	6450	6464
	6469	6471	6481	6483	6502	6505	6509	6511	6522	6525	6528	6532	6534	6536	6548
	6553	6568	6571	6575	6577	6601	6614	6624	6626	6628	6639	6646	6652	6654	6663
	6669	6673	6675	6686	6692	6693	6695	6705	6712	6716	6718	6720	6730	6735	6737
	6748	6759	6763	6765	6767	6781	6788	6793	6795	6797	6806	6814	6823	6828	6841
	6843	6845	6863	6865	6875	6878	6895	6899	6906	6911	6913	6928	6933	6940	6952
	6961	6962	6964	6966	6985	6999	7001	7003	7012	7018	7024	7026	7037	7043	7047
	7049	7060	7067	7068	7070	7080	7087	7091	7093	7095	7105	7110	7112	7123	7134
	7138	7140	7142	7156	7163	7168	7170	7172	7181	7189	7198	7206	7208	7219	7225
	7226	7228	7239	7247	7251	7253	7274	7275	7277	7288	7293	7300	7318	7324	7328
	7330	7341	7345	7352	7361	7362	7364	7372	7378	7381	7386	7390	7393	7400	7403
	7405	7406	7407	7413	7415	7418	7420	7421	7422	7427	7429	7431	7441	7448	7455
	7460	7464	7472	7479	7480	7492	7494	7543	7550	7556	7562	7569	7576	7583	7591
	7597	7604	7610	7616	7623										
.NTYPE	6181	6183	6194	6196	7403	7405	7418	7420							
.PAGE	4887	4889	4891	4905	5030	5033	5067	5093	5132	5169	5207	5242	5290	5353	5444
	5485	5541	5584	5624	5666	5735	5786	5849	5892	5933	5975	6041	6084	6141	6207
	6284	6374	6448	6474	6509	6575	6652	6693	6735	6793	6831	6863	6962	7024	7068
	7110	7168	7226	7275	7362	7429	7483	7522	7546						
.REPT	4624	4913													
.SBTTL	4918	5031	5032	5065	5093	5116	5132	5152	5169	5190	5207	5227	5242	5260	5290
	5323	5353	5417	5442	5449	5467	5485	5511	5541	5567	5584	5605	5624	5643	5666
	5698	5733	5744	5769	5786	5819	5849	5875	5892	5914	5933	5952	5975	6007	6041
	6063	6084	6111	6141	6171	6205	6213	6251	6284	6319	6374	6423	6448	6472	6481
	6509	6532	6575	6624	6652	6673	6693	6716	6735	6763	6793	6829	6841	6863	6911
	6962	6999	7024	7047	7068	7091	7110	7138	7168	7206	7226	7251	7275	7328	7362
	7390	7429	7481	7512	7525	7544									
.TITLE	4898														
.WORD	4918	5031	5032	6181	6183	6194	6196	7403	7405	7418	7420				

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0



L09

DVKAAA MACY11 27(732) 25-AUG-76 13:25 PAGE 57-6  
DVKAAA.P11 CROSS REFERENCE TABLE -- PERMANENT SYMBOLS

\*\*\* SEQ 0115

\*,DVKAAA/DS:ERFZ/CRF=DVKAAA.SML,DVKAAA.P11  
RUN-TIME: 37 49 8 SECONDS  
RUN-TIME RATIO: 1533/95=16.0  
CORE USED: 30K (59 PAGES)

