

TEST

TEST 10 ADD  
MD-11-DOJ-A

EP-DOJ-A-DL-A  
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FICHE 1 OF 1

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E01

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7.2 OPERATIONAL RESTRICTION  
NONE



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8. MISCELLANEOUS

IF A HALT OCCURES IN THE TRAP OR INTERRUPT VECTOR AREA, REGISTER SIX SHOULD BE EXAMINED TO DETERMINE ITS CONTENTS, THEN USE REGISTER SIX CONTENTS AS AN ADDRESS TO DETERMINE THE LOCATION THE PROGRAM WAS AT, WHEN THE INTERRUPT OR TRAP OCCURRED. (MEMORY AS SPECIFIED BY R6 CONTAINS THE PC OF THE INSTRUCTION FOLLOWING THE INSTRUCTION WHERE THE TRAP OCCURED)

8.1 EXECUTION TIME

TIME BETWEEN BELLS

FOR TAPE 1	5	MINUTES
FOR TAPE 2	2.5	MINUTES
FOR TAPE 3	3	MINUTES
FOR TAPE 4	6	MINUTES
FOR TAPE 5	5	MINUTES
FOR TAPE 6	6	MINUTES
FOR TAPE 7	5	MINUTES
FOR TAPE 8	6	MINUTES
FOR TAPE 9	3	MINUTES
FOR TAPE 10	4	MINUTES
FOR TAPE 11	2	MINUTES
FOR TAPE 12	4	MINUTES

9. PROGRAM DESCRIPTION

- TEST 1 THIS IS A TEST OF THE UNCONDITIONAL BRANCH INSTRUCTION, IT PROVES THAT A LONG PROGRAM CAN BE LOADED AND THE INSTRUCTIONS CAN BE EXECUTED THROUGHOUT MEMORY.
- TEST 2 THIS IS A CONDITIONAL BRANCH TEST USING ALL OF THE CONDITIONAL BRANCH INSTRUCTIONS. IT TESTS THAT ALL COMBINATIONS OF CONDITION CODES ARE RESPONDED TO CORRECTLY. (THE CONDITION CODES ARE MANIPULATED WITH THE MICRO-PROGRAM OPERATIONS.)
- TEST 3 THIS IS A TEST OF THE UNARY INSTRUCTIONS AND THEIR EFFECTS UPON THE CONDITION CODES ONLY. MOSTLY REGISTER OPERATIONS.
- TEST 4 THIS IS A COMBINED USE OF UNARY AND BINARY INSTRUCTIONS TESTING THE RESULTS AND THE CONDITION CODES.
- TEST 5 THIS TEST ROTATE AND SHIFT INSTRUCTION, BOTH THE REGISTERS AND MEMORY LOCATIONS, WORD AND BYTE, ARE TESTED.
- TEST 6 THIS IS A TEST OF THE COMPARE INSTRUCTIONS FOR



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EQUALITY. ALL CONDITIONAL BRANCHES THAT WILL  
BRANCH ON ANY FORM OF EQUALITY ARE ALSO TESTED.  
BOTH WORD AND BYTE OPERATIONS ARE USED.



(9 CONT'D)

TEST 7 COMPARE NOT. THIS IS A TEST OF THE COMPARE INSTRUCTIONS WHERE NO VALUES ARE EQUAL. HERE BRANCHES FOR NOT EQUAL, GREATER THAN AND LESS THAN, ETC., ARE TESTED.

TEST 8 CHECKS THE MOVE INSTRUCTION BOTH TO MEMORY AND REGISTERS.

TEST 9 CHECK THE BIS, BIC AND BIT INSTRUCTION BOTH TO MEMORY AND TO REGISTERS.

TEST 10 TESTS THE ADD INSTRUCTION.

TEST 11 TESTS THE SUBTRACT INSTRUCTION.

TEST 12 TEST OF THE JUMP INSTRUCTION IN MOST MODES.

NOTE THAT STACK OPERATION AND INSTRUCTIONS THAT USE THEM ARE NOT TESTED IN THIS GROUP OF TEST.

- 10. LISTING
- 11. FLOW CHART(S)

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260           ;TEST ADD INSTRUCTION
261
262 000200 000240 SCOPE
263 000202 012700 000000 MOV #0,%0 ;MOVE 0 TO REGISTER ZERO
264 000206 062700 000000 ADD #0,%0 ;ADD 0 TO 0
265 000212 022700 000000 CMP #0+0,%0 ;DOES REGISTER ZERO=0+0
266 000216 001401 BEQ .+4
267 000220 000000 HLT ;ADD 0 TO 0 FAILED
268 000222 000240 SCOPE
269 000224 012700 177777 MOV #177777,%0 ;MOVE 177777 TO REGISTER ZERO
270 000230 062700 177777 ADD #-1,%0 ;ADD -1 TO 177777
271 000234 022700 177776 CMP #-1+177777,%0 ;DOES REGISTER ZERO=-1+177777
272 000240 001401 BEQ .+4
273 000242 000000 HLT ;ADD -1 TO 177777 FAILED
274
275 000001 N=1
276 000244 000240 SCOPE
277 000246 012700 177777 MOV #177777,%0 ;MOVE 177777 TO REGISTER ZERO
278 000252 062700 000001 ADD #1,%0 ;ADD 1 TO 177777
279 000256 022700 000000 CMP #1+177777,%0 ;DOES REGISTER ZERO=1+177777
280 000262 001401 BEQ .+4
281 000264 000000 HLT ;ADD 1 TO 177777 FAILED
282 000002 N=N+N
283 000266 000240 SCOPE
284 000270 012700 177777 MOV #177777,%0 ;MOVE 177777 TO REGISTER ZERO
285 000274 062700 000002 ADD #2,%0 ;ADD 2 TO 177777
286 000300 022700 000001 CMP #2+177777,%0 ;DOES REGISTER ZERO=2+177777
287 000304 001401 BEQ .+4
288 000306 000000 HLT ;ADD 2 TO 177777 FAILED
289 000004 N=N+N
290 000310 000240 SCOPE
291 000312 012700 177777 MOV #177777,%0 ;MOVE 177777 TO REGISTER ZERO
292 000316 062700 000004 ADD #4,%0 ;ADD 4 TO 177777
293 000322 022700 000003 CMP #4+177777,%0 ;DOES REGISTER ZERO=4+177777
294 000326 001401 BEQ .+4
295 000330 000000 HLT ;ADD 4 TO 177777 FAILED
296 000010 N=N+N
297 000332 000240 SCOPE
298 000334 012700 177777 MOV #177777,%0 ;MOVE 177777 TO REGISTER ZERO
299 000340 062700 000010 ADD #10,%0 ;ADD 10 TO 177777
300 000344 022700 000007 CMP #10+177777,%0 ;DOES REGISTER ZERO=10+177777
301 000350 001401 BEQ .+4
302 000352 000000 HLT ;ADD 10 TO 177777 FAILED
303 000020 N=N+N
304 000354 000240 SCOPE
305 000356 012700 177777 MOV #177777,%0 ;MOVE 177777 TO REGISTER ZERO
306 000362 062700 000020 ADD #20,%0 ;ADD 20 TO 177777
307 000366 022700 000017 CMP #20+177777,%0 ;DOES REGISTER ZERO=20+177777
308 000372 001401 BEQ .+4
309 000374 000000 HLT ;ADD 20 TO 177777 FAILED
310 000040 N=N+N
311 000376 000240 SCOPE
312 000400 012700 177777 MOV #177777,%0 ;MOVE 177777 TO REGISTER ZERO
313 000404 062700 000040 ADD #40,%0 ;ADD 40 TO 177777
314 000410 022700 000037 CMP #40+177777,%0 ;DOES REGISTER ZERO=40+177777
  
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# K01

315	000414	001401		BEQ	.+4	
316	000416	000000		HLT		;ADD 40 TO 177777 FAILED
317		000100		N=N+N		
318	000420	000240		SCOPE		
319	000422	012700	177777	MOV	#177777,%0	;MOVE 177777 TO REGISTER ZERO
320	000426	062700	000100	ADD	#100,%0	;ADD 100 TO 177777
321	000432	022700	000077	CMP	#100+177777,%0	;DOES REGISTER ZERO=100+177777
322	000436	001401		BEQ	.+4	
323	000440	000000		HLT		;ADD 100 TO 177777 FAILED
324		000200		N=N+N		
325	000442	000240		SCOPE		
326	000444	012700	177777	MOV	#177777,%0	;MOVE 177777 TO REGISTER ZERO
327	000450	062700	000200	ADD	#200,%0	;ADD 200 TO 177777
328	000454	022700	000177	CMP	#200+177777,%0	;DOES REGISTER ZERO=200+177777
329	000460	001401		BEQ	.+4	
330	000462	000000		HLT		;ADD 200 TO 177777 FAILED
331		000400		N=N+N		
332	000464	000240		SCOPE		
333	000466	012700	177777	MOV	#177777,%0	;MOVE 177777 TO REGISTER ZERO
334	000472	062700	000400	ADD	#400,%0	;ADD 400 TO 177777
335	000476	022700	000377	CMP	#400+177777,%0	;DOES REGISTER ZERO=400+177777
336	000502	001401		BEQ	.+4	
337	000504	000000		HLT		;ADD 400 TO 177777 FAILED
338		001000		N=N+N		
339	000506	000240		SCOPE		
340	000510	012700	177777	MOV	#177777,%0	;MOVE 177777 TO REGISTER ZERO
341	000514	062700	001000	ADD	#1000,%0	;ADD 1000 TO 177777
342	000520	022700	000777	CMP	#1000+177777,%0	;DOES REGISTER ZERO=1000+177777
343	000524	001401		BEQ	.+4	
344	000526	000000		HLT		;ADD 1000 TO 177777 FAILED
345		002000		N=N+N		
346	000530	000240		SCOPE		
347	000532	012700	177777	MOV	#177777,%0	;MOVE 177777 TO REGISTER ZERO
348	000536	062700	002000	ADD	#2000,%0	;ADD 2000 TO 177777
349	000542	022700	001777	CMP	#2000+177777,%0	;DOES REGISTER ZERO=2000+177777
350	000546	001401		BEQ	.+4	
351	000550	000000		HLT		;ADD 2000 TO 177777 FAILED
352		004000		N=N+N		
353	000552	000240		SCOPE		
354	000554	012700	177777	MOV	#177777,%0	;MOVE 177777 TO REGISTER ZERO
355	000560	062700	004000	ADD	#4000,%0	;ADD 4000 TO 177777
356	000564	022700	003777	CMP	#4000+177777,%0	;DOES REGISTER ZERO=4000+177777
357	000570	001401		BEQ	.+4	
358	000572	000000		HLT		;ADD 4000 TO 177777 FAILED
359		010000		N=N+N		
360	000574	000240		SCOPE		
361	000576	012700	177777	MOV	#177777,%0	;MOVE 177777 TO REGISTER ZERO
362	000602	062700	010000	ADD	#10000,%0	;ADD 10000 TO 177777
363	000606	022700	007777	CMP	#10000+177777,%0	;DOES REGISTER ZERO=10000+177777
364	000612	001401		BEQ	.+4	
365	000614	000000		HLT		;ADD 10000 TO 177777 FAILED
366		020000		N=N+N		
367	000616	000240		SCOPE		
368	000620	012700	177777	MOV	#177777,%0	;MOVE 177777 TO REGISTER ZERO
369	000624	062700	020000	ADD	#20000,%0	;ADD 20000 TO 177777
370	000630	022700	017777	CMP	#20000+177777,%0	;DOES REGISTER ZERO=20000+177777



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371	000634	001401		BEQ	.+4	
372	000636	000000		HLT		;ADD 20000 TO 177777 FAILED
373		040000		N=N+N		
374	000640	000240		SCOPE		
375	000642	012700	177777	MOV	#177777,%0	;MOVE 177777 TO REGISTER ZERO
376	000646	062700	040000	ADD	#40000,%0	;ADD 40000 TO 177777
377	000652	022700	037777	CMP	#40000+177777,%0	;DOES REGISTER ZERO=40000+177777
378	000656	001401		BEQ	.+4	
379	000660	000000		HLT		;ADD 40000 TO 177777 FAILED
380		100000		N=N+N		
381	000662	000240		SCOPE		
382	000664	012700	177777	MOV	#177777,%0	;MOVE 177777 TO REGISTER ZERO
383	000670	062700	100000	ADD	#100000,%0	;ADD 100000 TO 177777
384	000674	022700	077777	CMP	#100000+177777,%0	;DOES REGISTER ZERO=100000+177777
385	000700	001401		BEQ	.+4	
386	000702	000000		HLT		;ADD 100000 TO 177777 FAILED
387		000000		N=N+N		
388						
389		000001		N=1		
390	000704	000240		SCOPE		
391	000706	012700	000001	MOV	#1,%0	;MOVE 1 TO REGISTER ZERO
392	000712	062700	177777	ADD	#-1,%0	;ADD -1 TO 1
393	000716	022700	000000	CMP	#-1+1,%0	;DOES REGISTER ZERO=-1+1
394	000722	001401		BEQ	.+4	
395	000724	000000		HLT		;ADD -1 TO 1 FAILED
396		000002		N=N+N		
397	000726	000240		SCOPE		
398	000730	012700	000002	MOV	#2,%0	;MOVE 2 TO REGISTER ZERO
399	000734	062700	177777	ADD	#-1,%0	;ADD -1 TO 2
400	000740	022700	000001	CMP	#-1+2,%0	;DOES REGISTER ZERO=-1+2
401	000744	001401		BEQ	.+4	
402	000746	000000		HLT		;ADD -1 TO 2 FAILED
403		000004		N=N+N		
404	000750	000240		SCOPE		
405	000752	012700	000004	MOV	#4,%0	;MOVE 4 TO REGISTER ZERO
406	000756	062700	177777	ADD	#-1,%0	;ADD -1 TO 4
407	000762	022700	000003	CMP	#-1+4,%0	;DOES REGISTER ZERO=-1+4
408	000766	001401		BEQ	.+4	
409	000770	000000		HLT		;ADD -1 TO 4 FAILED
410		000010		N=N+N		
411	000772	000240		SCOPE		
412	000774	012700	000010	MOV	#10,%0	;MOVE 10 TO REGISTER ZERO
413	001000	062700	177777	ADD	#-1,%0	;ADD -1 TO 10
414	001004	022700	000007	CMP	#-1+10,%0	;DOES REGISTER ZERO=-1+10
415	001010	001401		BEQ	.+4	
416	001012	000000		HLT		;ADD -1 TO 10 FAILED
417		000020		N=N+N		
418	001014	000240		SCOPE		
419	001016	012700	000020	MOV	#20,%0	;MOVE 20 TO REGISTER ZERO
420	001022	062700	177777	ADD	#-1,%0	;ADD -1 TO 20
421	001026	022700	000017	CMP	#-1+20,%0	;DOES REGISTER ZERO=-1+20
422	001032	001401		BEQ	.+4	
423	001034	000000		HLT		;ADD -1 TO 20 FAILED
424		000040		N=N+N		
425	001036	000240		SCOPE		
426	001040	012700	000040	MOV	#40,%0	;MOVE 40 TO REGISTER ZERO



# MO1

427	001044	062700	177777	ADD	#-1,%0	;ADD -1 TO 40
428	001050	022700	000037	CMP	#-1+40,%0	;DOES REGISTER ZERO=-1+40
429	001054	001401		BEQ	+.4	
430	001056	000000		HLT		;ADD -1 TO 40 FAILED
431		000100		N=N+N		
432	001060	000240		SCOPE		
433	001062	012700	000100	MOV	#100,%0	;MOVE 100 TO REGISTER ZERO
434	001066	062700	177777	ADD	#-1,%0	;ADD -1 TO 100
435	001072	022700	000077	CMP	#-1+100,%0	;DOES REGISTER ZERO=-1+100
436	001076	001401		BEQ	+.4	
437	001100	000000		HLT		;ADD -1 TO 100 FAILED
438		000200		N=N+N		
439	001102	000240		SCOPE		
440	001104	012700	000200	MOV	#200,%0	;MOVE 200 TO REGISTER ZERO
441	001110	062700	177777	ADD	#-1,%0	;ADD -1 TO 200
442	001114	022700	000177	CMP	#-1+200,%0	;DOES REGISTER ZERO=-1+200
443	001120	001401		BEQ	+.4	
444	001122	000000		HLT		;ADD -1 TO 200 FAILED
445		000400		N=N+N		
446	001124	000240		SCOPE		
447	001126	012700	000400	MOV	#400,%0	;MOVE 400 TO REGISTER ZERO
448	001132	062700	177777	ADD	#-1,%0	;ADD -1 TO 400
449	001136	022700	000377	CMP	#-1+400,%0	;DOES REGISTER ZERO=-1+400
450	001142	001401		BEQ	+.4	
451	001144	000000		HLT		;ADD -1 TO 400 FAILED
452		001000		N=N+N		
453	001146	000240		SCOPE		
454	001150	012700	001000	MOV	#1000,%0	;MOVE 1000 TO REGISTER ZERO
455	001154	062700	177777	ADD	#-1,%0	;ADD -1 TO 1000
456	001160	022700	000777	CMP	#-1+1000,%0	;DOES REGISTER ZERO=-1+1000
457	001164	001401		BEQ	+.4	
458	001166	000000		HLT		;ADD -1 TO 1000 FAILED
459		002000		N=N+N		
460	001170	000240		SCOPE		
461	001172	012700	002000	MOV	#2000,%0	;MOVE 2000 TO REGISTER ZERO
462	001176	062700	177777	ADD	#-1,%0	;ADD -1 TO 2000
463	001202	022700	001777	CMP	#-1+2000,%0	;DOES REGISTER ZERO=-1+2000
464	001206	001401		BEQ	+.4	
465	001210	000000		HLT		;ADD -1 TO 2000 FAILED
466		004000		N=N+N		
467	001212	000240		SCOPE		
468	001214	012700	004000	MOV	#4000,%0	;MOVE 4000 TO REGISTER ZERO
469	001220	062700	177777	ADD	#-1,%0	;ADD -1 TO 4000
470	001224	022700	003777	CMP	#-1+4000,%0	;DOES REGISTER ZERO=-1+4000
471	001230	001401		BEQ	+.4	
472	001232	000000		HLT		;ADD -1 TO 4000 FAILED
473		010000		N=N+N		
474	001234	000240		SCOPE		
475	001236	012700	010000	MOV	#10000,%0	;MOVE 10000 TO REGISTER ZERO
476	001242	062700	177777	ADD	#-1,%0	;ADD -1 TO 10000
477	001246	022700	007777	CMP	#-1+10000,%0	;DOES REGISTER ZERO=-1+10000
478	001252	001401		BEQ	+.4	
479	001254	000000		HLT		;ADD -1 TO 10000 FAILED
480		020000		N=N+N		
481	001256	000240		SCOPE		
482	001260	012700	020000	MOV	#20000,%0	;MOVE 20000 TO REGISTER ZERO



# NO1

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483	001264	062700	177777	ADD	#-1,%0	;ADD -1 TO 20000
484	001270	022700	017777	CMP	#-1+20000,%0	;DOES REGISTER ZERO=-1+20000
485	001274	001401		BEQ	+.4	
486	001276	000000		HLT		;ADD -1 TO 20000 FAILED
487		040000		N=N+N		
488	001300	000240		SCOPE		
489	001302	012700	040000	MOV	#40000,%0	;MOVE 40000 TO REGISTER ZERO
490	001306	062700	177777	ADD	#-1,%0	;ADD -1 TO 40000
491	001312	022700	037777	CMP	#-1+40000,%0	;DOES REGISTER ZERO=-1+40000
492	001316	001401		BEQ	+.4	
493	001320	000000		HLT		;ADD -1 TO 40000 FAILED
494		100000		N=N+N		
495	001322	000240		SCOPE		
496	001324	012700	100000	MOV	#100000,%0	;MOVE 100000 TO REGISTER ZERO
497	001330	062700	177777	ADD	#-1,%0	;ADD -1 TO 100000
498	001334	022700	077777	CMP	#-1+100000,%0	;DOES REGISTER ZERO=-1+100000
499	001340	001401		BEQ	+.4	
500	001342	000000		HLT		;ADD -1 TO 100000 FAILED
501		000000		N=N+N		
502						
503		000001		N=1		
504	001344	000240		SCOPE		
505	001346	012700	000001	MOV	#1,%0	;MOVE 1 TO REGISTER ZERO
506	001352	062700	000000	ADD	#0,%0	;ADD 0 TO 1
507	001356	022700	000001	CMP	#0+1,%0	;DOES REGISTER ZERO=0+1
508	001362	001401		BEQ	+.4	
509	001364	000000		HLT		;ADD 0 TO 1 FAILED
510		000002		N=N+N		
511	001366	000240		SCOPE		
512	001370	012700	000002	MOV	#2,%0	;MOVE 2 TO REGISTER ZERO
513	001374	062700	000000	ADD	#0,%0	;ADD 0 TO 2
514	001400	022700	000002	CMP	#0+2,%0	;DOES REGISTER ZERO=0+2
515	001404	001401		BEQ	+.4	
516	001406	000000		HLT		;ADD 0 TO 2 FAILED
517		000004		N=N+N		
518	001410	000240		SCOPE		
519	001412	012700	000004	MOV	#4,%0	;MOVE 4 TO REGISTER ZERO
520	001416	062700	000000	ADD	#0,%0	;ADD 0 TO 4
521	001422	022700	000004	CMP	#0+4,%0	;DOES REGISTER ZERO=0+4
522	001426	001401		BEQ	+.4	
523	001430	000000		HLT		;ADD 0 TO 4 FAILED
524		000010		N=N+N		
525	001432	000240		SCOPE		
526	001434	012700	000010	MOV	#10,%0	;MOVE 10 TO REGISTER ZERO
527	001440	062700	000000	ADD	#0,%0	;ADD 0 TO 10
528	001444	022700	000010	CMP	#0+10,%0	;DOES REGISTER ZERO=0+10
529	001450	001401		BEQ	+.4	
530	001452	000000		HLT		;ADD 0 TO 10 FAILED
531		000020		N=N+N		
532	001454	000240		SCOPE		
533	001456	012700	000020	MOV	#20,%0	;MOVE 20 TO REGISTER ZERO
534	001462	062700	000000	ADD	#0,%0	;ADD 0 TO 20
535	001466	022700	000020	CMP	#0+20,%0	;DOES REGISTER ZERO=0+20
536	001472	001401		BEQ	+.4	
537	001474	000000		HLT		;ADD 0 TO 20 FAILED
538		000040		N=N+N		



001476	000240		SCOPE		
001500	012700	000040	MOV	#40,%0	;MOVE 40 TO REGISTER ZERO
001504	062700	000000	ADD	#0,%0	;ADD 0 TO 40
001510	022700	000040	CMP	#0+40,%0	;DOES REGISTER ZERO=0+40
001514	001401		BEQ	.+4	
001516	000000		HLT		;ADD 0 TO 40 FAILED
	000100		N=N+N		
001520	000240		SCOPE		
001522	012700	000100	MOV	#100,%0	;MOVE 100 TO REGISTER ZERO
001526	062700	000000	ADD	#0,%0	;ADD 0 TO 100
001532	022700	000100	CMP	#0+100,%0	;DOES REGISTER ZERO=0+100
001536	001401		BEQ	.+4	
001540	000000		HLT		;ADD 0 TO 100 FAILED
	000200		N=N+N		
001542	000240		SCOPE		
001544	012700	000200	MOV	#200,%0	;MOVE 200 TO REGISTER ZERO
001550	062700	000000	ADD	#0,%0	;ADD 0 TO 200
001554	022700	000200	CMP	#0+200,%0	;DOES REGISTER ZERO=0+200
001560	001401		BEQ	.+4	
001562	000000		HLT		;ADD 0 TO 200 FAILED
	000400		N=N+N		
001564	000240		SCOPE		
001566	012700	000400	MOV	#400,%0	;MOVE 400 TO REGISTER ZERO
001572	062700	000000	ADD	#0,%0	;ADD 0 TO 400
001576	022700	000400	CMP	#0+400,%0	;DOES REGISTER ZERO=0+400
001602	001401		BEQ	.+4	
001604	000000		HLT		;ADD 0 TO 400 FAILED
	001000		N=N+N		
001606	000240		SCOPE		
001610	012700	001000	MOV	#1000,%0	;MOVE 1000 TO REGISTER ZERO
001614	062700	000000	ADD	#0,%0	;ADD 0 TO 1000
001620	022700	001000	CMP	#0+1000,%0	;DOES REGISTER ZERO=0+1000
001624	001401		BEQ	.+4	
001626	000000		HLT		;ADD 0 TO 1000 FAILED
	002000		N=N+N		
001630	000240		SCOPE		
001632	012700	002000	MOV	#2000,%0	;MOVE 2000 TO REGISTER ZERO
001636	062700	000000	ADD	#0,%0	;ADD 0 TO 2000
001642	022700	002000	CMP	#0+2000,%0	;DOES REGISTER ZERO=0+2000
001646	001401		BEQ	.+4	
001650	000000		HLT		;ADD 0 TO 2000 FAILED
	004000		N=N+N		
001652	000240		SCOPE		
001654	012700	004000	MOV	#4000,%0	;MOVE 4000 TO REGISTER ZERO
001660	062700	000000	ADD	#0,%0	;ADD 0 TO 4000
001664	022700	004000	CMP	#0+4000,%0	;DOES REGISTER ZERO=0+4000
001670	001401		BEQ	.+4	
001672	000000		HLT		;ADD 0 TO 4000 FAILED
	010000		N=N+N		
001674	000240		SCOPE		
001676	012700	010000	MOV	#10000,%0	;MOVE 10000 TO REGISTER ZERO
001702	062700	000000	ADD	#0,%0	;ADD 0 TO 10000
001706	022700	010000	CMP	#0+10000,%0	;DOES REGISTER ZERO=0+10000
001712	001401		BEQ	.+4	
001714	000000		HLT		;ADD 0 TO 10000 FAILED
	020000		N=N+N		



595	001716	000240		SCOPE		
596	001720	012700	020000	MOV	#20000,%0	:MOVE 20000 TO REGISTER ZERO
597	001724	062700	000000	ADD	#0,%0	:ADD 0 TO 20000
598	001730	022700	020000	CMP	#0+20000,%0	:DOES REGISTER ZERO=0+20000
599	001734	001401		BEQ	.+4	
600	001736	000000		HLT		:ADD 0 TO 20000 FAILED
601		040000		N=N+N		
602	001740	000240		SCOPE		
603	001742	012700	040000	MOV	#40000,%0	:MOVE 40000 TO REGISTER ZERO
604	001746	062700	000000	ADD	#0,%0	:ADD 0 TO 40000
605	001752	022700	040000	CMP	#0+40000,%0	:DOES REGISTER ZERO=0+40000
606	001756	001401		BEQ	.+4	
607	001760	000000		HLT		:ADD 0 TO 40000 FAILED
608		100000		N=N+N		
609	001762	000240		SCOPE		
610	001764	012700	100000	MOV	#100000,%0	:MOVE 100000 TO REGISTER ZERO
611	001770	062700	000000	ADD	#0,%0	:ADD 0 TO 100000
612	001774	022700	100000	CMP	#0+100000,%0	:DOES REGISTER ZERO=0+100000
613	002000	001401		BEQ	.+4	
614	002002	000000		HLT		:ADD 0 TO 100000 FAILED
615		000000		N=N+N		
616		000001		N=1		



618	002004	000240		SCOPE		
619	002006	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
620	002012	062700	000001	ADD	#1,%0	;ADD 1 TO 0
621	002016	022700	000001	CMP	#1+0,%0	;DOES REGISTER ZERO=1+0
622	002022	001401		BEQ	.+4	
623	002024	000000		HLT		;ADD 1 TO 0 FAILED
624		000002		N=N+N		
625	002026	000240		SCOPE		
626	002030	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
627	002034	062700	000002	ADD	#2,%0	;ADD 2 TO 0
628	002040	022700	000002	CMP	#2+0,%0	;DOES REGISTER ZERO=2+0
629	002044	001401		BEQ	.+4	
630	002046	000000		HLT		;ADD 2 TO 0 FAILED
631		000004		N=N+N		
632	002050	000240		SCOPE		
633	002052	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
634	002056	062700	000004	ADD	#4,%0	;ADD 4 TO 0
635	002062	022700	000004	CMP	#4+0,%0	;DOES REGISTER ZERO=4+0
636	002066	001401		BEQ	.+4	
637	002070	000000		HLT		;ADD 4 TO 0 FAILED
638		000010		N=N+N		
639	002072	000240		SCOPE		
640	002074	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
641	002100	062700	000010	ADD	#10,%0	;ADD 10 TO 0
642	002104	022700	000010	CMP	#10+0,%0	;DOES REGISTER ZERO=10+0
643	002110	001401		BEQ	.+4	
644	002112	000000		HLT		;ADD 10 TO 0 FAILED
645		000020		N=N+N		
646	002114	000240		SCOPE		
647	002116	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
648	002122	062700	000020	ADD	#20,%0	;ADD 20 TO 0
649	002126	022700	000020	CMP	#20+0,%0	;DOES REGISTER ZERO=20+0
650	002132	001401		BEQ	.+4	
651	002134	000000		HLT		;ADD 20 TO 0 FAILED
652		000040		N=N+N		
653	002136	000240		SCOPE		
654	002140	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
655	002144	062700	000040	ADD	#40,%0	;ADD 40 TO 0
656	002150	022700	000040	CMP	#40+0,%0	;DOES REGISTER ZERO=40+0
657	002154	001401		BEQ	.+4	
658	002156	000000		HLT		;ADD 40 TO 0 FAILED
659		000100		N=N+N		
660	002160	000240		SCOPE		
661	002162	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
662	002166	062700	000100	ADD	#100,%0	;ADD 100 TO 0
663	002172	022700	000100	CMP	#100+0,%0	;DOES REGISTER ZERO=100+0
664	002176	001401		BEQ	.+4	
665	002200	000000		HLT		;ADD 100 TO 0 FAILED
666		000200		N=N+N		
667	002202	000240		SCOPE		
668	002204	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
669	002210	062700	000200	ADD	#200,%0	;ADD 200 TO 0
670	002214	022700	000200	CMP	#200+0,%0	;DOES REGISTER ZERO=200+0
671	002220	001401		BEQ	.+4	
672	002222	000000		HLT		;ADD 200 TO 0 FAILED
673		000400		N=N+N		



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674	002224	000240		SCOPE		
675	002226	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
676	002232	062700	000400	ADD	#400,%0	;ADD 400 TO 0
677	002236	022700	000400	CMP	#400+0,%0	;DOES REGISTER ZERO=400+0
678	002242	001401		BEQ	.+4	
679	002244	000000		HLT		;ADD 400 TO 0 FAILED
680		001000		N=N+N		
681	002246	000240		SCOPE		
682	002250	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
683	002254	062700	001000	ADD	#1000,%0	;ADD 1000 TO 0
684	002260	022700	001000	CMP	#1000+0,%0	;DOES REGISTER ZERO=1000+0
685	002264	001401		BEQ	.+4	
686	002266	000000		HLT		;ADD 1000 TO 0 FAILED
687		002000		N=N+N		
688	002270	000240		SCOPE		
689	002272	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
690	002276	062700	002000	ADD	#2000,%0	;ADD 2000 TO 0
691	002302	022700	002000	CMP	#2000+0,%0	;DOES REGISTER ZERO=2000+0
692	002306	001401		BEQ	.+4	
693	002310	000000		HLT		;ADD 2000 TO 0 FAILED
694		004000		N=N+N		
695	002312	000240		SCOPE		
696	002314	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
697	002320	062700	004000	ADD	#4000,%0	;ADD 4000 TO 0
698	002324	022700	004000	CMP	#4000+0,%0	;DOES REGISTER ZERO=4000+0
699	002330	001401		BEQ	.+4	
700	002332	000000		HLT		;ADD 4000 TO 0 FAILED
701		010000		N=N+N		
702	002334	000240		SCOPE		
703	002336	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
704	002342	062700	010000	ADD	#10000,%0	;ADD 10000 TO 0
705	002346	022700	010000	CMP	#10000+0,%0	;DOES REGISTER ZERO=10000+0
706	002352	001401		BEQ	.+4	
707	002354	000000		HLT		;ADD 10000 TO 0 FAILED
708		020000		N=N+N		
709	002356	000240		SCOPE		
710	002360	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
711	002364	062700	020000	ADD	#20000,%0	;ADD 20000 TO 0
712	002370	022700	020000	CMP	#20000+0,%0	;DOES REGISTER ZERO=20000+0
713	002374	001401		BEQ	.+4	
714	002376	000000		HLT		;ADD 20000 TO 0 FAILED
715		040000		N=N+N		
716	002400	000240		SCOPE		
717	002402	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
718	002406	062700	040000	ADD	#40000,%0	;ADD 40000 TO 0
719	002412	022700	040000	CMP	#40000+0,%0	;DOES REGISTER ZERO=40000+0
720	002416	001401		BEQ	.+4	
721	002420	000000		HLT		;ADD 40000 TO 0 FAILED
722		100000		N=N+N		
723	002422	000240		SCOPE		
724	002424	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
725	002430	062700	100000	ADD	#100000,%0	;ADD 100000 TO 0
726	002434	022700	100000	CMP	#100000+0,%0	;DOES REGISTER ZERO=100000+0
727	002440	001401		BEQ	.+4	
728	002442	000000		HLT		;ADD 100000 TO 0 FAILED
729		000000		N=N+N		



730		000001		N=1		
731	002444	000240		SCOPE		
732	002446	012700	000001	MOV	#1,%0	;MOVE 1 TO REGISTER ZERO
733	002452	062700	000001	ADD	#1,%0	;ADD 1 TO 1
734	002456	022700	000002	CMP	#1+1,%0	;DOES REGISTER ZERO=1+1
735	002462	001401		BEQ	.+4	
736	002464	000000		HLT		;ADD 1 TO 1 FAILED
737		000002		N=N+N		
738	002466	000240		SCOPE		
739	002470	012700	000002	MOV	#2,%0	;MOVE 2 TO REGISTER ZERO
740	002474	062700	000002	ADD	#2,%0	;ADD 2 TO 2
741	002500	022700	000004	CMP	#2+2,%0	;DOES REGISTER ZERO=2+2
742	002504	001401		BEQ	.+4	
743	002506	000000		HLT		;ADD 2 TO 2 FAILED
744		000004		N=N+N		
745	002510	000240		SCOPE		
746	002512	012700	000004	MOV	#4,%0	;MOVE 4 TO REGISTER ZERO
747	002516	062700	000004	ADD	#4,%0	;ADD 4 TO 4
748	002522	022700	000010	CMP	#4+4,%0	;DOES REGISTER ZERO=4+4
749	002526	001401		BEQ	.+4	
750	002530	000000		HLT		;ADD 4 TO 4 FAILED
751		000010		N=N+N		
752	002532	000240		SCOPE		
753	002534	012700	000010	MOV	#10,%0	;MOVE 10 TO REGISTER ZERO
754	002540	062700	000010	ADD	#10,%0	;ADD 10 TO 10
755	002544	022700	000020	CMP	#10+10,%0	;DOES REGISTER ZERO=10+10
756	002550	001401		BEQ	.+4	
757	002552	000000		HLT		;ADD 10 TO 10 FAILED
758		000020		N=N+N		
759	002554	000240		SCOPE		
760	002556	012700	000020	MOV	#20,%0	;MOVE 20 TO REGISTER ZERO
761	002562	062700	000020	ADD	#20,%0	;ADD 20 TO 20
762	002566	022700	000040	CMP	#20+20,%0	;DOES REGISTER ZERO=20+20
763	002572	001401		BEQ	.+4	
764	002574	000000		HLT		;ADD 20 TO 20 FAILED
765		000040		N=N+N		
766	002576	000240		SCOPE		
767	002600	012700	000040	MOV	#40,%0	;MOVE 40 TO REGISTER ZERO
768	002604	062700	000040	ADD	#40,%0	;ADD 40 TO 40
769	002610	022700	000100	CMP	#40+40,%0	;DOES REGISTER ZERO=40+40
770	002614	001401		BEQ	.+4	
771	002616	000000		HLT		;ADD 40 TO 40 FAILED
772		000100		N=N+N		
773	002620	000240		SCOPE		
774	002622	012700	000100	MOV	#100,%0	;MOVE 100 TO REGISTER ZERO
775	002626	062700	000100	ADD	#100,%0	;ADD 100 TO 100
776	002632	022700	000200	CMP	#100+100,%0	;DOES REGISTER ZERO=100+100
777	002636	001401		BEQ	.+4	
778	002640	000000		HLT		;ADD 100 TO 100 FAILED
779		000200		N=N+N		
780	002642	000240		SCOPE		
781	002644	012700	000200	MOV	#200,%0	;MOVE 200 TO REGISTER ZERO
782	002650	062700	000200	ADD	#200,%0	;ADD 200 TO 200
783	002654	022700	000400	CMP	#200+200,%0	;DOES REGISTER ZERO=200+200
784	002660	001401		BEQ	.+4	
785	002662	000000		HLT		;ADD 200 TO 200 FAILED



786		000400		N=N+N		
787	002664	000240		SCOPE		
788	002666	012700	000400	MOV	#400,%0	;MOVE 400 TO REGISTER ZERO
789	002672	062700	000400	ADD	#400,%0	;ADD 400 TO 400
790	002676	022700	001000	CMP	#400+400,%0	;DOES REGISTER ZERO=400+400
791	002702	001401		BEQ	.+4	
792	002704	000000		HLT		;ADD 400 TO 400 FAILED
793		001000		N=N+N		
794	002706	000240		SCOPE		
795	002710	012700	001000	MOV	#1000,%0	;MOVE 1000 TO REGISTER ZERO
796	002714	062700	001000	ADD	#1000,%0	;ADD 1000 TO 1000
797	002720	022700	002000	CMP	#1000+1000,%0	;DOES REGISTER ZERO=1000+1000
798	002724	001401		BEQ	.+4	
799	002726	000000		HLT		;ADD 1000 TO 1000 FAILED
800		002000		N=N+N		
801	002730	000240		SCOPE		
802	002732	012700	002000	MOV	#2000,%0	;MOVE 2000 TO REGISTER ZERO
803	002736	062700	002000	ADD	#2000,%0	;ADD 2000 TO 2000
804	002742	022700	004000	CMP	#2000+2000,%0	;DOES REGISTER ZERO=2000+2000
805	002746	001401		BEQ	.+4	
806	002750	000000		HLT		;ADD 2000 TO 2000 FAILED
807		004000		N=N+N		
808	002752	000240		SCOPE		
809	002754	012700	004000	MOV	#4000,%0	;MOVE 4000 TO REGISTER ZERO
810	002760	062700	004000	ADD	#4000,%0	;ADD 4000 TO 4000
811	002764	022700	010000	CMP	#4000+4000,%0	;DOES REGISTER ZERO=4000+4000
812	002770	001401		BEQ	.+4	
813	002772	000000		HLT		;ADD 4000 TO 4000 FAILED
814		010000		N=N+N		
815	002774	000240		SCOPE		
816	002776	012700	010000	MOV	#10000,%0	;MOVE 10000 TO REGISTER ZERO
817	003002	062700	010000	ADD	#10000,%0	;ADD 10000 TO 10000
818	003006	022700	020000	CMP	#10000+10000,%0	;DOES REGISTER ZERO=10000+10000
819	003012	001401		BEQ	.+4	
820	003014	000000		HLT		;ADD 10000 TO 10000 FAILED
821		020000		N=N+N		
822	003016	000240		SCOPE		
823	003020	012700	020000	MOV	#20000,%0	;MOVE 20000 TO REGISTER ZERO
824	003024	062700	020000	ADD	#20000,%0	;ADD 20000 TO 20000
825	003030	022700	040000	CMP	#20000+20000,%0	;DOES REGISTER ZERO=20000+20000
826	003034	001401		BEQ	.+4	
827	003036	000000		HLT		;ADD 20000 TO 20000 FAILED
828		040000		N=N+N		
829	003040	000240		SCOPE		
830	003042	012700	040000	MOV	#40000,%0	;MOVE 40000 TO REGISTER ZERO
831	003046	062700	040000	ADD	#40000,%0	;ADD 40000 TO 40000
832	003052	022700	100000	CMP	#40000+40000,%0	;DOES REGISTER ZERO=40000+40000
833	003056	001401		BEQ	.+4	
834	003060	000000		HLT		;ADD 40000 TO 40000 FAILED
835		100000		N=N+N		
836	003062	000240		SCOPE		
837	003064	012700	100000	MOV	#100000,%0	;MOVE 100000 TO REGISTER ZERO
838	003070	062700	100000	ADD	#100000,%0	;ADD 100000 TO 100000
839	003074	022700	000000	CMP	#100000+100000,%0	;DOES REGISTER ZERO=100000+100000
840	003100	001401		BEQ	.+4	
841	003102	000000		HLT		;ADD 100000 TO 100000 FAILED



842		000000		N=N+N	
843					
844		000000		M=0	
845		000000		N=0	
846	003104	000240		SCOPE	
847	003106	012700	000000	MOV #0,%0	;MOVE 0 TO REGISTER ZERO
848	003112	062700	000000	ADD #0,%0	;ADD 0 TO 0
849	003116	022700	000000	CMP #0+0,%0	;DOES REGISTER ZERO=0+0
850	003122	001401		BEQ .+4	
851	003124	000000		HLT	;ADD 0 TO 0 FAILED
852		000000		N=N+1&175252+N	
853		000401		M=M+401	
854	003126	000240		SCOPE	
855	003130	012700	000000	MOV #0,%0	;MOVE 0 TO REGISTER ZERO
856	003134	062700	000401	ADD #401,%0	;ADD 401 TO 0
857	003140	022700	000401	CMP #401+0,%0	;DOES REGISTER ZERO=401+0
858	003144	001401		BEQ .+4	
859	003146	000000		HLT	;ADD 401 TO 0 FAILED
860		000000		N=N+1&175252+N	
861		001002		M=M+401	
862	003150	000240		SCOPE	
863	003152	012700	000000	MOV #0,%0	;MOVE 0 TO REGISTER ZERO
864	003156	062700	001002	ADD #1002,%0	;ADD 1002 TO 0
865	003162	022700	001002	CMP #1002+0,%0	;DOES REGISTER ZERO=1002+0
866	003166	001401		BEQ .+4	
867	003170	000000		HLT	;ADD 1002 TO 0 FAILED
868		000000		N=N+1&175252+N	
869		001403		M=M+401	
870	003172	000240		SCOPE	
871	003174	012700	000000	MOV #0,%0	;MOVE 0 TO REGISTER ZERO
872	003200	062700	001403	ADD #1403,%0	;ADD 1403 TO 0
873	003204	022700	001403	CMP #1403+0,%0	;DOES REGISTER ZERO=1403+0
874	003210	001401		BEQ .+4	
875	003212	000000		HLT	;ADD 1403 TO 0 FAILED
876		000000		N=N+1&175252+N	
877		002004		M=M+401	
878	003214	000240		SCOPE	
879	003216	012700	000000	MOV #0,%0	;MOVE 0 TO REGISTER ZERO
880	003222	062700	002004	ADD #2004,%0	;ADD 2004 TO 0
881	003226	022700	002004	CMP #2004+0,%0	;DOES REGISTER ZERO=2004+0
882	003232	001401		BEQ .+4	
883	003234	000000		HLT	;ADD 2004 TO 0 FAILED
884		000000		N=N+1&175252+N	
885		002405		M=M+401	
886	003236	000240		SCOPE	
887	003240	012700	000000	MOV #0,%0	;MOVE 0 TO REGISTER ZERO
888	003244	062700	002405	ADD #2405,%0	;ADD 2405 TO 0
889	003250	022700	002405	CMP #2405+0,%0	;DOES REGISTER ZERO=2405+0
890	003254	001401		BEQ .+4	
891	003256	000000		HLT	;ADD 2405 TO 0 FAILED
892		000000		N=N+1&175252+N	
893		003006		M=M+401	
894	003260	000240		SCOPE	
895	003262	012700	000000	MOV #0,%0	;MOVE 0 TO REGISTER ZERO
896	003266	062700	003006	ADD #3006,%0	;ADD 3006 TO 0
897	003272	022700	003006	CMP #3006+0,%0	;DOES REGISTER ZERO=3006+0



898	003276	001401		BEQ	.+4	
899	003300	000000		HLT		;ADD 3006 TO 0 FAILED
900		000000		N=N+1&175252+N		
901		003407		M=M+401		
902	003202	000240		SCOPE		
903	003304	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
904	003310	062700	003407	ADD	#3407,%0	;ADD 3407 TO 0
905	003314	022700	003407	CMP	#3407+0,%0	;DOES REGISTER ZERO=3407+0
906	003320	001401		BEQ	.+4	
907	003322	000000		HLT		;ADD 3407 TO 0 FAILED
908		000000		N=N+1&175252+N		
909		004010		M=M+401		
910	003324	000240		SCOPE		
911	003326	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
912	003332	062700	004010	ADD	#4010,%0	;ADD 4010 TO 0
913	003336	022700	004010	CMP	#4010+0,%0	;DOES REGISTER ZERO=4010+0
914	003342	001401		BEQ	.+4	
915	003344	000000		HLT		;ADD 4010 TO 0 FAILED
916		000000		N=N+1&175252+N		
917		004411		M=M+401		
918	003346	000240		SCOPE		
919	003350	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
920	003354	062700	004411	ADD	#4411,%0	;ADD 4411 TO 0
921	003360	022700	004411	CMP	#4411+0,%0	;DOES REGISTER ZERO=4411+0
922	003364	001401		BEQ	.+4	
923	003366	000000		HLT		;ADD 4411 TO 0 FAILED
924		000000		N=N+1&175252+N		
925		005012		M=M+401		
926	003370	000240		SCOPE		
927	003372	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
928	003376	062700	005012	ADD	#5012,%0	;ADD 5012 TO 0
929	003402	022700	005012	CMP	#5012+0,%0	;DOES REGISTER ZERO=5012+0
930	003406	001401		BEQ	.+4	
931	003410	000000		HLT		;ADD 5012 TO 0 FAILED
932		000000		N=N+1&175252+N		
933		005413		M=M+401		
934	003412	000240		SCOPE		
935	003414	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
936	003420	062700	005413	ADD	#5413,%0	;ADD 5413 TO 0
937	003424	022700	005413	CMP	#5413+0,%0	;DOES REGISTER ZERO=5413+0
938	003430	001401		BEQ	.+4	
939	003432	000000		HLT		;ADD 5413 TO 0 FAILED
940		000000		N=N+1&175252+N		
941		006014		M=M+401		
942	003434	000240		SCOPE		
943	003436	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
944	003442	062700	006014	ADD	#6014,%0	;ADD 6014 TO 0
945	003446	022700	006014	CMP	#6014+0,%0	;DOES REGISTER ZERO=6014+0
946	003452	001401		BEQ	.+4	
947	003454	000000		HLT		;ADD 6014 TO 0 FAILED
948		000000		N=N+1&175252+N		
949		006415		M=M+401		
950	003456	000240		SCOPE		
951	003460	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
952	003464	062700	006415	ADD	#6415,%0	;ADD 6415 TO 0
953	003470	022700	006415	CMP	#6415+0,%0	;DOES REGISTER ZERO=6415+0



954	003474	001401		BEQ	.+4	
955	003476	000000		HLT		;ADD 6415 TO 0 FAILED
956		000000		N=N+1&175252+N		
957		007016		M=M+401		
958	003500	000240		SCOPE		
959	003502	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
960	003506	062700	007016	ADD	#7016,%0	;ADD 7016 TO 0
961	003512	022700	007016	CMP	#7016+0,%0	;DOES REGISTER ZERO=7016+0
962	003516	001401		BEQ	.+4	
963	003520	000000		HLT		;ADD 7016 TO 0 FAILED
964		000000		N=N+1&175252+N		
965		007417		M=M+401		
966	003522	000240		SCOPE		
967	003524	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
968	003530	062700	007417	ADD	#7417,%0	;ADD 7417 TO 0
969	003534	022700	007417	CMP	#7417+0,%0	;DOES REGISTER ZERO=7417+0
970	003540	001401		BEQ	.+4	
971	003542	000000		HLT		;ADD 7417 TO 0 FAILED
972		000000		N=N+1&175252+N		
973		010020		M=M+401		
974	003544	000240		SCOPE		
975	003546	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
976	003552	062700	010020	ADD	#10020,%0	;ADD 10020 TO 0
977	003556	022700	010020	CMP	#10020+0,%0	;DOES REGISTER ZERO=10020+0
978	003562	001401		BEQ	.+4	
979	003564	000000		HLT		;ADD 10020 TO 0 FAILED
980		000000		N=N+1&175252+N		
981		010421		M=M+401		
982	003566	000240		SCOPE		
983	003570	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
984	003574	062700	010421	ADD	#10421,%0	;ADD 10421 TO 0
985	003600	022700	010421	CMP	#10421+0,%0	;DOES REGISTER ZERO=10421+0
986	003604	001401		BEQ	.+4	
987	003606	000000		HLT		;ADD 10421 TO 0 FAILED
988		000000		N=N+1&175252+N		
989		011022		M=M+401		
990	003610	000240		SCOPE		
991	003612	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
992	003616	062700	011022	ADD	#11022,%0	;ADD 11022 TO 0
993	003622	022700	011022	CMP	#11022+0,%0	;DOES REGISTER ZERO=11022+0
994	003626	001401		BEQ	.+4	
995	003630	000000		HLT		;ADD 11022 TO 0 FAILED
996		000000		N=N+1&175252+N		
997		011423		M=M+401		
998	003632	000240		SCOPE		
999	003634	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1000	003640	062700	011423	ADD	#11423,%0	;ADD 11423 TO 0
1001	003644	022700	011423	CMP	#11423+0,%0	;DOES REGISTER ZERO=11423+0
1002	003650	001401		BEQ	.+4	
1003	003652	000000		HLT		;ADD 11423 TO 0 FAILED
1004		000000		N=N+1&175252+N		
1005		012024		M=M+401		
1006	003654	000240		SCOPE		
1007	003656	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1008	003662	062700	012024	ADD	#12024,%0	;ADD 12024 TO 0
1009	003666	022700	012024	CMP	#12024+0,%0	;DOES REGISTER ZERO=12024+0



1010	003672	001401		BEQ	.+4	
1011	003674	000000		HLT		;ADD 12024 TO 0 FAILED
1012		000000		N=N+1&175252+N		
1013		012425		M=M+401		
1014	003676	000240		SCOPE		
1015	003700	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1016	003704	062700	012425	ADD	#12425,%0	;ADD 12425 TO 0
1017	003710	022700	012425	CMP	#12425+0,%0	;DOES REGISTER ZERO=12425+0
1018	003714	001401		BEQ	.+4	
1019	003716	000000		HLT		;ADD 12425 TO 0 FAILED
1020		000000		N=N+1&175252+N		
1021		013026		M=M+401		
1022	003720	000240		SCOPE		
1023	003722	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1024	003726	062700	013026	ADD	#13026,%0	;ADD 13026 TO 0
1025	003732	022700	013026	CMP	#13026+0,%0	;DOES REGISTER ZERO=13026+0
1026	003736	001401		BEQ	.+4	
1027	003740	000000		HLT		;ADD 13026 TO 0 FAILED
1028		000000		N=N+1&175252+N		
1029		013427		M=M+401		
1030	003742	000240		SCOPE		
1031	003744	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1032	003750	062700	013427	ADD	#13427,%0	;ADD 13427 TO 0
1033	003754	022700	013427	CMP	#13427+0,%0	;DOES REGISTER ZERO=13427+0
1034	003760	001401		BEQ	.+4	
1035	003762	000000		HLT		;ADD 13427 TO 0 FAILED
1036		000000		N=N+1&175252+N		
1037		014030		M=M+401		
1038	003764	000240		SCOPE		
1039	003766	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1040	003772	062700	014030	ADD	#14030,%0	;ADD 14030 TO 0
1041	003776	022700	014030	CMP	#14030+0,%0	;DOES REGISTER ZERO=14030+0
1042	004002	001401		BEQ	.+4	
1043	004004	000000		HLT		;ADD 14030 TO 0 FAILED
1044		000000		N=N+1&175252+N		
1045		014431		M=M+401		
1046	004006	000240		SCOPE		
1047	004010	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1048	004014	062700	014431	ADD	#14431,%0	;ADD 14431 TO 0
1049	004020	022700	014431	CMP	#14431+0,%0	;DOES REGISTER ZERO=14431+0
1050	004024	001401		BEQ	.+4	
1051	004026	000000		HLT		;ADD 14431 TO 0 FAILED
1052		000000		N=N+1&175252+N		
1053		015032		M=M+401		
1054	004030	000240		SCOPE		
1055	004032	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1056	004036	062700	015032	ADD	#15032,%0	;ADD 15032 TO 0
1057	004042	022700	015032	CMP	#15032+0,%0	;DOES REGISTER ZERO=15032+0
1058	004046	001401		BEQ	.+4	
1059	004050	000000		HLT		;ADD 15032 TO 0 FAILED
1060		000000		N=N+1&175252+N		
1061		015433		M=M+401		
1062	004052	000240		SCOPE		
1063	004054	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1064	004060	062700	015433	ADD	#15433,%0	;ADD 15433 TO 0
1065	004064	022700	015433	CMP	#15433+0,%0	;DOES REGISTER ZERO=15433+0



1066	004070	001401		BEQ	.+4	
1067	004072	000000		HLT		;ADD 15433 TO 0 FAILED
1068		000000		N=N+1&175252+N		
1069		016034		M=M+401		
1070	004074	000240		SCOPE		
1071	004076	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1072	004102	062700	016034	ADD	#16034,%0	;ADD 16034 TO 0
1073	004106	022700	016034	CMP	#16034+0,%0	;DOES REGISTER ZERO=16034+0
1074	004112	001401		BEQ	.+4	
1075	004114	000000		HLT		;ADD 16034 TO 0 FAILED
1076		000000		N=N+1&175252+N		
1077		016435		M=M+401		
1078	004116	000240		SCOPE		
1079	004120	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1080	004124	062700	016435	ADD	#16435,%0	;ADD 16435 TO 0
1081	004130	022700	016435	CMP	#16435+0,%0	;DOES REGISTER ZERO=16435+0
1082	004134	001401		BEQ	.+4	
1083	004136	000000		HLT		;ADD 16435 TO 0 FAILED
1084		000000		N=N+1&175252+N		
1085		017036		M=M+401		
1086	004140	000240		SCOPE		
1087	004142	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1088	004146	062700	017036	ADD	#17036,%0	;ADD 17036 TO 0
1089	004152	022700	017036	CMP	#17036+0,%0	;DOES REGISTER ZERO=17036+0
1090	004156	001401		BEQ	.+4	
1091	004160	000000		HLT		;ADD 17036 TO 0 FAILED
1092		000000		N=N+1&175252+N		
1093		017437		M=M+401		
1094	004162	000240		SCOPE		
1095	004164	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1096	004170	062700	017437	ADD	#17437,%0	;ADD 17437 TO 0
1097	004174	022700	017437	CMP	#17437+0,%0	;DOES REGISTER ZERO=17437+0
1098	004200	001401		BEQ	.+4	
1099	004202	000000		HLT		;ADD 17437 TO 0 FAILED
1100		000000		N=N+1&175252+N		
1101		020040		M=M+401		
1102	004204	000240		SCOPE		
1103	004206	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1104	004212	062700	020040	ADD	#20040,%0	;ADD 20040 TO 0
1105	004216	022700	020040	CMP	#20040+0,%0	;DOES REGISTER ZERO=20040+0
1106	004222	001401		BEQ	.+4	
1107	004224	000000		HLT		;ADD 20040 TO 0 FAILED
1108		000000		N=N+1&175252+N		
1109		020441		M=M+401		
1110	004226	000240		SCOPE		
1111	004230	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1112	004234	062700	020441	ADD	#20441,%0	;ADD 20441 TO 0
1113	004240	022700	020441	CMP	#20441+0,%0	;DOES REGISTER ZERO=20441+0
1114	004244	001401		BEQ	.+4	
1115	004246	000000		HLT		;ADD 20441 TO 0 FAILED
1116		000000		N=N+1&175252+N		
1117		021042		M=M+401		
1118	004250	000240		SCOPE		
1119	004252	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1120	004256	062700	021042	ADD	#21042,%0	;ADD 21042 TO 0
1121	004262	022700	021042	CMP	#21042+0,%0	;DOES REGISTER ZERO=21042+0



1122	004266	001401		BEQ	.+4		
1123	004270	000000		HLT			;ADD 21042 TO 0 FAILED
1124		000000		N=N+1&175252+N			
1125		021443		M=M+401			
1126	004272	000240		SCOPE			
1127	004274	012700	000000	MOV	#0,%0		;MOVE 0 TO REGISTER ZERO
1128	004300	062700	021443	ADD	#21443,%0		;ADD 21443 TO 0
1129	004304	022700	021443	CMP	#21443+0,%0		;DOES REGISTER ZERO=21443+0
1130	004310	001401		BEQ	.+4		
1131	004312	000000		HLT			;ADD 21443 TO 0 FAILED
1132		000000		N=N+1&175252+N			
1133		022044		M=M+401			
1134	004314	000240		SCOPE			
1135	004316	012700	000000	MOV	#0,%0		;MOVE 0 TO REGISTER ZERO
1136	004322	062700	022044	ADD	#22044,%0		;ADD 22044 TO 0
1137	004326	022700	022044	CMP	#22044+0,%0		;DOES REGISTER ZERO=22044+0
1138	004332	001401		BEQ	.+4		
1139	004334	000000		HLT			;ADD 22044 TO 0 FAILED
1140		000000		N=N+1&175252+N			
1141		022445		M=M+401			
1142	004336	000240		SCOPE			
1143	004340	012700	000000	MOV	#0,%0		;MOVE 0 TO REGISTER ZERO
1144	004344	062700	022445	ADD	#22445,%0		;ADD 22445 TO 0
1145	004350	022700	022445	CMP	#22445+0,%0		;DOES REGISTER ZERO=22445+0
1146	004354	001401		BEQ	.+4		
1147	004356	000000		HLT			;ADD 22445 TO 0 FAILED
1148		000000		N=N+1&175252+N			
1149		023046		M=M+401			
1150	004360	000240		SCOPE			
1151	004362	012700	000000	MOV	#0,%0		;MOVE 0 TO REGISTER ZERO
1152	004366	062700	023046	ADD	#23046,%0		;ADD 23046 TO 0
1153	004372	022700	023046	CMP	#23046+0,%0		;DOES REGISTER ZERO=23046+0
1154	004376	001401		BEQ	.+4		
1155	004400	000000		HLT			;ADD 23046 TO 0 FAILED
1156		000000		N=N+1&175252+N			
1157		023447		M=M+401			
1158	004402	000240		SCOPE			
1159	004404	012700	000000	MOV	#0,%0		;MOVE 0 TO REGISTER ZERO
1160	004410	062700	023447	ADD	#23447,%0		;ADD 23447 TO 0
1161	004414	022700	023447	CMP	#23447+0,%0		;DOES REGISTER ZERO=23447+0
1162	004420	001401		BEQ	.+4		
1163	004422	000000		HLT			;ADD 23447 TO 0 FAILED
1164		000000		N=N+1&175252+N			
1165		024050		M=M+401			
1166	004424	000240		SCOPE			
1167	004426	012700	000000	MOV	#0,%0		;MOVE 0 TO REGISTER ZERO
1168	004432	062700	024050	ADD	#24050,%0		;ADD 24050 TO 0
1169	004436	022700	024050	CMP	#24050+0,%0		;DOES REGISTER ZERO=24050+0
1170	004442	001401		BEQ	.+4		
1171	004444	000000		HLT			;ADD 24050 TO 0 FAILED
1172		000000		N=N+1&175252+N			
1173		024451		M=M+401			
1174	004446	000240		SCOPE			
1175	004450	012700	000000	MOV	#0,%0		;MOVE 0 TO REGISTER ZERO
1176	004454	062700	024451	ADD	#24451,%0		;ADD 24451 TO 0
1177	004460	022700	024451	CMP	#24451+0,%0		;DOES REGISTER ZERO=24451+0



1178	004464	001401		BEQ	.+4	
1179	004466	000000		HLT		;ADD 24451 TO 0 FAILED
1180		000000		N=N+1&175252+N		
1181		025052		M=M+401		
1182	004470	000240		SCOPE		
1183	004472	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1184	004476	062700	025052	ADD	#25052,%0	;ADD 25052 TO 0
1185	004502	022700	025052	CMP	#25052+0,%0	;DOES REGISTER ZERO=25052+0
1186	004506	001401		BEQ	.+4	
1187	004510	000000		HLT		;ADD 25052 TO 0 FAILED
1188		000000		N=N+1&175252+N		
1189		025453		M=M+401		
1190	004512	000240		SCOPE		
1191	004514	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1192	004520	062700	025453	ADD	#25453,%0	;ADD 25453 TO 0
1193	004524	022700	025453	CMP	#25453+0,%0	;DOES REGISTER ZERO=25453+0
1194	004530	001401		BEQ	.+4	
1195	004532	000000		HLT		;ADD 25453 TO 0 FAILED
1196		000000		N=N+1&175252+N		
1197		026054		M=M+401		
1198	004534	000240		SCOPE		
1199	004536	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1200	004542	062700	026054	ADD	#26054,%0	;ADD 26054 TO 0
1201	004546	022700	026054	CMP	#26054+0,%0	;DOES REGISTER ZERO=26054+0
1202	004552	001401		BEQ	.+4	
1203	004554	000000		HLT		;ADD 26054 TO 0 FAILED
1204		000000		N=N+1&175252+N		
1205		026455		M=M+401		
1206	004556	000240		SCOPE		
1207	004560	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1208	004564	062700	026455	ADD	#26455,%0	;ADD 26455 TO 0
1209	004570	022700	026455	CMP	#26455+0,%0	;DOES REGISTER ZERO=26455+0
1210	004574	001401		BEQ	.+4	
1211	004576	000000		HLT		;ADD 26455 TO 0 FAILED
1212		000000		N=N+1&175252+N		
1213		027056		M=M+401		
1214	004600	000240		SCOPE		
1215	004602	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1216	004606	062700	027056	ADD	#27056,%0	;ADD 27056 TO 0
1217	004612	022700	027056	CMP	#27056+0,%0	;DOES REGISTER ZERO=27056+0
1218	004616	001401		BEQ	.+4	
1219	004620	000000		HLT		;ADD 27056 TO 0 FAILED
1220		000000		N=N+1&175252+N		
1221		027457		M=M+401		
1222	004622	000240		SCOPE		
1223	004624	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1224	004630	062700	027457	ADD	#27457,%0	;ADD 27457 TO 0
1225	004634	022700	027457	CMP	#27457+0,%0	;DOES REGISTER ZERO=27457+0
1226	004640	001401		BEQ	.+4	
1227	004642	000000		HLT		;ADD 27457 TO 0 FAILED
1228		000000		N=N+1&175252+N		
1229		030060		M=M+401		
1230	004644	000240		SCOPE		
1231	004646	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1232	004652	062700	030060	ADD	#30060,%0	;ADD 30060 TO 0
1233	004656	022700	030060	CMP	#30060+0,%0	;DOES REGISTER ZERO=30060+0



1234	004662	001401		BEQ	.+4		
1235	004664	000000		HLT		;ADD 30060 TO 0 FAILED	
1236		000000		N=N+18175252+N			
1237		030461		M=M+401			
1238	004666	000240		SCOPE			
1239	004670	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO	
1240	004674	062700	030461	ADD	#30461,%0	;ADD 30461 TO 0	
1241	004700	022700	030461	CMP	#30461+0,%0	;DOES REGISTER ZERO=30461+0	
1242	004704	001401		BEQ	.+4		
1243	004706	000000		HLT		;ADD 30461 TO 0 FAILED	
1244		000000		N=N+18175252+N			
1245		031062		M=M+401			
1246	004710	000240		SCOPE			
1247	004712	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO	
1248	004716	062700	031062	ADD	#31062,%0	;ADD 31062 TO 0	
1249	004722	022700	031062	CMP	#31062+0,%0	;DOES REGISTER ZERO=31062+0	
1250	004726	001401		BEQ	.+4		
1251	004730	000000		HLT		;ADD 31062 TO 0 FAILED	
1252		000000		N=N+18175252+N			
1253		031463		M=M+401			
1254	004732	000240		SCOPE			
1255	004734	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO	
1256	004740	062700	031463	ADD	#31463,%0	;ADD 31463 TO 0	
1257	004744	022700	031463	CMP	#31463+0,%0	;DOES REGISTER ZERO=31463+0	
1258	004750	001401		BEQ	.+4		
1259	004752	000000		HLT		;ADD 31463 TO 0 FAILED	
1260		000000		N=N+18175252+N			
1261		032064		M=M+401			
1262	004754	000240		SCOPE			
1263	004756	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO	
1264	004762	062700	032064	ADD	#32064,%0	;ADD 32064 TO 0	
1265	004766	022700	032064	CMP	#32064+0,%0	;DOES REGISTER ZERO=32064+0	
1266	004772	001401		BEQ	.+4		
1267	004774	000000		HLT		;ADD 32064 TO 0 FAILED	
1268		000000		N=N+18175252+N			
1269		032465		M=M+401			
1270	004776	000240		SCOPE			
1271	005000	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO	
1272	005004	062700	032465	ADD	#32465,%0	;ADD 32465 TO 0	
1273	005010	022700	032465	CMP	#32465+0,%0	;DOES REGISTER ZERO=32465+0	
1274	005014	001401		BEQ	.+4		
1275	005016	000000		HLT		;ADD 32465 TO 0 FAILED	
1276		000000		N=N+18175252+N			
1277		033066		M=M+401			
1278	005020	000240		SCOPE			
1279	005022	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO	
1280	005026	062700	033066	ADD	#33066,%0	;ADD 33066 TO 0	
1281	005032	022700	033066	CMP	#33066+0,%0	;DOES REGISTER ZERO=33066+0	
1282	005036	001401		BEQ	.+4		
1283	005040	000000		HLT		;ADD 33066 TO 0 FAILED	
1284		000000		N=N+18175252+N			
1285		033467		M=M+401			
1286	005042	000240		SCOPE			
1287	005044	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO	
1288	005050	062700	033467	ADD	#33467,%0	;ADD 33467 TO 0	
1289	005054	022700	033467	CMP	#33467+0,%0	;DOES REGISTER ZERO=33467+0	



1290	005060	001401		BEQ	.+4	
1291	005062	000000		HLT		;ADD 33467 TO 0 FAILED
1292		000000		N=N+1&175252+N		
1293		034070		M=M+401		
1294	005064	000240		SCOPE		
1295	005066	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1296	005072	062700	034070	ADD	#34070,%0	;ADD 34070 TO 0
1297	005076	022700	034070	CMP	#34070+0,%0	;DOES REGISTER ZERO=34070+0
1298	005102	001401		BEQ	.+4	
1299	005104	000000		HLT		;ADD 34070 TO 0 FAILED
1300		000000		N=N+1&175252+N		
1301		034471		M=M+401		
1302	005106	000240		SCOPE		
1303	005110	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1304	005114	062700	034471	ADD	#34471,%0	;ADD 34471 TO 0
1305	005120	022700	034471	CMP	#34471+0,%0	;DOES REGISTER ZERO=34471+0
1306	005124	001401		BEQ	.+4	
1307	005126	000000		HLT		;ADD 34471 TO 0 FAILED
1308		000000		N=N+1&175252+N		
1309		035072		M=M+401		
1310	005130	000240		SCOPE		
1311	005132	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1312	005136	062700	035072	ADD	#35072,%0	;ADD 35072 TO 0
1313	005142	022700	035072	CMP	#35072+0,%0	;DOES REGISTER ZERO=35072+0
1314	005146	001401		BEQ	.+4	
1315	005150	000000		HLT		;ADD 35072 TO 0 FAILED
1316		000000		N=N+1&175252+N		
1317		035473		M=M+401		
1318	005152	000240		SCOPE		
1319	005154	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1320	005160	062700	035473	ADD	#35473,%0	;ADD 35473 TO 0
1321	005164	022700	035473	CMP	#35473+0,%0	;DOES REGISTER ZERO=35473+0
1322	005170	001401		BEQ	.+4	
1323	005172	000000		HLT		;ADD 35473 TO 0 FAILED
1324		000000		N=N+1&175252+N		
1325		036074		M=M+401		
1326	005174	000240		SCOPE		
1327	005176	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1328	005202	062700	036074	ADD	#36074,%0	;ADD 36074 TO 0
1329	005206	022700	036074	CMP	#36074+0,%0	;DOES REGISTER ZERO=36074+0
1330	005212	001401		BEQ	.+4	
1331	005214	000000		HLT		;ADD 36074 TO 0 FAILED
1332		000000		N=N+1&175252+N		
1333		036475		M=M+401		
1334	005216	000240		SCOPE		
1335	005220	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1336	005224	062700	036475	ADD	#36475,%0	;ADD 36475 TO 0
1337	005230	022700	036475	CMP	#36475+0,%0	;DOES REGISTER ZERO=36475+0
1338	005234	001401		BEQ	.+4	
1339	005236	000000		HLT		;ADD 36475 TO 0 FAILED
1340		000000		N=N+1&175252+N		
1341		037076		M=M+401		
1342	005240	000240		SCOPE		
1343	005242	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1344	005246	062700	037076	ADD	#37076,%0	;ADD 37076 TO 0
1345	005252	022700	037076	CMP	#37076+0,%0	;DOES REGISTER ZERO=37076+0



1346	005256	001401		BEG	.+4	
1347	005260	000000		HLT		;ADD 37076 TO 0 FAILED
1348		000000		N=N+18175252+N		
1349		037477		M=M+401		
1350	005262	000240		SCOPE		
1351	005264	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1352	005270	062700	037477	ADD	#37477,%0	;ADD 37477 TO 0
1353	005274	022700	037477	CMP	#37477+0,%0	;DOES REGISTER ZERO=37477+0
1354	005300	001401		BEG	.+4	
1355	005302	000000		HLT		;ADD 37477 TO 0 FAILED
1356		000000		N=N+18175252+N		
1357		040100		M=M+401		
1358	005304	000240		SCOPE		
1359	005306	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1360	005312	062700	040100	ADD	#40100,%0	;ADD 40100 TO 0
1361	005316	022700	040100	CMP	#40100+0,%0	;DOES REGISTER ZERO=40100+0
1362	005322	001401		BEG	.+4	
1363	005324	000000		HLT		;ADD 40100 TO 0 FAILED
1364		000000		N=N+18175252+N		
1365		040501		M=M+401		
1366	005326	000240		SCOPE		
1367	005330	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1368	005334	062700	040501	ADD	#40501,%0	;ADD 40501 TO 0
1369	005340	022700	040501	CMP	#40501+0,%0	;DOES REGISTER ZERO=40501+0
1370	005344	001401		BEG	.+4	
1371	005346	000000		HLT		;ADD 40501 TO 0 FAILED
1372		000000		N=N+18175252+N		
1373		041102		M=M+401		
1374	005350	000240		SCOPE		
1375	005352	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1376	005356	062700	041102	ADD	#41102,%0	;ADD 41102 TO 0
1377	005362	022700	041102	CMP	#41102+0,%0	;DOES REGISTER ZERO=41102+0
1378	005366	001401		BEG	.+4	
1379	005370	000000		HLT		;ADD 41102 TO 0 FAILED
1380		000000		N=N+18175252+N		
1381		041503		M=M+401		
1382	005372	000240		SCOPE		
1383	005374	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1384	005400	062700	041503	ADD	#41503,%0	;ADD 41503 TO 0
1385	005404	022700	041503	CMP	#41503+0,%0	;DOES REGISTER ZERO=41503+0
1386	005410	001401		BEG	.+4	
1387	005412	000000		HLT		;ADD 41503 TO 0 FAILED
1388		000000		N=N+18175252+N		
1389		042104		M=M+401		
1390	005414	000240		SCOPE		
1391	005416	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1392	005422	062700	042104	ADD	#42104,%0	;ADD 42104 TO 0
1393	005426	022700	042104	CMP	#42104+0,%0	;DOES REGISTER ZERO=42104+0
1394	005432	001401		BEG	.+4	
1395	005434	000000		HLT		;ADD 42104 TO 0 FAILED
1396		000000		N=N+18175252+N		
1397		042505		M=M+401		
1398	005436	000240		SCOPE		
1399	005440	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1400	005444	062700	042505	ADD	#42505,%0	;ADD 42505 TO 0
1401	005450	022700	042505	CMP	#42505+0,%0	;DOES REGISTER ZERO=42505+0



1402	005454	001401		BEQ	.+4	
1403	005456	000000		HLT		;ADD 42505 TO 0 FAILED
1404		000000		N=N+1&175252+N		
1405		043106		M=M+401		
1406	005460	000240		SCOPE		
1407	005462	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1408	005466	062700	043106	ADD	#43106,%0	;ADD 43106 TO 0
1409	005472	022700	043106	CMP	#43106+0,%0	;DOES REGISTER ZERO=43106+0
1410	005476	001401		BEQ	.+4	
1411	005500	000000		HLT		;ADD 43106 TO 0 FAILED
1412		000000		N=N+1&175252+N		
1413		043507		M=M+401		
1414	005502	000240		SCOPE		
1415	005504	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1416	005510	062700	043507	ADD	#43507,%0	;ADD 43507 TO 0
1417	005514	022700	043507	CMP	#43507+0,%0	;DOES REGISTER ZERO=43507+0
1418	005520	001401		BEQ	.+4	
1419	005522	000000		HLT		;ADD 43507 TO 0 FAILED
1420		000000		N=N+1&175252+N		
1421		044110		M=M+401		
1422	005524	000240		SCOPE		
1423	005526	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1424	005532	062700	044110	ADD	#44110,%0	;ADD 44110 TO 0
1425	005536	022700	044110	CMP	#44110+0,%0	;DOES REGISTER ZERO=44110+0
1426	005542	001401		BEQ	.+4	
1427	005544	000000		HLT		;ADD 44110 TO 0 FAILED
1428		000000		N=N+1&175252+N		
1429		044511		M=M+401		
1430	005546	000240		SCOPE		
1431	005550	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1432	005554	062700	044511	ADD	#44511,%0	;ADD 44511 TO 0
1433	005560	022700	044511	CMP	#44511+0,%0	;DOES REGISTER ZERO=44511+0
1434	005564	001401		BEQ	.+4	
1435	005566	000000		HLT		;ADD 44511 TO 0 FAILED
1436		000000		N=N+1&175252+N		
1437		045112		M=M+401		
1438	005570	000240		SCOPE		
1439	005572	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1440	005576	062700	045112	ADD	#45112,%0	;ADD 45112 TO 0
1441	005602	022700	045112	CMP	#45112+0,%0	;DOES REGISTER ZERO=45112+0
1442	005606	001401		BEQ	.+4	
1443	005610	000000		HLT		;ADD 45112 TO 0 FAILED
1444		000000		N=N+1&175252+N		
1445		045513		M=M+401		
1446	005612	000240		SCOPE		
1447	005614	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1448	005620	062700	045513	ADD	#45513,%0	;ADD 45513 TO 0
1449	005624	022700	045513	CMP	#45513+0,%0	;DOES REGISTER ZERO=45513+0
1450	005630	001401		BEQ	.+4	
1451	005632	000000		HLT		;ADD 45513 TO 0 FAILED
1452		000000		N=N+1&175252+N		
1453		046114		M=M+401		
1454	005634	000240		SCOPE		
1455	005636	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1456	005642	062700	046114	ADD	#46114,%0	;ADD 46114 TO 0
1457	005646	022700	046114	CMP	#46114+0,%0	;DOES REGISTER ZERO=46114+0



1458	005652	001401		BEQ	.+4	
1459	005654	000000		HLT		;ADD 46114 TO 0 FAILED
1460		000000		N=N+1&175252+N		
1461		046515		M=M+401		
1462	005656	000240		SCOPE		
1463	005660	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1464	005664	062700	046515	ADD	#46515,%0	;ADD 46515 TO 0
1465	005670	022700	046515	CMP	#46515+0,%0	;DOES REGISTER ZERO=46515+0
1466	005674	001401		BEQ	.+4	
1467	005676	000000		HLT		;ADD 46515 TO 0 FAILED
1468		000000		N=N+1&175252+N		
1469		047116		M=M+401		
1470	005700	000240		SCOPE		
1471	005702	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1472	005706	062700	047116	ADD	#47116,%0	;ADD 47116 TO 0
1473	005712	022700	047116	CMP	#47116+0,%0	;DOES REGISTER ZERO=47116+0
1474	005716	001401		BEQ	.+4	
1475	005720	000000		HLT		;ADD 47116 TO 0 FAILED
1476		000000		N=N+1&175252+N		
1477		047517		M=M+401		
1478	005722	000240		SCOPE		
1479	005724	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1480	005730	062700	047517	ADD	#47517,%0	;ADD 47517 TO 0
1481	005734	022700	047517	CMP	#47517+0,%0	;DOES REGISTER ZERO=47517+0
1482	005740	001401		BEQ	.+4	
1483	005742	000000		HLT		;ADD 47517 TO 0 FAILED
1484		000000		N=N+1&175252+N		
1485		050120		M=M+401		
1486	005744	000240		SCOPE		
1487	005746	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1488	005752	062700	050120	ADD	#50120,%0	;ADD 50120 TO 0
1489	005756	022700	050120	CMP	#50120+0,%0	;DOES REGISTER ZERO=50120+0
1490	005762	001401		BEQ	.+4	
1491	005764	000000		HLT		;ADD 50120 TO 0 FAILED
1492		000000		N=N+1&175252+N		
1493		050521		M=M+401		
1494	005766	000240		SCOPE		
1495	005770	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1496	005774	062700	050521	ADD	#50521,%0	;ADD 50521 TO 0
1497	006000	022700	050521	CMP	#50521+0,%0	;DOES REGISTER ZERO=50521+0
1498	006004	001401		BEQ	.+4	
1499	006006	000000		HLT		;ADD 50521 TO 0 FAILED
1500		000000		N=N+1&175252+N		
1501		051122		M=M+401		
1502	006010	000240		SCOPE		
1503	006012	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1504	006016	062700	051122	ADD	#51122,%0	;ADD 51122 TO 0
1505	006022	022700	051122	CMP	#51122+0,%0	;DOES REGISTER ZERO=51122+0
1506	006026	001401		BEQ	.+4	
1507	006030	000000		HLT		;ADD 51122 TO 0 FAILED
1508		000000		N=N+1&175252+N		
1509		051523		M=M+401		
1510	006032	000240		SCOPE		
1511	006034	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1512	006040	062700	051523	ADD	#51523,%0	;ADD 51523 TO 0
1513	006044	022700	051523	CMP	#51523+0,%0	;DOES REGISTER ZERO=51523+0



1514	006050	001401		BEQ	.+4	
1515	006052	000000		HLT		;ADD 51523 TO 0 FAILED
1516		000000		N=N+1&175252+N		
1517		052124		M=M+401		
1518	006054	000240		SCOPE		
1519	006056	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1520	006062	062700	052124	ADD	#52124,%0	;ADD 52124 TO 0
1521	006066	022700	052124	CMP	#52124+0,%0	;DOES REGISTER ZERO=52124+0
1522	006072	001401		BEQ	.+4	
1523	006074	000000		HLT		;ADD 52124 TO 0 FAILED
1524		000000		N=N+1&175252+N		
1525		052525		M=M+401		
1526	006076	000240		SCOPE		
1527	006100	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1528	006104	062700	052525	ADD	#52525,%0	;ADD 52525 TO 0
1529	006110	022700	052525	CMP	#52525+0,%0	;DOES REGISTER ZERO=52525+0
1530	006114	001401		BEQ	.+4	
1531	006116	000000		HLT		;ADD 52525 TO 0 FAILED
1532		000000		N=N+1&175252+N		
1533		053126		M=M+401		
1534	006120	000240		SCOPE		
1535	006122	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1536	006126	062700	053126	ADD	#53126,%0	;ADD 53126 TO 0
1537	006132	022700	053126	CMP	#53126+0,%0	;DOES REGISTER ZERO=53126+0
1538	006136	001401		BEQ	.+4	
1539	006140	000000		HLT		;ADD 53126 TO 0 FAILED
1540		000000		N=N+1&175252+N		
1541		053527		M=M+401		
1542	006142	000240		SCOPE		
1543	006144	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1544	006150	062700	053527	ADD	#53527,%0	;ADD 53527 TO 0
1545	006154	022700	053527	CMP	#53527+0,%0	;DOES REGISTER ZERO=53527+0
1546	006160	001401		BEQ	.+4	
1547	006162	000000		HLT		;ADD 53527 TO 0 FAILED
1548		000000		N=N+1&175252+N		
1549		054130		M=M+401		
1550	006164	000240		SCOPE		
1551	006166	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1552	006172	062700	054130	ADD	#54130,%0	;ADD 54130 TO 0
1553	006176	022700	054130	CMP	#54130+0,%0	;DOES REGISTER ZERO=54130+0
1554	006202	001401		BEQ	.+4	
1555	006204	000000		HLT		;ADD 54130 TO 0 FAILED
1556		000000		N=N+1&175252+N		
1557		054531		M=M+401		
1558	006206	000240		SCOPE		
1559	006210	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1560	006214	062700	054531	ADD	#54531,%0	;ADD 54531 TO 0
1561	006220	022700	054531	CMP	#54531+0,%0	;DOES REGISTER ZERO=54531+0
1562	006224	001401		BEQ	.+4	
1563	006226	000000		HLT		;ADD 54531 TO 0 FAILED
1564		000000		N=N+1&175252+N		
1565		055132		M=M+401		
1566	006230	000240		SCOPE		
1567	006232	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1568	006236	062700	055132	ADD	#55132,%0	;ADD 55132 TO 0
1569	006242	022700	055132	CMP	#55132+0,%0	;DOES REGISTER ZERO=55132+0



1570	006246	001401		BEQ	.+4	
1571	006250	000000		HLT		;ADD 55132 TO 0 FAILED
1572		000000		N=N+1&175252+N		
1573		055533		M=M+401		
1574	006252	000240		SCOPE		
1575	006254	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1576	006260	062700	055533	ADD	#55533,%0	;ADD 55533 TO 0
1577	006264	022700	055533	CMP	#55533+0,%0	;DOES REGISTER ZERO=55533+0
1578	006270	001401		BEQ	.+4	
1579	006272	000000		HLT		;ADD 55533 TO 0 FAILED
1580		000000		N=N+1&175252+N		
1581		056134		M=M+401		
1582	006274	000240		SCOPE		
1583	006276	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1584	006302	062700	056134	ADD	#56134,%0	;ADD 56134 TO 0
1585	006306	022700	056134	CMP	#56134+0,%0	;DOES REGISTER ZERO=56134+0
1586	006312	001401		BEQ	.+4	
1587	006314	000000		HLT		;ADD 56134 TO 0 FAILED
1588		000000		N=N+1&175252+N		
1589		056535		M=M+401		
1590	006316	000240		SCOPE		
1591	006320	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1592	006324	062700	056535	ADD	#56535,%0	;ADD 56535 TO 0
1593	006330	022700	056535	CMP	#56535+0,%0	;DOES REGISTER ZERO=56535+0
1594	006334	001401		BEQ	.+4	
1595	006336	000000		HLT		;ADD 56535 TO 0 FAILED
1596		000000		N=N+1&175252+N		
1597		057136		M=M+401		
1598	006340	000240		SCOPE		
1599	006342	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1600	006346	062700	057136	ADD	#57136,%0	;ADD 57136 TO 0
1601	006352	022700	057136	CMP	#57136+0,%0	;DOES REGISTER ZERO=57136+0
1602	006356	001401		BEQ	.+4	
1603	006360	000000		HLT		;ADD 57136 TO 0 FAILED
1604		000000		N=N+1&175252+N		
1605		057537		M=M+401		
1606	006362	000240		SCOPE		
1607	006364	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1608	006370	062700	057537	ADD	#57537,%0	;ADD 57537 TO 0
1609	006374	022700	057537	CMP	#57537+0,%0	;DOES REGISTER ZERO=57537+0
1610	006400	001401		BEQ	.+4	
1611	006402	000000		HLT		;ADD 57537 TO 0 FAILED
1612		000000		N=N+1&175252+N		
1613		060140		M=M+401		
1614	006404	000240		SCOPE		
1615	006406	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1616	006412	062700	060140	ADD	#60140,%0	;ADD 60140 TO 0
1617	006416	022700	060140	CMP	#60140+0,%0	;DOES REGISTER ZERO=60140+0
1618	006422	001401		BEQ	.+4	
1619	006424	000000		HLT		;ADD 60140 TO 0 FAILED
1620		000000		N=N+1&175252+N		
1621		060541		M=M+401		
1622	006426	000240		SCOPE		
1623	006430	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1624	006434	062700	060541	ADD	#60541,%0	;ADD 60541 TO 0
1625	006440	022700	060541	CMP	#60541+0,%0	;DOES REGISTER ZERO=60541+0



1626	006444	001401		BEQ	.+4	
1627	006446	000000		HLT		;ADD 60541 TO 0 FAILED
1628		000000		N=N+1&175252+N		
1629		061142		M=M+401		
1630	006450	000240		SCOPE		
1631	006452	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1632	006456	062700	061142	ADD	#61142,%0	;ADD 61142 TO 0
1633	006462	022700	061142	CMP	#61142+0,%0	;DOES REGISTER ZERO=61142+0
1634	006466	001401		BEQ	.+4	
1635	006470	000000		HLT		;ADD 61142 TO 0 FAILED
1636		000000		N=N+1&175252+N		
1637		061543		M=M+401		
1638	006472	000240		SCOPE		
1639	006474	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1640	006500	062700	061543	ADD	#61543,%0	;ADD 61543 TO 0
1641	006504	022700	061543	CMP	#61543+0,%0	;DOES REGISTER ZERO=61543+0
1642	006510	001401		BEQ	.+4	
1643	006512	000000		HLT		;ADD 61543 TO 0 FAILED
1644		000000		N=N+1&175252+N		
1645		062144		M=M+401		
1646	006514	000240		SCOPE		
1647	006516	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1648	006522	062700	062144	ADD	#62144,%0	;ADD 62144 TO 0
1649	006526	022700	062144	CMP	#62144+0,%0	;DOES REGISTER ZERO=62144+0
1650	006532	001401		BEQ	.+4	
1651	006534	000000		HLT		;ADD 62144 TO 0 FAILED
1652		000000		N=N+1&175252+N		
1653		062545		M=M+401		
1654	006536	000240		SCOPE		
1655	006540	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1656	006544	062700	062545	ADD	#62545,%0	;ADD 62545 TO 0
1657	006550	022700	062545	CMP	#62545+0,%0	;DOES REGISTER ZERO=62545+0
1658	006554	001401		BEQ	.+4	
1659	006556	000000		HLT		;ADD 62545 TO 0 FAILED
1660		000000		N=N+1&175252+N		
1661		063146		M=M+401		
1662	006560	000240		SCOPE		
1663	006562	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1664	006566	062700	063146	ADD	#63146,%0	;ADD 63146 TO 0
1665	006572	022700	063146	CMP	#63146+0,%0	;DOES REGISTER ZERO=63146+0
1666	006576	001401		BEQ	.+4	
1667	006600	000000		HLT		;ADD 63146 TO 0 FAILED
1668		000000		N=N+1&175252+N		
1669		063547		M=M+401		
1670	006602	000240		SCOPE		
1671	006604	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1672	006610	062700	063547	ADD	#63547,%0	;ADD 63547 TO 0
1673	006614	022700	063547	CMP	#63547+0,%0	;DOES REGISTER ZERO=63547+0
1674	006620	001401		BEQ	.+4	
1675	006622	000000		HLT		;ADD 63547 TO 0 FAILED
1676		000000		N=N+1&175252+N		
1677		064150		M=M+401		
1678	006624	000240		SCOPE		
1679	006626	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1680	006632	062700	064150	ADD	#64150,%0	;ADD 64150 TO 0
1681	006636	022700	064150	CMP	#64150+0,%0	;DOES REGISTER ZERO=64150+0



1682	006642	001401		BEQ	.+4	
1683	006644	000000		HLT		;ADD 64150 TO 0 FAILED
1684		000000		N=N+1&175252+N		
1685		064551		M=M+401		
1686	006646	000240		SCOPE		
1687	006650	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1688	006654	062700	064551	ADD	#64551,%0	;ADD 64551 TO 0
1689	006660	022700	064551	CMP	#64551+0,%0	;DOES REGISTER ZERO=64551+0
1690	006664	001401		BEQ	.+4	
1691	006666	000000		HLT		;ADD 64551 TO 0 FAILED
1692		000000		N=N+1&175252+N		
1693		065152		M=M+401		
1694	006670	000240		SCOPE		
1695	006672	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1696	006676	062700	065152	ADD	#65152,%0	;ADD 65152 TO 0
1697	006702	022700	065152	CMP	#65152+0,%0	;DOES REGISTER ZERO=65152+0
1698	006706	001401		BEQ	.+4	
1699	006710	000000		HLT		;ADD 65152 TO 0 FAILED
1700		000000		N=N+1&175252+N		
1701		065553		M=M+401		
1702	006712	000240		SCOPE		
1703	006714	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1704	006720	062700	065553	ADD	#65553,%0	;ADD 65553 TO 0
1705	006724	022700	065553	CMP	#65553+0,%0	;DOES REGISTER ZERO=65553+0
1706	006730	001401		BEQ	.+4	
1707	006732	000000		HLT		;ADD 65553 TO 0 FAILED
1708		000000		N=N+1&175252+N		
1709		066154		M=M+401		
1710	006734	000240		SCOPE		
1711	006736	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1712	006742	062700	066154	ADD	#66154,%0	;ADD 66154 TO 0
1713	006746	022700	066154	CMP	#66154+0,%0	;DOES REGISTER ZERO=66154+0
1714	006752	001401		BEQ	.+4	
1715	006754	000000		HLT		;ADD 66154 TO 0 FAILED
1716		000000		N=N+1&175252+N		
1717		066555		M=M+401		
1718	006756	000240		SCOPE		
1719	006760	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1720	006764	062700	066555	ADD	#66555,%0	;ADD 66555 TO 0
1721	006770	022700	066555	CMP	#66555+0,%0	;DOES REGISTER ZERO=66555+0
1722	006774	001401		BEQ	.+4	
1723	006776	000000		HLT		;ADD 66555 TO 0 FAILED
1724		000000		N=N+1&175252+N		
1725		067156		M=M+401		
1726	007000	000240		SCOPE		
1727	007002	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1728	007006	062700	067156	ADD	#67156,%0	;ADD 67156 TO 0
1729	007012	022700	067156	CMP	#67156+0,%0	;DOES REGISTER ZERO=67156+0
1730	007016	001401		BEQ	.+4	
1731	007020	000000		HLT		;ADD 67156 TO 0 FAILED
1732		000000		N=N+1&175252+N		
1733		067557		M=M+401		
1734	007022	000240		SCOPE		
1735	007024	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1736	007030	062700	067557	ADD	#67557,%0	;ADD 67557 TO 0
1737	007034	022700	067557	CMP	#67557+0,%0	;DOES REGISTER ZERO=67557+0



1738	007040	001401		BEQ	.+4	
1739	007042	000000		HLT		;ADD 67557 TO 0 FAILED
1740		000000		N=N+1&175252+N		
1741		070160		M=M+401		
1742	007044	000240		SCOPE		
1743	007046	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1744	007052	062700	070160	ADD	#70160,%0	;ADD 70160 TO 0
1745	007056	022700	070160	CMP	#70160+0,%0	;DOES REGISTER ZERO=70160+0
1746	007062	001401		BEQ	.+4	
1747	007064	000000		HLT		;ADD 70160 TO 0 FAILED
1748		000000		N=N+1&175252+N		
1749		070561		M=M+401		
1750	007066	000240		SCOPE		
1751	007070	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1752	007074	062700	070561	ADD	#70561,%0	;ADD 70561 TO 0
1753	007100	022700	070561	CMP	#70561+0,%0	;DOES REGISTER ZERO=70561+0
1754	007104	001401		BEQ	.+4	
1755	007106	000000		HLT		;ADD 70561 TO 0 FAILED
1756		000000		N=N+1&175252+N		
1757		071162		M=M+401		
1758	007110	000240		SCOPE		
1759	007112	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1760	007116	062700	071162	ADD	#71162,%0	;ADD 71162 TO 0
1761	007122	022700	071162	CMP	#71162+0,%0	;DOES REGISTER ZERO=71162+0
1762	007126	001401		BEQ	.+4	
1763	007130	000000		HLT		;ADD 71162 TO 0 FAILED
1764		000000		N=N+1&175252+N		
1765		071563		M=M+401		
1766	007132	000240		SCOPE		
1767	007134	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1768	007140	062700	071563	ADD	#71563,%0	;ADD 71563 TO 0
1769	007144	022700	071563	CMP	#71563+0,%0	;DOES REGISTER ZERO=71563+0
1770	007150	001401		BEQ	.+4	
1771	007152	000000		HLT		;ADD 71563 TO 0 FAILED
1772		000000		N=N+1&175252+N		
1773		072164		M=M+401		
1774	007154	000240		SCOPE		
1775	007156	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1776	007162	062700	072164	ADD	#72164,%0	;ADD 72164 TO 0
1777	007166	022700	072164	CMP	#72164+0,%0	;DOES REGISTER ZERO=72164+0
1778	007172	001401		BEQ	.+4	
1779	007174	000000		HLT		;ADD 72164 TO 0 FAILED
1780		000000		N=N+1&175252+N		
1781		072565		M=M+401		
1782	007176	000240		SCOPE		
1783	007200	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1784	007204	062700	072565	ADD	#72565,%0	;ADD 72565 TO 0
1785	007210	022700	072565	CMP	#72565+0,%0	;DOES REGISTER ZERO=72565+0
1786	007214	001401		BEQ	.+4	
1787	007216	000000		HLT		;ADD 72565 TO 0 FAILED
1788		000000		N=N+1&175252+N		
1789		073166		M=M+401		
1790	007220	000240		SCOPE		
1791	007222	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1792	007226	062700	073166	ADD	#73166,%0	;ADD 73166 TO 0
1793	007232	022700	073166	CMP	#73166+0,%0	;DOES REGISTER ZERO=73166+0



1794	007236	001401		BEQ	.+4	
1795	007240	000000		HLT		;ADD 73166 TO 0 FAILED
1796		000000		N=N+1&175252+N		
1797		073567		M=M+401		
1798	007242	000240		SCOPE		
1799	007244	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1800	007250	062700	073567	ADD	#73567,%0	;ADD 73567 TO 0
1801	007254	022700	073567	CMP	#73567+0,%0	;DOES REGISTER ZERO=73567+0
1802	007260	001401		BEQ	.+4	
1803	007262	000000		HLT		;ADD 73567 TO 0 FAILED
1804		000000		N=N+1&175252+N		
1805		074170		M=M+401		
1806	007264	000240		SCOPE		
1807	007266	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1808	007272	062700	074170	ADD	#74170,%0	;ADD 74170 TO 0
1809	007276	022700	074170	CMP	#74170+0,%0	;DOES REGISTER ZERO=74170+0
1810	007302	001401		BEQ	.+4	
1811	007304	000000		HLT		;ADD 74170 TO 0 FAILED
1812		000000		N=N+1&175252+N		
1813		074571		M=M+401		
1814	007306	000240		SCOPE		
1815	007310	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1816	007314	062700	074571	ADD	#74571,%0	;ADD 74571 TO 0
1817	007320	022700	074571	CMP	#74571+0,%0	;DOES REGISTER ZERO=74571+0
1818	007324	001401		BEQ	.+4	
1819	007326	000000		HLT		;ADD 74571 TO 0 FAILED
1820		000000		N=N+1&175252+N		
1821		075172		M=M+401		
1822	007330	000240		SCOPE		
1823	007332	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1824	007336	062700	075172	ADD	#75172,%0	;ADD 75172 TO 0
1825	007342	022700	075172	CMP	#75172+0,%0	;DOES REGISTER ZERO=75172+0
1826	007346	001401		BEQ	.+4	
1827	007350	000000		HLT		;ADD 75172 TO 0 FAILED
1828		000000		N=N+1&175252+N		
1829		075573		M=M+401		
1830	007352	000240		SCOPE		
1831	007354	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1832	007360	062700	075573	ADD	#75573,%0	;ADD 75573 TO 0
1833	007364	022700	075573	CMP	#75573+0,%0	;DOES REGISTER ZERO=75573+0
1834	007370	001401		BEQ	.+4	
1835	007372	000000		HLT		;ADD 75573 TO 0 FAILED
1836		000000		N=N+1&175252+N		
1837		076174		M=M+401		
1838	007374	000240		SCOPE		
1839	007376	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1840	007402	062700	076174	ADD	#76174,%0	;ADD 76174 TO 0
1841	007406	022700	076174	CMP	#76174+0,%0	;DOES REGISTER ZERO=76174+0
1842	007412	001401		BEQ	.+4	
1843	007414	000000		HLT		;ADD 76174 TO 0 FAILED
1844		000000		N=N+1&175252+N		
1845		076575		M=M+401		
1846	007416	000240		SCOPE		
1847	007420	012700	000000	MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1848	007424	062700	076575	ADD	#76575,%0	;ADD 76575 TO 0
1849	007430	022700	076575	CMP	#76575+0,%0	;DOES REGISTER ZERO=76575+0



1850	007434	001401			BEQ	.+4	
1851	007436	000000			HLT		;ADD 76575 TO 0 FAILED
1852		000000			N=N+1&175252+N		
1853		077176			M=M+401		
1854	007440	000240			SCOPE		
1855	007442	012700	000000		MOV	#0,%0	;MOVE 0 TO REGISTER ZERO
1856	007446	062700	077176		ADD	#77176,%0	;ADD 77176 TO 0
1857	007452	022700	077176		CMP	#77176+0,%0	;DOES REGISTER ZERO=77176+0
1858	007456	001401			BEQ	.+4	
1859	007460	000000			HLT		;ADD 77176 TO 0 FAILED
1860		000000			N=N+1&175252+N		
1861		077577			M=M+401		
1862	007462	005267	000022		INC	LOOP	
1863	007466	001006			BNE	FIN	
1864	007470	012737	000207	177566	MOV	#207,@#177566	
1865	007476	105737	177564		TSTB	@#177564	
1866	007502	100375			BPL	-4	
1867	007504	000167	170470		JMP	200	
1868	007510	000000			0		
1869		000001			.END		

FIN:  
LOOP:



FIN = 007504  
HLT = 000000

1863	1867#	273	281	288	295	302	309	316	323	330	337	344
257#	267	365	372	379	386	395	402	409	416	423	430	437
351	358	458	465	472	479	486	493	500	509	516	523	530
444	451	551	558	565	572	579	586	593	600	607	614	623
537	544	644	651	658	665	672	679	686	693	700	707	714
630	637	736	743	750	757	764	771	778	785	792	799	806
721	728	827	834	841	851	859	867	875	883	891	899	907
813	820	931	939	947	955	963	971	979	987	995	1003	1011
915	923	1035	1043	1051	1059	1067	1075	1083	1091	1099	1107	1115
1019	1027	1139	1147	1155	1163	1171	1179	1187	1195	1203	1211	1219
1123	1131	1243	1251	1259	1267	1275	1283	1291	1299	1307	1315	1323
1227	1235	1347	1355	1363	1371	1379	1387	1395	1403	1411	1419	1427
1331	1339	1451	1459	1467	1475	1483	1491	1499	1507	1515	1523	1531
1435	1443	1555	1563	1571	1579	1587	1595	1603	1611	1619	1627	1635
1539	1547	1659	1667	1675	1683	1691	1699	1707	1715	1723	1731	1739
1643	1651	1763	1771	1779	1787	1795	1803	1811	1819	1827	1835	1843
1747	1755											
1851	1859											

LOCP = 007510  
M = 077577

1862*	1868#	853#	854	861#	862	869#	870	877#	878	885#	886	893#
844#	846	902	909#	910	917#	918	925#	926	933#	934	941#	942
894	901#	957#	958	965#	966	973#	974	981#	982	989#	990	997#
949#	950	1006	1013#	1014	1021#	1022	1029#	1030	1037#	1038	1045#	1046
998	1005#	1061#	1062	1069#	1070	1077#	1078	1085#	1086	1093#	1094	1101#
1053#	1054	1110	1117#	1118	1125#	1126	1133#	1134	1141#	1142	1149#	1150
1102	1109#	1165#	1166	1173#	1174	1181#	1182	1189#	1190	1197#	1198	1205#
1157#	1158	1214	1221#	1222	1229#	1230	1237#	1238	1245#	1246	1253#	1254
1206	1213#	1269#	1270	1277#	1278	1285#	1286	1293#	1294	1301#	1302	1309#
1261#	1262	1318	1325#	1326	1333#	1334	1341#	1342	1349#	1350	1357#	1358
1310	1317#	1373#	1374	1381#	1382	1389#	1390	1397#	1398	1405#	1406	1413#
1365#	1366	1422	1429#	1430	1437#	1438	1445#	1446	1453#	1454	1461#	1462
1414	1421#	1477#	1478	1485#	1486	1493#	1494	1501#	1502	1509#	1510	1517#
1469#	1470	1526	1533#	1534	1541#	1542	1549#	1550	1557#	1558	1565#	1566
1518	1525#	1581#	1582	1589#	1590	1597#	1598	1605#	1606	1613#	1614	1621#
1573#	1574	1630	1637#	1638	1645#	1646	1653#	1654	1661#	1662	1669#	1670
1622	1629#	1685#	1686	1693#	1694	1701#	1702	1709#	1710	1717#	1718	1725#
1677#	1678	1734	1741#	1742	1749#	1750	1757#	1758	1765#	1766	1773#	1774
1726	1733#	1789#	1790	1797#	1798	1805#	1806	1813#	1814	1821#	1822	1829#
1781#	1782	1845#	1846	1853#	1854	1861#						
1830	1837#	1838										

N = 000000

275#	276	282#	283	289#	290	296#	297	303#	304	310#	311	317#
318	324#	325	331#	332	338#	339	345#	346	352#	353	359#	360
366#	367	373#	374	380#	381	387#	389#	390	396#	397	403#	404
410#	411	417#	418	424#	425	431#	432	438#	439	445#	446	452#
453	459#	460	466#	467	473#	474	480#	481	487#	488	494#	495
501#	503#	504	510#	511	517#	518	524#	525	531#	532	538#	539
545#	546	552#	553	559#	560	566#	567	573#	574	580#	581	587#
588	594#	595	601#	602	608#	609	615#	617#	618	624#	625	631#
632	638#	639	645#	646	652#	653	659#	660	666#	667	673#	674
680#	681	687#	688	694#	695	701#	702	708#	709	715#	716	722#
723	729#	730#	731	737#	738	744#	745	751#	752	758#	759	765#
766	772#	773	779#	780	786#	787	793#	794	800#	801	807#	808
814#	815	821#	822	828#	829	835#	836	842#	845#	846	852#	854
860#	862	868#	870	876#	878	884#	886	892#	894	900#	902	908#
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964#	966	972#	974	980#	982	988#	990	996#	998	1004#	1006	1012#
1014	1020#	1022	1028#	1030	1036#	1038	1044#	1046	1052#	1054	1060#	1062



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1082	1854	288	276	374	381	390	397	404	411	418	425	432
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1085	1854	626	543	653	660	667	674	681	688	695	702	709
1086	1854	727	635	745	752	759	766	773	780	787	794	801
1087	1854	819	727	836	843	850	857	864	871	878	885	892
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1089	1854	1022	938	1046	1053	1060	1067	1074	1081	1088	1095	1102
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1097	1854	272	280	287	294	301	308	315	322	329	336	343
1098	1854	357	371	378	385	394	401	408	415	422	429	436
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1100	1854	543	557	564	571	578	585	592	599	606	613	622
1101	1854	626	640	647	654	661	668	675	682	689	696	703
1102	1854	727	742	749	756	763	770	777	784	791	798	805
1103	1854	819	833	840	847	854	861	868	875	882	889	896
1104	1854	922	938	946	953	960	967	974	981	988	995	1002
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..SR2AZ	18
..SSAVE	18
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..SBR20P	18
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..SUPRM	18
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	690	697	704	711	718	725	733	740	747	754	761	768	775	782	789
	796	803	810	817	824	831	838	848	856	864	872	880	888	896	904
	912	920	928	936	944	952	960	968	976	984	992	1000	1008	1016	1024
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BEQ	266	272	280	287	294	301	308	315	322	329	336	343	350	357	364
	371	378	385	394	401	408	415	422	429	436	443	450	457	464	471
	478	485	492	499	508	515	522	529	536	543	550	557	564	571	578
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BPL	370	377	384	393	400	407	414	421	428	435	442	449	456	463	470
CMP	477	484	491	498	507	514	521	528	535	542	549	556	563	570	577
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	691	698	705	712	719	726	734	741	748	755	762	769	776	783	790
	797	804	811	818	825	832	839	849	857	865	873	881	889	897	905
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HALT	257														
INC	1862														
JMP	1867														
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	582	589	596	603	610	619	626	633	640	647	654	661	668	675	682
	689	696	703	710	717	724	732	739	746	753	760	767	774	781	788
	795	802	809	816	823	830	837	847	855	863	871	879	887	895	903
	911	919	927	935	943	951	959	967	975	983	991	999	1007	1015	1023
	1031	1039	1047	1055	1063	1071	1079	1087	1095	1103	1111	1119	1127	1135	1143
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.MACRO	1														
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.REM	1														
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ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

\*.DOJA.SEG/SOL/CRF/PAGNUM/NL:TOC=SYSMAC.CO,DOJA.P11  
RUN-TIME: 25 31 2 SECONDS  
RUN-TIME RATIO: 335/60=5.5  
CORE USED: 32K (63 PAGES)



