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.TITLE CZMLABO ML-11 LOGIC TEST .SBTTL USER DOCUMENTATION .REM &

IDENTIFICATION

PRODUCT CODE:

AC-S3888-MC

PRODUCT NAME:

CZMLABO ML11 LOGIC TEST

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- 1.1 PROGRAM ABSTRACT

DIAGNOSTIC ENGINEERING WAS CONTRACTED BY MEMORY ENGINEERING TO MAKE THIS DIAGNOSTIC IN ORDER TO AID MEMORY ENGINEERING TO DESIGN AND DEBUG THE ML-11, AID FIELD SERVICE FOR FIELD REPAIRS AND INSTALLATIONS OF THE

THIS DIAGNOSTIC PRODUCT WILL BE DESIGNED TO TEST FROM ONE TO EIGHT ML-11 UNITS OFF A SINGLE RH11 OR RH70 CONTROLLER.

THE FUNCTIONAL LEVEL (FRU) OF THIS DIAGNOSTIC PRODUCT WILL BE TO THE LOGIC FUNCTION LEVEL (I.E. DRIVE SELECTION). UPON DETECTION OF AN ERROR BY THE DIAGNOSTIC, THE LOGIC FUNCTION AND RESPECTIVE MODULE WHICH IT IS LOCATED ON WILL BE PRINTED TO THE OPERATOR.

THIS DIAGNOSTIC HAS BEEN WRITTEN FOR USE WITH THE DIAGNOSTIC RUNTIME SERVICES SOFTWARE (SUPERVISOR). THESE SERVICES PROVIDE THE INTERFACE TO THE OPERATOR AND TO THE SOFTWARE ENVIRONMENT. THIS PROGRAM CAN BE USED WITH XXDP+, ACT, APT, SLIDE AND PAPER TAPE. FOR A COMPLETE DESCRIPTION OF THE RUNTIME SERVICES, REFER TO THE XXDP+ USER'S MANUAL. THERE IS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES IN SECTION 2 OF THIS DOCUMENT.

1.2 SYSTEM REQUIREMENTS

THE HARDWARE DESIGN IS EXPECTED TO CONFORM TO THE STANDARDS SET FORTH IN THE MASSBUS SPECIFICATION (DEC STANDARD 159).

PDP-11 WITH MINIMUM OF 28K WORDS OF MEMORY

CONSOLE TERMINAL

RH11 OR RH70

1 TO 8 ML-11 DRIVES ON INTERMIXED BUS

XXDP+ LOAD MEDIA

1.3 RELATED DOCUMENTS AND STANDARDS

- 1. SUPPRGC.DOC
- 2. SUPINT.MEN
- 3. SUPFUN.C
- 4. XXDPPLUS.DOC
- 5. BLISS LANGUAGE GUIDE
- 6. BLISS-16 USER'S GUIDE

1.4 DIAGNOSTIC HIERARCY PREREQUISITES

IT WILL BE ASSUMED THAT PRIOR TO THE RUNNING OF THIS DIAGNOSTIC THAT ALL APPROPRIATE CPU, MAIN MEMORY AND RH CONTROLLER DIAGNOSTICS HAVE BEEN SUCCESSFULLY RUN.

THIS DIAGNOSTIC WILL HOWEVER PERFORM MINIMAL RH TESTS TO ENSURE ITS EXISTANCE AND BASIC FUNCTIONALITY BEFORE LOGIC TESTS ARE ALLOWED TO RUN.

2.0 OPERATING INSTRUCTIONS

THIS SECTION CONTAINS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES. FOR DETAILED INFORMATION, REFER TO THE XXDP+ USER'S MANUAL (CHQUS).

2.1 COMMANDS

THERE ARE ELEVEN LEGAL COMMANDS FOR THE DIAGNOSTIC RUNTIME SERVICES (SUPERVISOR). THIS SECTION LISTS THE COMMANDS AND GIVES A VERY BRIEF DESCRIPTION OF THEM. THE XXDP+ USER'S MANUAL HAS MORE DETAILS.

COMPIAND	EFFECT
START RESTART CONTINUE	START THE DIAGNOSTIC FROM AN INITIAL STATE START THE DIAGNOSTIC WITHOUT INITIALIZING CONTINUE AT TEST THAT WAS INTERRUPTED (AFTER *C)
PROCEED EXIT ADD	CONTINUE FROM AN ERROR HALT RETURN TO XXDP+ MONITOR (XXDP+ OPERATION ONLY!) ACTIVATE A UNIT FOR TESTING (ALL UNITS ARE CONSIDERED TO BE ACTIVE AT START TIME
DROP PRINT	DEACTIVATE A UNIT PRINT STATISTICAL INFORMATION (IF IMPLEMENTED BY THE DIAGNOSTIC - SECTION 4.0)
DISPLAY FLAGS ZFLAGS	TYPE A LIST OF ALL DEVICE INFORMATION TYPE THE STATE OF ALL FLAGS (SEE SECTION 2.3) CLEAR ALL FLAGS (SEE SECTION 2.3)

A COMMAND CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. SO YOU MAY, FOR EXAMPLE, TYPE 'STA' INSTEAD OF 'START'.

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2.2 SWITCHES

THERE ARE SEVERAL SWITCHES WHICH ARE USED TO MODIFY SUPERVISOR OPERATION. THESE SWITCHES ARE APPENDED TO THE LEGAL COMMANDS. ALL OF THE LEGAL SWITCHES ARE TABULATED BELOW WITH A BRIEF DESCRIPTION OF EACH. IN THE DESCRIPTIONS BELOW, A DECIMAL NUMBER IS DESIGNATED BY 'DDDDD'.

SWITCH	EFFECT
/TESTS:LIST	EXECUTE ONLY THOSE TESTS SPECIFIED IN THE LIST. LIST IS A STRING OF TEST NUMBERS, FOR EXAMPLE - /TESTS:1:5:7-10.
/PASS:DDDDD /FLAGS:FLGS	THIS LIST WILL CAUSE TESTS 1,5,7,8,9,10 TO BE RUN. ALL OTHER TESTS WILL NOT BE RUN. EXECUTE DDDDD PASSES (DDDDD = 1 TO 64000) SET SPECIFIED FLAGS. FLAGS ARE DESCRIBED
//LAGS./LGS	IN SECTION 2.3.
/EOP:DDDDD	REPORT END OF PASS MESSAGE AFTER EVERY DDDDD PASSES ONLY. (DDDDD = 1 TO 64000)
/UNITS:LIST	TEST/ADD/DROP ONLY THOSE UNITS SPECIFIED IN THE LIST. LIST EXAMPLE - /UNITS:0:5:10-12 USE UNITS 0,5,10,11,12 (UNIT NUMBERS = 0-63)

EXAMPLE OF SWITCH USAGE:

START/TESTS:1-5/PASS:1000/E0P:100

THE EFFECT OF THIS COMMAND WILL BE: 1) TESTS 1 THROUGH 5 WILL BE EXECUTED, 2) ALL UNITS WILL TESTED 1000 TIMES AND 3) THE END OF PASS MESSAGES WILL BE PRINTED AFTER EACH 100 PASSES ONLY. A SWITCH CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. YOU MAY, FOR EXAMPLE, TYPE "/TES:1-5" INSTEAD OF "/TESTS:1-5".

BELOW IS A TABLE THAT SPECIFIES WHICH SWITCHES CAN BE USED BY EACH COMPAND.

	TESTS	PASS	FLAGS	EOP	UNITS
START RESTART CONTINUE PROCEED DROP ADD PRINT	X	X X	X X X	X X X	X X X
DISPLAY FLAGS ZFLAGS EXIT					x

2.3 FLAGS

FLAGS ARE USED TO SET UP CERTAIN OPERATIONAL PARAMETERS SUCH AS LOOPING ON ERROR. ALL FLAGS ARE CLEARED AT STARTUP AND REMAIN CLEARED UNTIL EXPLICITLY SET USING THE FLAGS SWITCH. FLAGS

ARE ALSO CLEARED AFTER A START COMMAND UNLESS SET USING THE FLAG SWITCH. THE ZFLAGS COMMAND MAY ALSO BE USED TO CLEAR ALL FLAGS. WITH THE EXCEPTION OF THE START AND ZFLAGS COMMANDS, NO COMMANDS AFFECT THE STATE OF THE FLAGS; THEY REMAIN SET OR CLEARED AS SPECIFIED BY THE LAST FLAG SWITCH.

FLAG	EFFECT
HOE	HALT ON ERROR - CONTROL IS RETURNED TO RUNTIME SERVICES COMMAND MODE
LOE IER*	LOOP ON ERROR INHIBIT ALL ERROR REPORTS
IBE*	INHIBIT ALL ERROR REPORTS EXCEPT FIRST LEVEL (FIRST LEVEL CONTAINS
IXE*	ERROR TYPE, NUMBER, PC, TEST AND UNIT) INHIBIT EXTENDED ERROR REPORTS (THOSE
PRI PNT	CALLED BY PRINTX MACRO'S) DIRECT MESSAGES TO LINE PRINTER PRINT TEST NUMBER AS TEST EXECUTES
BOE	"BELL" ON ERROR UNATTENDED MODE (NO MANUAL INTERVENTION)
ISR	INHIBIT STATISTICAL REPORTS (DOES NOT APPLY TO DIAGNOSTICS WHICH DO NOT SUPPORT
IDU ADR	STATISTICAL REPORTING) INHIBIT PROGRAM DROPPING OF UNITS EXECUTE AUTODROP CODE
LOT	EXECUTE EVALUATION (ON DIAGNOSTICS WHICH HAVE EVALUATION SUPPORT)

*ERROR MESSAGES ARE DESCRIBED IN SECTION 3.1

SEE THE XXDP+ USER'S MANUAL FOR MORE DETAILS ON FLAGS. YOU MAY SPECIFY MORE THAN ONE FLAG WITH THE FLAG SWITCH. FOR EXAMPLE, TO CAUSE THE PROGRAM TO LOOP ON ERROR, INHIBIT ERROR REPORTS AND TYPE A 'BELL' ON ERROR, YOU MAY USE THE FOLLOWING STRING:

/FLAGS:LOE: IER:BOE

2.4 HARDWARE QUESTIONS

WHEN A DIAGNOSTIC IS STARTED, THE RUNTIME SERVICES WILL PROMPT THE USER FOR HARDWARE INFORMATION BY TYPING 'CHANGE HW (L) ?'' YOU MUST ANSWER 'Y' AFTER A START COMMAND UNLESS THE HARDWARE INFORMATION HAS BEEN 'PRELOADED'' USING THE SETUP UTILITY (SEE CHAPTER 6 OF THE XXDP+ USER'S MANUAL). WHEN YOU ANSWER THIS QUESTION WITH A "Y", THE RUNTIME SERVICES WILL ASK FOR THE NUMBER OF UNITS (IN DECIMAL). YOU WILL THEN BE SKED THE FOLLOWING QUESTIONS FOR EACH UNIT.

PARAMETER CODING CALLS

GPRMA MSGH1,0,0,0,177777,YES

:RH ADDRESS

GPRMD MSGH2.2.0.77,11.70,YES ;RH TYPE
GPRMD MSGH3.4.0.777,0,777,YES ;RH VECTOR ADDRESS
GPRMD MSGH4.6.D.77,1.16..YES ;NUMBER FO ARRAYS
GPRML MSGH5.10.1.YES ;DRIVE OPTIONS
GPRML MSGH6.12.0.7,0.7.YES ;DRIVE NUMBER
GPRML MSGH7.14.1.YES ;PARITY DISABLED

PARAMETER CODING MESSAGES

MSGH1: .ASCIZ /RH ADDRESS?/
MSGH2: .ASCIZ /IS RH AN '70' OR '11?/
MSGH3: .ASCIZ /RH VECTOR ADDRESS?/
MSGH4: .ASCIZ /NUMBER OF ARRAY MODULES?/
MSGH5: .ASCIZ /IS DRIVE OPTION AN ML11A?/
MSGH6: .ASCIZ /ML-11 DRIVE NUMBER?/
MSGH7: .ASCIZ /IS PARITY DISABLED?/

SAMPLE DIALOGE

DR> STA <CR>

CHANGE HW <L> ? Y <CR>

UNITS <D> ? 1 <CR>

UNIT 0
RH ADDRESS <0> 176400 ? <CR>
IS RH AN '70' OR '11' <0> ? <CR>
RH VECTOR ADDRESS <0> 204 ? <CR>
NUMBER OF ARRAYS MODULES ? <D> 16 ? 14 <CR>
IS DRIVE OPTION AN ML11A ? <L> Y ? <CR>
ML-11 DRIVE NUMBER ? <0> 0 ? <CR>
IS PARITY DISABLED ? <L> N ? <CR>

2.5 SOFTWARE QUESTIONS

AFTER YOU HAVE ANSWERED THE HARDWARE QUESTIONS OR AFTER A RESTART OR CONTINUE COMMAND, THE RUNTIME SERVICES WILL ASK FOR SOFTWARE PARAMETERS. THESE PARAMETERS WILL GOVERN SOME DIAGNOSTIC SPECIFIC OPERATION MODES. YOU WILL BE PROMPTED BY "CHANGE SW (L)?" IF YOU WISH TO CHANGE ANY PARAMETERS, ANSWER BY TYPING "Y". THE SOFTWARE QUESTIONS AND THE DEFAULT VALUES ARE DESCRIBED IN THE NEXT PARAGRAPH(S).

PARAMETER CODING CALLS

GPRML MSGS1,0,1,NO

:PRINT THE DRIVE SERIAL NUMBER

PARAMETER CODING MESSAGES

MSG1: .ASCIZ /PRINT SERIAL NO.?/

SAMPLE DIALOGE

PRINT SERIAL NO. ? <L> N ? Y <CR>

2.6 EXTENDED P-TABLE DIALOGUE

WHEN YOU ANSWER THE HARDWARE QUESTIONS, YOU ARE BUILDING ENTRIES IN A TABLE THAT DESCRIBES THE DEVICES UNDER TEST. THE SIMPLEST WAY TO BUILD THIS TABLE IS TO ANSWER ALL QUESTIONS FOR EACH UNIT TO BE TESTED. IF YOU HAVE A MULTIPLEXED DEVICE SUCH AS A MASS STORAGE CONTROLLER WITH SEVERAL DRIVES OR A COMMUNICATION DEVICE WITH SEVERAL LINES, THIS BECOMES TEDIOUS SINCE MOST OF THE ANSWERS ARE REPETITIOUS.

TO ILLUSTRATE A MORE EFFICIENT METHOD, SUPPOSE YOU ARE TESTING A FICTIONAL DEVICE, THE XY11. SUPPOSE THIS DEVICE CONSISTS OF A CONTROL MODULE WITH EIGHT UNITS (SUB-DEVICES) ATTACHED TO IT. THESE UNITS ARE DESCRIBED BY THE OCTAL NUMBERS O THROUGH 7. THERE IS ONE HARDWARE PARAMETER THAT CAN VARY AMONG UNITS CALLED THE G FACTOR. THIS Q-FACTOR MAY BE O OR 1. BELOW IS A SIMPLE WAY TO BUILD A TABLE FOR ONE XY11 WITH EIGHT UNITS.

UNITS (D) ? 8<CR>

UNIT 1 CSR ADDRESS (0) ? 160000<CR> SUB-DEVICE # (0) ? 0<CR> Q-FACTOR (0) 0 ? 1<CR>

UNIT 2 CSR ADDRESS (0) ? 160000<CR> SUB-DEVICE # (0) ? 1<CR> Q-FACTOR (0) 1 ? 0<CR>

UNIT 3
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 2<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 4 CSR ADDRESS (0) ? 160000<CR> SUB-DEVICE # (0) ? 3<CR> Q-FACTOR (C) 0 ? <CR>

UNIT 5 CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 4<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 6
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 5<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 7 CSR ADDRESS (0) ? 160000<CR> SUB-DEVICE # (0) ? 6<CR> Q-FACTOR (0) 0 ? 1<CR>

UNIT 8
CSR ADDRESS (0) 160000<CR>
SUB-DEVICE # (0) ? 7<CR>
Q-FACTOR (0) 1 ? <CR>

NOTICE THAT THE DEFAULT VALUE FOR THE Q-FACTOR CHANGES WHEN A NON-DEFAULT RESPONSE IS GIVEN. BE CAREFUL WHEN SPECIFYING MULTIPLE UNITS!

AS YOU CAN SEE FROM THE ABOVE EXAMPLE, THE HARDWARE PARAMETERS DO NOT VARY SIGNIFICANTLY FROM UNIT TO UNIT. THE PROCEDURE SHOWN IS NOT VERY EFFICIENT.

THE RUNTIME SERVICES CAN TAKE MULTIPLE UNIT SPECIFICATIONS HOWEVER. LET'S BUILD THE SAME TABLE USING THE MULTIPLE SPECIFICATION FEATURE.

UNITS (D) ? 8<CR>

UNIT 1 CSR ADDRESS (0) ? 160000<CR> SUB-DEVICE # (0) ? 0,1<CR> Q-FACTOR (0) 0 ? 1,0<CR>

UNIT 3 CSR ADDRESS (0) ? 160000<CR> SUB-DEVICE # (0) ? 2-5<CR> Q-FACTOR (0) 0 ? 0<CR>

UNIT 7 CSR ADDRESS (0) ? 160000<CR> SUB-DEVICE # (0) ? 6,7<CR> Q-FACTOR (0) 0 ? 1<CR>

AS YOU CAN SEE IN THE ABOVE DIALOGUE, THE RUNTIME SERVICES WILL BUILD AS MANY ENTRIES AS IT CAN WITH THE INFORMATION GIVEN IN ANY ONE PASS THROUGH THE QUESTIONS. IN THE FIRST PASS, TWO ENTRIES ARE BUILT SINCE TWO SUB-DEVICES AND Q-FACTORS WERE SPECIFIED. THE SERVICES ASSUME THAT THE CSR ADDRESS IS 160000 FOR BOTH SINCE IT WAS SPECIFIED ONLY ONCE. IN THE SECOND PASS, FOUR ENTRIES WERE BUILT. THIS IS BECAUSE FOUR SUB-DEVICES WERE SPECIFIED. THE '-' CONSTRUCT TELLS THE RUNTIME SERVICES TO INCREMENT THE DATA FROM THE FIRST NUMBER TO THE SECOND. IN THIS CASE, SUB-DEVICES 2, 3, 4 AND 5 WERE SPECIFIED. (IF THE SUB-DEVICE WERE SPECIFIED BY ADDRESSES, THE INCREMENT WOULD BE BY 2 SINCE ADDRESSES MUST BE ON AN EVEN BOUNDARY.) THE CSR ADDRESSES AND Q-FACTORS FOR THE FOUR ENTRIES ARE ASSUMED TO BE 160000 AND 0 RESPECTIVELY SINCE THEY WERE ONLY SPECIFIED ONCE. THE LAST TWO UNITS ARE SPECIFIED IN THE THIRD PASS.

THE WHOLE PROCESS COULD HAVE BEEN ACCOMPLISHED IN ONE PASS AS SHOWN BELOW.

UNITS (D) ? 8<CR>

UNIT 1 CSR ADDRESS (0) ? 160000<CR> SUB-DEVICE # (0) ? 0-7<CR> Q-FACTOR (0) 0 ? 0,1,0,...1,1<CR>

AS YOU CAN SEE FROM THIS EXAMPLE, NULL REPLIES (COMMAS ENCLOSING A NULL FIELD) TELL THE RUNTIME SERVICES TO REPEAT THE LAST REPLY.

2.7 QUICK START-UP PROCEDURE (XXDP+)

TO START-UP THIS PROGRAM:

- 1. BOOT XXDP+
- 2. GIVE THE DATE AND ANSWER THE LSI AND 50HZ (IF THERE IS A CLOCK) QUESTIONS
- 3. TYPE 'R NAME', WHERE NAME IS THE NAME OF THE BIN OR BIC FILE FOR THIS PROGRAM
- 4. TYPE "START"
- 5. ANSWER THE "CHANGE HW" QUESTION WITH "Y"
- 6. ANSWER ALL THE HARDWARE QUESTIONS
- 7. ANSWER THE "CHANGE SW" QUESTION WITH "N"

WHEN YOU FOLLOW THIS PROCEDURE YOU WILL BE USING ONLY THE DEFAULTS FOR FLAGS AND SOFTWARE PARAMETERS. THESE DEFAULTS ARE DESCRIBED IN SECTIONS 2.3 AND 2.5.

- 3.0 ERROR INFORMATION
- 3.1 TYPES OF ERROR MESSAGES

THERE ARE THREE LEVELS OF ERROR MESSAGES THAT MAY BE ISSUED BY A DIAGNOSTIC: GENERAL, BASIC AND EXTENDED. GENERAL ERROR MESSAGES ARE ALMAYS PRINTED UNLESS THE "IER" FLAG IS SET (SECTION 2.3). THE GENERAL ERROR MESSAGE IS OF THE FORM:

NAME TYPE NUMBER ON UNIT NUMBER TST NUMBER PC:XXXXXXX ERROR MESSAGE

JUNET: NAME = DIAGNOSTIC NAME

TYPE = ERROR TYPE (SYS FATAL, DEV FATAL, HARD OR SOFT)

NUMBER = ERROR NUMBER

UNIT NUMBER = 0 - N (N IS LAST UNIT IN PTABLE)

TST NUMBER = TEST AND SUBTEST WHERE ERROR OCCURRED

PC:XXXXXXX = ADDRESS OF ERROR MESSAGE CALL

BASIC ERROR MESSAGES ARE MESSAGES THAT CONTAIN SOME ADDITIONAL INFORMATION ABOUT THE ERROR. THESE ARE ALWAYS PRINTED UNLESS THE 'IER' OR 'IBR' FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL MESSAGE.

EXTENDED ERROR MESSAGES CONTAIN SUPPLEMENTARY ERROR INFORMATION

SUCH AS REGISTER CONTENTS OR GOOD/BAD DATA. THESE ARE ALWAYS PRINTED UNLESS THE 'IER', 'IBR' OR 'IXR' FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL ERROR MESSAGE AND ANY ASSOCIATED BASIC ERROR MESSAGES.

3.2 SPECIFIC ERROR MESSAGES

ERROR MESSAGES ARE HANDLED VIA A DICTIONARY STRUCTURE.

WORDS AND PHRASES ARE MULTIPLY REFERENCED USING ONLY ONE COPY OF THE WORD OR PHRASE IN CORE.

THIS PERMITS EXTENSIVE ERROR MESSAGE PRINTING AT MINIMAL STORAGE REQUIREMENTS.

THE FOLLOWING DEMONSTRATES TYPICAL ERROR MESSAGES:

ML11 DVC FTL ERR 00077 ON UNIT 07 TST 027 SUB 002 PC: 050432 ASYNCHRONOUS MODULE FAILURE EXCESSIVE DATA ERRORS DURING INITIAL ARR RD_WRT

ML11 DVC FTL ERR 00112 ON UNIT 03 TST 037 SUB 000 PC: 056466 ASYNCHRONOUS MODULE FAILURE ARRAY ADRS MULTIPLEXER FAILURE FAILED AT DSA: 000000

3.2.1 ERROR NUMBER DEFINITION

ERROR NO.	FAILING LOGIC
1	DRIVE DID NOT RESPOND WITHIN 1.5 US
2	DSA REG READ/WRITE ERROR DURING DRIVE SEL TEST
3	UNIQUE DRIVE SELECTION ERROR
4	ML REGISTER READ WRITE ONES/ZEROES ERROR
5	ML REGISTER READ WRITE ONES/ZEROES ERROR
6	ML REGISTER INITIALIZATION ERROR
7	CONTROL BUS BAD PARITY NOT DETECTED
8	CONTROL BUS GOOD PARITY NOT DETECTED.
9	CONTROL BUS BAD PARITY

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		GENERATED.
	10	ARRAY SIZING LOGIC ERROR
	11	GO BIT NOT CLR AFTER NOOP FUNCTION
	12	ILF BIT SET DURING NOOP FUNCTION
	13	OPI BIT SET DURING NOOP FUNCTION
	14	GO BIT NOT SET DURING WRITE CHECK FUNCTION
	15	DRY BIT NOT CLEAR DURING WRITE CHECK FUNCTION
	16	DRY BIT SET WHEN GO SET DURING WRITE CHECK FUNCTION
	17	ILF SET DURING WRITE CHECK FUNCTION
	18	OPI BIT SET DURING WRITE CHECK FUNCTION
	19	GO BIT NOT CLEAR AFTER WRITE CHECK FUNCTION 'ASYNC FAILURE'
	20	GO BIT NOT CLEAR AFTER WRITE CHECK FUNCTION'
	21	DRY BIT NOT SET AFTER WRITE CHECK FUNCTION
	22	GO BIT NOT CLEAR AFTER WRITE CHECK FUNCTION 'ASYNC FAILURE'
	23	GO BIT NOT CLEAR AFTER WRITE CHECK FUNCTION 'SYNC FAILURE'
	24	GO BIT NOT SET DURING WRITE FUNCTION
	25	DRY BIT CLEAR WITH GO CLEAR DURING WRITE FUNCTION.
	26	DRY BIT SET WITH GO BIT DURING WRITE FUNCTION.
	27	ILF BIT SET DURING WRITE FUNCTION

OPI BIT SET DURING WRITE FUNCTION

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	29	GO BIT NOT CLEAR AFTER WRITE FUNCTION "ASYNC FAILURE"
	30	GO BIT NOT CLEAR AFTER WRITE FUNCTION 'SYNC FAILURE'
	31	DRY BIT NOT SET AFTER WRITE FUNCTION.
	32	GO BIT NOT CLEAR AFTER WRITE FUNCTION "ASYNC FAILURE"
	33	GO BIT NOT CLEAR AFTER WRITE FUNCTION "ASYNC FAILURE"
	34	GO BIT NOT SET DURING READ FUNCTION.
	35	DRY BIT SET WHILE GO CLEAR DURING READ FUNCTION.
	36	DRY BIT SET WHILE GO SET DURING READ FUNCTION.
	37	ILF BIT SET DURING READ FUNCTION.
	38	OPI BIT SET DURING READ FUNCTION
	39	GO BIT NOT CLEAR AFTER READ FUNCTION 'ASYNC FAILURE'
	40	GO BIT NOT CLEAR AFTER READ FUNCTION 'SYNC FAILURE'
	41	DRY BIT NOT SET AFTER READ FUNCTION
	42	GO BIT NOT CLEAR AFTER READ FUNCTION 'ASYNC FAILURE'
	43	GO BIT NOT CLEAR AFTER READ FUNCTION 'SYNC FAILURE'
	44	GO BIT NOT CLEAR AFTER CLEAR FUNCTION
	45	DRY BIT SET WHILE GO SET DURING CLEAR FUNCTION
	46	DRY BIT NOT SET AFTER CLEAR FUNCTION.
	47	ILF BIT SET DURING CLEAR FUNCTION

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USER DOCUMENTATION		ODI DIT CET DIDING CLEAD	
48		OPI BIT SET DURING CLEAR FUNCTION	
50		COMPOSITE ERROR BIT NOT SET AFTER MLER BITS SET	
51		ATA BIT SETTING ERROR	
52		ATTN BIT SETTING ERROR	
53		ATTN BIT NOT CLEARED BY MLCS1 NOOP FUNCTION	
54		ATA BIT NOT CLEAR AFTER WRITING A ONE TO IT	
55		WRITING A ONE TO OTHER DRIVES ATA BIT CLEARED THIS DRIVES ATA BIT	
56		GO BIT NOT CLEARED AFTER SEARCH FUNCTION	
57		ILF BIT SET DURING SEARCH FUNCTION	
58		NO-OP FUNCTION DID NOT CLEAR THE ATA BIT AFTER BEING SET	
.59		ATA BIT NOT SET AFTER SEARCH FUNCTION	
. 60		OPI BIT SET DURING SEARCH FUNCTION AT PRESENT ARRAYS	
61		ATA BIT SET AFTER SEARCH AT NOT PRESENT ARRAYS.	
62		GO BIT NOT CLEAR AFTER READ-IN-PRESET FUNCTION	
43		ILF BIT SET DURING READ-IN-SET FUNCTION	
64		OPI BIT SET DURING READ-IN-PRESET FUNCTION	
65		UV BIT NOT SET AFTER READ-IN-PRESET	
, 66		GO BIT NOT CLEARED AFTER ILLEGAL FUNCTION	
67		ILLEGAL FUNCTION NOT DETECTED	

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68		OPI BIT SET WITH ILLEGAL FUNCTION
69		RMR BIT NOT SET AFTER MODIFYING REG WITH FUNCTION IN PROGRESS.
70		MEMORY ARRAY PROM CHECK SUM ERRORS DURING INITIAL PROM READS
71		NIBBLE OFF SET COUNTS GREATER THAN 14 DETECTED.
72		UNS BIT SET WITH GOOD
73		UNS BIT SET WITH GOOD
74		UNS BIT NOT SET WITH BAD UV DATA
75		UNS BIT NOT SET WITH BAD
76		MEMORY ARRAY PROM ROW/ COL DATA ORING ERROR
. 77		BAD NIBBLE THRESHOLD OF 36 EXCEEDED DURING INITIAL ARRAY READ/WRITE TEST
5 78		UNIQUE PROM SELECTION ERROR
79		FAILURE TO FIND GOOD ROW DURING READ WRITE ARRAY WITH PROM DATA
80		MEMORY ARRAY TIMING AND CONTROL FAILURE TO REFRESH MEMORY
81		DATA ERRORS DETECTED AT LAST BLOCK DURING ADDRESSES COUNTER TEST. (TEST ABORTED)
82		ADDRESS COUNTER ERROR
83		UNIQUE MEMORY ARRAY MODULE SELECTION FAILURE
84		ALL BITS IN ALL NIBBLES TESTED DURING SEQUENCER EXISTENCE TEST WERE IN

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		ERROR (FAIL UNIT)
8	15	INTERMEDIATE FAILURE. SOME BITS IN NIBBLES TESTED WERE IN ERROR (CONTINUE TESTING)
8	16	SYNC BUS DATA BIT WRITE PATH CONTINUITY FAILURE
8	17	SYNC BUS DATA BIT READ PATH CONTINUITY FAILURE.
8	8	RAM BUS ADRS COUNTER FAILURE TO LOAD/UNLOAD SKIP RAM DURING WRITE FUNCTION
8	9	RAM BUS ADRS COUNTER FAILURE TO LOAD/UNLOAD SKIP RAM DURING READ FUNCTION.
9	0	SYNC DATA BUS WRITE PATH UNIQUE DATA BIT FAILURE (ALL ONES NIBBLE PATTERN)
9	1	SYNC DATA BUS WRITE PATH UNIQUE DATA BIT FAILURE (SHIFTED BIT NIBBLE PATTERN)
9	2	SYNC DATA BUS WRITE PATH UNIQUE DATA BIT FAILURE (ALL ONES NIBBLE PATTERN)
9	3	SYNC DATA BUS READ PATH UNIQUE DATA BIT FAILURE
9	4	NIBBLE OFF SET COUNTERS FAILURE
9	95	CS1 FUNCTION ABORT FAILURE DURING CLASS 'A' ERROR.
9	6	CS1 FUNCTION ABORT FAILURE DURING CLASS 'B' ERROR
9	7	LBT BIT SET BEFORE A LAST BLOCK TRANSFER
. 9	8	DSA REGISTER INCREMENT

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	FAILURE DURING NON LAST BLOCK TRANSFERS.
99	LBT BIT NOT CLEAR AFTER LOADING DSA REG
100	LBT BIT NOT SET AFTER A LAST BLOCK TRANSFER
101	DSA REGISTER INCREMENT FAILURE AFTER A LAST BLOCK TRANSFER
102	IAE BIT NOT SET AT INVALID SECTOR ADDRESSES
103	ACE BIT NOT SET AFTER ADDRESS OVERFLOW
104	SC BIT NOT AFTER CS1 FUNCTION ADORT
105	GOOD DATA BUS PARITY NOT DETECTED
106	GOOD DATA BUS PARITY NOT GENERATED
107	UNS BIT SET AFTER WRITING TO A SECTOR DURING PROM DATA TEST
108	UNS BIT NOT SET WITH BAD UV DATA
109	WCE BIT SET DURING MBUS WRITE/READ FUNCTION TROUBLE SHOOTING LOOP TEST
110	UNIQUE REGISTER SELECTION TEST FAILURE
· 111	FAILURE TO FIND GOOD MOS RAM ROW DURING ARRAY ADRS MUX TEST (INTERMEDIATE DIAG MSG)
112	UNIQUE ARRAY MODULE ROW/COL ADDRESSING FAILURE
113	DRIVE TYPE REGISTER VALUE

USER DOCUMENTATION	CZMLABO ML-11 LOGIC TEST USER DOCUMENTATION	MACRO M1113	22-DEC-80 12:16	PAGE 2-16
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-	004	-
SEQ	$(\mathbf{R})^{T}$	×
35.4	VV I	•

E2
ND
E2 ORS
E2 E ERRORS
N READ
LATCH MATION
CLEAR
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TA
ET
RORS
E N RE

CZMLABO ML-11 LOGIC TEST USER DOCUMENTATION	MACRO M1113 22-DEC-80 12:16 PAGE 2-17
132	UNIQUE NIBBLE CRC GENERATION CODE FAILURES
133	UNIQUE WORD CRC GENERATION CODE FAILURES
134	CORRECTABLE ERROR SYNDRONE DECODE FAILURE. FAILURE TO COMPLIMENT EXPECTED FAILING BIT(S)
135	CORRECTABLE ERROR SYNDRONE DECODE FAILURE. UNEXPECTED BIT(S) FOUND COMPLIMENTED
136	ECH OR UNC BIT NOT SET DURING READS WITH ECC ERRORS IN CHANNEL > 35
137	ERROR CORRECTION WAS NOT INHIBITED DURING UNCORRECTABLE ECC ERRORS. UNEXPECTED BIT(S) WERE FOUND COMPLIMENTED
138	ECH OR UNC BIT NOT SET WHEN MULTIPLE CHANNEL ERRORS WERE READ
139	ERROR CORRECTION WAS NOT INHIBITED DURING UNCORRECTABLE ECC ERROR. UNEXPECTED BIT(S) WERE FOUND COMPLIMENTED
140	ECC FAILURE TO DETECT AND CORRECT SINGLE BIT CHANNEL ERRORS
141	ECC FAILURE TO DETECT AND CORRECT MULTIPLE BIT CHANNEL ERRORS

4.0 PERFORMANCE AND PROGRESS REPORTS

AT THE END OF EACH PASS, THE PASS COUNT IS GIVEN ALONG WITH THE TOTAL NUMBER OF ERRORS REPORTED SINCE THE DIAGNOSTIC WAS STARTED. THE 'EOP' SWITCH CAN BE USED TO CONTROL HOW OFTEN THE END OF PASS MESSAGE IS PRINTED. SECTION 2.2 DESCRIBES SWITCHES.

5.0 DEVICE INFORMATION TABLES

HARDWARE DEFAULT PTABLE

. WORD	176400	:RH ADDRESS
. WORD	70	RH TYPE
. WORD	204	RH VECTOR ADDRESS
. WORD	70 204 16.	NUMBER OF ARRAY MODULES
. WORD	1	:IS DRVE OPTION ML11A, 1=16K, 0=64K
. WORD	0	:ML-11 DRIVE NUMBER
. WORD	0	; IS PARITY DISABLED, 1=YES, 0=NO

SOFTWARE DEFAULT TABLE

PRSN: .WORD 0

;PRINT SERIAL NUMBER, 1=YES, 0=NO

6.0 TEST SUMMARIES

TST1. MASS BUS READY

TEST THE RH CONTROLLER FOR EXISTANCE

TST2. MASS BUS HANDSHAKE

TEST MASS BUS ---> UNIBUS COMMUNICATIONS

TST3. DRIVE PRESENT

TEST TO SEE IF THE DRIVE UNDER TEST EXIST.

TST4. DRIVE SELECTION

SEE IF SELECTING OTHER DRIVES ON RH EFFECTS DRIVE UNDER TEST.

TST5. READ WRITE REGISTER ONE'S ZERO'S TEST

TEST REGISTERS READ WRITE CAPABILITY AND UNIQUENESS

TST6. READ WRITE REGISTER SHIFTING ONE'S AND ZEROES

TEST REGISTERS FOR UNIQUE DATA BITS.

TST7. REGISTER INITIALIZATION

TEST REGISTERS FOR CORRECT INIT DATA.

TST8 REGISTER SELECTION TEST

TEST FOR UNIQUE REGISTER SELECTION

TST9. PRINT DRIVE SERIAL NUMBER

PRINT THE CONTENTS OF MLSN IF THE SOFTWARE QUESTION WAS ANSWERED 'YES'.

TST10. C-BUS PARITY

TEST IF DRIVE CAN DETECT BAD PARITY ON C-BUS AND GENERATE GOOD PARITY.

TST11. MEMORY SIZING

SEE IF MEMORY SIZING LOGIC DETECTS AND RECORD CORRECT NUMBER OF ARRAYS PRESENT.

TST12. NO-OP FUNCTION

SEE IF A NO-OP FUNCTION CAN BE EXECUTED.

TST13. WRITE CHECK FUNCTION

SEE IF A WRITE CHECK FUNCTION CAUSES THE DRIVE TO HANG.

TST14. WRITE FUNCTION

SEE IF A WRITE FUNCTION CAUSES THE DRIVE TO HANG.

TST15. READ FUNCTION

SEE IF A READ FUNCTION CAUSES THE DRIVE TO HANG.

TST16. CLEAR FUNCTION

SEE IF A CLEAR FUNCTION CAN BE EXECUTED.

TST17. DIAGNOSTIC REGISTER READ WRITE

TEST THE DIAGNOSTIC REGISTERS FOR 1'S/0'S , SHIFTING 1'S/0'S AND INITIALIZATION

TST18. COMPOSITE ERROR BIT TEST

SEE IF EACH INDIVIDUAL ERROR BIT IN MLER CAUSES A COMPOSITE ERROR.

TST19. ATA BIT

TEST IF THE ATA BIT CAN BE SET AND CLEARED.

TST20. SEARCH FUNCTION

SEE IF A SEARCH FUNCTION CAN BE EXECUTED ON ALL PRESENT ARRA-

TST21. READ IN PRESET

TEST IF A READ IN PRESET FUNCTION SETS VOL V H.

TST22. ILLEGAL FUNCTION

SEE IF WRITING AN ILLEGAL FUNCTION TO CS1 CAN BE DETECTED AND THAT A TRANSFER IS NOT INITIATED.

TST23. REGISTER MODIFICATION REFUSED

TEST TO SEE IF WRITING TO SPECIFIC REGISTERS ARE ABORTED WHILE THE DRIVE IS ACTIVE. SEE IF WRITING TO NON-SPECIFIC REGISTERS ARE ALLOWED WHILE DRIVE IS ACTIVE.

TST24. INITIAL PROM TEST

TEST PROMS FOR EXISTENCE.

TST25. PROM 'OR' FUNCTION TEST

TEST THE PROM DATA ORING FUNC

TST26. UV ERROR TEST

TEST ABILITY OF UV ERR PROMS TO DETECT ALL POSSIBLE CHECK SUM ERRORS.

TST27. INITIAL ARRAY TEST
TEST ARRAY TIMING AND CONTROLFOR EXISTENCE.

TST28. PROM SELECTION TEST
TEST FOR UNIQUE PROM SELECTION.

TST29. READ WRITE MEMORY ARRAY WITH PROM DATA (DIAG MODE)

SEE IF MEMORY CAN BE WRITTEN AND READ.

ALSO FIND ERROR FREE BLOCK OF MEMORY FOR FUTURE TESTS.

TST30. REFRESH TIMING

TEST TO SEE IF MEMORY CAN BE REFRESHED.

TST31. ADDRESS COUNTER

TEST THE ADDRESS COUNTER FOR ABILITY TO COUNT THROUGH ALL POSSIBLE MEMORY ADDRESSES.

TST32. ARRAY MODULE SELECTION

TEST FOR UNIQUE ARRAY MODULE SELECTION

TST33. SEQUENCER EXISTENCE TEST

TEST TO SEE IF BASIC SEQUENCER TIMING EXISTS.

TST34. SYNC DATA BUS CONTINUITY/WRITE PATH

TEST SYNCHRONOUS DATA BUS WRITE PATH FOR CONTINUITY BY READING WRITING ONE'S AND ZERO'S.

TST35. SYNC DATA BUS CONTINUITY/READ PATH

TEST SYNCHRONOUS DATA BUS READ PATH FOR CONTINUITY BY READING WRITING ONE'S AND ZEROES.

TST36. RAM-BUS ADDRESS COUNTER/WRITE PATH

TEST ABILITY OF THE RAM-BUS ADDRESS COUNTERS TO LOAD/UNLOAD THE SKIP DURING WRITE FUNCTIONS.

TST37. RAM BUS ADRS COUNTER/READ PATH

TEST ABILITY OF RAM/BUS ADRS COUNTERS TO LOAD/UNLOAD THE SKIP RAM DURING READ FUNCTIONS.

TST38. SYNC DATA BUS BIT UNIQUENESS/WRITE PATH

TEST SYNCHRONOUS DATA BUS FOR DATA BIT UNIQUENESS BY WRITING SHIFTING PATTERNS OF ONE'S AND ZERO'S TO THE ML.

TST39. SYNC DAT BUS BIT UNIQUENESS/READ PATH

TEST SYNCHRONOUS DATA BUS READ PATH FOR DATA BIT UNIQUENESS BY WRITING SHIFTING PATTERNS OF ONES AND ZEROES TO THE

TST40. ARRAY ADDRESS MUX

TEST FOR UNIQUE ROW AND COLUMN ADDRESSING

TST41. NIBBLE OFFSET

TEST NIBBLE OFFSET COUNTERS TO COUNT TO 14 NIBBLE DATA TO BE SHIFTED ON DETECTION OF BAD NIBBLES.

TST42. CS1 FUNCTION ABORT

SEE IF A CLASS 'B' ERROR ABORTS A FUNCTION WHILE IN PROGRESS.

SEE IF A CLASS'A' ERROR IS DETECTED BUT FUNCTION IS ALLOWED TO COMPLETE.

TST43. LAST BLOCK INDICATOR

TEST THE LAST BLOCK INDICATOR BIT FOR NOT SETTING BELOW THE LAST AND SETTING AND CLRING AT THE LAST BLOCK

TST44. INVALID ADDRESS TEST

FOR ALL ILLEGAL DSA ADDRESSES READ THE IAE BIT SET.

TST45. ADDRESS OVERFLOW

TEST FOR AGE ON TRANSFERS WHICH EXTEND BEYOND THE LAST BLOCK.

TST46. SYNC BUS PARITY

TEST FOR BAD PARITY DETECTION AND GOOD PARITY GENERATION.

TST47. WRITE READ MEMORY ARRAY (M-BUS BLOCK MODE)

WRITE READ MEMORY VIA M-BUS BLOCK WITH MINIMUM OVERHEAD

TST48. TEST THE CRC DATA BUS BETWEEN THE CRC GENERATORS AND THE CRC/MBUS DATA MUX FOR CONTINUITY AND BIT UNIQUENESS

TST49. TEST CRC CODES GENERATED FOR ONE CRC GROUP (52 UNIQUE NIBBLES)

TST50. TEST CRC CODES GENERATED FOR ONE

CRC GROUP (13 UNIBUS WORDS)

- TST51. TEST SYNDROME DECODE AND ERROR CORRECTION TO DECODE AND CORRECT SINGLE BIT AND MULTIPLE BIT CHANNEL ERRORS
- TST52. TEST SYNDRONE DECODE TO DETECT BUT NOT CORRECT UNCORRECTABLE CHANNEL ERRORS
- TST53. TEST SYNDRONE DECODE TO DETECT BUT NOT CORRECT UNCORRECTABLE MULTIPLE CHANNEL ERRORS
- TST54. TEST SYNDRONE GENERATION, SYNDRONE DECODE AND ERROR CORRECTION FOR SIGLE BIT CHANNEL ERRORS
- TST55. TEST SYNDRONE GENERATION, SYNDRONE DECODE AND ERROR CORRECTION FOR MULTIPLE BIT CHANNEL ERRORS
- TST56. TEST THE ECC ERROR REGISTER FOR CLEARING AND LATCHING OF ECC ERROR INFORMATION ON DETECTION OF ECC ERRORS
- TST57. TEST THE ECC ERROR LOCATION REGISTER FOR CLOCKING, BIT UNIQUENESS, CLEARING AND LATCHING
- TST58. VIA ECC_DM AND ECC_DIS TEST THE ECH BIT FOR SETTING AND NOT SETTING
- TST59. VIA ECC_EN, ECC_DM AND ECC_DIS TEST THE DCK BIT FOR SETTING AND NOT SETTING
- TST60. PROM DATA TEST

VERIFY THAT CHECK SUM VALUES FOR ALL PROM LOCATIONS ARE CORRECT.

CZMLABO PROGRAM	ML-11 L HEADER	OGIC TEST MACRO	M1113 22-DEC-80 12:16 PAGE 4 2
.1			.SBTTL PROGRAM HEADER AND TABLES
35 36	000000	002000	.ENABL ABS.AMA 2000
39 40	002000		BGNMOD
42			THE PROGRAM HEADER IS THE INTERFACE BETWEEN THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
46 47	002000		POINTER AL'.
64 65 66	002000		HEADER ML11,A,0,1800.,0
77 78 79			NAMES OF DEVICES SUPPORTED BY THIS PROGRAM
80 81 82	002122		DEVTYP ML-11>
83 84 85 86	000000 002000 002000 002122 002130		TEST DESCRIPTION
88 89 90	002130		DESCRIPT <ml-11 logic="" test=""></ml-11>
91 92 93 94 95 96			THE GLOBAL ERROR TABLE (INFORMATION USED IN A CALL TO THE MACRO "ERROR")
	002152 002152 002154 002156 002160	000000 000000 000000 000000	ERRTBL ERRTYP:: .WORD 0 ERRNBR:: .WORD 0 ERRMSG:: .WORD 0 ERRBLK:: .WORD 0
97 98 99 100 101 102 103			THE DISPATCH TABLE CONTAINS THE STARTING ADDRES OF EACH TEST.
104	002162		DISPATCH 60
111 112 113 114 115 116 117 118			THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF THE TEST-DEVICE PARAMETERS. THE STRUCTURE OF THIS TABLE IS IDENTICAL TO THE STRUCTURE OF THE HARDWARE P-TABLES. AND IS USED AS A 'TEMPLATE' FOR BUILDING THE P-TABLES.

ENDSW

161

162 002400 163

MSGS1,0,1,YES GPRML

.EVEN

ENDSFT

MSGS1:

122

002720

002726

002726

120

.ASCIZ /PRINT SERIAL NO.?/ .EVEN

; PRINT DRIVE SERIAL NUMBER?

ML3				.SBTTL	MISCELLANE	OUS CODING SE 17-Oct 29-Sep	CTION -1980 11:31: -1980 10:13:	46 18	TOPS-20 Bliss-16 V2(206) PA: <neale>ML3.BLI.2 (1)</neale>	
,	0001 0002 0003 0004 0718	MODULE ML3 = BEGIN REQUIRE 'MACRO.	REQ";							
	0719 0720 0721 0722	THE REPORT C	ODING SEC	TION CO	NTAINS THE STATISTICA	L REPORTS.				
:	0722 0723 0724 0725 0726	BGNRPT; RETURN; ENDRPT;			,					
004152	000207		LRPT:	RTS	PC		,			071
			; Routing ; Maximu	e Size: m stack	1 word depth per	invocation:	0 words			
004154 004160 004162	004767 104425 000207	177772		JSR TRAP RTS	PC,LRPT 25 PC		;			072
			; Routing ; Maximu	e Size: m stack	4 words depth per	invocation:	0 words			
:	0727									
	0728 0729 0730 0731 0732 0733	THE AUTODROP CODE IF THE CHECKED TO SE DROPPED FROM	'ADR' FLAG	WAS SE	T. THE UN	LY AFTER THE IIT(S) UNDER T HOSE THAT DON OR THOSE THAT	EST ARE IMMED	IATELY		
	0735 0735 0736 0737	BGNAUTO; RETURN; ENDAUTO;								
004164	000207		LAUTO:	RTS	PC		:			0726
			; Routing ; Maximu	e Size: m stack	1 word depth per	invocation:	0 words			
	004767									

004172	104461 000207			RTS	61 PC				
			; Routi	ne Size:	4 words depth per in	vocation:	0 words		
ML3	0738					17-0ct 29-Sep	-1980 11:31:46 -1980 10:13:18	TOPS-20 Bliss-16 V2(206) PA: <neale>ML3.BLI.2 (1)</neale>	
004172 004174 004174	0739 0740 0741 0742 0743 0744 0745	!+ ! THE DROP-UNIT ! TO NO LONGER !- BGNDU; RETURN; ENDDU;	SECTION BE TESTE	CONTAIN	S THE CODING	THAT CAUSE	S A DEVICE		
004176	000207		LDU:	RTS	PC		;		
				ne Size: num stack	1 word depth per in	vocation:	0 words		
9 004204	004767 104453 000207	177772	L\$DU::	JSR TRAP RTS	PC,LDU 53 PC				
8					4 words depth per in	vocation:	0 words		
:	0747								
	0748 0749 0750 0751 0752	THE ADD-UNIT	SECTION D IN CON YCLE.	CONTAINS JUNCTION	ANY CODE THE	PROGRAMME ING OF A U	R WISHES NIT BACK		
ML3						17-0ct 29-Sep	-1980 11:31:46 -1980 10:13.18	TOPS-20 Bliss-16 V2(206) PA: <neale>ML3.BLI.2 (1)</neale>	
004206 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	0753 0754 0755 0756	BGNAU; RETURN; ENDAU;							
004210	000207		LAU:	RTS	PC		:		
5			; Routi	ne Size: num stack	1 word depth per in	vocation:	0 words		

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 43-2

198 004212 004767 177772 L\$AU:: JSR PC.LAU ; TRAP 52 RTS PC ; Routine Size: 4 words ; Maximum stack depth per invocation: 0 words 208 208 ; Maximum stack depth per invocation: 0 words 210 : 0757 END 211 : 0758 ELUDOM 212 212 : 0759 ELUDOM 213 : 212 212 213 222 223 224 225 226 227 228 229 230 : Size: 20 code + 0 data words ; Run Time: 00:07.1 ; Memory Used: 12 pages ; Compilation Complete

SEQ 0031

0755

TOPS PA:<

```
6 :ML4
7 :
                                                                                                                                  22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                             TOPS-20 Bliss-16 V2(212)
                                                                                                                                                                                 PA: <NEALE BL3ML4.BL1.2 (1)
                                    MODULE ML4 =
101121314151617181920122232452627829331323334536378394412434454647849551525354556578596
                                    BEGIN
                                       PRETTY BLF COMMANDS
                        0005
0006
0007
0008
0009
0010
0011
0012
1496
1497
1500
1501
1502
1503
1504
1505
1506
1507
1508
1509
                                     !<BLF/NOERROR>
                                     ! BLF/LOWERCASE_KEY>
                                     ! REQUIRE
                                   require 'BLSMAC.REQ':
                                                                                                                                  !BLISS INTERFACE MODULE
                                    CONSTANT LITERALS
                                   literal
                                            ONE = 1,
ONES = %0'177777',
ZERO = 0,
                                                                                                                                   !DATA BIT OF ONE !DATA PATTERN OF ONES
                                                                                                                                  !DATA BIT OF ZERO
!DATA PATTERN OF ZEROES
!NUMBER OF BLOCKS IN GLOBAL STORAGE 'ML-REG'
!FIELD SIZ FOR FIELD DECLARATIONS 'WORD_MAP'
                                            ZEROES = 0,
NUM_OF_REG = 22,
FIELD_SIZ = 4,
                                             !MLCS1 FUNCTION CODES
                                           NOOP = 1,

DRV CLR = %o'11',

RD IN PRE = %o'21',

SEARCH = %o'31',

WRT_CHK = %o'51',

write = %o'61',

read = %o'71',
                                                                                                                                  NOOP FUNCTION
DRIVE CLEAR FUNCTION
READ IN PRESET FUNCTION
                                                                                                                                   SEARCH FUNCTION
                                                                                                                                   WRITE CHECK FUNCTION
                                                                                                                                   !READ FUNCTION
                                             ! DELAY ARGUMENTS
                                            ONE US = 1,
FRTY US = 40,
TWO_TH_US = 2000;
                                                                                                                                  !ONE MICRO SECOND DELAY
                                                                                                                                   !FORTY MICRO SECOND DELAY
                                                                                                                                  !TWO THOUSAND MICRO SECOND DELAY
                         1524
1525
1526
1527
1528
1529
1530
1531
1532
                                       FIELD DECLARATIONS
                                   field
                                            WORD MAP =
                                                                                                                                  !MAPS GLOBAL STORAGE 'ML_REG' INTO REGISTER PERSONALITIES
                                                   REGISTER_ADD = [0, 0, 16, 0],

FORCE_HI = [1, 0, 16, 0],

FORCE_LO = [2, 0, 16, 0],

DONT_CARE = [3, 0, 16, 0]
                                                                                                                                  !REGISTERS ADDRESS
!REGISTERS FORCED HI BITS
!REGISTERS FORCED LO BITS
!REGISTERS IGNORE BITS
```

```
62 :ML4
63:
66: 1537 NIB_MAP =
67: 1538 set
68: 1539 NIB_0 = [0, 0, 4, 0],
69: 1540 NIB_1 = [0, 4, 4, 0],
70: 1541 NIB_2 = [0, 8, 4, 0],
71: 1542 NIB_3 = [0, 12, 4, 0],
72: 1543 NIB_4 = [1, 0, 4, 0],
73: 1544 NIB_5 = [1, 4, 4, 0],
74: 1545 NIB_6 = [1, 8, 4, 0],
75: 1546 NIB_7 = [1, 12, 4, 0],
76: 1547 NIB_8 = [2, 8, 4, 0],
77: 1548 NIB_9 = [2, 12, 3, 0]
78: 1549 tes,
79: 1550 DT1_FLD =
80: 1551 set
81: 1552 E0_5 = [0, 0, 6, 0],
82: 1553 CRC_DATA = [0, 13, 1, 0],
84: 1555 SGL_DATA = [0, 14, 1, 0],
85: 1556 UNC_DATA = [0, 15, 1, 0],
86: 1557 EE_DATA = [0, 0, 6, 0],
87: 1558 A0_5 = [1, 0, 6, 0],
88: 1559 PO_5 = [1, 0, 6, 0],
89: 1560 ECR_DATA = [1, 12, 1, 0],
89: 1561 B0_5 = [2, 0, 6, 0]
91: 1562
92: 1563
```

```
22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (1)
```

!MAPS OWN STORAGE NIB_SAVE INTO TEN FOUR EIT NIBBLES

```
!NIBBLE 0 BITS <0:3>
!NIBBLE 1 BITS <4:7>
!NIBBLE 2 BITS <8:11>
!NIBBLE 3 BITS <12:15>
!NIBBLE 4 BITS <16:19>
!NIBBLE 5 BITS <20:23>
!NIBBLE 6 BITS <24:27>
!NIBBLE 7 BITS <28:31>
!NIBBLE 8 BITS <32:35>
!NIBBLE 9 BITS <36:39>
```

```
95 :ML4
96 :
97
98 :
99 :
100 :
                                                                                                                                                                                                                                                                                                                                                                                                                                                        22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 
22-Dec-1980 09:21:22 PA:<NEALE>BL3ML4.BLI.2 (2)
                                                                                                                                              OWN STORAGE
 NIB_SAVE : block [3] field (NIB_MAP) volatile,

!STORAGE LOCATION TO SAVE NIBBLE DATA READ DURING DIAG MODE

!STORAGE LOCATION TO SAVE NIBBLE DATA READ DURING DIAG MODE

!STORAGE LOCATION TO SAVE NIBBLE DATA READ DURING DIAG MODE

!STORES HARDWARE ORED PROM DATA DURING PROM OR FUNC TEST

PTBL PTR : volatile,

OPERATORS NUMBER OF ARRAY INPUTTED

ARRAY SELECTION INCREMENT VALUE

GOOD BLK: volatile,

CHIP SIZ: volatile,

LAST ADDRESSABLE BLOCK

!MAX MUMBER OF ARRAY ALLOWED

!LAST ADDRESSABLE ARRAY

ID BUF : volatile,

STACK : volatile,

STACK OF FISE STORAGE LOCATION

STACK OF FISE STORAGE LOCATION

STACK OF FISE BYTE LOCATIONS

PD TEMP : bitvector [16] volatile,

PROM DATA STORAGE LOCATION DURING DIAG MODES

!STORES MORD COUNT FOR 16K OR 64K XFERS

!PROM DATA STORAGE LOCATION DURING DIAG MODES

!STORES MORD COUNT FOR 16K OR 64K XFERS

!PROM DATA STORAGE LOCATION DURING DIAG MODES

!STORES MORD COUNT FOR 16K OR 64K XFERS

!PROM DATA STORAGE LOCATION DURING DIAG MODES

!STORES MORD COUNT FOR 16K OR 64K XFERS

!PROM DATA STORAGE LOCATION DURING DIAG MODES

!STORES MORD COUNT FOR 16K OR 64K XFERS

!PROM DATA STORAGE LOCATION DURING DIAG MODES

!STORES MORD COUNT FOR 16K OR 64K XFERS

!PROM DATA STORAGE LOCATION DURING DIAG MODES

!STORES MORD COUNT FOR 16K OR 64K XFERS

!PROM DATA STORAGE LOCATION DURING DIAG MODES

!STORES MORD COUNT FOR 16K OR 64K XFERS

!PROM DATA STORAGE LOCATION DURING DIAG MODES

!PROM DATA STOR
                                                                                                                              OWN
                                                                                                                                                     NIB_SAVE : block LSJ field (NIB_MAP) volume

HW OR TBL : vector [127] volatile,

PTBL PTR : volatile,

OP_NOM_ARR : volatile,

GOOD BLK : volatile,

CHIP SIZ : volatile,

LST_BLK : volatile,

LST_BLK : volatile,

LST_ARR : volatile,

IO BUF : vector [256] volatile,

STR_OFF : vector [10, byte] volatile,

STR_OFF : vector [16] volatile,

PD_TEMP : bitvector [16] volatile,

W_C_SIZE : volatile,

W_C_SIZE : volatile,

WT_DATA : volatile,

DRIVE TYPE : volatile,

LST_DOT_REG : volatile,

REG_INIT_FLG : initial (0) volatile,

A_CAL : volatile,

B_CAL : volatile,

B_GEN : volatile,
                                                                                             1577
                                                                                             1578
                                                                                           1579
                                                                                           1580
                                                                                          1581
1582
1583
1584
1585
1586
1587
1588
1589
1590
                                                                                                                                                                                                                                                                                                                                                                                                                                                    STORES WORD COUNT FOR 16K OR 64K XFERS
ROW ADRS STROBE INCREMENT
SAVE WRITE DATA DURING REG READ WRITE TEST
SAVE READ DATA DURING REG READ WRITE TEST
DRIVE TYPE STORAGE LOCATION
LAST ML-11 REG INDEX FOR THIS TYPE RH CONTROLLER
FLAG TO DETECT DOING REG INIT TEST
CALCULATED CRC STORAGE LOCATION
CALCULATED CRC STORAGE LOCATION
GENERATED CRC STORAGE LOCATION
GENERATED CRC STORAGE LOCATION
GENERATED CRC STORAGE LOCATION
GENERATED CRC STORAGE LOCATION
                                                                                             1591
                                                                                           1598
1599
                                                                                           1600
1601
1602
1603
                                                                                                                                   ! <BLF/NOFORMAT>
                                                                                                                                                    ML_REG: blockvector [NUM_OF_REG,FIELD_SIZ] field(WORD_MAP) !ML11 REGISTERS
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 STRUCTURE TO STORE INDIVIDULA REGISTER PERSONALITIES
                                                                                                                                                                      preset (
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 ie. REGISTER ADDRESSES
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 BITS FORCED HIGH
                                                                                           1607
1608
1609
1610
1611
1612
1613
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 BITS FORCED LOW
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 !READ ONLY BITS
                                                                                                                                                                                     [0,FORCE_HI] = %0'004000', !MLCS1
                                                                                                                                                                                     [0,FORCE_LO]
[0,DONT_CARE]
                                                                                                                                                                                                                                                                                    = %o'173701'.
= %o'160200'.
                                                                                                                                                                                   [5,FORCE_HI]
[5,FORCE_HI]
[5,DONT_TARE]
                                                                                                                                                                                                                                                                                      = %0'25077'
= %0'010600'
= %0'000100',
                                                                                                                                                                                                                                                                                                                                                                                                       !MLDS
```

1 :ML4		22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 22-Dec-1980 09:21:22 PA: <neale>BL3ML4.BLI.2 (3)</neale>
4: 1	1765 !	
5:	1765 ! 1766 EQUALS; 1767 !	
7: 1	768 MACRO DEFINITIONS 1769 1770 1771 macro 1772 1773 REGISTER NAMES:	
ó : 1	771 macro	
2 : 1	773 REGISTER NAMES:	
3:	1774 ! 1775 MLCS1 =	아이들이 나는 아이들은 아이들이 얼마나 얼마나 아니는 아이들이 살아 먹었다면 했다.
5: "1	1776 _ML REG [O_REGISTER ADD]%.	!CONTROL AND STATUS REGISTER 1
6 : M	1777 PLWC = 1778 .ML REG [1, REGISTER_ADD]%, 1779 PLBA =	!WORD COUNT REGISTER
8 : M 1	779 MLBA = 1780 .ML REG [2,REGISTER_ADD]%, FLDA = 1781 FLDA = 1781	!UNIBUS ADDRESS REGISTER
890123456789012345678901234567890123456789012345	.ML REG [1, REGISTER_ADD]%, T79	!DESIRED ADDRESS REGISTER
2 : M	782	!CONTROL AND STATUS REGISTER 2
5 : M	786 .ML_REG [5, REGISTER_ADD]%,	!DRIVE STATUS REGISTER
6 : M 1	788 .ML REG [6, REGISTER_ADD]%,	!ERROR REGISTER
88 : M 1	790 .ML REG [7, REGISTER_ADD]%,	!ATTENTION SUPPARY REGISTER
0 : M :	792 .ML REG [8, REGISTER_ADD]%, 1793 MLPA =	!LOOK AHEAD REGISTER
3 M	793 MLPA =	!PROM ADDRESS REGISTER
5	796 .ML_REG [9,REGISTER_ADD]%,	!DATA BUFFER REGISTER
6 ; M	1790 .ML_REG LIU, REGISTER_ADDJX,	!MAINTENANCE REGISTER
9;	1799 MLDT = 1800 .ML_REG [11, REGISTER_ADD]%,	!DRIVE TYPE REGISTER
1 :	1801 FLSN = 1802 .ML REG [12, REGISTER_ADD]%,	!SERIAL NUMBER REGISTER
3	1803 PLE1 = 1804 .ML REG [13, REGISTER_ADD]%,	!ECC CRC WORD REGISTER 1
5 M	1805 PLE2 = 1806 .ML REG [14, REGISTER_ADD]%,	!ECC CRC WORD REGISTER 2
6 : M	## REG [11, REGISTER_ADD]%, ## REG [12, REGISTER_ADD]%, ## REG [12, REGISTER_ADD]%, ## REG [13, REGISTER_ADD]%, ## REG [14, REGISTER_ADD]%, ## REG [14, REGISTER_ADD]%, ## REG [15, REGISTER_ADD]%, ## REG [15, REGISTER_ADD]%, ## REG [16, REGISTER_ADD]%, ## REG [16, REGISTER_ADD]%, ## REG [16, REGISTER_ADD]%, ## REG [16, REGISTER_ADD]%,	!DATA DIAGNOSTIC REGISTER 1
8 : M 1	1809 MLD2 = 1810 .ML REG [16, REGISTER ADD]%.	!DATA DIAGNOSTIC REGISTER 2
M 1	1811 PLEE = 1812 -ML REG [17.REGISTER ADD]X	!ECC ERROR REGISTER
2 : M	.ML REG [15, REGISTER_ADD]%, 1809 ML REG [16, REGISTER_ADD]%, 1810 ML REG [16, REGISTER_ADD]%, 1811 ML REG [17, REGISTER_ADD]%, 1813 ML REG [18, REGISTER_ADD]%, 1815	
4: M	IOIS FILTY -	!ECC ERROR LCOATION REGISTER
));	1816 .ML_REG [19,REGISTER_ADD]%,	!PROM DATA REGISTER

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                          M 1817
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M 1819
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M 1824
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M 1826
M 1826
M 1828
1829
M 1830
1831
                                                  MLBAE =
                                             .ML REG [20, REGISTER_ADD]%,
                                             .ML_REG [21, REGISTER_ADD]%,
                                              BIT ASSIGNMENTS:
                                                 SC =
                                              (MLCS1)<15,1>%,
                                                  TRE =
                                              (MLCS1)<14,1>%,
                                                 MCPE =
                                              (MLCS1)<13,1>%,
                                                  DVA =
                                1831
                                              (MLCS1)<11,1>%,
                           M 1832
1833
M 1834
1835
M 1836
M 1837
                                                 RDY =
                                              (MLCS1)<7,1>%,
                                                  IE =
                                              (MLCS1)<6,1>%,
                                                  GO =
                                             (MLCS1)<0,1>%,
                                              ML FUNC =
(MLCS1)<0,6>%,
                                1839
                           M 1840
1841
M 1842
1843
M 1844
1845
M 1846
1847
M 1848
1849
M 1850
                                                 DLT =
                                              (MLCS2)<15,1>%,
                                                  WCE =
                                              (MLCS2)<14,1>%,
                                                  PE =
                                              (MLCS2)<13,1>%,
                                                  NED =
                                              (MLCS2)<12,1>%,
                                                  NEM =
                                              (MLCS2)<11,1>%,
                                                  PGE =
                           1851
M 1852
1853
M 1854
1855
M 1856
1857
M 1858
                                              (MLCS2)<10,1>%,
                                                 MXF =
                                              (MLCS2)<9,1>%.
                                              MDPE =
(MLCS2) <8,1>%,
                                                 ORDY =
                                              (MLCS2)<7,1>%,
                                                 IRDY =
                           M 1858
1859
M 1860
1861
M 1862
1863
M 1864
1865
M 1866
1867
M 1868
                                              (MLCS2)<6,1>%,
                                                 CLR =
                                              (MLCS2) <5,1>%,
                                                 PAT =
                                              (MLCS2)<4,1>%,
                                                  BAI =
                                              (MLCS2) <3,1>%,
                                             DRV_NUM = (MLCS2)<0,3>%,
                                                  ATTN =
```

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!BUS ADDRESS EXTENSION REGISTER ! CONTROL AND STATUS REGISTER 3

!MLCS1 BIT ASSIGNMENTS

!MLCS2 BIT ASSIGNMENTS

7

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(MLDS)<15,1>%,
COMP ERR =
(MLDS)<14,1>%,
                                      M 1870
M 1871
M 1872
M 1873
M 1874
M 1875
M 1876
M 1876
M 1877
M 1878
M 1880
M 1881
M 1882
M 1883
M 1884
M 1885
M 1886
M 1887
M 1896
M 1897
M 1898
M 1897
M 1898
M 1899
M 1900
M 1901
M 1902
M 1903
M 1904
M 1907
M 1908
M 1909
M 1910
M 1911
M 1912
M 1913
M 1914
M 1918
M 1919
                                                                   MOL =
                                                               (MLDS)<12,1>%,
                                                                   LBT =
                                                               (MLDS)<10,1>%,
                                                                   DPR =
                                                               (MLDS) <8,1>%,
                                                                   DRY =
                                                               (MLDS)<7,1>%,
                                                                   VV =
                                                               (MLDS)<6,1>%,
                                                                   DCK =
                                                              (MLER)<15,1>%,
                                                                   UNS =
                                                               (MLER)<14,1>%,
                                                                   OPI =
                                                               (MLER)<13,1>%,
                                                               IAE = (MLER)<10,1>%,
                                                              AOE =
(MLER)<9,1>%,
ECH_ERR =
(MLER)<6,1>%,
                                                                   DPAR =
                                                               (MLER) <5,1>%,
                                                                   CPAR =
                                                               (MLER) <3,1>%,
                                                                   RMR =
                                                               (MLER) <2,1>%,
                                                                  ILR =
                                                               (MLER)<1,1>%,
                                                                  ILF =
                                                              (MLER)<0,1>%,
                                                              ARR TYP = (MLMR)<10,1>%,
                                                             ML_NUM_ARR = (MLMR)<11,5>%,
                                                            REF_MAR =

(MLMR)<7,1>%,

PROM_RW =

(MLMR)<6,1>%,

PROM_DIS =

(MLMR)<5,1>%,

DAT_CLK =
                                                              DAT_CLK = (MLMR)<4,1>%,
                                                               DAT_DM = (MLMR)<3,1>%,
                                                            DCK_EN =
(MLMR) <2,1>%,
ECC_DIS =
```

22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 22-Dec-1980 09:21:22 PA:</br>
!MLDS BIT ASSIGNMENTS

. !MLER BIT ASSIGNMENTS

!MLMR BIT ASSIGNMENTS

```
22-Dec-1980 109:24:31
22-Dec-1980 09:21:22
                                                                                                                                                        TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (3)
                     M 1923
M 1923
M 1924
M 1925
M 1926
M 1927
M 1930
M 1931
M 1933
M 1933
M 1933
M 1934
M 1935
M 1936
M 1943
M 1943
M 1943
M 1944
M 1945
M 1948
M 1953
M 1964
M 1965
M 1963
M 1964
M 1965
M 1966
M 1967
M 1968
M 1968
M 1968
M 1969
M 1969
M 1969
M 1969
M 1969
                                     (MLMR)<1,1>%,
                                      ECC_DM =
(MLMR)<0,1>%,
DRV_TYP =
                                      (MLDT)<0,1>%,
                                                                                                                 !MLDT BIT ASSIGNMENTS
                                         CRC_A =
                                                                                                    !MLE1 BIT ASSIGNMENTS
                                    (MLE1)<0,6>%,
                                     PAR CRC WRD = (MLE1) <8,65%,
                                         CRC_B =
                                                                                                    !MLF2 BIT ASSIGNMENTS
                                     (MLE2)<0,6>%,
                                      UNC_ERR = (MLEE)<15,1>%,
                                                                                                                 !MLEE BIT ASSIGNMENTS
                                      SGL_ERR = (MLEET<14,1>%,
                                     CRC ERR =

(MLEE)<13,1>%,

BIT IN ERR =

(MLEE)<0,6>%,

CHAN IN ERR =
                                     (MLEE) <6,65%,
                                     MISCELLANEOUS MACRO DEFINITIONS:
                                    (MLSN) <12,4>%,
                                    SN2 =
(MLSN) <8,4>%,
                                          SN1 =
                                     (MLSN) <4,4>%,
                                          SNO =
                                     (MLSN) <0,4>%,
                                         IS_SET =
                                                                                                    !TEST IF BIT IS EQUAL 1
                                    egl 1%,
                                         IS_NOT_SET =
                                                                                                    !TEST IF BIT IS EQUAL 0
                                    egl 0%,
                                         HI =
                                                                                                    !READS REGISTERS FORCED HI BITS FROM PERSONALITY TABLE
                                    ML_REGE.index,FORCE_HIJ%,
                                                                                                    !READS REGISTERS FORCED LO BITS FROM PERSONALITY TABLE
                                    ML_REG[.index,FORCE_LO]%,
                                         IGNORE =
                                                                                                    !READS, REGISTERS DONT_CARE BITS FROM PERSONALITY TABLE
                                    ML_REG[.index,DONT_CARE]%,
MLE2_MASK =
                     M 1970
M 1971
                                                                                                    !READS MLE2 DONT CARE MASK EITHER DATA DIAG OR ECC CIE REG
                                    ML_REG[14,DONT_CARE]%,
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22-Dec-1980 09:21:22
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                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       PA: <NEALE>BL 3ML4.BL1.2 (3)
                                                                                                                                         M 1973
M 1974
1975
M 1976
M 1977
1980
1981
1982
1983
M 1984
M 1985
M 1986
M 1987
M 1991
M 1991
M 1993
M 1994
M 1995
M 1998
M 199
                                                                                                                                                                                                                                                WRT_MASK =
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                !GENERATE MASK DATA PATTERN USING REGISTER FORCE LO, HI AND IGNORE B
                                                                                                                                                                                                                          .IGNORE or ((not .LO) and (.HI or .TST_PAT))%,
                                                                                                                                                                                                                                                CLR MBUS =
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              !CLEAR MASS BUS RESTORE DRIVE NUMBER
                                                                                                                                                                                                               CLR = ONE; DRY_NUM = .ML_DUT%,
!<BLF/SYNONYM IS_SET = EQL 1 * >
!<BLF/SYNONYM IS_NOT_SET = EQL 0 * >
                                                                                                                                                                                                                            DIAGNOSTIC DATA REGISTER MACROS
                                                                                                                                                                                                                                               RD_LNG_WRD =
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          !READ DATA DIAG REGS INTO BIND LOCATIONS
                                                                                                                                                                                                                                         D1_TEMP = .MLD1;
                                                                                                                                                                                                                                       D2 TEMP = .MLD2;
E2_TEMP = .MLE2%,
                                                                                                                                                                                                                                               WRT_LNG_WRD =
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         !LOADS DATA DIAG REG WITH CONTENTS OF BIND LOCATIONS
                                                                                                                                                                                                                                    MLD1 = .D1_TEMP;
MLD2 = .D2_TEMP;
MLE2 = .E2_TEMPX,
TIME_OUT_LOOP =
                                                                                                                                                                                                                                                                                                                                                      !WAIT LOOP DIRING MASS BUS TRANSFER
                                                                                                                                                                                                                                                                                  do
                                                                                                                                                                                                                                                                                                 0
                                                                                                                                                                                                                                                                                 unitil .DRY; %;
                                                                                                                                                                                                               ! BIND DECLARATIONS
                                                                                                                                                                                                               bind
                                                                                                                                                                                                                               ERROR DATA MAPPING FORMATS
                                                                                                                                                                                                                                         FMT_1 = uplit (%asciz'%AEXPECTED: %06%A READ: %06%N%N'),
FMT_2 = uplit (%asciz'%AGOOD DATA: %06%A BAD DATA: %06%A XOR: %06%N%N'),
FMT_3 = uplit (%asciz'%ADRIVE SN: %06%N%N'),
FMT_5 = uplit (%asciz'%ABIT IN ERROR: %06%N%N'),
FMT_5 = uplit (%asciz'%ABIT IN ERROR: %06%N%N'),
FMT_6 = uplit (%asciz'%AGOOD NIB DATA: %02%A BAD NIB DATA: %02%A NIB POS: %0
FMT_7 = uplit (%asciz'%AFAILED AT: %06%N%N'),
FMT_7 = uplit (%asciz'%AFAILED AT SA: %06%N%N'),
FMT_9 = uplit (%asciz'%AFAILED AT DSA: %06%N%N'),
FMT_10 = uplit (%asciz'%ABIT<15:10>: %B6%A BIT<9:0>: %B10%N%N'),
FMT_11 = uplit (%asciz'%AFAILING REG ADRS: %06%N%N'),
FMT_12 = uplit (%asciz'%AFAILING FUNC: %06%N%N'),
FMT_13 = uplit (%asciz'%AFAILING FUNC: %06%N%N'),
FMT_14 = uplit (%asciz'%AAROTE: %D2%A READ: %D2%N%N'),
FMT_15 = uplit (%asciz'%AAROTE: %D2%A READ: %D2%N%N'),
FMT_16 = uplit (%asciz'%AFAILING REG: %06%A GOOD DATA: %06%A BAD DATA: %06%N%N'),
FMT_17 = uplit (%asciz'%AFAILING REG: %06%A GOOD DATA: %06%A BAD DATA: %06%N%N'),
FMT_17 = uplit (%asciz'%AFAILING REG: %06%A GOOD DATA: %06%A BAD DATA: %06%N%N'),
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             NIB POS: 2042N2N'),
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                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        PA: (NEALEXBL3ML4.BL1.2 (3)
                                                                                                                                                                                                                                                                                                                                       FMT_18 = uplit (%asciz'%ATIMED OUT DURING MBUS %02%A FUNC%N%N'),
FMT_19 = uplit (%asciz'%ACRC GEN = 8: %B6%A A: %B6%A P: %B6%N'),
FMT_20 = uplit (%asciz'%ACRC CAL = 8: %B6%A A: %B6%A P: %B6%N%N'),
FMT_21 = uplit (%asciz'%AFAILED AT PLOG: %D2%A CHANNEL: %D2%N%N'),
FMT_22 = uplit (%asciz'%AFAILED AT WRD: %D2%A BIT: %D2%N%N'),
FMT_23 = uplit (%asciz'%AFAILED AT WRD: %D2%A BIT: %D2%N%N'),
FMT_24 = uplit (%asciz'%AFAILED AT WRD: %D2%A BIT: %D2%N%N'),
FMT_25 = uplit (%asciz'%AFAILED AT WRD: %D2%A BIT: %D2%N%N'),
FMT_26 = uplit (%asciz'%AFXA: %S%O6%A%O16%N'),
                                                                                                                                                                                                                           ERROR MESSAGE MAPPING FORMATS
                                                                                                                                                                                                                                                                                                                                              ONE_FMT = uplit (%asciz'%T%N'),
TWO_FMT = uplit (%asciz'%T%T%N')
                                                                                                                                                                                                                                                                                                                                        TWO_FMT = uplit (%asciz'%T%T%N'),
THR_FMT = uplit (%asciz'%T%T%T%N'),
FOR_FMT = uplit (%asciz'%T%T%T%T%N'),
FIV_FMT = uplit (%asciz'%T%T%T%T%N'),
SIX_FMT = uplit (%asciz'%T%T%T%T%T%N'),
SEV_FMT = uplit (%asciz'%T%T%T%T%T%T%N'),
EIG_FMT = uplit (%asciz'%T%T%T%T%T%T%N'),
NIN_FMT = uplit (%asciz'%T%T%T%T%T%T%T%N'),
TEN_FMT = uplit (%asciz'%T%T%T%T%T%T%T%T%N'),
ELV_FMT = uplit (%asciz'%T%T%T%T%T%T%T%T%N'),
                                                                                                                                                                                                                                                                                              DIAGNOSTIC VOCABULARY
                                                                                                                                                                                                                                                                                                                                                                     WORDS
                                                                                                                                                                                                                                                                                                                                    WRD 1 = uplit (%asciz' GO'),
WRD 2 = uplit (%asciz' DRV RDY'),
WRD 3 = uplit (%asciz' ILF'),
WRD 4 = uplit (%asciz' OPI'),
WRD 5 = uplit (%asciz' BAD'),
WRD 6 = uplit (%asciz' GOOD'),
WRD 7 = uplit (%asciz' FARITY NOT'),
WRD 8 = uplit (%asciz' GENERATED'),
WRD 9 = uplit (%asciz' BETECTED'),
WRD 10 = uplit (%asciz' AFTER'),
WRD 11 = uplit (%asciz' AFTER'),
WRD 12 = uplit (%asciz' ATTR'),
WRD 13 = uplit (%asciz' ATTN'),
WRD 14 = uplit (%asciz' ATTN'),
WRD 15 = uplit (%asciz' WRITING'),
WRD 16 = uplit (%asciz' WRITING'),
WRD 17 = uplit (%asciz' FUNC'),
WRD 18 = uplit (%asciz' FUNC'),
WRD 19 = uplit (%asciz' FUNC'),
WRD 20 = uplit (%asciz' RMR'),
WRD 21 = uplit (%asciz' EXCESSIVE'),
WRD 23 = uplit (%asciz' MBUS'),
```

TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (3)

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                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   WRD 24 = Uplit (%asciz' DATA'),
WRD 25 = Uplit (%asciz' CONTINUITY'),
WRD 26 = Uplit (%asciz' AOE'),
WRD 27 = Uplit (%asciz' LBT')
WRD 30 = Uplit (%asciz' FREMATURLY'),
WRD 31 = Uplit (%asciz' IAE'),
WRD 33 = Uplit (%asciz' INCREMENT'),
WRD 32 = Uplit (%asciz' WITH'),
WRD 33 = Uplit (%asciz' UNS'),
WRD 34 = Uplit (%asciz' UNS'),
WRD 35 = Uplit (%asciz' PROM'),
WRD 35 = Uplit (%asciz' REG'),
WRD 37 = Uplit (%asciz' SELECT'),
WRD 38 = Uplit (%asciz' WITHUE'),
WRD 39 = Uplit (%asciz' WITHUE'),
WRD 40 = Uplit (%asciz' WITHUE'),
WRD 41 = Uplit (%asciz' MIBBLE CNT'),
WRD 42 = Uplit (%asciz' MIBBLE CNT'),
WRD 43 = Uplit (%asciz' TRE'),
WRD 44 = Uplit (%asciz' TRE'),
WRD 45 = Uplit (%asciz' TRE'),
WRD 46 = Uplit (%asciz' TRE'),
WRD 47 = Uplit (%asciz' TRE'),
WRD 48 = Uplit (%asciz' TRETS'),
WRD 49 = Uplit (%asciz' TESTS'),
WRD 50 = Uplit (%asciz' TESTS'),
WRD 50 = Uplit (%asciz' TESTS'),
WRD 55 = Uplit (%asciz' NIBBLE'),
WRD 56 = Uplit (%asciz' NIBBLE'),
WRD 57 = Uplit (%asciz' MLTIPLEXER'),
WRD 68 = Uplit (%asciz' WITTPLEXER'),
WRD 69 = Uplit (%asciz' UNEXPECTED'),
WRD 68 = Uplit (%asciz' UNEXPECTED'),
WRD 69 = Uplit (%asciz' ECH'),
WRD 69 = Uplit (%asciz' DECODE'),
WRD 77 = Uplit (%asciz' DECODE'),
WRD 77 = Uplit (%asciz' UNCORRECTABLE'),
```

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22-Dec-1980 09:21:22
TOPS-20 Bl iss-16 V2(212)
PA: <NEALE> BL3ML4.BLI.2 (3)
                                                                                                               PHRASES
                                                                                                                                                                                       PHR 1 = uplit (%asciz' BIT NOT SET'),
PHR 2 = uplit (%asciz' BIT NOT CLR'),
PHR 3 = uplit (%asciz' NO RESPONCE AFTER 1.5 US'),
PHR 4 = uplit (%asciz' DATA ERRORS'),
PHR 5 = uplit (%asciz' BIT SET'),
PHR 6 = uplit (%asciz' BIT CLR'),
PHR 7 = uplit (%asciz' OF OTHER DRIVES'),
PHR 8 = uplit (%asciz' CLASS A'),
PHR 9 = uplit (%asciz' CLASS B'),
PHR 10 = uplit (%asciz' TO FIND'),
PHR 11 = uplit (%asciz' NOT LAT(HED'),
PHR 12 = uplit (%asciz' SINGLE BIT ERROR'),
PHR 13 = uplit (%asciz' MULTIPLE BIT ERROR'),
PHR 14 = uplit (%asciz' REGISTER DUMP'),
PHR 15 = uplit (%asciz' SERIAL N'),
                                                                                                                                                                                              ! FUNCTIONS
                                                                                                                                                                                      FNC 1 = uplit (%asciz' MEM SIZING'),
FNC 2 = uplit (%asciz' NOOP'),
FNC 3 = uplit (%asciz' DRV'),
FNC 4 = uplit (%asciz' WRITE CHECK'),
FNC 5 = uplit (%asciz' WRITE'),
FNC 6 = uplit (%asciz' READ'),
FNC 7 = uplit (%asciz' CLEAR'),
FNC 8 = uplit (%asciz' COMP ERROR'),
FNC 9 = uplit (%asciz' SYS CLR'),
FNC 10 = uplit (%asciz' SEARCH'),
FNC 11 = uplit (%asciz' READ-IN-PRESET'),
FNC 12 = uplit (%asciz' ABORT'),
FNC 13 = uplit (%asciz' ARR RD WRT'),
FNC 14 = uplit (%asciz' ARR RD WRT'),
FNC 15 = uplit (%asciz' REFRESH'),
FNC 16 = uplit (%asciz' REFRESH'),
FNC 17 = uplit (%asciz' REFRESH'),
FNC 18 = uplit (%asciz' RAM-BUS'),
FNC 19 = uplit (%asciz' CHK SUM'),
FNC 21 = uplit (%asciz' CHK SUM'),
FNC 22 = uplit (%asciz' LAST BLK'),
FNC 23 = uplit (%asciz' INITIALIZE'),

| REGISTERS
                                                                                                                                                                                                          REGISTERS
                                                                                                                                                                                          REG_1 = uplit (%asciz' MLCS1'),
REG_2 = uplit (%asciz' MLDS'),
REG_3 = uplit (%asciz' MLER'),
REG_4 = uplit (%asciz' MLMR'),
REG_5 = uplit (%asciz' MLAS'),
REG_6 = uplit (%asciz' MLDA'),
```

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759 :ML4
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22-Dec-1980 09:21:22
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               TOPS-20 Bl iss-16 V2(212)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               PA: (NEALE) BL3ML4.BL1.2 (3)
                                                                                                                                  REG_7 = uplit (%asciz' MLDT'),
REG_8 = uplit (%asciz' MLPA'),
REG_9 = uplit (%asciz' MLSN'),
REG_10 = uplit (%asciz' MLE1'),
REG_11 = uplit (%asciz' MLE2'),
REG_12 = uplit (%asciz' MLD1'),
REG_13 = uplit (%asciz' MLD2'),
REG_14 = uplit (%asciz' MLEE'),
REG_15 = uplit (%asciz' MLEE'),
REG_16 = uplit (%asciz' MLEL'),
REG_17 = uplit (%asciz' MLPD'),
REG_18 = uplit (%asciz' MLCS2'),
REG_19 = uplit (%asciz' MLBA'),
REG_20 = uplit (%asciz' MLBA'),
REG_21 = uplit (%asciz' MLBA'),
REG_21 = uplit (%asciz' MLBA'),
                                                                               2184
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                                                                                                                                               MODULES IN ERROR MESSAGES
                                                                                                                                   ASYNC = uplit (%asciz'ASYNCHRONOUS FAILURE MODULE 7361'),
SYNC = uplit (%asciz'SYNCHRONOUS FAILURE MODULE 7362'),
ARR_DAT = uplit (%asciz'ARRAY DATA FAILURE MODULE 7363'),
MEM_ARR = uplit (%asciz'MEMORY ARRAY FAILURE MODULE 7357'),
INTER = uplit (%asciz'INTERMEDIATE DIAGNOSTIC MESSAGE'),
DATA_LATE = uplit (%asciz'DATA_LATE ERROR DURING TRANSFER'),
SC_SET = uplit (%asciz'SC_BIT_SET_DURING TRANSFER'),
TRBLE_LOOP = uplit (%asciz'TROUBLE SHOOT LOOP ERRORS'),
RH_ERROR = uplit (%asciz'RH_CONTROLLER_ERRORS'),
TIME_OUT = uplit (%asciz'DRIVE HUNG_AFTER_MASS_BUS_TRANSFER'),
                                                                                                                         DATA DIAGNOSTIC REGISTER SAVE LOCATIONS
                                                                                                                                    D1_TEMP = NIB_SAVE,
D2_TEMP = NIB_SAVE [1, 0, 16, 0],
E2_TEMP = NIB_SAVE [2, 0, 16, 0];
                                                                                                                ! BLF/PAGE>
```

TOPS-20 Bliss-16 V2(212)
PA: <NEALEX3L3ML4.BLI.2 (5)

799 :ML4 800 : 801 : 802 : 803 : 804 : 805 : 806 : 807 : 808 : 809 : 811 : 812 : 813 : 814 : 815 : 817 : 818 : 821 : 821 : 822 : 823 : 824 : 825 : 827 : 828 : 829 : 830 : 831 : 842 : 843 : 843 : 844 : 845 : 846 : 847 : 848 : 848 : 849 routine LOAD_STACK (STK_PTR, NIB_PTR) : novalue = begin FUNCTIONAL DESCRIPTION:
LOAD STACK TAKES GOOD NIBBLE DATA
FOUND IN THE STRUCTURE 'NIB SAVE'
AND STORES IT INTO THE STRUCTURE
'STACK' REWRITING ANY BAD NIBBLE
'STACK' LOCATIONS WITH GOO'D NIBBLE DATA FORMAL PARAMETERS: STK_PTR POINTS TO PRESENT DEPTH OF THE 'STACK' WHERE PRESENT GOOD NIBBLE DATA IS TO BE STORED. NIB_PTR POINTS TO CURRENT NIBBLE POSITION BEING MANIPULATED. IMPLICIT INPUTS: STACK VECTOR OF 198 BYTE LOCATIONS WHERE GOOD NIBBLE DATA IS STORED DURING DIAGNOSTIC MODE READS, AFTER BAD NIBBLE LOCATIONS HAVE BEEN STRIPPED AWAY. STK_OFF vector of 9 byte LOCATIONS WHICH STORES AWAY A BAD NIBBLE OFF_SET COUNT FOR EACH NIBBLE POSITION NIB_SAVE BLOCK OF 3 WORDS TO STORE THE DATA FOUND IN MLD1, MLD2 AND MLE2 AFTER A DIAGNOSTIC MODE READ. IMPLICIT OUTPUTS:
 'STACK' LOADED WITH GOOD NIBBLE DATA COMPLETETION CODES: NONE SIDE EFFECTS: NONE case .NIB_PTR from 0 to 9 of

SELECT NIBBLE DATA TO BE LOADED INTO THE STACK

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end:

SEQ 0048

ZMLABO ML-11 LO IISCELLANEOUS CO	DING SECTIO	N			THUE UE
909 910 911			:ML4		
912 004222 913 004225	045 130 103 104 045 045 045 116 045 116 045	101 105 120 105 124 105 072 040 117 060 101 040 040 040 105 105 1072 040 117 060 117 060 116 045 000 000 101 107	P.AAA:		/%AE/ /XPE/ /CTE/ /D: / /%06/ /%A / /REA/
920 004252 921 004255 922 004260 923 004263 924 004266 925 004271 926 004274 927 004277 928 004302	116 045 117	000 000 101 107 117 104	P.AAB:	ASCII	/REA/ /D: / /%76/ /%7%/ /N/<00><00> /%AG/ /OOD/ / DA/ /TA:/ / %O/
929 004305 930 004310 931 004313 932 004316 933 004321 934 004324 935 004327 936 004332 937 004335 938 004340 939 004343	040 124 040 066 040 104 101 072 117 101 040	117 104 104 101 101 077 045 117 045 101 040 040 102 101 040 104 124 101 040 045 066 045 040 045 040 130 122 077 045 117 045 116		ASCII ASCII ASCII ASCII ASCII ASCII ASCII ASCII ASCII	/ BA/ /D D/ /ATA/ /: %/ /06%/ /A /
938 004340 939 004343 940 004346 941 004351 942 004354 943 004357 944 004362 945 004365 946 004370	117 040 066 045 045 122 105 116	116 000 101 104 111 126	1	.ASCII	/OR:/ / %O/ /6%N/ /%N/<00>
941 004351 942 004354 943 004357 944 004362 945 004373 946 004373 948 004376 949 004400 950 004403 951 004406 952 004411 953 004414 954 004417 955 004422 956 004425 957 004430 958 004433 959 004444 961 004444	045 122 105 116 045 045 116 045 111 105 117 040 045	101 102 124 040 116 040 122 122 122 072	P.AAD:	ASCII	/%AD/ /RIV/ /E S/ /N: / /%06/ /%N%/ /N/<00> /%AB/ /IT / /IN / /ERR/ /OR:/ /%N/<00> /%AG/
955 004422 956 004425 957 004430 958 004433 959 004436 960 004441 961 004444 962 004447 963 004452	045 117 040 102 101	045 116 116 000 101 107 117 104 116 111 040 104 124 101 040 045 062 045	P.AAE:	ASCII	/6%N/ /%N/<00> /%AG/ /00D/ / NI/ /B D/ /ATA/ /: %/ /02%/

965 966			MI_4			
967 968 004455 969 004460 970 004463 971 004466 972 004471 973 004474 974 004572 976 004505 977 004510 978 004513 979 004516 980 004521 981 004524 982 004527 983 004532 984 004535 985 004540 986 004543 987 004546 988 004551 989 004554 990 004557 991 004562 992 004565 993 004573 995 004573 996 004601 997 004604 998 004607 999 004610 1000 004613 1001 004616 1002 004621 1003 004621 1003 004632 1006 004635 1007 004640 1008 004643 1009 004644 1010 004652 1011 004652 1012 004655	101 040 101 116 040 040 040 040 040 102 117 040 045 111 105 101 105 101	040 040 104 111 104 101 045 045 045 116 101 102 116 116 1101 1111	040 102 (140 102 101 072 117 101 040 111 120 072 117 116 000 116 P.AAF 040 040 122 072 104 116 000 116 000 116 040	.ASCII .ASCII .ASCII .ASCII .ASCII	/A / / B/ /AD / /NIB/ / DA/ /TA:/ / XO/ /2XA/ / NI/ /B P/ /OS:/ / XO/ /4XN/ /XN/<00> /XAN/ /IN / /ERR/ /OR:/ / XD/ /XN/<00> /XAF/	
996 004601	101 105 101 040 066 045 000 045 105 101 040 122	111 104 124 045 045 116 101 120 103 101 040 104 045 045	114 040 072 117 116 000 122 P.AA 114 105 122 115 072	.ASCII	<00> /%AR/	
997 004604 998 004607 999 004610 1000 004613 1001 004616 1002 004621 1003 004624 1004 004627 1005 004632 1006 004635 1007 004640 1008 004643 1009 004644 1010 004655 1011 004655 1012 004655 1013 004660 1014 004663 1015 004666 1016 004671	040 062 045 000 045 101 105 101 104 072 117 116 000 045 111	045 045 045 116 101 111 104 124 123 040 066 045 000 101 124	104 116 000 106 P.AAI 114 040 040 101 045 045 116	ASCII	/EPL/ /ACE/ / AR/ /R M/ /OD:/ / %D/ /2%N/ /2%N/ <00> /%AF/ /AIL/ /ED / /AT / /DSA/ /: %/ /O6%/ /N%N/ <00><00>	

TOPS PA:<

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1021				ML4			
1021 1022 1023 1024 004704 1025 004707 1026 004712 1027 004715 1028 004720 1029 004723 1030 004726 1031 004731 1032 004734 1033 004737 1034 004742 1035 004745 1036 004750 1037 004753 1038 004756 1039 004761 1040 004764 1041 004767 1042 004772	061 061 072 102 101 040 111 076 045 045 045 101 111 040 107 104 072 117	065 060 040 040 040 124 072 072 102 045 116 122 040 122 046 045	072 076 045 045 040 102 074 060 040 061 116 000 106 P	.AAK:	ASCII	/15:/ /10>/ /: %/ /86%/ /A / /B6%/ /9:0/ /%N/<0 /%N/ /%N/ /%N/ /%N/ /AIL/ /ING/ / RE/ /G A/ /DRS/ /N%N/	00>
1039 004761 1040 004764 1041 004767 1042 004772 1043 004775 1044 005000 1045 005003 1046 005004 1049 005015 1050 005020 1051 005023 1052 005026 1053 005031 1054 005034 1055 005037 1056 005045 1058 005050 1059 005053 1060 005064 1061 005061 1062 005064 1063 005075 1066 005100 1067 005113 1068 005110 1069 005110 1069 005113 1071 005116 1072 005121 1073 005124 1074 005127 1075 005132	000 045 101 111 040 116 040 045 040 117 137 124 116 106 040 102 040 040 101 040 040 102 040 116 040 102 040 117	101 111 116 106 103 045 045 116 045 123 040 124 117	114 107 125 072 117 116 000	.AAL:	.ASCII	/NIN/ <00> /XAF/ /AIL/ /ING/ / FU/ /XO/ /XN/ /XN/ /XN/ /XN/ /XN/ /XN/ /YE/ /T C/ /NT / /FOR/ /NI/ /R :/	
1062 005064 1063 005067 1064 005072 1065 005075 1066 005100 1067 005103 1068 005106 1069 005110 1070 005113 1071 005116 1072 005121 1073 005124 1074 005127 1075 005132	040 062 101 040 062 116 000 045 122 105 045 045 045	040 045 040 045 040 045 000 101 117 072 104 101 040	104 045 075 104 045 116	.AAN:	ASCII	/FOR/ / NI/ /B :/ / XD/ /2 %/ /XD/ /2 %/ /NXN/ <00><0 /%AM/ /ROT/ /E: / /%D2/ /%A / /REA/)0>

1077 1078					:ML4			
1079 1080 005 1081 005 1082 005 1083 005 1084 005 1085 005 1086 005 1087 005 1088 005 1090 005 1091 005 1092 005 1093 005 1094 005 1095 005 1096 005 1097 005 1098 005 1099 005 1100 005 1101 005 1102 005 1103 005 1104 005 1105 005 1106 005 1107 005 1108 005 1109 005 1109 005 1100 005 1101 005 1102 005 1103 005 1104 005 1105 005 1106 005 1107 005 1108 005 1109 005 1110 005 1110 005	143 143 143 153 164 167 175 160 167 175 161 161 161 161 161 161 161 161 161 16	045 045 116 045 111 114 040 105 105 107 107 040 045 101 101 101 101 101 101 101 101 104 101	000 101 102 105 130 122 040 105 122 045 045 045 045 117 117 117 040 124 040 040	040 062 045 116 102 123 106 105 107 107 107 107 107 107 107 107 107 104 107 104 107 104 107 104 104 107 104 104 104 104 104 105 104 105 106 107 107 107 107 107 107 107 107 107 107	P.AAP:	ASCII	/D: / /%D2/ /%N%/ /N/<00> /%AN/ /IBB/ /LES/ / XF/ /ERE/ /D B/ /EFO/ /RE / /ERR/ /OR:/ / %D/ / %SM/ <00><00> /%AF/ / ING/ / RE/ /GOO/ / ING/ / RE/ /GOO/ / ATA/ / CG%/ /AB/ /AD / / MAT/ / AB/ / AB/ / AB/ / AB/ / ANX/ / %N%/ / *N%/ / *N/	
1115 005 1116 005 1117 005 1118 005 1119 005 1120 005 1121 005	504 506 511 514 517 522 525	045	000 116 104 107 123 107 116	045 111 116 111 040 111 045	P.AAQ:	ASCII ASCII ASCII ASCII ASCII ASCII	/N/<00> /ZNZ/ /ADI/ /AGN/	
1113 005 1114 005 1115 005 1116 005 1117 005 1118 005 1119 005 1120 005 1121 005 1122 005 1124 005 1125 005 1126 005 1129 005 1129 005 1130 005 1131 005	333 336 341 342 345 350 353	116 000 045 111	045 101 115 040	045 116 124 105 117 040 122 107	P.AAR:	ASCII	/NG / /UNI / /T %/ /01% / /N%N/ <00> /%AT/ /IME/ /D 0/ /UT / /DUR/ /ING/	

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SEC	TION	MACRO M	1113	22-DEC-80	12:16	PAGE 66
1133 1134 1135			:ML4			
1135 1136 005364 040 1137 005367 125 1138 005372 045 1139 005375 045 1140 005400 106 1141 005403 103 1142 005406 045 1143 005411 000	115 123 117 101 125 045 116	102 040 062 040 116 116		ASCII ASCII ASCII ASCII ASCII ASCII	/ MB/ /US / /%02/ /%A / /FUN/ /%N/<(<00>	00>
1144 005412 045 1145 005415 122 1146 005420 107 1147 005423 040	101 103 105 075 072 102 101 072 101 072 102	103 040 116 040 040 066 040 040 040 040	P.AAS	ASCII	/%AC/ /RC / /GEN/ /B: / /%B6/ /%A / /%B6/ /%A / /P: / /%B6/	
1162 005476 102 1163 005501 045	116 101 103 101 075 072 102 101 072	000 103 040 114 040 040 066 040	P.AAT	.ASCII	/IN/<0 <00> /IAC/ /RC / /CAL/ / = / /B: / /IIB6/ /IA / /A: /	00>
1167 005515 045 1168 005520 120 1169 005523 045 1170 005526 045 1171 005531 116 1172 005534 045 1173 005537 101 1174 005542 105 1175 005545 101 1176 005550 120 1177 005553 107	102 101 072 102 116 000 101 111 104 124 114	066 040 040 066 045 000 106 114 040 040 117 040	P.AAU	.ASCII .ASCII .ASCII .ASCII .ASCII .ASCII .ASCII .ASCII	/XB6/ /XA / /P: / /XB6/ /XNX/ /NV<00 /XAF/ /AIL/ /ED / /PL0/ /G: / /XD2/)><00>
1178 005556 045 1179 005561 045 1180 005564 103 1181 005567 116 1182 005572 114 1183 005575 045 1184 005600 045 1185 005603 116 1186 005606 045 1187 005611 101	104 101 110 116 072 104 116 000 101	062 040 101 105 040 062 045 000 106 114	P.AAV	ASCII	/XD2/ /XA / /CHA/ /NNE/ /XD2/ /XD2/ /XNX/ /N/<00 /XAF/ /AIL/)><00>

TOPS PA:<

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1189 1190				:ML4		
1190 1191 1192 005614 1193 005617 1194 005622 1195 005625 1196 005630 1197 005633 1198 005636 1199 005641 1200 005644 1201 005647 1202 005655 1204 005660 1205 005663 1206 005666 1207 005671 1208 005674 1209 005677	105 101 127 072 104 101 111 040 045 045 105 123 103 124 124 116	104 124 122 040 062 040 124 045 116 101 107 124 045 104 105 045	040 040 104 045 102 072 104 116 000 122 111 105 123 123	P.AAW:	ASCII	/ED / /AT / /MRD/ /: %/ /D2%/ /A B/ /IT:/ / %D/ /2%N/ /%N/<00> /%AR/ /EGI/ /STE/ /R%S/ /ADD/ /RES/ /S%S/
1210 005702 1211 005705 1212 005710 1213 005713 1214 005716 1215 005721 1216 005722 1217 005725 1218 005730 1219 005733 1220 005736 1221 005741	000 045 124 065 066 064 061 116	123 045 123 045 045 045 045 045 045	101 116 116 045 116 045 123 117 123 117 645	P.AAX:	.ASCII	/7%A/ /CON/ /TEN/ /TS%/ /N%N/ <00> /%S%/ /T%S/ /5%0/ /6%S/ /4%0/ /16%/
1222 005744 1223 005746 1224 005751 1225 005754 1226 005757 1227 005762 1228 005764 1229 005767 1230 005772 1231 005775 1232 006000 1233 006003 1234 006006 1235 006011 1236 006014 1237 006016 1238 006027 1241 006032 1242 006034 1242 006037	045 045 101 123 066 045 045 101 123 061 061	000 123 124 072 045 000 123 124 045 045 045 045 045 045 045 045	064 045 045 117 064 045 045	P.AAZ:	ASCII	/N/<00> /XS4/ /XTX/ /A:X/ /SX0/ /6/<00> /XS4/ /XTX/ /A:X/ /XTX/ /A:X/ /IXD/ /1XD/ /1XD/ /1XN/ <00><00>
1232 006000 1233 006003 1234 006006 1235 006011 1236 006014 1237 006016 1238 006021 1239 006024	061 061 061 061 000 045 116 045 124 000 045	045 045 045 045 000 124 000	104 104 104 104 116 045 000 045 116	P.ABA: P.ABB:	ASCII ASCII ASCII ASCII ASCII ASCII ASCII ASCII ASCII	/1%D/ /1%D/ /1%D/ /1%N/ <00><00> /%T%/ /N/<00><00> /%T%/ /T%N/ <00><00> /%T%/ /T%T/
1241 006032 1242 006034	000	000	045	P.ABC:	.ASCII	<00><00> /XTX/

1245 1246 1247				:ML4		
1248 006042 1249 006045	045	116	000		.ASCII	/%N/<00> <00>
1250 006046 1251 006051	045 124	124	045 124	P.ABD:	.ASCII	/XTX/ /TXT/
1251 006051 1252 006054 1253 006057 1254 006062 1255 006065 1256 006070 1257 006073 1258 006076 1259 006100 1260 006103 1261 006106 1262 006111 1263 006114 1264 006117 1265 006120 1266 006123 1267 006126 1268 006131 1269 006134 1270 006137	045 116	124	045		ASCII ASCII ASCII	/XTX/ /N/<00><00>
1252 006057 1253 006057 1254 006062 1255 006065 1256 006070 1257 006073 1258 006076 1259 006100 1260 006103 1261 006106 1262 006111	045	124	045	P.ABE:	.ASCII	/XTX/
1256 006070	124 045 124 000	045 124 045	124 045		.ASCII	/TXT/
1257 006073 1258 006076	124	045	116		-ASCII	/TEN/ <00><00>
1259 006100	045	000 124 045	045	P.ABF:	.ASCII	/XTX/ /TXT/
1261 006106	045	124	045		ASCII	/XTX/
1262 006111 1263 006114	124 045 124 045	045	124 045 124 000		.ASCII	/TXT/ /XN/<00>
1264 006117	000	124	045	P.ABG:	ASCII	<00> /XTX/
1265 006120 1266 006123	124	045	124	P.ABG:	.ASCII	/TXT/
1267 006126 1268 006131	045 124	124 045	045 124 045		.ASCII	/XTX/ /TXT/
1268 006131 1269 006134 1270 006137	045 116	124	045		.ASCII	/XTX/ /N/<00><00>
1271 006142	045	124	045	P.ABH:	.ASCII	/XTX/
1272 006145 1273 006150	124 045	045 124	124 045		.ASCII	/TXT/ /XTX/
1274 006153 1275 006156	124 045	045 124	124 045		.ASCII	/TXT/
1276 006161	124	045	116		.ASCII	/TIN/
1277 006164 1278 006166 1279 006171	000 045	000 124 045	045	P.ABI:	.ASCII	<00><00> /%T%/
1279 006171 1280 006174	124 045	124	124		.ASCII	/TXT/
1281 006177	124	124 045 124 045 116	045 124 045 124 000		.ASCII	/TXT/
1283 006205	124	045	124		.ASCII	/XTX/ /TXT/
1285 006210	000	116	000		.ASCII	/XN/<00> <00> /XTX/
1286 006214	124 045 124 045 124 045 124 045 124 045 124	124	045	P.ABJ:	.ASCII	/XTX/
1288 006222	045	124	045		.ASCII	/TXT/ /XTX/
1290 006230	045	124	045		.ASCII	/TXT/ /XTX/
1291 006233	124	124	045 124 045 124 045 124 045 000		ASCII	/TXT/
1293 006241	116	000	000	D ADV.	.ASCII	/N/<00><00>
1280 006174 1281 006177 1282 006202 1283 006205 1284 006210 1285 006213 1286 006214 1287 006217 1288 006222 1289 006230 1291 006233 1292 006236 1293 006241 1294 006244 1295 006247 1296 006252 1297 006255 1298 006260 1299 006263	045 124 045 124 045 124	124 045 124 045 124 045 124 045 124 045	045 124 045 124 045 124	P.ABK:	ASCII	/XTX/ /TXT/
1296 006252	124	124	124		ASCII	/XTX/ /TXT/

1301 1302 1303				:ML4		
304 006266 305 006271	045 124	124 045	045 116		.ASCII	/XTX/ /TXN/
306 006274 307 006276 308 006301	124 000 040 000	107	117	P.ABL:	.ASCII	<00><00> / GO/ <00>
308 006301 309 006302 310 006305	040	104 137 131	122	P.ABM:	.ASCII .ASCII	/ DR/ /V_R/
311 006310 312 006313	126 104 000	131	000		.ASCII	/DY/<00>
313 006314 314 006317	000 040 106 040	111 000 117	114	P.ABN:	.ASCII	/F/<00><00
315 006322 316 006325	111	000 102	000 120 000 101	P.ABO:	.ASCII	/ OP/ /1/<00><00
515 006322 316 006325 517 006330 318 006333 519 006336	040 104 040	000 107	000	P.ABP: P.ABQ:	.ASCII	/BA/ /D/<00><00 / GO/
320 006341 321 006344 322 006347 323 006352	040 117 040	104 120 111	000	P.ABR:	.ASCII	/0D/<00>
322 006347 323 006352	122 131	040	000 101 124 116		.ASCII	/RIT/ /Y N/
324 006355 325 006360 326 006363	117 040 116	124 107 105	000 105 122 105	P.ABS:	.ASCII	/0T/<00> / GE/ /NER/
327 006366 328 006371	101	124	105		ASCII	/ATE/ /D/<00><00
329 006374 330 006377	040 124	104 105 105	105 103 104	P.ABT:	.ASCII	/ DE/ /TEC/
331 006402 332 006405	000			D ADIL.	.ASCII	/TED/ <00>
333 006406 334 006411 335 006414	040 122 000	105 117 000	122 122	P.ABU:	.ASCII	/ ER/ /ROR/ <00><00>
336 006416 337 006421	040 124 000	000 101 105 000 104 111	106 122	P.ABV:	.ASCII	/ AF/ /TER/
338 006424 339 006426	000	104	125	P.ABW:	.ASCII	<00><00> / DU/ /RIN/
341 006434	107	000	116	P.ABX:	ASCIII	/G/<00>
343 006441 344 006442	000	106		P.ABY:	.ASCII	<00> / FA/
345 006445 346 006450	111	114 105	101 125 000		.ASCII	/ILU/ /RE/<00>
348 006454 348 006454	040	101	124	P.ABZ:	.ASCII	<00> / AT/ /A/<00><00
336 006416 337 006421 338 006424 339 006426 340 006431 341 006436 342 006436 343 006441 344 006442 345 006450 347 006453 348 006454 349 006457 350 006462 351 006465 352 006470 353 006473 354 006476 355 006501	040 122 107 040 000 040 111 122 000 040 101 040 124	000 101 116 127 124 107	124 000 124 000 122 111	P.ACA:	ASCII	/AT/ /TN/<00>
352 006470 353 006473	040 111 116	127	122 111 000	P.ACB:	.ASCII	/ WR/ /ITI/

57				:ML4		
57 58 59 60 006502				•		
60 006502 61 006505	040	126	126	P.ACC:	.ASCII	<00>
52 006506 53 006511	040 116	106	125	P.ACD:	ASCII	/ FU/
63 006511 64 006514	116	103	000	P.ACE:	.ASCII	/NC/<00> / TR/
55 006517	105	000	125 000 122 000 115	P.ALE:	.ASCII	/E/<00><00>
66 006522	040	122	115	P.ACF:	.ASCII	/ RM/ /R/<00><00>
68 006530	040	105	130	P.ACG:	-ASCII	/ EX/
69 006533	103	105	123		.ASCII	/ EX/ /CFS/ /SIV/
71 006541	105	000	000		-ASCII	/E/<00><00>
2 006544	040	115	102	P.ACH:	.ASCII	/ MB/
65 006517 66 006522 67 006525 68 006530 69 006536 71 006541 72 006544 73 006547 74 006552 75 006555 76 006560 77 006563 78 006571	040 122 040 103 123 105 040 125 040 124 040	104	000 130 123 126 000 102 000 101 000 117	P.ACI:	-ASCII	/ MB/ /US/<00> / DA/ /TA/<00>
75 006555	124	101	000		.ASCII	/TA/<00>
76 006560 77 006563	116	105	111	P.ACJ:	ASCII	/ CO/ /NTI/
78 006566	116	125	111		.ASCII	/NUI/
79 006571 80 006574	040	151	111 000 117	P.ACK:	ASCII	/TY/<00> / AO/
81 006577 82 006602	116 124 040 105 040	000	000		.ASCII	/E/<00><00>
82 006602 83 006605	124	000	102	P.ACL:	ASCII	/ LB/ /T/<00><00>
84 006610	124 040 105 124 114	106 103 124 000 122 000 105 105 101 103 124 125 131 101 000 115 125 131 111	000 102 000 122 101 122 000 101	P.ACM:	ASCII	/ PR/ /EMA/
85 006613 86 006616	105	115	101		ASCII	/EMA/
87 006621 88 006624	114	131	000	_	.ASCII	/LY/<00>
88 006624 89 006627	040 105	000	000	P.ACN:	.ASCII	/ IA/ /E/<00><00>
90 006632	040	111	116	P.ACO:	.ASCII	/ IN/
97 006635	103 115	122 105 000 127 110 125	105 116		.ASCII	/CRE/ /MEN/
93 006643	124	000	000		.ASCII	/T/<00><00>
94 006646 95 006651	124 040 124 040 000 040 123 040	127	000	P.ACP:	.ASCII	/TH/<00>
96 006654	040	125	126	P.ACQ:	.ASCII	/ UV/
97 006657 98 006660	000		116	P.ACR:	-ASCII	/ UV/ <00> / UN/ /S/<00><00>
99 006663	123	000	000		.ASCII	/\$/<00><00>
00 006666	117	120	122	P.ACS:	.ASCII	/ PR/
02 006674	040	125 000 120 115 117	000 122 000 122	P.ACT:	ASCII	/ OR/
03 006677	000		105	P.ACU:	.ASCII	<00>
05 006703	114	105	103	r.Acu:	ASCII	/ PR/ /OM/<00> / OR/ <00> / SE/ /LEC/ /T/<00>
90 006632 91 006635 92 006640 93 006643 94 006646 95 006651 96 006657 98 006660 99 006663 00 006666 01 006671 02 006674 03 006700 05 006703 06 006710 08 006713	040 000 040 114 124 040 107	122	105	P ACV.	.ASCII	/T/<00>
99 006627 90 006632 91 006635 92 006640 93 006643 94 006646 95 006651 96 006657 98 006660 99 006663 00 006666 01 006671 02 006674 03 006677 04 006700 05 006710 06 006710	107	123 105 000 122 000 125 121 000	000	P.ACV:	ASCII	/6/<00><00>
09 006716 10 006721	040 111	125	116 125	P.ACW:	.ASCII	\ IN\

CELLANEOUS COD	ING SEC	TION				12:16 PAGE 71
1413 1414				:ML4		
1415 1416 006726	040	061	064	P.ACX:	.ASCII	/ 14/
1/17 004771	040	001	004	r.ALA.	.ASCII	<00>
1418 006732 1419 006735 1420 006740 1421 006743 1422 006746 1423 006751 1424 006754 1425 006757	040 102	116	111	P.ACY:	.ASCII	/ NI/
1419 006735	102	102	114		.ASCII	/BBL/
1421 006743	105 116	124	103		.ASCII	/E C/
1422 006746	040	124 107	000 124	P.ACZ:	.ASCII	/ GT/
1423 006751	122 040	000 127 114	000		-ASCII	/R/<00><00>
1424 006754	040	127	110	P.ADA:	.ASCII .ASCII .ASCII	/ WH/
1425 006757	111	000	105		ASCII	/I'.E/ <00><00>
1427 006764	040	124	122	P.ADB:	ASCII	/ TR/
1428 006767	105	000	000		.ASCII	/E/<00><00>
1429 006772	040	000	116	P.ADC:	.ASCII	/ IN/
1430 006775	111	124	111		.ASCII	/111/
1431 007000 1432 007003	101	114	000		.ASCII	/AL/<00>
1433 007004	040	117	106	P.ADD:	.ASCII	/ OF/
1434 007007	106	137	123		.ASCII	/F S/
1435 007012	105	124	000		.ASCII	/ET/<00>
1436 007015 1437 007016	000 040	103	117	P.ADE:	.ASCII	<00>
1438 007021	125	116	124	P.ADE.	.ASCII	/ CO/
1439 007024	000	000			.ASCII	<00><00>
1440 007026	040	104	105	P.ADF:	.ASCII	/ DE/
1441 007031	114	101	131		.ASCII	/LAY/
1442 007034 1443 007036	000	000 124	105	P.ADG:	.ASCII	<00><00> / TE/
	040 123	124	105 123	r. ADG.	.ASCII	/STS/
1445 007044	000	000			.ASCII	<00><00>
1446 007046	040	101 123 103	104 000 117	P.ADH:	.ASCII	/ AD/
1447 007051	122	123	000		.ASCII	/RS/<00>
1449 007057	125	116	124	P.ADI:	ASCII	/ CO/ /UNT/
1450 007062	040 125 105	116 122	000		ASCII	/ER/<00>
1451 007065	000				.ASCII	<00>
1452 007066	040 107	122	105	P.ADJ:	.ASCII	/ RE/
1444 007041 1445 007044 1446 007046 1447 007051 1448 007054 1449 007057 1450 007065 1451 007065 1452 007066 1453 007071 1454 007074 1455 007077 1456 007102 1457 007104 1458 007117 1460 007114 1461 007117 1462 007122	040	000	000	P.ADK:	ASCII	/G/<00><00>
1455 007077	123	124	105 105	r.AUK:	ASCII	/ TE/ /STE/
1456 007102	040 123 104 040	000			.ASCII	/D/<00>
1457 007104	040	116	111	P.ADL:	.ASCII	/ NI/
1458 00/10/	102	102	114		.ASCII	/BBL/
1460 007114	105 040	000 101	114	P.ADM:	ASCII	/E/<00>
1461 007117	114	000	000	ר. אשרו:	ASCII	/L/<00><00>
1461 007117 1462 007122	040	124	105	P.ADN:	.ASCII	/ TE/
1463 007125	123	124 124 130 122	000		.ASCII	/ST/<00>
1464 007130	040	130	106 105	P.ADO:	.ASCII	/ XF/
1465 007133 1466 007136	105	000	105		.ASCII	/ERE/ /D/<00>
1467 007140	040	116	111	P.ADP:	.ASCII	/ NI/

CZMLABO ML-11 LOGIO MISCELLANEOUS CODIO	C TEST NG SECTION	MACRO M1113 2	2-DEC-80 12:16	PAGE 72 5
1469 1470		ML4		
1471 1472 007143 1473 007146	102 102 105 123	114	.ASCII /BBL.	(00)
1473 007146 1474 007151 1475 007152 1476 007155 1477 007156 1478 007161 1479 007164	000	103 P.ADQ:	.ASCII <00> .ASCII / SCASCII <00>	/
1476 007155 1477 007156	000	125 P.ADR:	.ASCII <00>	/
1478 007161 1479 007164 1480 007167	114 124 120 114 130 105 000 000	111 105 122	.ASCII /LTI. .ASCII /PLE. .ASCII /XER	/
1480 007167 1481 007172 1482 007174	130 105 000 000 040 125 105 130 105 103	116 P.ADS:	ASCII (00)	<00>
1483 007177 1484 007202	105 130 105 103 105 104	120 124	ASCII /ECT	/
1485 007205 1486 007210 1487 007213 1488 007216	105 104 040 116 104 000	120 124 000 105 P.ADT: 000 114 P.ADU:	.ASCII / NE	<00> / 00><00>
1489 007221	040 111 122 000	114 P.ADU:	.ASCII / IL	
1490 007224 1491 007227	040 103 103 000	000 122 P.ADV: 000 107 P.ADW:	.ASCII / CR.	00><00>
1491 007227 1492 007232 1493 007235 1494 007240	040 123 114 000 040 105	107 P.ADW: 000 103 P.ADX:	.ASCII / SG. .ASCII / L/<	00><00>
1495 00/243	110 000 125 116	000 103 P.ADY:	.ASCII /H/<	00><00>
1497 007251 1498 007252 1499 007255 1500 007260 1501 007263	000 040 124 000	111 P.ADZ:	.ASCII <00>	00><00>
1500 007260 1501 007263	040 103 101 116	110 P.AEA:	.ASCII /T/<	/
1502 007266 1503 007271	105 114	000	.ASCII /EL/	<00>
1505 007275 1505 007375	040 114 124 103 000 000	101 P.AEB: 110	ASCII /TCH	(00)
1507 007302 1508 007305	124 103 000 000 040 104 113 000	103 P.AEC:	.ASCII / DC.	(00> (00> (00><00>
1509 007310 1510 007313 1511 007314	040 104 113 000 040 102 123 000 040 123 116 104 117 116	125 P.AED: 000 131 P.AEE:	.ASCII / BU.	00><00>
1512 007321 1513 007324	116 104 117 116	103 P.AEC: 000 125 P.AED: 000 131 P.AEE: 122 105	.ASCII /NDR.	00><00>
1514 007327 1515 007330	000 040 104 103 117	105 P.AEF:	.ASCII / DE. .ASCII / COD.	,
1517 007336 1518 007340	105 000 040 116	104 117 P.AEG:	ASCII /E/<	00>
1519 007343 1520 007346	124 000 040 125	000 116 P.AFH:	.ASCII <00> .ASCII / LA .ASCII / TCH .ASCII <00> .ASCII / DC .ASCII / BU .ASCII / SY .ASCII / NDR .ASCII / NDR .ASCII / ONE .ASCII / COD .ASCII / COD .ASCII / LE/< .ASCII / LE/ .A	00><00>
1502 007266 1503 007271 1504 007272 1505 007275 1506 007300 1507 007302 1508 007305 1509 007310 1510 007313 1511 007316 1512 007321 1513 007324 1514 007327 1515 007330 1516 007333 1517 007336 1518 007340 1519 007343 1520 007346 1521 007351 1522 007354	103 117 122 105 124 101	122 103 102	.ASCII /COR.	

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECT	ION	MACRO M	11113	22-DEC-80	12:16 PAG	SE 73 5
1525 1526			:ML4			
1527 1528 007362 114 1529 007365 000	105	000		.ASCII	/LE/<00>	
1530 007366 040 1531 007371 124 1532 007374 117 1533 007377 123	102 040 124 105	111 116 040 124	P.AEI	ASCII ASCII ASCII	/ BI/ /T N/ /OT / /SET/	
1533 007377 123 1534 007402 000 1535 007404 040 1536 007407 124 1537 007412 117 1538 007415 103 1539 007420 000 1540 007422 040	000 102 040 124 114	111 116 040 122	P.AE.	ASCII ASCII ASCII ASCII	<00><00> / BI/ /T N/ /OT / /CLR/	
1529 007365 000 1530 007366 040 1531 007371 124 1532 007374 117 1533 007377 123 1534 007402 000 1535 007404 040 1536 007407 124 1537 007412 117 1538 007415 103 1539 007420 000 1540 007422 040 1541 007425 040 1542 007430 123 1543 007433 116 1544 007436 040 1545 007441 124 1546 007444 040 1547 007447 065 1548 007454 040 1550 007457 124 1551 007462 105 1552 007465 117 1553 007470 000 1554 007472 040 1555 007475 124	000 116 122 120 103 101 105	117 105 117 105 106	P.AEK	ASCII	<00><00> / NO/ / RE/ /SPO/ /NCE/ / AF/	
1545 007441 124 1546 007444 040 1547 007447 065 1548 007452 123 1549 007454 040 1550 007457 124 1551 007462 105 1552 007465 117	105 061 040 000 104	106 122 056 125	P.AEL	.ASCII .ASCII .ASCII	/TER/ /1./ /5 U/ /S/<00> / DA/	
1550 007457 124 1551 007462 105 1552 007465 117 1553 007470 000	061 040 000 104 101 122 122 000 102	040 122 123		.ASCII .ASCII .ASCII	/TA / /ERR/ /ORS/ <00><00>	
1554 007472 040 1555 007475 124 1556 007500 105 1557 007503 000 1558 007504 040	040 124 102	111 123 000	P.AEM	.ASCII .ASCII	/ BI/ /T S/ /ET/<00> <00> / BI/	
1559 007507 124	122	103		.ASCII .ASCII	/T C/ /LR/<00> <00>	
1562 007516 040 1563 007521 040 1564 007524 110 1565 007527 040 1566 007532 111	117 117 105 104 126 000 103 123 101	106 124 122 122 105	P.AEC	ASCII ASCII	/ OF/ / OT/ /HER/ / DR/ /IVE/	202
1567 007535 123 1568 007540 040 1569 007543 101 1570 007546 040 1571 007551 000		000 114 123 000	P.AEF	ASCII	/S/<00><(/ CL/ /ASS/ / A/<00> <00> / CL/ /ASS/ / B/<00>	NI>
1570 007546 040 1571 007551 000 1572 007552 040 1573 007555 101 1574 007560 040 1575 007563 000 1576 007564 040 1577 007567 040 1578 007572 116	103 123 102	114 123 000	P.AEG	ASCII ASCII ASCII	/ CL/ /ASS/ / B/<00> <00> / TO/	
1576 007564 040 1577 007567 040 1578 007572 116 1579 007575 000	124 106 104	117 111 000	P.AEF	ASCII ASCII ASCII	/ TO/ / FI/ /ND/<00> <00>	

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 74
1581 1582	:ML4
1583 1584 007576 040 116 1585 007601 124 040 1586 007604 101 124 1587 007607 110 105	117 P.AES: .ASCII / NO/ 114 .ASCII /T L/ 103 .ASCII /ATC/ 104 .ASCII /HED/
1585 007601 124 040 1586 007604 101 124 1587 007607 110 105 1588 007612 000 000 1589 007614 040 123 1590 007617 116 107 1591 007622 105 040 1592 007625 111 124 1593 007630 105 122 1594 007633 117 122 1596 007641 114 124	114
1597 007644 120 114 1598 007647 040 102	105 ASCII /PLE/
1599 007652 124 040 1600 007655 122 122 1601 007660 122 000 1602 007662 040 040 1603 007665 040 040	105 .ASCII /T E/ 117 .ASCII /RRO/ .ASCII /R/<00> 040 P.AEV: .ASCII / /
1605 007673 122 105 1606 007676 111 123 1607 007701 105 122 1608 007704 040 040	040 .ASCII / / 107 .ASCII /REG/ 124 .ASCII /IST/ 040 .ASCII /ER / 104 .ASCII / D/ 120 .ASCII /UMP/
1610 007712 000 000 1611 007714 123 105 1612 007717 111 101 1613 007722 040 043	122 P.AEW: .ASCII <00><00> 114 .ASCII /SER/ 114 .ASCII /IAL/ 000 .ASCII / #/<00>
1615 007726 040 115 1616 007731 115 040 1617 007734 111 132 1618 007737 116 107 1619 007742 040 116	105 P.AEX: .ASCII / ME/
1619 007742 040 116 1620 007745 117 120 1621 007750 040 104 1622 007753 126 000 1623 007756 040 127	117 P.AEY: .ASCII / NO/ 000 .ASCII /OP/<00> 122 P.AEZ: .ASCII / DR/ 000 .ASCII /V/<00><00> 122 P.AFA: .ASCII / WR/ 105 .ASCII / ITE/ 110 .ASCII / CH/ 113 .ASCII / ECK/
1614 007725 000 1615 007726 040 115 1616 007731 115 040 1617 007734 1111 132 1618 007737 116 107 1619 007742 040 116 1620 007745 117 120 1621 007750 040 104 1622 007753 126 000 1623 007756 040 127 1624 007761 111 124 1625 007764 040 103 1626 007767 105 103 1627 007772 000 000 1628 007777 105 103	105 .ASCII /ITE/ 110 .ASCII / CH/ 113 .ASCII /ECK/ .ASCII <00><00> 122 P.AFB: .ASCII / WR/
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CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 75
1637 1638	ML4
1639 1640 010022 040 103 1641 010025 115 120 1642 010030 105 122 1643 010033 117 122 1644 010036 040 123 1645 010041 123 040 1646 010044 114 122 1647 010047 000	117 P.AFE: .ASCII / CO/ 040 .ASCII /MP / 122 .ASCII /ERR/ 000 .ASCII /OR/<00> 131 P.AFF: .ASCII / SY/ 103 .ASCII /S C/ 000 .ASCII /LR/<00> .ASCII <00>
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1644 010036 040 123 1645 010041 123 040 1646 010044 114 122 1647 010047 000 1648 010050 040 123 1650 010056 110 000 1651 010060 040 122 1652 010063 101 104 1653 010066 111 116 1654 010071 120 122 1655 010074 123 105 1656 010077 000 1657 010100 040 111 1658 010103 114 105 1659 010106 101 114 1660 010111 000 1661 010112 040 101 1662 010115 117 122 1663 010120 000 000 1664 010122 040 101 1665 010125 122 040 1666 010130 104 137 1667 010133 122 124 1668 010136 040 107 1669 010141 117 104 1669 010144 102 114	105 P.AFH: .ASCII / RE/ 055 .ASCII /AD-/ 055 .ASCII /IN-/ 105 .ASCII /PRE/ 124 .ASCII /SET/ .ASCII /OO> .114 P.AFI: .ASCII / IL/ 107 .ASCII /LEG/ 000 .ASCII /AL/<00> .ASCII <00> .ASCII /AD/
1657 010100 040 111 1658 010103 114 105 1659 010106 101 114	114 P.AFI: .ASCII / IL/ 107 .ASCII /LEG/ 000 .ASCII /AL/<00>
1660 010111 000 1661 010112 040 101 1662 010115 117 122 1663 010120 000 000 1664 010122 040 101	102 P.AFJ: .ASCII <00> 124 .ASCII / AB/ .ASCII <00><00>
1664 010122 040 101 1665 010125 122 040 1666 010130 104 137 1667 010133 122 124 1668 010136 040 107	122 P.AFK: .ASCII / AR/ 122 .ASCII /R R/ 127 .ASCII /D W/
1668 010136 040 107 1669 010141 117 104 1670 010144 102 114	040 .ASCII /OD /
1671 010147 000 1672 010150 040 122 1673 010153 106 122 1674 010156 123 110 1675 010161 000	105 P.AFM: .ASCII / RE/ 105 ASCII / RE/ 105 .ASCII / FRE/ 000 .ASCII /SH/<00> 122 P.AFN: .ASCII / AR/ 131 .ASCII / RAY/ .ASCII <00><00>
1677 010165 122 101	122 P.AFN: ASCII (00> 131 ASCII / AR/ ASCII /RAY/
1676 010162 040 101 1677 010165 122 101 1678 010170 000 000 1679 010172 040 122 1680 010175 115 055 1681 010200 125 123	102 ASCII /M-D/
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1686 010215 000 1687 010216 040 103 1688 010221 113 137 1689 010224 125 115 1690 010227 000 1691 010230 040 114	.A3(11 /UP/\UP/
1690 010227 000 1691 010230 040 114	101 P.AFR: ASCII (00)

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1693 1694				:ML4		
1/05				;		
1695 1696 010233	123	124	040		.ASCII	/ST /
1697 010236	123	124	040 113		.ASCII	/BLK/ <00>
1698 010241	000	111			.ASCII	<00>
1700 010245	111	111	116 111	P.AFS:	.ASCII	/111/
1701 010250	101	114	111		.ASCII	/ALI/
1702 010253	132	105	000		.ASCII	/ZE/<00>
1704 010261	101 132 040 103	123	114 061	P.AFT:	-ASCII	/ IN/ /ITI/ /ALI/ /ZE/<00> / ML/ /CS1/
1698 010241 1699 010242 1700 010245 1701 010250 1702 010253 1703 010266 1704 010261 1705 010264 1706 010266 1707 010271 1708 010274 1709 010277 1710 010302 1711 010305 1712 010310 1713 010313 1714 010316 1715 010321 1716 010324 1717 010327 1718 010332 1719 010335	000 040 104 040 105	105 115 123 000 115 123 115 122 115 123 115		_	ASCII	くいりとくいり
1706 010266	040	115	114	P.AFU:	.ASCII	/ ML/ /DS/<00> / ML/
1708 010274	040	115	000 114	P.AFV:	-ASCII	/ ML/
1709 010277	105	122	000		.ASCII	/ER/<00>
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1713 010313	101	123	000 114		.ASCII	/AS/<00>
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1718 010332	040 120	101	114	P.AGA:	.ASCII	/PA/<00>
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1721 010343	123	116	000 114		.ASCII	/SN/<00>
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1/25 01035/	105	062	000		.ASCII	/E2/<00>
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1733 010407	105	105 115 114 115 104 115 123 000 115 103 115	000	D ACT.	.ASCII	/FI /<00>
1735 010415	120	104	114	P.AGI:	-ASCII	/PD/<00>
1736 010420	040	115	114	P.AGJ:	.ASCII	/ ML/ /PD/<00> / ML/ /CS2/
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1744 010447 1745 010452	102	101	111		.ASCII	/BAI/
1745 010452	000 040 103	000 115 123	114	P.AGN:	ASCII	<00><00> / ML/

CZMLABO ML-11 LOGIC T MISCELLANEOUS CODING	EST SECTION	MACRO M1113	22-DEC-80	12:16 PAGE 77 5
1749 1750		;ML	4	
1751 1752 010462 00 1753 010464 10 1754 010467 11 1755 010472 12 1756 010475 11 1757 010500 04 1758 010503 11 1759 010506 12 1760 010511 11 1761 010514 12 1762 010517 04	1 123 6 103 2 117 7 125 0 106 1 114	131 P.AG 110 116 123 101 125 040 104 105 063 000	ASCII	<00><00> /ASY/ /NCH/ /RON/ /OUS/ / FA/ /ILU/ /RE / /MOD/ /U_E/ / 73/
1763 010522 06 1764 010525 00 1765 010526 12 1766 010531 10 1767 010534 11 1768 010537 12 1769 010542 10 1770 010545 11 1771 010550 10 1772 010553 11 1773 010566 11 1774 010561 06 1775 010567 00 1777 010570 10	061 07 131 133 110 77 116 155 123 164 101 44 125 040 77 104 44 105	000 116 P.AG 122 117 040 111 122 115 125 040 066 000	.ASCII	/61/<00> <00> /SYN/ /CHR/ /ONO/ /US / /FAI/ /LUR/ /E M/ /ODU/ /LE / /736/
1779 010576 10	10 11 122 11 131 14 101 11 040 11 111 15 122 10 115	122 P.AC 040 124 106 114 105 117 114 067 063	.ASCII .ASCII .ASCII .ASCII .ASCII	/2 /<00> <00> /ARR/ /AY / /DAT/ /A F/ /AIL/ /URE/ / MO/ /DUL/ /E 7/ /363/ / /00>
1780 010601 10 1781 010604 10 1782 010607 12 1783 010612 04 1784 010615 10 1785 010620 10 1786 010623 06 1787 010626 04 1788 010630 11 1789 010633 11 1790 010636 04 1791 010641 12 1792 010644 04 1793 010652 12 1795 010655 11 1796 010660 12 1797 010663 04 1798 010671 00 1800 010672 11 1801 010675 10	122 115 125 125 125 126 130 130 130 130 130 130 130 130 130 130	115 P.AG 131 122 131 101 125 040 104 105 063 000	ASCII ASCII	// <00> /MEM/ /ORY/ / AR/ /RAY/ / FA/ /ILU/ /RE / /MOD/ /ULE/ / 73/ /57/<00> <00>
1799 010671 00 1800 010672 11 1801 010675 10 1802 010700 10 1803 010703 10	5 122	124 P.AC 115 111 105	ASCII	/INT/ /ERM/ /EDI/ /ATE/

MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 78 5
1805 1806	ML4
1805 1806 1807 1808 010706 040 104 1809 010711 101 107 1810 010714 117 123 1811 010717 111 103 1812 010722 115 105 1813 010725 123 101 1814 010730 105 000 1815 010732 104 101 1816 010735 101 040 1817 010740 101 124 1818 010743 040 105 1819 010746 122 117 1820 010751 040 104 1821 010754 122 117 1822 010757 107 040 1823 010762 122 101 1824 010765 123 106 1825 010770 122 000 1826 010772 123 103 1827 010775 102 111 1828 011000 040 123 1829 011003 124 040 1830 011006 125 122 1831 011011 116 123 1834 011022 105 122 1835 011025 000 1836 011026 124 122 1837 011031 125 102 1838 011026 124 122 1839 011037 110 117 1840 011042 124 040 1841 011045 117 117 1842 011050 040 1843 011056 123 107 1844 011056 123 107 1845 011066 124 122 1848 011071 114 114	111
1847 011066 124 122 1848 011071 114 114 1849 011074 122 040	117
1848 011071 114 114 1849 011074 122 040 1850 011077 122 122 1851 011102 122 123 1852 011105 000 1853 011106 104 122 1854 011111 126 105 1855 011114 110 125 1856 011117 107 040 1857 011122 106 124 1858 011125 122 040 1859 011130 101 123	000

CZMLABO ML-11 L MISCELLANEOUS C	OGIC TEST	MACRO M	1113 22-DEC-80	12:16 PAGE 79 6
1861 1862			:ML4	
1861 1862 1863 1864 011133 1865 011136 1866 011141 1867 011144 1868 011147 1869 1870	040 1 123 0 122 1 123 1 122 0	02 125 40 124 01 116 06 105 00 000	.ASCII .ASCII .ASCII .ASCII	/S T/
1870 1871 1872 1873 011152 1873 011152 1874 011160 1875 011160 1876 011566 1877 011566 1878 011560 1880 011562 1881 011564 1882 011564 1883 011566 1884 011570 1885 011570 1886 011572 1887 011574 1888 011576 1889 011600 1891 012612 1892 013120 1893 013122 1894 013122 1895 013124 1896 013122 1897 013130 1898 013132 1899 013132 1899 013132 1899 013134 1901 013134 1901 013134 1902 013136 1903 013136 1903 013136 1903 013146 1906 013146 1907 013146 1908 013155 1910 013156 1909 013156	000000 000 000 004 000 0173701 160200		NIB.SAVE: BLKW HW.OR.TBL: BLKW PTBL.PTR: BLKW OP.NUM.ARR: BLKW GOOD.BLK: BLKW GOOD.BLK: BLKW CHIP.SIZ: BLKW ARR.16: BLKW ARR.16: BLKW ARR.16: BLKW STACK: BLKW STACK: BLKW PD.TEMP: BLKW W.C.SIZE: BLKW RAS.INC: BLKW WT.DATA: BLKW WT.DATA: BLKW DRIVE.TYPE: LST.DUT.REG: BLKW REG.INIT.FLG: WORD A.CAL: BLKW P.CAL: BLKW P.CAL: BLKW P.CAL: BLKW P.CAL: BLKW P.CAL: BLKW MB.CAL: BLKW B.CAL: BLKW B.CAL: BLKW MB.CAL: BLK	3 177 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

1916 1917	MACRO M1113 22-DEC-		22-Dec-1980 09:24: 22-Dec-1980 09:21:
1918 1919 013164 000 1920 013165 000 1921 013166 000 1922 013167 000 1923 013170 000 1924 013171 000 1925 013173 000 1926 013174 000 1928 013175 000 1929 013176 000 1930 013177 000 1931 013200 000 1932 013201 000 1933 013202 000 1934 013203 000 1935 013204 000 1936 013205 000 1937 013206 000 1938 013207 000 1938 013210 000 1940 013211 000 1940 013211 000 1940 013211 000 1940 013211 000 1941 013212 000 1942 013213 000 1943 013221 000 1944 013215 000 1945 013221 000 1946 013221 000 1947 013220 000 1948 013221 000 1948 013221 000 1949 013222 000 1950 013223 000 1951 013224 000 1948 013230 025077 1955 013230 025077 1955 013234 000 1959 013225 000 1959 013225 000 1959 013225 000 1958 013236 010600 1959 013225 000 1959 013225 000 1959 013225 000 1959 013225 000 1959 013225 000 1959 013225 000 1959 013240 014620 1961 013242 000 1968 013245 000 1968 013251 000 1968 013251 000 1968 013251 000 1968 013251 000 1968 013251 000 1968 013251 000 1968 013251 000 1968 013251 000 1969 013252 177400 1969 013252 177400	.BYT .BYT .BYT .BYT .BYT .BYT .BYT .BYT	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	

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CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 81	SEQ 006
1972 1973	ML4	22-Dec-1980 09:24:31 TOP 22-Dec-1980 09:21:22 PA
1974 1975 013255 000 1976 013256 000 1977 013257 1978 013260 100000 1980 013263 000 1981 013264 000 1982 013265 000 1983 013266 000 1986 013270 000 1986 013271 000 1987 013272 000 1988 013273 000 1989 013274 000 1999 013275 000 1991 013276 000 1992 013277 000 1993 013300 000020 1994 013302 177400 1995 013304 000 1996 013310 177666 1999 013312 000001 1998 013310 177666 1999 013312 000001 1998 013310 177666 1999 013312 000001 2000 013314 000 2001 013321 000 2003 013316 000 2004 013320 000 2005 013321 000 2006 013321 000 2007 013323 000 2008 013324 000 2010 013326 000 2011 013327 000 2012 013330 140300 2013 013337 000 2014 013333 000 2015 013334 000 2016 013334 000 2017 013336 000 2018 013337 000 2019 013340 000 2019 013340 000 2020 013344 000 2021 013344 000 2022 013347 000 2026 013347 000 2026 013347 000 2026 013347 000 2026 013347 000 2026 013347 000 2026 013347 000 2026 013347 000 2026 013347 000 2026 013347 000 2026 013347 000	.BYTE 0 .BYTE	

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 82 6	SEQ 0069
2028	ML4		22-Dec-1980 09:24:31 TOP: 22-Dec-1980 09:21:22 PA:
2030 2031 013351 000 2032 013352 000 2033 013353 000 2034 013354 000 2035 013355 000 2036 013356 000 2037 013357 000 2038 013361 000 2040 013362 000 2041 013363 000 2042 013364 000 2043 013365 000 2044 013366 000 2045 013367 000 2046 013370 010000 2047 013372 000 2048 013373 000 2049 013374 000 2050 013375 000 2051 013376 000 2052 013376 000 2054 013401 000 2055 013402 000 2056 013403 000 2057 013404 000 2058 013405 000 2058 013407 000 2058 013407 000 2059 013406 000 2059 013406 000 2059 013406 000 2060 013411 000 2062 013411 000 2063 013411 000 2064 013413 000 2065 013414 000 2066 013415 000 2067 013416 000 2068 013417 000 2069 013421 000 2069 013421 000 2069 013421 000 2069 013421 000 2069 013421 000 2069 013421 000 2069 013421 000 2070 013421 000 2071 013422 000 2070 013421 000 2071 013422 000 2072 013423 000 2073 013424 000 2074 013425 000 2077 013430 000 2077 013430 000 2078 013431 000 2078 013431 000 2079 013431 000 2079 013431 000 2079 013432 000000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000 2079 013431 000	BYTE BYTE BYTE BYTE BYTE BYTE BYTE BYTE	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	

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MISCELLANEO	11 LOGIC TEST US CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 85	SE
2196 2197 2198 2199 2200 2201 2202 2203 2204 2205 2206 2207 2208	002000 004000 010000 020000 040000 100000 004222 004266	;ML4 ; PRI== IXE== IBE== IER== LOE== HOE== FMT.1= FMT.2=	2000 4000 10000 20000 40000 -100000 P.AAA P.AAB	22-Dec-1980 09:24:31 22-Dec-1980 09:21:22
2209 2210 2211 2212 2213 2214 2215	004354 004400 004430 004532 004562 004610 004644 004676 004750 005004 005034 005110 005150 005216	FMT.1= FMT.2= FMT.3= FMT.4= FMT.5= FMT.6= FMT.7= FMT.8= FMT.10= FMT.10= FMT.11= FMT.12= FMT.12= FMT.13= FMT.14= FMT.15= FMT.15= FMT.16= FMT.16= FMT.18=	P.AAA P.AAB P.AAC P.AAC P.AAC P.AAF P.AAF P.AAG P.AAH P.AAJ P.AAJ P.AAAL P.AAM P.AAR P.AAC P.AAR P.AAC	
2216 2217 2218 2219 2220 2221 2222 2223 2224 2225 2226 2227 2228 2229 2230 2231 2232 2233	005342 005412 005462 005534 005606 005652 005722 005746 005764 006016 006024 006034	FMT.19= FMT.20= FMT.21= FMT.22= FMT.23= FMT.24=	P.AAV P.AAW P.AAX	
2229 2230 2231 2232 2233 2234 2235 2236 2237 2238 2239 2240 2241 2242 2243 2243 2244 2245 2244 2245 2248 2248 2249 2250	005746 005764 006016 006024 006034 006046 006062 006100 006120 006142 006142 006214 006214 006276 006302 006314 006322 006330 006336 006344 006360 006374	FMT.25= FMT.26= ONE.FMT= TWO.FMT= THR.FMT= FOR.FMT= FIV.FMT= SIX.FMT= SEV.FMT= EIG.FMT= NIN.FMT= TEN.FMT= URD.1= URD.2= URD.3= URD.5= URD.5= URD.6= URD.6= URD.9=	P.AAY P.AAZ P.ABA P.ABB P.ABC P.ABC P.ABF P.ABF P.ABG P.ABI P.ABJ P.ABJ P.ABJ P.ABJ P.ABN	

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CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 86
2252 2253 2254 2255 2256 2256 2257 2258 2259 2260 2261 2262 2263 2263 2264 2265 2265 2265 2266 2267 2268 2267 2268 2269 2270 2268 2270 2271 2272 206002 2271 2272 20602 2277 2268 2277 2268 2277 2268 2277 2268 2277 2268 2277 2268 2277 2268 2277 2268 2277 2268 2277 2268 2277 2268 2277 2268 2277 2268 2277 2268 2277 2268 2277 2268 2277 2266 2277 2278 2266 2277 2280 2281 2282 2283 2284 2282	### ### ##############################
2285 006732 2286 006746 2287 006754 2288 006764 2289 006772 2290 007004 2291 007016 2292 007026 2293 007036 2294 007046 2295 007054 2296 007066 2297 007074 2298 007104 2299 007114 2300 007122 2301 007130 2302 007140 2303 007152 2304 007156 2305 007174 2306 007210	WRD.41= P.ACY WRD.42= P.ACZ WRD.43= P.ADA WRD.44= P.ADB WRD.45= P.ADC WRD.46= P.ADD WRD.47= P.ADE WRD.49= P.ADF WRD.50= P.ADH WRD.51= P.ADI WRD.51= P.ADI WRD.52= P.ADI WRD.53= P.ADI WRD.53= P.ADK WRD.54= P.ADK WRD.55= P.ADM WRD.55= P.ADM WRD.56= P.ADM WRD.57= P.ADM WRD.57= P.ADO WRD.58= P.ADP WRD.59= P.ADP WRD.60= P.ADR WRD.61= P.ADS WRD.62= P.ADT

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CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 87	SEQ 0074
	;ML4	22-9 ec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
2308 2309 2311 2311 007216 2312 007224 2313 007232 2314 007240 2315 007246 2316 007252 2317 2318 007272 2318 007302 2320 007310 2321 007316 2322 007330 2324 007340 2324 007346 2325 007366 2326 007404 2327 007422 2328 007472 2338 007540 2331 007516 2332 007540 23331 007564 23333 007564 23337 007662 2334 007564 23357 007662 2338 007766 2338 007766 2338 007766 2338 007766 2338 007762 2341 007726 2341 007742 2342 007750 2343 007756 2336 007764 2341 007742 2345 010004 2341 007774 2345 010004 2347 010022 2348 010036 2351 010102 2355 010150 2356 010162 2357 010172 2358 010204 2359 010216 2359 010256	WRD.63= P.ADU WRD.64= P.ADV WRD.65= P.ADV WRD.65= P.ADX WRD.68= P.ADY WRD.69= P.ADZ WRD.70= P.AEA WRD.71= P.AEB WRD.71= P.AEB WRD.73= P.AEC WRD.73= P.AEC WRD.75= P.AEF WRD.75= P.AEF WRD.75= P.AEF WRD.75= P.AEF WRD.76= P.AEG WRD.77= P.AEA PHR.1= P.AEJ PHR.3= P.AEJ PHR.3= P.AEL PHR.5= P.AEM PHR.6= P.AEM PHR.6= P.AEM PHR.6= P.AED PHR.10= P.AEC PHR.11= P.AEC PHR.11= P.AEC PHR.11= P.AEC PHR.12= P.AEC PHR.12= P.AEC PHR.13= P.AEC PHR.14= P.AEC PHR.15= P.AEC PHR.15= P.AEC PHR.15= P.AEC PHR.15= P.AEC PHR.15= P.AEC PHR.16= P	

BO ML-11 LOGIC TEST LLAMEOUS CODING SECTION 64 65 66 67 010266 68 010274	MACRO M1113 22-DEC-80 12:16 PAGE :ML4 : REG.2= P.AFU P.AFV		SEQ 0075 22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<	
2367 2368 2369 2370 2371 2371 2372 2373 2374 2375 2374 2375 2376 2376 2377 2378 2378 2379 2380 2381 2381 2382 2383 2384 2385 2384 2385 2388 2388 2388 2389 210454 2389 2390 2390 2391 2392 2391 2392 2392 2393 2394 211060 2397 211156 2397 211156 2398 211156	REG.4= P.AFW REG.5= P.AFX REG.6= P.AFY REG.7= P.AFZ REG.8= P.AGA REG.9= P.AGB REG.10= P.AGC REG.12= P.AGC REG.13= P.AGF REG.15= P.AGF REG.15= P.AGH REG.16= P.AGI REG.16= P.AGI REG.17= P.AGJ REG.18= P.AGK REG.19= P.AGK REG.20= P.AGM REG.21= P.AGM REG.21= P.AGN ASYNC= P.AGO SYNC= P.AGO SYNC= P.AGO SYNC= P.AGC SYNC= P.AGC ARR.DAT= P.AGC INTER= P.AGC DATA.LATE= P.AGC TRBLE.LOOP= P.AGV RH.ERROR= P.AGW TIME.OUT= P.AGX D1.TEMP= NIB.SA D2.TEMP= NIB.SA			
N 0134N	LOAD STACK.			
406 013604 407 013604 004167 170250 408 013610 016601 000012 409 013614 012702 012600 410 013620 060102 411 013622 016601 000014 412 013626 016600 000012 413 013632 006300 414 013634 066007 013640 415 013640 000024 416 013642 000042	JSR R1,\$SAVE3 MOV 12(SP),R1 MOV #STK.OFF,R	: NIB.PTR,* : STK.PTR,* : NIB.PTR,*	2217 2272 2268	
2412 013626 016600 000012 2413 013632 006300 2414 013634 066007 013640 2415 013640 000024 2416 013642 000042	ASL RO ADD 1\$(RO),PC 1\$: .WORD 2\$-1\$.WORD 3\$-1\$,	

SEQ 007			*** 4	OGIC TEST ODING SECTION	
22-9 ec-1980 09:24:31 TO 22-9 ec-1980 09:21:22 PA			ML4		2418 2419
		RD 4\$-1\$ RD 5\$-1\$. WORD	000060	2421 013644
		0 6\$-1\$. WORL	000076	2423 013650
		0 /3-15 0 9\$-1\$. WORD	000160	2425 013654
		105-15 125-15 10 155-15	. WORD	000176	2427 013660
22	,	W 155-15 R3	2\$: CLR	000260	2428 013664
		R1,R0 R3,R0	. WORD .	010100	2430 013670
		NIB.SAVE,R3	WOY	016703 175252	2433 013674
22	;	NIB.SAVE,R3 14\$ R3	3\$: BR CLR BISB MOV	000500	2434 013700
		R1,R0 R1,R0 R3,R0 NJB.SAVE,R3	MOV	010100	2436 013704
		NIB.SAVE,R3	SUB	016703 175234	2438 013710
22	:	85 R3	4\$: CLR	005003	2440 013716 2441 013720
		8\$ R3 (R2),R3 R1,R0 R3,R0 NIB.SAVE,R3	4\$: CLR BISB MOV SUB MOV	010100	2443 013724
		NIB. SAVE, R3	WOA	160300 016703 175216	2444 013726 2445 013730
22	:	R3 (93) P3	5\$: BR CLR BISB	000461 005003 151203	2447 013736
		R1,R0	MOV	010100	2449 013742
		NIB. SAVE , R3	MOV	016703 175200	2451 013746
22	:	R3 (R2) R3	6\$: CLR	160300 016703 000437 005003 151203 010100	2453 013754
		R1,R0 R3,R0	MOV	010100	2455 013760 2456 013762
		NIB.SAVE+2,R3	MOV	016703 175164	2457 013764 2458 013770
22	;	13\$ R3 (R2),R3 R1,R0 R3,R0 NIB.SAVE,R3 11\$ R3 (R2),R3 R1,R0 R3,R0 NIB.SAVE+2,R3 14\$ R3 (R2),R3 R1,R0 R3,R0 NIB.SAVE+2,R3 R3 R	SUB MOV BR CLR BISB MOV SUB	010100 160300 016703 000444 005003 151203 010100 160300 016703 006203 006203 006203 006203 006203 006203 006203 006203 006203 006203 006203 006203 006203 006203	2450 013744 2451 013746 2452 013752 2453 013754 2454 013756 2455 013760 2456 013762 2457 013764 2458 013770 2459 013772 2460 013774 2461 013776 2462 014000 2463 014000 2463 014000 2464 014010 2466 014010 2466 014010 2467 014020 2470 014020 2471 014024 2472 014026
		R1 .R0 R3 .R0	MOV	010100 160300	2461 013776 2462 014000
		NIB.SAVE+2,R3 R3	8\$: MOV	016703 175146 006203	2463 014002 2464 014006
		R3 R3	ASR ASR	006203 006203	2465 014010 2466 014012
		R3 14\$	ASR BR	006203 000431	2467 014014 2468 014016
22		R3 (R2),R3	9\$: CLR BISB	005003 151203	2469 014020 2470 014022
		R1,R0 R3,R0	MOV	010100 160300	2471 014024 2472 014026

- .

MLABO ML-11 LO SCELLANEOUS CO								SEQ 0077
2474			ML4				22-Dec-1980 09:24:3 22-Dec-1980 09:21:2	TOP
2477 014030 2478 014034 2479 014036 2480 014040 2481 014042	016703 000421 005003 151203 010100	175120	10\$:	MOV BR CLR BISB MOV	NIB.SAVE+2,R3 13\$ R3 (R2),R3 R1,R0			2300
2482 014044 2483 014046 2484 014052 2485 014054 2486 014056	160300 016703 006203 006203	175102	115:	SUB MOV ASR ASR ASR BR	R3,R0 NIB.SAVE+2,R3 R3 R3 R3 R3 R3 13\$ R3 (R2),R3			
2486 014056 2487 014060 2488 014062 2489 014064 2490 014066 2491 014070 2492 014072	000406 005003 151203 010100		12\$:	BISB	R3 R3 (R2) ,R3 R1 ,R0	,		2304
2493 014074	016703	175056	13\$:	MOV SWAB	R1,R0 R3,R0 NIB.SAVE+4,R3 R3			
2495 014102 2496 014106 2497 014112	042703 105060 150360	177760 012612 012612	13\$: 14\$:	BIC CLRB BISB	STACK(RO)			224
2499 014120 2500 014122 2501 014124	005003 151203		15\$:	CLR	PC R3 (R2),R3 R3,R1 NIB.SAVE+4,R3			2266 2306
2500 014122 2501 014124 2502 014126 2503 014132 2504 014134 2505 014136 2506 014140 2507 014142	160301 016703 006203 006203 006203	175024		BIC CLRB BISB RTS CLR BISB SUB MOV ASR ASR ASR SWAB	NIB.SAVE+4,R3 R3 R3 R3 R3 R3 R3 R3 W177770,R3			
2507 014142 2508 014144 2509 014150 2510 014154 2511 014160 2512 2513 2514 2519 2520	006203 006203 000303 042703 105061 150361 000207	177770 012612 012612		SWAB BIC CLRB BISB RTS	R3 #177770,R3 STACK(R1) R3,STACK(R1) PC			2217

2522 :ML4 2523 :		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (6)</neale>	
2522 :ML4 2524 : 2313 2526 : 2314 2527 : 2315 2528 : 2316 2529 : 2317 2530 : 2318 2531 : 2319 2532 : 2320 2533 : 2321 2534 : 2322 2535 : 2323 2536 : 2324 2537 : 2325 2538 : 2326 2539 : 2327 2540 : 2328 2541 : 2329 2542 : 2330 2543 : 2331 2544 : 2332 2544 : 2333 2544 : 2333 2545 : 2336 2546 : 2335 2547 : 2335 2548 : 2336 2557 014162 152777 2560 014204 012777 2560 014204 012777 2561 014212 000207 2562 2563 2564 2569 2570	routine DAT_DM_XFER : novalue = ++ FUNCTIONAL DESCRIPTION: A REPEATEDLY CALLED SEQUENCE OF ASSIGNMENT EXPRESSIONS TO LOAD THE DSA, BUS ADRS AND WORD COUNT REGISTERS WITH APPROPRIATE INFORMATION BEFORE A MASS BUS TRANSFER CAN COMMENCE LOADS A MASS BUS BLOCK TRANSFER, IN DIAGNOSTIC MODE, AT THE GOOD			
2538	begin DAT_DM = ONE; MLDA = .GOOD_BLK; MLBA = IO_BUF; MLWC = not 255; end;	SET DATA DIAG MODE LOAD DSA REG WITH THE LOAD BUS ADRS REG WIT LOAD WORD COUNT REG W	GOOD BLOOK ADRS TH THE IO BUF ADRS VITH COMPEIMENT 256	
2556 014162 2557 014162 152777 2558 014170 016777 2559 014176 012777 2560 014204 012777 2561 014212 000207 2562 2563 2564 2569	000010 177104 175370 177006 011600 176770 177400 176752 BISB #10.aml.REG+12 MOV GOOD.BLK,aml.R MOV #10.BUF,aml.REG+ RTS PC ROUTINE Size: 13 words RAXIMUM Stack depth per invo	•		2332 2333 2334 2335 2313

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22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                     TOPS-20 Bliss-16 V2(212)
                                                                                     PA: <NEALE>BL3ML4.BL1.2 (7)
routine STRIPPER (WRD_CNT, NIB_CNT) : novalue =
    begin
! FUNCTIONAL DESCRIPTION:
         STRIPPER RUNS IN DATA DIAG MODE AND STRIPS OUT A VARIABLE NUMBER OF GOOD NIBBLES FROM THE GOOD BLOCK AND STORES THEM INTO A CONTIGIOUS
         STACK FOR SEQUENTIAL ACCESSING
FORMAL PARAMETERS:
WRD CNT
TELES STRIPPER HOW MANY WORDS IN THE GOOD
         BLOCK TO READ OUT
         NIB CNT
TELES STRIPPER HOW MANY NIBBLES TO
         STRIP OUT OF EACH WORD.
 IMPLICIT INPUTS:
     local
         STK_PTR;
                                                         !STACK POINTER
     incr CNT from 0 to .NIB_CNT do
                                                        !CLEAR OUT THE STACK OFFSETS
         STK_OFF [.CNT] = ZEROES;
    CLR_MBUS;
STK_PTR = -1;
DAT_DM_XFER ();
MLCS1 = read;
                                                         !RESET THE STACK POINTER
                                                         SET UP A DATA XFER AT THE GOOD BLOCK
                                                         !DO A READ XFER
    DELAY (ONE_US);
                                                         !ALLOW PROM DATA TO GET INTO THE MLPD REG
     incr CNT from 0 to .WRD_CNT do
                                                         !LOAD THE STACK WITH ALL GOOD NIBBLE DATA
         begin
         PD_TEMP = .MLPD;
                                                         !GET THE PROM DATA
         DAT CLK = ONE;
                                                         !CLOCK OUT THE DATA WORD
         DELAY (ONE_US);
                                                         !READ DATA DIAG REGS INTO NIBBLE SAVE
         RD_LNG_WRD;
         incr NIB_PTR from 0 to .NIB_CNT do
                                                        STRIP OUT X NUMBER OF NIBBLES
              begin
STK_PTR = .STK_PTR + 1;
                                                         !INCREMENT THE STACK POINTER
              if .PD_TEMP [.NIB_PTR] IS_SET
                                                         !SEE IF THIS A GOOD NIBBLE
                  STK_OFF [.NIB_PTR] = (.STK_OFF [.NIB_PTR]) + (.NIB_CNT + 1)
!THEN INCREMENT IT'S STACK OFFSET
                   LOAD_STACK (.STK_PTR, .NIB_PTR);
                                                                 !ELSE LOAD THE NIBBLE IN THE STACK
```

628 :ML4			,				22-Dec-1980 22-Dec-1980	09:24:31	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (7)</neale>	
30							22-Dec-1980	09:21:22	PA: <neale>BL3ML4.BL1.2 (7)</neale>	
31 :	2389 2390		end	;						
33	2391		end;			1				
331 : 332 : 333 : 334 : 335 :	2391 2392 2393 2394	CLR	MBUS;							
21 22					.GLOBIL	LSDLY				
\$47 014214 \$48 014214				STRIPP						
48 014214 49 014220	004167	167676			JSR TST	R1.\$SAVE5 -(SP)		:		2337
548 014214 649 014220 650 014222 651 014226 652 014230 653 014232 654 014236 655 014240 656 014242 657 014244 658 014252 659 014256 660 014262 661 014270 663 014300 664 014316 667 014320 668 014324 669 014326	004167 005746 016604 005001 000403 105061 005201 020104 003773 152777 016705 042705 142777 150577 012705 0014767 012777 012701 001410 016702 001403	000020			MOV CLR	20(SP),R4 R1		: NIR.	CNT,*	2362
553 014232	105061	012600		15:	BR CLRB	2\$ STK.OFF(R1)		; *(CN	T)	2363 2362
653 014232 654 014236 655 014240	005201 020104			2\$:	INC	R1 R1.R4		; CNT		2362
556 014242 557 014244 558 014252 559 014256 560 014262 561 014270	003773 152777	000040 177324	176742		BLE	12		,		2363
558 014252 559 014256	016705 042705	1////0			BIC	#40, aml.REG+40 ML.DUT, R5 #177770, R5 #7, aml.REG+40 R5, aml.REG+40				
60 014262 61 014270	142777	000007 176720	176724		BICB	#7, aML.REG+40				
62 014274 63 014300 64 014304 65 014312 66 014316	012705	1/////			MOV	N-1.R5 PC.DAT.DM.XFER		; *,STI	K.PTR	2366
64 014300	012777	177656	176642		JSR MOV	PC.DAT.DM.XFER		:		2366 2367 2368 2369
55 014312	012701	000071 000001		70	MOV	#1,R1		: *,\$\$	TMP2	2369
57 014320	016702	165572		3\$:	MOV	LSDLY,R2		; *,\$\$	TMP1	
69 014326	005016 005302			45:	BEQ CLR	5\$ (SP) R2		; SSTM		
70 014330	005302				DEC	R2 4\$; SSTM	P1	
72 01334	005301			5\$:	BNE DEC BR	R1		; SSTM	P2	
74 014340	005003			6\$:	CLR	3\$ R3 16\$; CNT		2371
76 014344	017767	177034	176546	75:	BR MOV	amL .REG+230 .PD.	TEMP			2373
77 014352	152777	177034 000020 000001	176546 176714		BISB	aml.REG+230,PD. #20,aml.REG+120 #1,R1			7402	2373 2374 2375
79 014364	001410			8\$:	MOV BEQ	115		; *,\$\$		23/3
570 014330 571 014332 572 014334 573 014336 574 014340 575 014342 576 014344 577 014352 578 014360 579 014364 580 014366 581 014372	005302 001375 005301 000767 005003 000502 017767 152777 012701 001410 016702 001403	165524			MOV BEQ	LSDLY,R2 10\$; *,\$\$	TMP1	

2683 2684				:ML4				22-9 ec-1980 09:24:3 22-9 ec-1980 09:21:2	1 TOP
2685	005016			9\$:	CLR	(SP)	: SSTMP ; SSTMP1	22-7-60-1700 07:21:2	2 74.
2688 014400	001375			100.	BNE	9\$			
2686 014374 2687 014376 2688 014400 2689 014404 2690 014404 2691 014406 2692 014414 2693 014422 2694 014430 2695 014434 2696 014434 2697 014436 2698 014440 2699 014442	000767 017767	176732	174536	10\$: 11\$:	CLR DEC BNE DEC BR MOV	(SP) R2 9\$ R1 8\$ aML.REG+170.D1.TEMP aML.REG+200.D2.TEMP aML.REG+160.E2.TEMP	; \$\$TMP2		
2692 014414 2693 014422	017767	176732 176734 176706	174536 174532 174526		MOV	aml.REG+200,D2.TEMP			
2695 014432	005002				CLR BR INC	15 %	; NIB.PTR		237
2696 014434 2697 014436 2698 014440	005205 010201 006201			12\$:	MOV	R2 15\$ R5 R2,R1 R1 R1	: STK.PTR : NIB.PTR,*		238 238
2699 014442 2700 014444	006201 006201				ASR ASR ADD MOV	R1 R1			
2702 014452	010146	013120			MOV	MPD.TEMP,RT R1,-(SP)			
2704 014456 2705 014456	010246	177770 000001			MOV BIC MOV	MPD.TEMP,R1 R1,-(SP) R2,-(SP) M177770,(SP) M1,-(SP) -(SP) PC,BL\$GT2 M10,SP	; NIB.PTR,*		
2706 014466 2707 014470	005046				CLR	-(SP) PC_BL\$GT2			
2708 014474 2709 014500	062706 005300	166444 000010			CLR JSR ADD DEC BNE	#10,SP R0			
2710 014502 2711 014504 2712 014506 2713 014512	001011 005001 156201	012600			CLR BISB	R0 13\$ R1	: 4/NID DID) 4		238
2712 014506 2713 014512 2714 014514	060401	012000			ADD	STK.OFF (R2) ,R1 R4,R1 R1,R0	; *(NIB.PTR),*		
2715 014516 2716 014520	005200 110062	012600			MOV INC MOVB	RO STK.OFF (R2)	; *,*(NIB.PTR)		
2717 014524 2718 014526	010546	1.		13\$:	BR MOV	145 R5,-(SP)	STK.PTR.*		2382 2382
2715 014516 2716 014520 2717 014524 2718 014526 2719 014530 2720 014532 2721 014536 2722 014540 2723 014542 2724 014544 2725 014544 2726 014550 2727 014554 2728 014556 2729 014564 2730 014570 2731 014574 2732 014606 2733 014606 2734 014610 2735 2736	110062 000405 010546 010246 004767	177046			MOV JSR CMP INC CMP BLE INC CMP BLE BISB	RO STK.OFF (R2) 14\$ R5,-(SP) R2,-(SP) PC,LOAD.STACK (SP)+,(SP)+ R2 R2,R4 12\$ R3,22(SP) 75	; NIB.PTR,*		
2722 014540 2723 014542	005202			14\$: 15\$:	INC	R2 R2.R4	: NIB.PTR : NIB.PTR,*		2370
2724 014544 2725 014546	003733				BLE	12\$ R3			2371
2726 014550 2727 014554	020366	000022		16\$:	BLE	R3,22(SP)	: CNT : CNT, WRD. CNT		
2728 014556 2729 014564	152777 016705	000040 177012 177770 000007 176406	176430		MOV	#40,aML.REG+40 ML.DUT,R5	:		2391
2730 014570	142777	000007	176412		BIC BISB BISB	#177770,R5 #7,aML.REG+40			
2733 014606 2734 014610	152777 016705 042705 142777 150577 005726 000207	1/0400			TST RTS	#40,aML.REG+40 ML.DUT.R5 #177770.R5 #7,aML.REG+40 R5,aML.REG+40 (SP)+ PC	:		2337

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION MACRO M1113 22-DEC-80 12:16 PAGE 95 7

SEQ 0082

:ML4

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22

TOPS PA:<

```
2749 :ML4
2750 :
2751 :
2752 :
2753 :
2754 :
2755 :
2756 :
2757 :
2758 :
2759 :
2760 :
                                                                                         22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                        TOPS-20 Bliss-16 V2(212)
                                                                                                                        PA: <NEALEXBL3ML4.BLI.2 (8)
                          routine CAL_CRC : novalue =
                                begin
                           ! FUNCTIONAL DESCRIPTION:
                                     CALCULATE THE CRC CODE FOR THE GOOD BLOCKS FIRST CRC GROUP (60 NIBBLES) BY STRIPPING
                                     OUT GOOD NIBBLES AND MODULO 2 ADDING EACH
                                     BITS WEIGHT RESULTING IN A CAL, B CAL, P CAL.
                             IMPLICIT IMPUTS:
                                     P CAL A CAL B CAL
GOLBAL STORAGE LOCATION WHERE CALCULATED
CRC CODES ARE STORED AND EXAMINED FROM
                                     REM_TBL
                                     TABLE CONTAINING PRECALCULATED BIT POSITION
                                     WEIGHTS USED IN THE MODULO 2 CALCULATION OF THE
                                     CRC CODE
2771
                                Local
2774
2775
2776
                                     CHANNEL,
                                                                                         !CHANNEL POINTS TO THE BITS IN A WORD
                                     STK_PTR,
NIB_SAV : bitvector [4],
                                                                                         STACK POINTER
                                                                                         INIBBLE SAVE LOCATION
                                     ALOG,
2777
                                                                                         !INDEX INTO REMAINDER TABLE
2778
2779
2780
2781
2782
2783
2784
2785
2786
2786
2789
2790
2791
2792
2793
2794
2795
2796
2797
2798
                                     BLOG:
                                                                                         !INDEX INTO REMAINDER TABLE
                                STRIPPER (12, 9);
                                                                                         ISTRIP OUT ALL 10 NIBBLE FROM 13 ARRAY WORDS
                                STK_PTR = -1;
                                                                                         !RESET STACK POINTER
                                incr PLOG from 0 to 5 do
                                                                                         !PLOG POINTS TO ONE CRC GROUP '6 WORDS'
                                     begin
CHANNEL = -1;
                                                                                         !RESET CHANNEL FOR EACH WORD
                                     incr NIB_CNT from 0 to 8 do
                                                                                         !READ DATA NIBBLE 0-8 AND CALCULATE CRC
                                          begin
STK_PTR = .STK_PTR + 1:
                                                                                         !INCREMENT THE STACK POINTER
                                          NIB_SAV = .stack [.STK_PTR];
                                                                                         !LOAD NIV_SAV WITH A STACK NIBBLE
                                          incr BIT_TST from 0 to 3 do
                                                                                         !TEST THE BIT FOR BEING SET
                                               CHANNEL = . CHANNEL + 1;
                                                                                         !CHANNEL POINTS TO THIS BIT
                                               if .NIB_SAV [.BIT_TST] IS_SET !SEE IF THIS BIT IS SET
                                               then
                                                    begin
                                                                                         !IF SET THEN MOD 2 ADD THE REMAINDER
                                                    ALOG = .PLOG + .CHANNEL;
BLOG = .PLOG + .CHANNEL*2;
                                                                                         CALCULATE THE ALOG
                                                    while .ALOG geg 63 do
ALOG = .ALOG - 63;
                                                                                         !REDUCE ALOG UNTIL < 64
```

#1,-(SP) -(SP)

MOV CLR

SEQ 008						CTION	ODING SE	O ML-11 LO
22-Dec-1980 09:24:31 TO 22-Dec-1980 09:21:22 PA					:ML4			0
			PC_BL\$GT2 #10_SP R0 7\$	JSR ADD DEC		166164 000010	004767 062706 005300 001066	3 014750 4 014754 5 014760
24	; PLOG,ALOG	:	7\$ 4(SP)_R1	DEC BNE MOV ADD MOV		000004	001066 016601	6 014762
24	; PLOG, ALOG ; CHANNEL, ALOG ; CHANNEL, *	:	4(SP),R1 10(SP),R1 10(SP),R5 R5	MOV ASL		000004 000010 000010	016605 006305 066605	9 014774 0 015000
	; PLOG,* ; *,BLOG ; ALOG,*		4(SP) .R5	ADD		000004	066605	7 015002
24	: ALOG,*	:	R5 .R2 R1 .W77 S\$	CMP	4\$:	000077	020127	3 015010
24 24 24	: *,ALOG		5\$ #77,R1 4\$	ADD MOV CMP BLT SUB BR CMP		000077	020127 002403 162701 000772 020227	5 015016 6 015022
	; BLOG,*	;	4\$ R2.#77 6\$	BLT	5\$:	000077	002403	7 015024 8 015030
24 24 24	: *.BLOG	:	6\$ #77,R2 5\$	SLB		000077	162702	9 015032 0 015036
24	PLOG,*	:	DE	BR CLR MOV BISB	6\$:	000004 013434	005005 016604 156405	3 014750 4 014754 5 014760 6 014762 7 014764 8 014770 9 014774 10 015000 11 015000 12 015016 15 015016 15 015016 16 015022 17 015030 18 015030 19 015036 10 015036 11 015040 12 015042 13 015046 14 015052 15 015054 16 015070 17 015064 18 015070 19 015076 10 015100 12 015104
			RS 4(SP),R4 REM.TBL (R4),R5 R5,-(SP) P.CAL, (SP) R5,P.CAL (SP)+,P.CAL R5 REM.TBL (R1),R5 R5,R4 A CAL P4	MOV BIC BIC BIS		176064 176060 176054	046716 040567 052667	S 015054 6 015060 7 015064
24	*(ALOG) .*	:	R5 REM. TBL (R1), R5	BISB		013434	005005 156105 010504	6 015060 17 015064 18 015070 19 015072 10 015076
			R5.A.CAL	BIC BIC BIS		176034 176030 176024	046704	
24	* (BLOG) .*	:	R5 REM. TBL (R2),R5	CLR BISB		013434	050467 005005 156205	4 015114 5 015116
			R5 REM. TBL (R2), R5 R5, R4 B. CAL, R4 R5, B. CAL R4, B. CAL R3 R3, M3 38	MOV BIC BIS INC CMP BLE INC CMP BLE INC		176012 176006 176002	010504 046704 040567	15 015110 14 015114 15 015116 16 015122 17 015124 18 015130 19 015134 10 015140 11 015142 12 015164 13 015154 15 015164 16 015164 17 015170 18 015174 19 015204 11 015210
24	BIT.TST,*	:	R3 R3,#3	INC	7\$:	000003	005203 020327	ó ő15140 1 015142
24	; NIB.CNT ; NIB.CNT,*		12(SP) #10	BLE INC CMP	000010	000012	003657 005266 026627	12 015146 13 015150 14 015154
24	: STK.PTR		2\$ 6(SP) 4(SP) 4(SP) #5	INC INC		000006	026627 003637 005266 005266 026627 003622 062706 000207	3 015150 4 015154 05 015162 16 015164 17 015170
	PLOG,*	;	15	CMP BLE ADD RTS	000005		026627 003622	08 015174 09 015202
23	:	;	#16,SP PC	RTS		000016	000207	9 015202 0 015204 1 015210

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION MACRO M1113 22-DEC-80 12:16 PAGE 99 7

SEQ 0086

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22

TOPS PA:<

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                                                                                                                                                                                                                                                                                                                                                           TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (9)
                                                                                    routine ERR_CHK_CRC =
                                                                                                  begin
                                                                                    ! FUNCTIONAL DESCRIPTION:
                                                                                                                  "ERROR CHECK CRC" COMPARES HARDWARE GENERATED CRC
CODES TO SOFTWARE CALCULATED CRC CODE FOR ONE
                                                                                                                 CRC GROUP AND RETURNS A ONE ON DETECTION OF ERRORS
                                                                                           IMPLICIT INPUTS:
                                                                                                                 P CAL, A CAL, B CAL
GEOBAL STORAGE LOCATIONS WHERE CALCULATED
                                                                                                                 CRC CODES ARE STORED AND EXAMINED FROM
                                                                                                                P_GEN,A_GEN,B_GEN
GEOBAL STROAGE LOCATIONS WHERE HARDWARE GENERATED
CRC CODES ARE STORED AND EXAMINED FROM
                                                                                                  local
                                                                                                                                                                                                                                                                    !VARIABLE FOR FIELD SELECTOR
                                                                                                                  TEMP:
                                                                                                                                                                                                                                                                   !TEMPORARY STORAGE LOCATION
                                                                                                  POS = -1:
                                                                                                                                                                                                                                                                   !RESET THE POS
                                                                                                   incr STK_PTR from 9 to 59 by 10 do
                                                                                                                                                                                                                                                                   !CONVERT VERTICAL STACK CRC TO HOR/ZONTAL REPRESENTATION
                                                                                                               begin

POS = .POS + 1;

TEMP = .stack [.STK_PTR];

(P_GEN)<.POS, 1> = .TEMP<0, 1>;

(A_GEN)<.POS, 1> = not .TEMP<1, 1>;

(B_GEN)<.POS, 1> = not .TEMP<2, 1>;
                                                                                                                                                                                                                                                                    !INCREMENT POS
                                                                                                                                                                                                                                                                     READ CRC NIB FROM THE STACK
                                                                                                                                                                                                                                                                     !CONVERT 1ST BIT
                                                                                                                                                                                                                                                                    CONVERT 2ND BIT
                                                                                                                 end:
                                                                                                  if (.P_GEN neg .P_CAL) or (.A_GEN neg .A_CAL) or (.B_GEN neg .B_CAL) then return ONE else return ZERO;
                                                                                                                                                                                                                                                                   !COMPARE GENERATED CRC CODE TO CALCULATED CRC CODE
                                                                                                  end:
                                                                                                                                              ERR.CHK.CRC:
                                                      004167
012701
012702
005201
005003
                                                                                                                                                                                                        R1,$SAVE3
                                                                                                                                                                            JSR
                                                                                                                                                                            MOV
                                                                                                                                                                                                        #11,R2
R1
R3
                                                                                                                                                                                                                                                                                                                              * STK.PTR
                                                                                    000011
                                                                                                                                                                            MOV
                                                                                                                                              15:
                                                                                                                                                                             INC
                                                                                                                                                                                                                                                                                                                              ; TEMP
```

2081				;ML4				22-01090 00-24-71	TOPS
2981 2982 3083				:				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
2984 015232	156203	012612		*	BISB	STACK(R2),R3 MP.GEN,-(SP) R1,-(SP) M1,-(SP) R3,-(SP) M177776,(SP)		*(STK.PTR),TEMP	2/01
2986 015242	010146				MOV	R1,-(SP)	:	POS,*	2493
2988 015250	010346	000001			MOV BIC	R3,-(SP)	;	TEMP,*	
2989 015252 2990 015256	042716	177776 166114 013146			BIC JSR	#177776,(SP) PC.BL\$PU2			
2991 015262 2992 015266	012716	013146			JSR MOV MOV	#A.GEN, (SP) R1(SP)		POS,*	2494
2993 015270	012746	000001			MOV	#1,-(SP)	•	-	
2995 015276	032703	000002			MOV CLR BIT BEQ INC	#177776, (SP) PC,BL\$PU2 #A.GEN, (SP) R1,-(SP) #1,-(SP) -(SP) #2,R3 2\$ (SP) (SP) PC,BL\$PU2 #B.GEN, (SP) R1,-(SP) #1,-(SP) #1,-(SP) -(SP) #4,R3	:	*,TEMP	
2997 015304	005216				INC	(SP)			
2999 015310	005116	166062 013150		2\$:	JSR	PC,BL\$PU2			
3000 015314 3001 015320	012716	013150			MOV	MB.GEN, (SP) R1,-(SP)		POS,*	2495
002 015322 003 015326	012746	000001			MOV	#1,-(SP) -(SP)			
004 015330	032703	000004			BIT	#4,R3	;	*,TEMP	
006 015336	005216			70.	BEQ	#4,R3 3\$ (SP) (SP)			
008 015342	004767	166030		3\$:	COM JSR	PC ,BL\$PU2			
3010 015352	062702	166030 000024 000012 000073			ADD	PC.BL\$PU2 #24,\$P #12,R2 R2,#73 1\$:	*,STK.PTR STK.PTR,*	2490 2489
5011 015356 5012 015362	020227	000073			CMP BLE CMP	R2,#73 1\$;	STK.PTR,*	
8013 015364 8014 015372	026767	175562	175552		CNP	P. GEN.P. CAL	:		2498
3014 015372 3015 015374 3016 015402 3017 015404 3018 015412 3019 015414 3020 015420 3021 015422 3022 015424 3023 015426	001010 026767 001004 026767 001403 012701 000401 005001 010100 000207	175546	175536		BNE CMP BNE CMP BEQ MOV BR CLR MOV RTS	A. GEN.A. CAL			
5017 015404	026767	175540	175530		CMP	B.GEN,B.CAL			
3017 015404 3018 015412 3019 015414 3020 015420 3021 015422 3022 015424 3023 015426 3024 3025 3026 3031 3032	012701	000001		45:	MOV	B.GEN,B.CAL 5\$ #1,R1 6\$ R1			
3020 013420 3021 015422	005001			5\$: 6\$:	CLR	65 R1			
5022 015424 3023 015426	010100 000207			6\$:	MOV RTS	R1,R0 PC	1		2465 2464
3024 8025				· Pou		: 71 words k depth per invocation: 14			

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22-Dec-1980 09:21:22
                                                                                                                                                                                                                                                                                                                                                                                  TOPS-20 Bliss-16 V2(212)
PA: <NEALEXBL3ML4.BLI.2 (10)
                                                                                   routine FIND_COMP_BIT (BITS_XFERED) =
                                                                                                   begin
                                                                                         FUNCTIONAL DESCRIPTION:

'FIND COMPLIMENT' BIT IS CALLED BY THE

SYNDROME DECODE TESTS AND INDEXES INTO

THE IO_BUF LOOKING FOR COMPLIMENTED
                                                                                                                   BITS. A COMPLIMENTED BIT IS BY DEF A ONE
                                                                                                                   A ONE IS RETURNED IF THE INDEXED BIT IS
                                                                                                                  NOT SET
                                                                                           FORMAL PARAMETERS:
                                                                                                                   BITS_XFERED
                                                                                                                   REPRESENTS THE BIT BEING TESTED
                                                                                                                  IN THE DRIVE AND FROM THIS THE WORD AND BIT INDEX INTO THE
                                                                                                                   IO_BUF CAN BE CALCULATED
                                                                                           IMPLICIT INPUTS:
                                                                                                   local
                                                                                                                  COMP_WRD,
COMP_BIT,
BUF_SAV : bitvector [16];
                                                                                                                                                                                                                                                                                   WORD WHERE COMPLINEMT BIT IS LOCATED BIT THAT IS COMPLINEMTED
                                                                                                                                                                                                                                                                                    ISTORES THE WORD IN WHICH THE BIT IS COMPLIMENTED
                                                                                                  COMP_WRD = .BITS_XFERED/16;
COMP_BIT = .BITS_XFERED mod 16;
BUF_SAV = .IO_BUF [.COMP_WRD];
                                                                                                                                                                                                                                                                                   !CALCULATE THE COMP WORD !CALCULATE THE COMP BIT !LOAD THE COMP WORD INTO BUF_SAY !ZERO THE COMP WORD
                                                                                                    IO_BUF [.COMP_WRD] = ZEROES;
                                                                                                   if .BUF_SAV [.COMP_BIT] IS_NOT_SET then return ZERO else return ONE;
                                                                                                                                                                                                                                                                                   !FIND COMPLIMENTED BIT AND RETURN ERROR STATUS
                                                                                                   end;
                                                                                                                                                  FIND. COMP.BIT:
                                                  004167
005746
016646
012746
004767
010001
                                                                                                                                                                                                                  R1,$SAVE2
-(SP)
                                                                                   166410
                                                                                                                                                                                    JSR
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  2502
                                                                                                                                                                                    TST
                                                                                  000012
000020
166300
                                                                                                                                                                                                                  12(SP),-(SP)
#20,-(SP)
PC,BL$DIV
RO,R1
                                                                                                                                                                                   MOV
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  2530
                                                                                                                                                                                                                                                                                                                                                 : BITS.XFERED.*
                                                                                                                                                                                   MOV
                                                                                                                                                                                    JSR
                                                                                                                                                                                   MOV
                                                                                                                                                                                                                                                                                                                                                 : *, COMP. WRD
```

89 90				:ML4				22-Dec-1980 09:24: 22-Dec-1980 09:21:	31 TOP
91	016616	000016		•	MOV	16/50) (50)	; BITS.)		253
93 015460	016616	000016 000020 166274			MOV	16(SP),(SP) #20,-(SP)	; 6113.7	TERED,*	233
92 015454 93 015460 94 015464 95 015470 96 015472 97 015474 98 015476 99 015504 00 015510 01 015516 03 015516 04 015520 05 015524 06 015530	004767 010002	100274			JSR MOV ASL MOV CLR MOV ASR ASR ASR ADD MOV BIC MOV CLR JSR ADD TST BNE CLR BR MOV MOV	PC.BL\$MOD RO.R2 R1.R0	: *,COMP.	TIB.	253
97 015474	006300				ASL	RO			273
98 013476 99 015504	005060	011600 011600	000006		CLR	IO.BUF(RO),6(SP) IO.BUF(RO)	: *,8UF.	SAV	253
00 015110	010200	he bro			MOV	R1,R0 R0 IO.BUF(R0),6(SP) IO.BUF(R0) R2,R0 R0 R0 R0 R0 R0,R1 SP,R1 R1,R0 R0,-(SP) R2,-(SP) W1,77770,(SP) W1,-(SP) -(SP) PC,BL\$GT2 W10,SP R0	; comp.s	*,118	253 253
02 0155 4	006200				ASR	RO			
04 015520	012701	000006			MOV	#6,R1			
06 015526	060100				ADD	R1,R0	; BUF.SA	(V,*	
07 015530 08 015532	010046 010246				MOV	RO,-(SP) R2,-(SP)	; COMP.E	NT.*	
015532 09 015534 10 015540	042716	177770 000001			BIC	#177770 (SP)			
1 015544	005046				CLR	-(SP)			
3 015552	062706	165366 000010			ADD	#10,SP			
15 015560	005700				BNE	R0 1\$ R1 2\$ #1,R1 R1,R0			
16 015562 17 015564	005001				CLR	R1 25			
18 015566 19 015572	012701	000001		1\$: 2\$:	MOV	#1,R1			250
20 015574 21 015600 22 23 24 29	062706 000207	000010		20.	ADD RTS	#10,SP PC	;		250 250
22 23				; Rou	tine Size	: 53 words k depth per invocation: 1			

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3132 :ML4
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                                                                                                                                                      22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (11)
                                                    routine FORCE_REM (PLOG, CHAN) : novalue =
                                                             begin
                                                      FUNCTIONAL DESCRIPTION:

VIA ECC_DM AND THE PRECALCULATED MODULO 2

REMAINDERS STORED IN REM TBLCJ, THE

CRC_A,CRC_B AND PAR_CRC_GRD

ARE FORCED WITH ERRONEOUS CRC DATA TO

FORCE PERDICTABLE ECC ERRORS
                                                        FORMAL PARAMETERS:
          3148
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3159
                                                                     POINTS TO ONE OF THE SIX WORDS OF
                                                                     A CRC GROUP
                                                                     POINTS TO ONE OF 36 CHANNELS IN
                                                                     A CRC GROUP
                                                        IMPLICIT INPUTS:
                                                             local
                                                                     ALOG.
                                                                                                                                                       !INDEX INTO REM_TBL
          3160 :
3161 :
3162 :
3163 :
                                                                     BLOG;
                                                                                                                                                       !INDEX INTO REM_TBL
          3161
3162
3163
3164
3165
3166
3167
3168
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3171
3172
3173
3174
3175
                                                             ALOG = .PLOG + .CHAN;
BLOG = .PLOG + .CHAN*2;
                                                                                                                                                      CALCULATE A_LOG
                                                            while .ALOG geq 63 do
ALOG = .ALOG - 63;
                                                                                                                                                       !REDUCE A_LOG UNTIL < 64
                                                            while .BLOG geq 63 do
BLOG = .BLOG - 63;
                                                                                                                                                       !REDUCE B_LOG UNTIL < 64
                                                            ECC_DM = ONE;
PAR_CRC_WRD = .REM_TBL [.PLOG];
CRC_A = .REM_TBL [.ALOG];
CRC_B = .REM_TBL [.BLOG];
end;
                                                                                                                                                       !SET ECC DIAG MODE
!LOAD PAR_CRC_WRD WITH REM_TBL
!LOAD CRC_A WITH REM_TBL
!LOAD CRC_B WITH REM_TBL
         3183 015602
3184 015602
3185 015606
                                                                                     FORCE . REM:
                                    004167
                                                                                                                      R1,$SAVE3
14(SP),R0
                                                                                                      JSR
                                                                                                                                                                                       PLOG,*
                                    016600
                                                                                                     MOV
```

CZMLABO ML-11 L MISCELLANEOUS (OGIC TES	CTION	MACRO M		22-DEC-80	12:16 PAGE 105					0092
3187 3188 3189				:ML4					22-9 ec-1980 (22-9 ec-1980 (09:24:31 09:21:22	TOPS PA:<
3190 015612 3191 015614 3192 015620 3193 015624	010002 066602 016601 006301	000012 000012			MOV ADD MOV ASL	RO,R2 12(SP),R2 12(SP),R1 R1		: *,ALOG : CHAN,ALOG : CHAN,*			2567
3194 015626 3195 015630	060001 020227 003403	000077		1\$:	CMP	RO,R1 R2,#77 2\$: ALOG,*			2569
3197 015636 3198 015642	162702	000077			CMP BLT SUB BR	#77,R2 1\$: *,ALOG			2570 2569 2572
3199 015644 3200 015650	020127	000077		2\$:	BLT	R1 ,#77 3\$ #77 ,R1		; BLOG,*			
3202 015656 3202 015656	162701 000772 152777	000077	175/04	70.	SUB BR	25		: *,BL0G			2573 2572 2575 2576
3204 015666 3205 015672	116003	000001 013434	175406	3\$:	BISB MOVB SWAB	N1.aML.REG+120 REM.TBL(RO),R3 R3		;			2576
3200 015650 3201 015652 3202 015656 3203 015660 3204 015666 3205 015672 3206 015674 3207 015700 3208 015706 3209 015716 3211 015722 3212 015730 3213 015734 3214 015740 3215 015744 3216 015756 3218 3219 3220 3220	042703 042777 050377	140377 037400 175412 013434	175416		BIC BIS MOVB BIC BISB MOVB	#140377,R3					
3209 015712 3210 015716 3211 015722	116203 042703 142777	177700	175374		MOVB BIC BICB	REM. TBL (R2),R3 #177700,R3 #77, am. REG+150		; *(ALOG),*			2577
3212 015730 3213 015734 3214 015740	142777 150377 116103 042703 142777	000077 175370 013434	117514		BISB	R3, aML.REG+150 REM.TBL(R2),R3 #177700,R3 #77, aML.REG+150 R3, aML.REG+150 REM.TBL(R1),R3 #177700,R3 #77, aML.REG+160 R3, aML.REG+160		: *(BLOG),*			2578
3215 015744 3216 015752	142777	177700 000077 175356	175362		BIC BICB BISB	#77,aML.REG+160 R3,aML.REG+160					
3217 015756 3218	150377				RTS			:			2539
3219 3220 3225 3226				; Rout	tine Size: imum stack	55 words depth per invocation:	4 words				

```
22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                        TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (12)
                  2580
2581
2582
2583
2584
2585
2586
2586
2587
2588
2588
2590
2591
2592
2593
2593
2594
2595
2600
2601
2602
2603
2604
2608
2609
2610
2611
2611
2613
2614
2615
2616
2617
2618
                            routine FOR_ECC_ERR (SGL, UNC) : novalue =
                                   begin
                            FUNCTIONAL DESCRIPTION:

'FORCE ECC ERROR' WHEN CALLED WILL FORCE

VIA ECC DIAG MODE, SINGLE BIT ECC ERRORS

OR UNCORRECTABLE ECC ERRORS INTO THE
                                          ECC DIAGNOSTIC REGISTERS
                             ! FORMAL PARAMETERS:
                                          WHEN SET TO A ONE THIS
                                          ROUTINE WILL FORCE SINGLE BIT ERRORS
                                          UNC WHEN SET TO A ONE THIS ROUTINE WILL
                                          FORCE UNCORRECTABLE ECC ERRORS
                               IMPLICIT INPUTS:
                                   ECC_DM = ONE;
                                                                                                               !SET ECC DIAG MODE
                                   if .SGL
                                                                                                               !IF SGL IS TRUE
                                   then
                                        begin
PAR_CRC_WRD = %b'111111';
CRC_A = %b'000000';
CRC_B = %b'000000';
                                                                                                               !THEN FORCE A SINGLE BIT ERROR
                                   else
                                         begin
PAR_CRC_WRD = %b'111111';
CRC_A = %b'111111';
CRC_B = %b'000000';
                                                                                                               !ELSE FORCE A UNCORRECTABLE ERROR
                                          end;
                                   end;
015760
015760
015766
015774
                                                       FOR.ECC.ERR:
              152777
032766
001407
052777
                            000001
                                                                     BISB
                                                                                                                                                                                                               2602
2604
                                                                                   #1, aML. REG+120
                                                                                                                                          : *,SGL
                                                                                   #1,4(SP)
                                          000004
                                                                      BEQ
                            037400
                                         175320
                                                                                    #37400, aML.REG+150
                                                                                                                                                                                                               2607
```

ZMLABO ML-11 LOGIC TEST	MACRO M1113 22-DEC-8	0 12:16 PAGE 107°		SEQ	0094
3283 3284 3285 3286 016004 142777 000 3287 016012 000406 3288 016014 052777 037 3289 016022 152777 000 3290 016030 142777 000 3291 016036 000207 3292 3293 3294 3299 3300	00 175302 1\$: BIS 77 175274 BISB 77 175276 2\$: BICB RTS	#77.aml.REG+150 2\$ #37400.aml.REG+150 #77.aml.REG+150 #77.aml.REG+160 PC e: 24 words ck depth per invocation: 0 wor	rds	22-9ec-1980 09:24:31 22-Dec-1980 09:21:22	TOP PA: 260 260 261 261 261 258

.

; Routine Size: 9 words ; Maximum stack depth per invocation: 0 words

3348 3349	ML4					22	2-Dec-1980 09:24:31 2-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(21 PA: <nealexbl3ml4.bli.2< th=""><th>(14)</th></nealexbl3ml4.bli.2<>	(14)
3348 3349 3350 3351 3352 3353 3353 3356 3356 3363 3363 3363		2640 2641 2642 2643 2644 2645 2646 2647 2648 2650 2651 2652 2653 2656 2657 2658 2659 2660 2661 2662 2663	!++	IONAL DE A REPEA ASSIGN THE DSA COUNT R INFORMA TRANSFE LOADS A AT THE BY THE	SCRIPTION: ATEDLY CALLED SECUENT EXPRESSIONS A, BUS ADRS AND WEGISTERS WITH AFTION BEFORE A MARKET CAN COMMENCE A MASS BUS BLOCK GOOD BLOCK ADRS READ WRITE ARRAY ATA TEST	OUENCE OF TO LOAD WORD PPROPRIATE ASS BUS			
3369 3370 3371 3372 3373 3374		2658 2659 2660 2661 2662 2663	MLD	DIS = 0 A = .600 A = IO_8 C = not	D_BLK; BUF:		DISABLE ERROR CORRECT LOAD DSA REG WITH THE LOAD BUS ADRS REG WIT LOAD WORD COUNT REG W	TION E GOOD BLOOK ADRS TH IO BUF ADRS VITH COMPLIMENT 256	
33/8 3382 3384 3385 3386 3387 3389 3390 3395	016062 016062 016070 016076 016104 016112	152777 016777 012777 012777 000207	000002 173470 011600 177400	175204 175106 175070 175052	GD.BLK.XFER: BISB MOV MOV MOV RTS ; Routine Size: ; Maximum stack	#2,aML.REG+120 GOOD.BLK,aML.REG+3 #10.BUF,aML.REG+20 #-400,aML.REG+10 PC 13 words depth per invocati			

3398 3399	ML4						22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (15</neale>	5)
3399 3400 3400 3400 3400 3400 3400 3400		2664 2665 2666 2667 2668 2669 2671 2672 2673 2674 2675 2676 2677 2678 2679 2680 2681 2682 2683 2684 2685 2686	FUNCT	IONAL DE A REPEA ASSIGNM THE DSA COUNT R INFORMA TRANSFE LOADS A TRANSFE ADDRESS IN A = .LST A = IO_B IC = not	NE; BLK; OF; 255;	OUENCE OF TO LOAD WORD PROPRIATE BUS	DISABLE ERROR CORRECT LOAD DSA REG WITH THE LOAD BUS ADRS REG WIT LOAD WORD COUNT REG W	ION LAST BLOCK ADRS TH THE IO BUF ADRS VITH COMPEIMENT 256	
3432 3433 444	016114 016114 016122 016130 016136 016144	152777 016777 012777 012777 000207	000002 173444 011600 177400	175152 175054 175036 175020	LAST.BLK.XFER: BISB MOV MOV MOV RTS	#2,aML.REG+120 LST.BLK,aML.REG+3 #10.BUF,aML.REG+3 #-400,aML.REG+10 PC	30 :		20 20 20 20 20
3435 3436 3437 3438 3439 3444 3445					; Routine Size: ; Maximum stack	: 13 words depth per invocat	tion: 0 words		

```
3447 :ML4
3448 :
3449
                                                                                             22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                             TOPS-20 Bliss-16 V2(212)
                                                                                                                             PA: <NEALEXBL3ML4.BL1.2 (16)
routine TST_LNG_WRD (NIB_NUM, NIB_PAT, ERR_FLG) : novalue =
                                 begin
                             FUNCTIONAL DESCRIPTION:
COMPARES THE CURRENT NIBBLE
POSITION IN 'NIB SAVE' WITH THE
CURRENT TEST PATTERN. IF THE
                                      TWO VALUES ARE NOT EQUAL AN ERROR FLG IS SET WHICH THE CALLER CAN INTERIGATE
                              FORMAL PARAMETERS:
                                      NIB NUM
                                       CASE SELECT EXPRESSION TO SELECT THE
                                      CURRENT NIBBLE TO BE EXAMINED
                                      NIB PAT
                                      CURRENT NIBBLE PATTERN TO BE
                                      COMPARED
                                      ERR_FLG
                                       CONTAINS THE ADDRESS (PASSED BY REF)
                                      OF THE CALLERS ERROR FLG
TO ENABLE THE CALLER TO EXAMINE
THE ERROR STATUS OF THE ROUTINE CALL
                              IMPLICIT INPUTS:
                                      NIB SAVE
BLOCK OF 3 WORDS TO STORE
THE DATA FOUND IN MLD1, MLD2
AND MLE2 AFTER A DIAGNOSTIC MODE
                                      READ
                              IMPLICIT OUTPUTS:
                                                            NONE
                                 .ERR_FLG = ZERO;
                                                                                            !CLEAR THE ERROR FLAG BACK IN THE CALLING ROUTINE
                                 case .(NIB_NUM) from 0 to 9 of
                                                                                            SELECT THE NIBBLE TO BE TESTED
                                      set
                                      : [0]
                                            if .NIB_SAVE [NIB_0] neq .(NIB_PAT)<0, 4> then .ERR_FLG = ONE;
                                                                                            !TEST NIBBLE O AND SET ERR FLG IF NEQ
                                      [1] :
                                            if .NIB_SAVE [NIB_1] neq .(NIB_PAT)<0, 4> then .ERR_FLG = ONE;
```

if .NIB_SAVE [NIB_8] neq .(NIB_PAT)<0, 4> then .ERR_FLG = ONE;

!TEST NIBBLE 8 AND SET ERR FLG IF NEQ

SEQ 0099

[9] :

if .NIB_SAVE [NIB_9] neq .(NIB_PAT)<0, 3>

.ERR_FLG = ONE tes:

!TEST NIBBLE 9 AND SET ERR FLG IF NEQ

end:

CZMLABO ML-11	LOGIC TEST CODING SECTION	MACRO M1113	22-DEC-80	12:16	PAGE	113	,
WT2CELLYMEON2	CODING SECTION						

CEA	α	α	
SEQ			

HISCELLANEOUS CODING SEC	TION					SEW UTOU
3559 :ML4 3560 : 3561				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (16)</neale>	
3569 016146 3570 016146 004167 3571 016152 016600 3572 016156 005010 3573 016160 016601 3574 016164 006301	165672 000010	TST.LNG.WRD: JSR MOV	R1,\$SAVE2 10(SP),R0 (R0)	: ERR.	FLG,*	2687 2724
3573 016160 016601 3574 016164 006301	000014	CLR MOV ASL	14(SP),R1 R1	; NIB.	NŮM,*	2726
3575 016166 066107 3576 016172 000024 3577 016174 000052 3578 016176 000110 3579 016200 000140	016172	ADD 1\$: .WORD	1\$(R1),PC 2\$-1\$ 3\$-1\$ 4\$-1\$ 5\$-1\$ 6\$-1\$ 7\$-1\$			
3583 016210 000314 3584 016212 000354		. WORD	85-15 95-15 105-15			
3585 016214 000404 3586 016216 016602 3587 016222 042702 3588 016226 016701 3589 016232 042701 3590 016236 020102 3591 016240 001577	000012 177760 172720 177760	2\$: MORD BIC MOV BIC CMP BEQ BR	11\$-1\$ 12(\$P),R2 #177760,R2 NIB.SAVE,R1 #177760,R1 R1,R2	; NIB.	PAT,*	2731
3580 016202 000200 3581 016204 000226 3582 016206 000264 3583 016210 000314 3584 016212 000354 3585 016214 000404 3586 016216 016602 3587 016222 042702 3588 016232 042701 3590 016232 042701 3591 016240 001577 3592 016242 000574 3593 016244 016601 3594 016250 042701 3595 016254 016702 3596 016260 006202 3598 016264 006202 3599 016266 006202	000012 177760 172672	BIC MOV	135 12\$ 12(SP),R1 #177760,R1 NIB.SAVE,R2 R2 R2 R2 R2 R2 R2	; NIB.	PAT,*	2737
3600 016270 042702 3601 016274 020201	177760	ASR ASR ASR BIC CMP BEQ BR 4\$: MOV BIC MOV SWAB	#177760,R2 R2,R1 13\$			
3604 016302 016601 3605 016306 042701 3606 016312 016702	000012 177760 172634	4\$: MOV BIC MOV	12(SP),R1 #177760,R1 NIB.SAVE,R2	; NIB.	PAT,*	2743
3602 016276 001560 3603 016300 000555 3604 016302 016601 3605 016306 042701 3606 016312 016702 3607 016316 000302 3608 016320 042702 3609 016324 020201 3610 016326 001544 3611 016330 000541	177760	BIC CMP BEQ BR 5\$: MOV	R2 #177760,R2 R2,R1 13\$			
3612 016332 016601	000012	5\$: MOV	12\$ 12(SP),R1	; NIB.	PAT,*	2749

SEQ 010		12:16 PAGE 114			CTION	DDING SE	CELLANEOUS C
22-Dec-1980 09:24:31 TO 22-Dec-1980 09:21:22 PA				;ML4	3	` ;	3614 3615
		#177760,R1 NIB.SAVE,R2 R2 R2 R2 R2 R2 R2 R2	BIC MOV ASR ASR ASR ASR		177760 172604	042701 016702 006202 006202 006202 006202	3617 016336 3618 016342 3619 016346 3620 016350 3621 016352 3622 016354
		136	SWAB BIC CMP BEQ		177760	000302 042702 020201 001524	3623 016356 3624 016360 3625 016364 3626 016366 3627 016370
27	; NIB.PAT,*	12(SP),R1 #177760,R1 NIB.SAVE+2,R2 #177760,R2 R2,R1 13\$ 12\$ 12(SP),R1 #177760,R1 NIB.SAVE+2,R2	ASR ASR ASR SWAB BIC CMP BEQ BR MOV BIC CMP BEQ BR MOV BIC CMP	6\$:	000012 177760 172546 177760	016601 042701 016702 042702 020201 001511	3628 016372 3629 016376 3630 016402 3631 016406 3632 016412 3633 016414
27	; NJB.PAT,*	12(SP),R1 #177760,R1 NIB.SAVE+2,R2 R2 R2 R2 R2 R2 R2 R2	MOV BIC MOV ASR ASR ASR	7\$:	000012 177760 172520	016601 042701 016702 006202 006202 006202	3614 3615 3616 3617 016336 3618 016346 3619 016346 3620 016350 3621 016352 3622 016354 3623 016356 3624 016360 3625 016364 3625 016366 3627 016370 3628 016372 3630 016402 3631 016406 3632 016412 3633 016414 3634 016416 3635 016424 3636 016436 3637 016430 3638 016434 3639 016436 3640 016440 3641 016442 3642 016444 3643 016456
		#177760,R2 R2,R1 13\$	BIC CMP BEQ		177760	042702 020201 001472	3642 016444 3643 016450 3644 016452 3645 016454
27	; NIB.PAT,*	R2,R1 13\$ 12\$ 12(SP),R1 #177760,R1 NIB.SAVÉ+2,R2 R2 #177760,R2	MOV BIC MOV SHAR	8\$:	000012 177760 172462	016601 042701 016702 000302	3647 016462 3648 016466
		13\$	BIC CMP BEQ BR		177760	042702 020201 001456 000453	3650 016474 3651 016500 3652 016502 3653 016504
27	; NIB.PAT,*	12(SP),R1 #177760,R1 NIB.SAVE+2,R2 R2 R2 R2 R2 R2 R2 R2 R2	ASR ASR ASR BIC CMP BEQ BR MOV SWAB BIC CMP BEQ BR MOV ASR ASR ASR ASR ASR ASR ASR ASR ASR ASR	9\$:	000012 177760 172432	016601 042701 016702 006202 006202 006202	3649 016472 3650 016474 3651 016500 3652 016502 3653 016504 3654 016506 3655 016512 3656 016516 3657 016522 3658 016524 3659 016526 3660 016530 3661 016532 3662 016534 3663 016546 3665 016546 3666 016556
		R2 #177760,R2 R2,R1 13\$	SHAB BIC CMP BEQ		177760	000302 042702 020201 001436	3661 016532 3662 016534 3663 016540 3664 016542 3665 016544 3666 016546 3667 016552
27	; NIB.PAT,*	12(SP) R1 #177760 R1 NIB.SAVE+4,R2	MOV BIC MOV	10\$:	000012 177760 172374	000433 016601 042701 016702	3666 016546 3667 016552 3668 016556

	CZMLABO ML-11 LO MISCELLANEOUS CO	OGIC TES	CTION	MACRO M1113 2	2-DEC-80	12:16 PAGE 115		SE	0102
-	3670 3671			ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
	3673 016562 3674 016564 3675 016570 3676 016572 3677 016574 3678 016576 3679 016602 3680 016606 3681 016612 3682 016614 3683 016616	000302 042702 020201 001422 000417 016601 042701 016702 006202 006202	177760 000012 177770 172344	118:	SWAB BIC CMP BEQ BR MOV BIC MOV ASR ASR	R2 #177760,R2 R2,R1 13\$ 12\$ 12(SP),R1 #177770,R1 NIB.SAVÉ+4,R2 R2 R2 R2 R2 R2 R2 R2 R2	; NIB.PAT,		2785
	3684 016620 3685 016622 3686 016624 3687 016630 3688 016632 3689 016634 3690 016640 3691 3692 3693 3698 3699	006202 000302 042702 020201 001402 012710 000207	177770 000001	12\$: 13\$: ; Rout ; Maxi	ASR ASR ASR SWAB BIC CMP BEQ MOV RTS	R2 R2 #177770,R2 R2,R1 13\$ #1,(R0) PC: 158 words k depth per invocation:	; 3 words		2787 2687

```
3701 :ML4

3702 :

3703 :

3704 :

3705 :

3706 :

3707 :

3708 :

3709 :

3710 :

3711 :

3712 :

3713 :

3714 :

3715 :

3716 :
                                                                                                                      22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                               TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (17)
                                   routine XOR_LNG_WRD (NIB_NUM, NIB_PAT, RESULT) : novalue =
                                          begin
                                      FUNCTIONAL DESCRIPTION:
                                                EXCLUSIVE ORS THE CURRENT NIBBLE POSITION IN 'NIB SAVE' WITH THE CURRENT TEST PATTERN AND ASSIGNS THE RESULTS TO THE FORMAL PARAMETER 'RESULT'.
                                       FORMAL PARAMETERS:
3716
3717
                                                 NIB_NUM
                                                 CASE SELECT EXPRESSION TO SELECT THE CURRENT NIBBLE TO BE
3718
3719
3720
3721
3722
3723
3724
3725
3726
3727
3728
3729
3730
3731
3732
3733
3733
3734
3735
3736
3737
3741
3742
3743
3745
3746
3747
3748
3749
3750
3751
3752
3753
                                                 NIB PAT
                                                 CURRENT NIBBLE PATTERN TO BE
                                                 RESULT
                                                 CONTAINS THE ADDRESS (PASSED BY REF)
                                                 OF AN OWN STORAGE LOCATION TO ENABLE THE CALLER TO EXAMINE THE XOR RESULTS.
                                       IMPLICIT IMPUTS:
                                                NIB_SAVE
BLOCK OF 3 WORDS TO STORE
THE DATA FOUND IN MLD1
MLD2 AND MLE2 AFTER A
                                                 DIAGNOSTIC MODE READ.
                                       IMPLICIT OUTPUTS:
                                                                          NONE
                                          case .(NIB_NUM) from 0 to 9 of
                                                                                                              SELECT THE NIBBLE TO BE XOR'ED
                                                 set
                                                        .RESULT = .NIB_SAVE [NIB_0] xor .NIB_PAT: !XOR NIBBLE O AND STORE RESULTS IN THE ADRS CONTAINED IN .RESULTS
                                                 [1] :
                                                        .RESULT = .NIB_SAVE [NIB_1] xor .NIB_PAT: !XOR NIBBLE 1 AND STORE RESULTS IN THE ADRS CONTAINED IN .RESULTS
                                                       .RESULT = .NIB_SAVE [NIB_2] xor .NIB_PAT; !XOR NIBBLE 2 AND STORE RESULTS IN THE ADRS CONTAINED IN .RESULTS
                                               [3] :
```

. WORD

.WORD

.WORD

. WORD . WORD 85-15 95-15 105-15

016674

016676 016700

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECT	ION MACRO M1113 2	2-DEC-80 12:16 PAGE 118		SEQ 0105
I was a second of the second o	;ML4			
3812 3813 3814				22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
3815 016710 000162 3816 016712 000176 3817 016714 016712 1		.WORD 12\$-1\$.WORD 15\$-1\$		
3817 016714 016712 1	72232 2\$: 77760	MOV NIB. SAVE, (R2)	;	2831
3819 016724 000472		BR 173		
3820 016726 016703 1 3821 016732 006203	72220 3\$:	MOV NIB. SAVE, R3 ASR R3	•	2835
3822 016734 006203 3823 016736 006203 3824 016740 006203		ASR R3 ASR R3 ASR R3 ASR R3 BIC #177760,R3 MOV R3,(R2) BR 7\$		
3824 016740 006203 3825 016742 042703 1	77760	ASR R3 BIC #177760.R3		
3826 016746 010312 3827 016750 000412		BIC #177760,R3 MOV R3,(R2) BR 7\$		
3828 016752 016704 17 3829 016756 000437	72174 4\$:	MOV NIB.SAVE,R4 BR 13\$;	2839
3830 016760 016704 1	72166 5\$:	MOV NIB.SAVE,R4		2843
3816 016712 000176 3817 016714 016712 1 3818 016720 042712 1 3819 016724 000472 3820 016726 016703 1 3821 016732 006203 3822 016734 006203 3823 016736 006203 3824 016740 006203 3825 016742 042703 1 3826 016746 010312 3827 016750 000412 3828 016752 016704 1 3829 016756 000437 3830 016760 016704 1 3831 016764 000425 3832 016766 016712 1 3833 016772 042712 1 3834 016776 010104 3835 017000 041204 3836 017002 040112 3837 017004 050412 3838 017006 000207 3839 017010 016704 1 3840 017014 006204 3841 017016 006204 3842 017020 006204	72162 6 \$:	BR 11\$ MOV NIB.SAVE+2,(R2) BIC #177760,(R2)	;	2847
3833 016772 042712 1 3834 016776 010104	75:	BIC #177760,(R2) MOV R1,R4		
3835 017000 041204 3836 017002 040112		MOV R1,R4 BIC (R2),R4 BIC R1,(R2) BIS R4,(R2)		
3835 017000 041204 3836 017002 040112 3837 017004 050412 3838 017006 000207 3839 017010 016704 1		RIS PC		2827
3839 017010 016704 1 3840 017014 006204	72140 8\$:	MOV NIB.SAVE+2.R4		2827 2851
3841 017016 006204 3842 017020 006204		ASR R4 ASR R4 ASR R4 ASR R4 BR 14\$		
3843 017022 006204		ASR R4 BR 14\$ MOV NIB.SAVE+2,R4		
3844 017024 000415 3845 017026 016704 1	72122 9\$:		: 1	2855
3844 017024 000415 3845 017026 016704 1 3846 017032 000411 3847 017034 016704 1 3848 017040 006204	72114 10\$: 11\$:	MOV NIB.SAVE+2,R4 BR 13\$ MOV NIB.SAVE+2,R4 ASR R4 ASR R4 ASR R4 ASR R4 BR 13\$ MOV NIB.SAVE+4,R4 SWAB R4		2859
3849 017040 006204	113:	ASR R4 ASR R4		
3850 017044 006204 3851 017046 006204		ASR R4 ASR R4 BR 13\$		
3852 017050 000402 3853 017052 016704 1	72100 12\$:	BR 13\$ MOV NIB.SAVE+4,R4		2863
3854 017056 000304 3855 017060 042704 1	72100 12\$: 77760 14\$:	SWAB R4 BIC #177760,R4		
3856 017064 000411 3857 017066 016704 1	72064 15\$:	BR 103		2867
3851 017046 006204 3852 017050 000402 3853 017052 016704 11 3854 017056 000304 3855 017060 042704 11 3856 017064 000411 3857 017066 016704 11 3858 017072 006204 3859 017074 006204 3860 017076 006204	130.	ASR R4 ASR R4		2001
3860 017076 006204		ASR R4		
3862 017102 000304	77770	SWAB R4		
3846 017032 000411 3847 017034 016704 3848 017040 006204 3849 017042 006204 3850 017044 006204 3851 017046 006204 3852 017050 000402 3853 017052 016704 1 3855 017060 042704 1 3856 017064 000411 3857 017066 016704 1 3858 017072 006204 3859 017074 006204 3860 017076 006204 3861 017100 006204 3863 017104 042704 1 3865 017110 010412 3865 017112 010103 3866 017114 041203	77770 16\$: 17\$:	MOV NIB.SAVE+4,R4 ASR R4 ASR R4 ASR R4 SWAB R4 BIC #177770,R4 MOV R4,(R2) MOV R1,R3 BIC (R2),R3		
3861 017100 006204 3862 017102 000304 3863 017104 042704 11 3864 017110 010412 3865 017112 010103 3866 017114 041203	1/3:	SWAB R4 BIC #177770,R4 MOV R4,(R2) MOV R1,R3 BIC (R2),R3		

3884 3885	ML4		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (18)</neale>
3886 3887	: 2872	routine LD_LNG_WRD (NIB_NUM, NIB_PAT) : novalue	e =	
3888 3889	2873 2874	begin		
3886 3887 3888 3890 3891 3892 3893 3894 3895 3896 3896 3897 3898 3900 3901 3905 3906 3907 3908 3909 3911 3912 3913 3914 3915 3916 3917	2872 2873 2874 2875 2876 2877 2878 2879 2880 2881 2882 2883 2884 2885 2886 2887 2889 2891 2891 2892 2893 2894 2895 2896 2897 2898 2899 2890 2891 2898 2899 2900 2901 2902	FUNCTIONAL DESCRIPTION: LOADS 'NIB SAVE' WITH UNIQUE NIBBLE PATTERNS PRIOR TO WRITING TO MLD1, MLD2 AND MLE2 DATA DIAGNOSTIC REGISTERS.		
3897 3898 3899 3900	2882 2883 2884 2885	FORMAL PARAMETERS: NIB_NUM CASE SELECT EXPRESSION TO SELECT THE CURRENT NIBBLE TO BE LOADED		
3901 3902 3903 3904	2886 2887 2888 2889	NIB PAT CURRENT NIBBLE PATTERN TO BE LOADED		
3905 3906 3907 3908 3909 3910	2890 2891 2892 2893 2894 2895	IMPLICIT IMPUTS: NIB_SAVE BLOCK OF 3 WORDS TO STORE THE DATA TO BE WRITTEN INTO MLD1 MLD2 MLE2		
3911 3912 3913 3914 3915	2896 2897 2898 2899 2900	IMPLICIT OUTPUTS: NIB_SAVE IS LOADED WIH THE CURRENT NIBBLE PATTERN		
3917 3918 3919	2902 2903 2904	case .(NIB_NUM) from 0 to 9 of set	' !SELECT THE NIBBLE LOCA	ATION IN NIB_SAYE TO BE LOADED
	2905 2906 2907	(0): NIB_SAVE [NIB_0] = .NIB_PAT;	!LOAD NIBBLE O WITH NIE	
3923 3924 3925	2908 2909 2910	[1] : NIB_SAVE [NIB_1] = .NIB_PAT;	!LOAD NIBBLE 1 WITH NI	B_PAT
3926 3927 3928	2911 2912 2913	NIB_SAVE [NIB_2] = .NIB_PAT;	!LOAD NIBBLE 2 WITH NIE	B_PAT
3920 3921 3922 3923 3924 3925 3926 3927 3930 3931 3932 3933 3935 3936 3937 3938	2911 2912 2913 2914 2915 2916 2917 2918 2919 2920 2921 2922	NIB_SAVE [NIB_3] = .NIB_PAT;	!LOAD NIBBLE 3 WITH NIE	B_PAT
3933 3934 3935	2918 2919 2920	[4]: NIB_SAVE [NIB_4] = .NIB_PAT; [5]:	!LOAD NIBBLE 4 WITH NIE	B_PAT
3936 3937 3938	2921 2922 2923	[5]: NIB_SAVE [NIB_5] = .NIB_PAT; [6]:	!LOAD NIBBLE 5 WITH NI	B_PAT
2.33				

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECT	MACRO M1113	22-DEC-80 12:16 PAGE 1219		SEQ 0108
3940 :ML4 3941 :			22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212 PA: <neale>BL3ML4.BLI.2</neale>	(18)
3943 : 2924 3944 : 2925	NIB_SAVE	[NIB_6] = .NIB_PAT;	!LOAD NIBBLE 6 WITH NIB_PAT	
3945 : 2926 3946 : 2927 3947 : 3928	[7] : NIB_SAVE	[NIB_7] = .NIB_PAT;	!LOAD NIBBLE 7 WITH NIB_PAT	
3948 : 2929 3949 : 2930 3950 : 2031	[8] : NIB_SAVE	[NIB_8] = .NIB_PAT;	!LOAD NIBBLE 8 WITH NIB_PAT	
3940 :ML4 3941 3943 : 2924 3944 : 2925 3945 : 2926 3946 : 2927 3947 : 2928 3948 : 2929 3948 : 2929 3950 : 2931 3951 : 2932 3952 : 2933 3953 : 2934 3955 : 2936 3955 : 2936	[9] : NIB_SAVE tes;	TAP_PIN. = [P_BIN]	!LOAD NIBBLE 9 WITH NIB_PAT	
3953 : 2934 3954 : 2935 3955 : 2936	end;			
3964 017124 010146 3965 017126 016600 0 3966 017132 016601 0 3967 017136 006301	D.LJ 000004 000006 017144 1\$:	MOV R1,-(SP) MOV 4(SP),R0 MOV 6(SP),R1 ASL R1 ADD 1\$(R1),PC .WORD 2\$-1\$.WORD 3\$-1\$.WORD 4\$-1\$.WORD 5\$-1\$.WORD 7\$-1\$.WORD 7\$-1\$.WORD 8\$-1\$.WORD 9\$-1\$.WORD 9\$-1\$: NIB.PAT.* : NIB.NUM.*	2872 2906 2902
3976 017162 000212 3977 017164 000244 3978 017166 000262 3979 017170 042700 1 3980 017174 142767 0 3981 017202 000433 3982 017204 006300	177760 000017 171750 2\$:	WORD 13\$-1\$ BIC #177760,R0 BICB #17,NIB.SAVE BR 6\$		2906
3983 017206 006300 3984 017210 006300	3\$: 177417 000360 171724	ASL RO BIC #177417,RO BICB #360,NIB,SAVE		2909
3986 017214 042700 1 3987 017220 142767 0 3988 017226 000421 3989 017230 000300 3990 017232 042700 1 3991 017236 042767 0 3992 017244 000412 3993 017246 000300	170377 007400 171706	SWAB RO BIC #170377,RO BIC #7400,NIB.SAVE		2912
3993 017246 000300	5\$:	BR 6\$ SWAB RO		2915

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 122		SEQ 0109
3995 3996	:ML4		22-Dec-1980 22-Dec-1980	09:24:31 TOPS 09:21:22 PA:<
3777 3998 017250 006300 4000 017254 006300 4001 017256 006300 4002 017260 042700 007777 4003 017264 042767 170000 4004 017272 050067 171654 4005 017276 000467 4006 017300 042700 177760 4007 017304 142767 000017 4008 017312 000433 4009 017314 006300	ASL ASL ASL ASL BIC BIC BIC	RO RO RO RO #7777,RO #170000,NIB.SAVE RO,NIB.SAVE		
4003 017264 042767 170000 4004 017272 050067 171654 4005 017276 000467 4006 017300 042700 177760 4007 017304 142767 000017 4008 017312 000433	7\$: BIC 171642 BICB	#177760,R3 #17,NIB.SAVE+2		2902 2918
4008 017312 000433 4009 017314 006300 4010 017316 006300 4011 017320 006300 4012 017322 006300	88: ASL ASL ASL ASL	11\$ RO RO RO		2921
4013 017324 042700 177417 4014 017330 142767 000360 4015 017336 000421 4016 017340 000300	171616 BICB BR 9\$: SWAB	#1//41/,R0 #360,NIB.SAVE+2 11\$		2924
4017 017342 042700 170377 4018 017346 042767 007400 4019 017354 000412	10\$: SWAB	RO #170377,RO #7400,NIB.SAVE+2 11\$ RO		2927
(000 017740 004700	ASL ASL ASL BIC	RO RO RO RO W7777, RO W170000, NIB. SAVE+2		
4029 017410 000300	11\$: BIS BR 12\$: SWAB	RO.NIB.SAVE+2 15\$ RO		2902 2930
4030 017412 042700 170377 4031 017416 042767 007400 4032 017424 000412 4033 017426 000300	171532 BIC BIC BR 13\$: SWAB	#170377,R0 #7400,NIB.SAVE+4 14\$ R0		2933
4027 017402 050067 171546 4028 017406 000423 4029 017410 000300 4030 017412 042700 170377 4031 017416 042767 007400 4032 017424 000412 4033 017426 000300 4034 017430 006300 4035 017432 006300 4036 017434 006300 4037 017436 006300 4038 017440 042700 107777 4039 017444 042767 070000 4040 017452 050067 171500 4041 017456 012601 4042 017460 000207	ASL ASL ASL BIC BIC BIC	14\$ R0 R0 R0 R0 R0 R0 R0 R0		
4030 017412 042700 170377 4031 017416 042767 007400 4032 017424 000412 4033 017426 000300 4034 017430 006300 4035 017434 006300 4037 017436 006300 4038 017440 042700 107777 4039 017444 042767 070000 4040 017452 050067 171500 4041 017456 012601 4042 017460 000207 4043 4044 4045	171504 BIC 14\$: BIS 15\$: MOV RTS	#107777,R0 #70000,NIB.SAVE+4 RO,NIB.SAVE+4 (SP)+,R1 PC		2872
4045 4045	; Routine Size: ; Maximum stack	111 words depth per invocation: 2 words		

. II OCCEDITE COOL C	CD 2110 DE						354 0110
4051 :ML4 4052 :				22-Dec-1980 09:2 22-Dec-1980 09:2	24:31 TOPS 21:22 PA:<	-20 Bliss-16 V2(212) NEALE>BL3ML4.BLI.2 (19)	
4054 : 4055 : 4056 :	2937 2938 2939	routine WRT_CS1 (TST_PA	T, index) : novalu	e =			
4057 4058 4059	2940 2941 2942	FUNCTIONAL DESCRIPTION	N:				
4060 : 4061 :	2943 2944 2945	LOADS THE CONTR	OL & STATUS REGIST E MACRO WRT_MASK.	ER 1 WITH A DATA PATTER	RN		
4063 4064 4065	2946 2947 2948 2948	FORMAL PARAMETERS: TST_PAT CURRENT DATA PA	TTERN TO BE LOADED	IN THE REGISTER.			
4054 4055 4056 4057 4058 4059 4060 4061 4062 4063 4064 4065 4066 4067 4068 4069 4070 4071 4072 4073 4074	2941 2942 2943 2944 2945 2946 2947 2948 2950 2951 2952 2953 2955 2956 2957	INDEX USED BY THE MAC FORCED HI, FORCE	RO WRT MASK TO SEL ED LO AND DON'T CA	ECT THE CURRENT REGISTE RE MASK INFORMATION.	ERS ADDRESS,		
4072 : 4073 : 4074 :	2956 2957	MLCS1 = WRT_MASK; end;		!LOAD GENERATED	WRT_MASK PAT	TERN INTO MLCS1	
4082 017462 4083 017466 4084 017472 4085 017474 4086 017476	004167 016600 006300 006300 006300	164356 WRT.CS1	MOV 10(SP),RO ASL RO ASL RO		; INDEX,*		2937 2956
4087 017500 4088 017502 4089 017506 4090 017512 4091 017516 4092 017522	010001 016100 056600 046100 016102 050002	013156 000012 013160 013162	MOV RO,R1 MOV ML.REG+2(BIS 12(SP),R0 BIC ML.REG+4(MOV ML.REG+6(BIS RO,R2 MOV R2,aML.RE	R1),R0 R1),R0 R1),R2	; TST.PAT,*		
4093 017524 4094 017530 4095	010277 000207	173424	MOV R2, aML.RE RTS PC	G	:		2937
4091 017516 4092 017522 4093 017524 4094 017530 4095 4096 4097 4102 4103		; Routi ; Maxim	ne Size: 20 words um stack depth per	invocation: 3 words			

TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BL1.2 (20)

4105 :ML4 4106 : 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 4108 4109 4110 4111 4112 4113 4114 4115 routine RD_CS1 (TST_PAT, index, ERR_FLG) : novalue = begin FUNCTIONAL DESCRIPTION: COMPARES THE CONTENTS OF THE CONTROL & STATUS REGISTER 1 WITH THE MASKED DATA PATTERN GENERATED BY THE MACRO 'WRT_MASK' IF THE COMPARE IS NOT EQUAL THEN THE FORMAL PARAMETER 'ERR_FLG' IS ASSIGNED A ONE TO INDICATE THE ERROR. FORMAL PARAMETERS: TST_PAT DATA PATTERN TO BE MASKED AND COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST. INDEX USED BY THE MACRO WRT_MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION. ERR FLG
CONTAINS THE ADDRESS (PASSED BY REF)
OF THE CALLERS ERROR FLG TO ENABLE THE CALLER TO EXAMINE THE ERROR STATUS OF THE ROUTINE CALL. IMPLICIT INPUTS: WT DATA GETS LOADED WITH THE GENERATED WRT_MASK DATA PATTERN THUS ALLOWING CALTER TO PRINT FAILING GOOD DATA. RD_DATA GETS LOADED WITH DATA READ FROM THE REGISTER THUS ALLOWING CALLER TO PRINT FAILING BAD DATA. IMPLICIT OUTPUTS: GLOBAL LOCATION WR_DATA AND RD_DATA LOADED WITH GOOD AND BAD REGISTER DATA

MISCELLIMEOUS COL	DING 2E	CITUN							SEU UTIZ
4161 :ML4 4162 :							22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BL1.2 (20</neale>)
4161 :ML4 4162 : 4163 : 4164 : 4165 : 4166 : 4167 : 4168 : 4169 : 4170 : 4171 :	3010 3011 3012	WT_ RD_	DATA = W.	ZERO; IRT_MASK; MLCS1 or	.IGNOR	E;	CLEAR ERROR FLAG SAVE THE DATA WRITTEN READ AND SAVE THE REGI	TO THE REGISTER	
4168	3012 3013 3014 3015	if	.WT_DATA	neg .RD	DATA t	hen .ERR_FLG = ONE	READ THE REG F	OR WRT_MASK	
4169 ; 4170 ; 4171 ;	3015 3016 3017	end					SET ERROR FLAG IF NEQ		
4179 017532 (4180 017536 (4181 017542 (4182 017546 (4183 017550 (4184 017552 (4185 017554 (004167 005076 016600 006300 006300 006300 010001	164306 000010 000012		RD.CS1:	JSR CLR MOV ASL ASL MOV MOV BIS BIC MOV BIS MOV BIS MOV CMP	R1.\$SAVE2 a10(SP) 12(SP),R0 R0 R0 R0 R0 R0,R1	: ERR.F : INDEX	LG ,*	2958 3010 3011
4187 017562 (4188 017566 (4189 017572 (4190 017576 (4191 017600 (016100 056600 046100 016102 050002 010267	013156 000014 013160 013162 173322			BIS BIC MOV BIS MOV	RO RO,R1 ML.REG+2(R1),RO 14(SP),RO ML.REG+4(R1),RO ML.REG+6(R1),R2 RO,R2 R2,WT.DATA aML.REG,R2 ML.REG,R2 ML.REG+6(R1),R2 R2,RD.DATA WT.DATA,RD.DATA	; TST.F	AT,*	
4191 017600 (4192 017604 (4193 017610 (4194 017614 (017702 056102 010267 026767 001403 012776	173322 173344 013162 173310 173302			MOV BIS MOV	ML.REG.R2 ML.REG+6(R1),R2 R2.RD.DATA	•		3012
4194 017614 (4195 017620 (4196 017626 (026767	173302	173302		BEQ	WT.DATA,RD.DATA	:		3014
4197 017630 (4198 017636 (012776 000207	000001	000010	1\$:	MOV	#1,a10(SP) PC	; *,ERR	.FLG	2958
4198 017636 (4199 4200 4201 4206 4207				; Routi ; Maxim	ine Size num stac	: 35 words k depth per invoca	tion: 3 words		

	00 2.10 00						354 0113
4209 :ML4 4210 :					22-Dec-1980 09:24: 22-Dec-1980 09:21:	31 TOPS-20 Bliss-16 V2(21 PA: <neale>BL3ML4.BLI.2</neale>	2)
4212 : 4213 :	3018 3019 3020	routine WRT_ER begin	(TST_PAT, index)	: novalue =			
4215 : 4216 :	3021 3022	FUNCTIONAL DE	SCRIPTION:				
4218 ; 4218 ;	3023 3024 3025	LOADS T	HE ERROR REGISTER	R WITH A DATA PAT	TERN GENERATED BY	THE MACRO WRT_MASK	
4220 4221	3026 3027	FORMAL PARAME					
4222 : 4223 :	3028 3029	TST_PAT		BE LOADED IN THE	REGISTER.		
4217 4218 4219 4220 4221 4222 4223 4224 4225 4226 4227 4228 4229 4230 4231	3031 3032 3033 3034	INDEX USED BY FORCED	THE MACRO WRT M	ASK TO SELECT THE D DON'T CARE MASK	CURRENT REGISTERS	ADDRESS,	
4229 : 4230 : 4231 :	3035 3036 3037	MLER = WRT_ end;	MASK;		!LOAD GENERATE WRT	MASK PATTERN INTO MLER	
4239 017640 4240 017644 4241 017650 4242 017652	004167 016600 006300 006300 006300	164200 000010	WRT.ER: JSR MOV ASL ASL	R1,\$SAVE2 10(SP),R0 R0 R0	;	INDEX,*	3018 3036
4243 017654 4244 017656 4245 017660 4246 017664 4247 017670 4248 017674 4249 017700	010001 016100 056600 046100 016102 050002	013156 000012 013160 013162	ASL MOV MOV BIS BIC MOV BIS	R0 R0,R1 ML.REG+2(R1),R0 12(SP),R0 ML.REG+4(R1),R0 ML.REG+6(R1),R2 R0,R2		TST.PAT,*	
4250 017702 4251 017706 4252	010277 000207	173326	RTS	R2, aML.REG+60 PC			3018
4253 4254 4259 4260			; Routine Size: ; Maximum stack	20 words depth per invoca	tion: 3 words		

TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML 4.BLI.2 (22)

4262 :ML4
4263 :4264
4265 :4266
4267 :4268
4269 :4273
4273 :4278
4278 :4278
4279 :4281
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4289 :4291
4291 :4292
4293 :4294
4293 :4294
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4296 :4297
4298 :4298
4299 :4300
4301 :4302
4308 :4308
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4309 :4308 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 routine RD_ER (TST_PAT, index, ERR_FLG) : novalue = begin ! FUNCTIONAL DESCRIPTION: COMPARES THE CONTENTS OF THE ERROR REGISTER WITH THE MASKED DATA PATTERN GENERATED BY THE MACRO 'WRT_MASK'. IF THE COMPARE IS NOT EQUAL THEN THE FORMAL PARAMETER 'ERR_FLG' IS ASSIGNED A ONE TO INDICATE THE ERROR. FORMAL PARAMETERS: TST_PAT
DATA PATTERN TO BE MASKED AND
COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST. INDEX USED BY THE MACRO WRT MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION. ERR FLG CONTAINS THE ADDRESS (PASSED BY REF)
OF THE CALLERS ERROR FLG TO ENABLE THE
CALLER TO EXAMINE THE ERROR STATUS OF THE ROUTINE CALL. IMPLICIT IMPUTS: WT_DATA GETS LOADED WITH THE GENERATED WRT MASK DATA PATTERN THUS ALLOWING CALLER TO PRINT FAILING GOOD DATA. RD_DATA GETS LOADED WITH DATA READ FROM THE REGISTER THUS ALLOWING CALLER TO PRINT FAILING BAD DATA. IMPLICIT OUTPUTS: GLOBAL LOCATION WR DATA AND RD DATA LOADED WITH GOOD AND BAD REGISTER DATA

010001

164130 000010 000012

000001

173124

000010

3038

22-De 22-De	ec-1980 09:24:31 ec-1980 09:21:22	TOPS-20 Bliss-16 V2(212 PA: <neale>BL3ML4.BL1.2</neale>) (22)
!SAVE	AR THE ERROR FLAG E THE DATA WRITTEN D AND SAVE THE REG	TO THE REGISTER	
nen .ERR_FLG = ONE;	!READ THE REG		
!SET	ERROR FLAG IF NEQ		
R1,\$SAVE2 @10(SP) 12(SP),RO RO RO	ERR.		3038 3091 3092
RO,R1 ML.REG+2(R1),RO 14(SP),RO ML.REG+4(R1),RO ML.REG+6(R1),R2 RO,R2	; TST.	PAT,*	
RZ,WT.DATA aml.REG+60,R2 ML.REG+6(R1),R2	;		3093
R2,RD.DATA WT.DATA,RD.DATA	:		3095
1\$ #1,a10(SP)	; *,ER	R.FLG	

; Routine Size: 35 words

JSR CLR

MOV

ASL ASL MOV MOV BIS BIC

MOV

MOV MOV

MOV CMP BEQ MOV

RD.ER:

: Maximum stack depth per invocation: 3 words

#1,a10(SP)

4367 4368	ML4						22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (23)</neale>	
4370 4371		3099 3100	routine begin	RT_DA (TST_PAT,	index)	: novalue =			
4373 4374		3102 3103 3104 3105	FUNCTIO	NAL DESCRIPTION:					
4376		3105		DADS THE DESIRED	SECTO	WITH A DATA PAT	TERN GENERATED BY TH	E MACRO WRT_MASK	
4378		3106 3107 3108 3109	FORMAL	PARAMETERS:					
4380 4381 4382		3109 3110 3111	1	ST PAT URRENT DATA PATT	TERN TO	BE LOADED IN THE	REGISTER.		
4367 4368 4369 4370 4371 4373 4373 4376 4377 4378 4379 4383 4383 4384 4385 4386 4387 4388 4389 4398 4399 4400 4401 4404 4405 4406		3112 3113 3114 3115	_ }	NDEX ISED BY THE MACRO ORCED HI, FORCED	URT_M	ASK TO SELECT THE	CURRENT REGISTERS A	DDRESS,	
4387 4388 4389		3116 3117 3118	MLDA end;	= WRT_MASK;			!LOAD MLDA WITH GENE	RATED WRT_MASK PAFTERN	
4397 4398 4399	020016 0 020022 0 020026 0 020030 0	04167 16600 06300	164022 000010	WRT.DA: J	NOV.	R1,\$SAVE2 10(SP),R0 R0 R0	: IN	DEX,*	3099 3117
4401 4402 4403	020030 020032 020034 020036	06300 10001 16100	013156		ASL ASL MOV MOV BIS BIC	R0 R0 R1			
4405 4406 4407	020042 0 020046 0 020052 0 020056 0	56600 46100 16102 50002	013156 000012 013160 013162		SIS SIS	ML.REG+2(R1),R0 12(SP),R0 ML.REG+4(R1),R0 ML.REG+6(R1),R2 R0,R2	; 15	T.PAT,*	
4408 4409 4410	020060 0	10277 00207	173120		RTS	R2,aML.REG+30 PC	:		3099
4410 4411 4412 4417 4418				; Routine ; Maximum	e Size: n stack	20 words depth per invoca	ition: 3 words		

TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BLI.2 (24)

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 routine RD_DA (TST_PAT, index, ERR_FLG) : novalue = begin FUNCTIONAL DESCRIPTION: COMPARES THE CONTENTS OF THE DESIRED SECTOR ADDRESS REGISTER WITH THE MASKED DATA PATTERN GENERATED BY THE MACRO 'WRT_MASK' IF THE COMPARE IS NOT EQUAL THEN THE FORMAL PARAMETER 'ERR FLG' IS ASSIGNED A ONE TO INDICATE THE ERROR FORMAL PARAMETERS: TST PAT DATA PATTERN TO BE MASKED AND COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST INDEX USED BY THE MACRO WRT_MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION ERR_FLG CONTAINS THE ADDRESS (PASSED BY REF) OF THE CALLERS ERROR FLG TO ENABLE THE CALLER TO EXAMINE THE ERROR STATUS OF THE ROUTINE CALL.
IMPLICIT INPUTS: WT DATA GETS LOADED WITH THE GENERATED WRT_MASK DATA PATTERN THUS ALLOWING CALTER TO PRINT FAILING GOOD DATA. RD DATA
GETS LOADED WITH DATA READ FROM THE REGISTER THUS ALLOWING CALLER TO PRINT FAILING BAD DATA. IMPLICIT OUTPUTS: GLOBAL LOCATION WR DATA AND RD_DATA LOADED WITH GOOD AND BAD REGISTER DATA 3168 3169 3170

3173

3175

3119

CMP

MOV

172746

000001

172746

000010

; Routine Size: 35 words ; Maximum stack depth per invocation: 3 words

WT.DATA, RD.DATA

: *, ERR.FLG

#1,a10(SP)

TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML 4.BLI.2 (26)

4577 :ML4 4578 : 4579 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 routine RD_MR (TST_PAT, index, ERR_FLG) : novalue = begin ! FUNCTIONAL DESCRIPTION: COMPARES THE CONTENTS OF THE MAINTENANCE REGISTER WITH THE MASKED DATA PATTERN GENERATED BY THE MACRO 'WRT_MASK'. IF THE COMPARE IS NOT EQUAL THEN THE FORMAL PARAMETER 'ERR_FLG' IS ASSIGNED A ONE TO INDICATE THE FRROR. FORMAL PARAMETERS: TST_PAT DATA PATTERN TO BE MASKED AND COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST. INDEX USED BY THE MACRO WRT MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION. ERR_FLG CONTAINS THE ADDRESS (PASSED BY REF) OF THE CALLERS ERROR FLG TO ENABLE THE CALLER TO EXAMINE THE ERROR STATUS OF THE ROUTINE CALL. ! IMPLICIT INPUTS: GETS LOADED WITH THE GENERATED WRT_MASK DATA PATTERN THUS ALLOWING CALTER TO PRINT FAILING GOOD DATA. RD_DATA GETS LOADED WITH DATA READ FROM THE REGISTER THUS ALLOWING CALLER TO PRINT FAILING BAD DATA. IMPLICIT OUTPUTS: GLOBAL LOCATION WR DATA AND RD DATA LOADED WITH GOOD AND BAD REGISTER DATA

4633 :ML4 4634 :							22-Dec-1980 09:24 22-Dec-1980 09:21	:31 TOPS-20 BLis	SS-16 V2(212) 3ML 4.BLI.2 (26)
4636 : 4637 : 4638 : 4639 :	3251 3252 3253 3254	.ERI WT_I RD_I	R_FLG = 1 DATA = WI DATA = .1	ZERO; RT MASK, MLMR or	. I GNORE	•	CLEAR THE ERROR SAVE THE DATA WR READ AND SAVE TH	FLAG RITTEN TO THE REGIST BE REGISTER	TER
4641	3256	if.	.WT_DATA	neq .RI	D_DATA t	hen .ERR_FLG = ONE	: !READ THE	REG FOR WRT_MASK	
4643 4644 4644	3256 3257 3258 3259	end					!SET ERROR FLAG I	F NEQ	
4652 020244 4653 020250 4654 020254 4655 020260 4656 020262	004167 005076 016600 006300 006300 006300 010001	163574 000010 000012		RD.MR:		R1,\$SAVE2 @10(SP) 12(SP),R0 R0 R0	:	ERR.FLG INDEX,*	3199 3252 3253
4636 : 4637 : 4638 : 4639 : 4640 : 4641 : 4642 : 4643 : 4644 : 4648 : 4652 020244 4653 020250 4654 020254 4655 020262 4657 020264 4658 020264 4659 020270 4660 020274 4661 020300 4662 020304 4663 020310 4664 020312 4666 020322 4667 020340 4669 020340 4670 020342	016100 056600	013156 000014 013160 013162			CLR MOV ASL ASL ASL MOV BIS BIC MOV BIS MOV BIS MOV CMP BEQ MOV RTS	RO RO RO RO RO RO RO,R1 ML.REG+2(R1),RO 14(SP),RO ML.REG+4(R1),RO ML.REG+6(R1),R2 RO,R2 R2,WT.DATA aML.REG+6(R1),R2 R2,RD.DATA UT.DATA,RD.DATA 15	,	TST.PAT,*	
4662 020304 4663 020310 4664 020312 4665 020316 4666 020322 4667 020326 4668 020332 4669 020340 4670 020340	010267 017702 056102 010267 026767 001403 012776 000207	172610 172752 013162 172576 172570			MOV MOV BIS MOV	R2,WT.DATA aML.REG+120,R2 ML.REG+6(R1),R2 R2,RD.DATA			3254
4668 020332 4669 020340	026767 001403	172570	172570		BEQ	WT.DATA,RD.DATA	:		3256
4670 020342 4671 020350	012776 000207	000001	000010	1\$:	MOV	1\$ #1,a10(SP) PC		*,ERR.FLG	3199
4671 020350 4672 4673 4674 4679 4680				; Rout ; Maxi	ine Size mum stac	: 35 words k depth per invoca	ation: 3 words		

4682 4683	ML4						22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML 4.BLI.2 (27)</neale>	
4682 4683 4684 4685 4686 4687 4688 4691 4692 4693 4694 4695 4696 4698 4699 4700 4701 4702		3260 3261 3262	routine beg	WRT_PA	(TST_PAT, ind	ex) : novalue =			
4688 4689		3263	FUNCT	IONAL DE	SCRIPTION:				
4690 4691 4692		3264 3265 3266 3267 3268 3269 3270 3271 3272 3273 3274 3275 3276	FORMA	LOADS T L PARAME TST PAT	TERS:	SS REGISTER WITH A	DATA PATTERN GENERATED E	BY THE MACRO WRT_MASK	
4693 4694		3268 3269		INDEX		TO BE LOADED IN TH			
4695 4696		3270 3271		USED BY FORCED	THE MACRO WR	T_MASK_TO_SELECT_TH AND_DON'T_CARE_MASK	CURRENT REGISTERS ADDR	RESS,	
4697 4698		3272 3273	!	M DIC -	ONE.		ICET DOOM DICADLE DIT		
4700 4701		3275 3276	MLP.	M_DIS = A = WRT_ M_DIS =	MASK; ZERO:		SET PROM DISABLE BIT LOAD MLPA WITH GENERAL CLEAR PROM DISABLE BIT	TED WRT_MASK PATTERN	
4706			end	;					
4710 4711	020352 020356 020364 020370	004167 152777 016600 006300 006300	163466 000040 000010	172710	WRT.PA: JSR BISB	R1,\$SAVE2 #40,aML.REG+120 10(SP),R0	:		3260 3274 3275
4712 4713 4714	020370	006300	000010		MOV ASL ASI	RO RO	: INDE		3275
4715 4716	020372 020374 020376	006300 010001			ASL ASL MOV	RO RO,R1			
4717	020400 020404 020410	016100	013156 000012 013160 013162		MOV BIS BIC	ML_RFG+2(R1)_R0	; 151.	PAT,*	
4719 4720 4721	020414 020420	056600 046100 016102 050002	013162		MOV BIS	12(SP),R0 ML.REG+4(R1),R0 ML.REG+6(R1),R2			
4722	020422	010277	172626	172640	MOV	RO,R2 R2,aML.REG+100 #40,aML.REG+120			3276
4724	020426 020434	000207			RTS	PC			3276 3260
4726 4727					; Routine Si ; Maximum st	ze: 26 words ack depth per invoc	ation: 3 words		
4732									

TOPS-20 Bliss-16 V2(212)

PA: <NEALE>BL3ML 4.BL1.2 (28)

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4780 : 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 routine RD_PA (TST_PAT, index, ERR_FLG) : novalue = begin ! FUNCTIONAL DESCRIPTION: COMPARES THE CONTENTS OF THE PROM ADDRESS REGISTER WITH THE MASKED DATA PATTERN GENERATED BY THE MACRO 'WRT_MASK'. IF THE COMPARE IS NOT EQUAL THEN THE FORMAL PARAMETER 'ERR_FLG' IS ASSIGNED A ONE TO INDICATE THE ERROR. ! FORMAL PARAMETERS: TST_PAT DATA PATTERN TO BE MASKED AND COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST. INDEX USED BY THE MACRO WRT_MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION. ERR_FLG CONTAINS THE ADDRESS (PASSED BY REF) OF THE CALLERS ERROR FLG TO ENABLE THE CALLER TO EXAMINE THE ERROR STATUS. OF THE ROUTINE CALL. ! IMPLICIT IMPUTS: WT_DATA GETS LOADED WITH THE GENERATED WRT MASK DATA PATTERN THUS ALLOWING CALTER TO PRINT FAILING GOOD DATA. RD_DATA GETS LOADED WITH DATA READ FROM THE REGISTER THUS ALLOWING CALLER TO PRINT FAILING BAD DATA. IMPLICIT OUTPUTS: GLOBAL LOCATION WR_DATA AND RD_DATA LOADED WITH GOOD AND BAD REGISTER DATA

4791 4792	ML4							22-Dec-1980 09:20 22-Dec-1980 09:20	4:31 1:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML 4.BLI.2 (28)</neale>	
4794 4795 4796 4797		3330 3331 3332 3333	PRO WT_ RD_	R_FLG = M_DIS = DATA = U DATA = .	ZERO; ONE; IRT MASK; MLPA or	.IGNORE		!CLEAR THE ERROR !SET THE PROM DIS !SAVE THE DATA W !READ AND SAVE TO	FLAG SABLE BI RITTEN THE REGIS	TO THE REGISTER	
4799		3335	if	.WT_DATA	neq .RI	D_DATA ti	hen .ERR_FLG = ON	: !READ THE	E REG FO	OR WRT_MASK	
4801 4802		3337 3338	PR0 end	M_DIS =	ZERO;			SET ERROR IF NEC	OISABLE	BIT	
4792 4793 4794 4795 4796 4797 4798 4806 4801 4802 4806 4811 4813 4814 4814 4815 4816 4817 4818 4819 4821 4821 4821 4821 4821 4821 4821 4821	020462	004167 005076 152777 016600 006300 006300 010001 016100 046100 046100	163402 000010 000040 000012 013156 000014 013160 013162	172620		JSR CLR BISB MOV ASL ASL MOV BIS BIC MOV BIS MOV BIS MOV CMP BEQ MOV BICB RTS	R1,\$SAVE2 a10(SP) #40,aML.REG+120 12(SP),RO RO RO RO RO,R1 ML.REG+2(R1),RO 14(SP),RO ML.REG+4(R1),RO ML.REG+6(R1),R2		ERR.FL		3278 3330 3331 3332
4822 4823 4824 4825 4826 4827	020510 020512 020516 020522 020526 020532	050002 010267 017702 056102 010267 026767	172410 172532 013162 172376 172370	172370		MOV MOV BIS MOV CMP	RO RO,R1 ML.REG+2(R1),R0 14(SP),R0 ML.REG+4(R1),R0 ML.REG+6(R1),R2 RO,R2 R2,WT.DATA aML.REG+100,R2 ML.REG+6(R1),R2 R2,RD.DATA WT.DATA,RD.DATA 15		:		3333 3335
4829 4829 4830 4831	020540 020542 020550 020556	001403 012776 142777 000207	000001 000040	000010 172516	1\$:	MOV BICB RTS	#1,a10(SP) #40,aML.REG+120 PC		*,ERR.	FLG	3337 3278
4834 4834 4839 4840					; Rout	ine Size	: 41 words k depth per invoca	ation: 3 words			

HISCELL	MIEUUS (ODING SE	CITOR					SER OIS
4842 4843	ML4					22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML 4.BLI.2 (29</neale>	9)
4842 4843 4844 4845 4846 4849 4851 4853 4853 4854 4855 4856 4856 4861 4862 4866 4860		3339 3340 3341 3342 3343	begin	DESCRIPTION:				
4850 4851 4852 4853		3344 3345 3346 3347	CURRE	NT DATA PATTERN TO		PATTERN GENERATED BY REGISTER.	THE MACRO WRT_MASK	
4854 4855 4856 4857		3348 3349 3350 3351	USED FORCE	BY THE MACRO WRT I	MASK TO SELECT THE ND DON'T CARE MASK	CURRENT REGISTERS ADD	RESS,	
4858 4859 4860 4861 4862	:	3345 3346 3347 3348 3350 3351 3352 3353 3354 3355	ECC_DM = MLET = WR ECC_DM = end;	RT_MASK;		!SET ECC DIAG MODE !LOAD MLE1 WITH GENERA !CLEAR ECC DIAG MODE	TED WRT_MASK PATTERN	
4871 4872 4873 4874 4875	020564 020572 020576 020600 020602	004167 152777 016600 006300 006300 006300	163260 000001 000010 17250	WRT.E1: JSR BISB MOV ASL ASL ASL MOV	R1,\$SAVE2 #1,aML.REG+120 10(SP),R0 R0 R0 R0	INDE	x,*	3339 3353 3354
4876 4877 4878 4879 4880	020604 020606 020612 020616 020622	016100 056600 046100 016102	013156 000012 013160 013162	MOV BIS BIC MOV	RO,R1 ML.REG+2(R1),RO 12(SP),RO ML.REG+4(R1),RO ML.REG+6(R1),R2 RO,R2	; TST.	PAT,*	
4878 4879 4880 4881 4882 4883 4884 4885 4886 4887 4892 4893	020612 020616 020622 020626 020630 020634 020642	050002 010277 142777 000207	172470 000001 17243	BIS MOV BICB RTS	R2, aML.REG+150 W1, aML.REG+120 PC	:		3355 3339
4886 4887 4892 4893				; Routine Size ; Maximum stack	: 26 words k depth per invoca	tion: 3 words		

PA: <NEALE>BL3ML 4.BL1.2 (30)

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TOPS-20 Bliss-16 V2(212)
4895 :ML4
4896 :4897
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                                                                                                                                    22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                           routine RD_E1 (TST_PAT, index, ERR_FLG) : novalue =
                                                  begin
                                           ! FUNCTIONAL DESCRIPTION:
                                                          COMPARES THE CONTENTS OF THE
                                                         ECC CRC WORD REGISTER 1
WITH THE MASKED DATA PATTERN
                                                         GENERATED BY THE MACRO 'WRT_MASK'
                                                         IF THE COMPARE IS NOT EQUAL THEN
THE FORMAL PARAMETER 'ERR_FLG' IS
ASSIGNED A ONE TO INDICATE THE ERROR.
                                              FORMAL PARAMETERS:
                                                         TST PAT
DATA PATTERN TO BE MASKED AND
COMPARED AGAINST THE CONTENTS
                                                         OF THE REGISTER UNDER TEST.
                                                         USED BY THE MACRO WRT MASK TO SELECT THE CURRENT REGISTERS ADDRESS,
                                                         FORCED HI, FORCED LO AND DON'T CARE
                                                         MASK INFORMATION.
                                                         ERR_FLG
CONTAINS THE ADDRESS (PASSED BY REF)
OF THE CALLERS ERROR FLG TO ENABLE THE
CALLER TO EXAMINE THE ERROR STATUS
                                                         OF THE ROUTINE CALL.
                                              IMPLICIT INPUTS:
                                                         WT DATA
                                                             GETS LOADED WITH THE GENERATED
                                                             WRT_MASK DATA PATTERN THUS ALLOWING
                                                             CALTER TO PRINT FAILING GOOD DATA.
                                                             GETS LOADED WITH DATA READ FROM THE
                                                             REGISTER THUS ALLOWING CALLER TO PRINT FAILING BAD DATA.
                                              IMPLICIT OUTPUTS:
                                                         GLOBAL LOCATION WR_DATA
AND RD_DATA LOADED WITH GOOD
                                                         AND BAD REGISTER DATA
                                                  .ERR_FLG = ZERO:
                                                                                                                                 !CLEAR THE ERROR FLAG
```

4951 4952	:ML4							22-Dec-1980 09:24: 22-Dec-1980 09:21:	TOPS-20 Bliss-1	6 V2(212) 4.BLI.2 (30)
4954 4955 4956 4957		3409 3410 3411 3412 3413 3414 3415 3416	ECC_DM = ONE; WT_DATA = WRT_MASK; RD_DATA = .MLE1 or .IGNORE;					SET ECC DIAG MODE SAVE THE DATA WRITTEN TO THE REGISTER READ AND SAVE THE REGISTER		
4958 4959		3413 3414	if	.WT_DATA	neq .RI	D_DATA ti	hen .ERR_FLG = ON	READ THE		
4960 4961 4962		3415 3416 3417	end	_DM = ZE ;	RO;			SET ERROR FLAG IF	NEQ	
4952 4953 4954 4956 4957 4958 4960 4961 4962 4963 4973 4973 4976 4978 4978 4978 4978 4981 4983 4983 4984 4985 4985 4986 4987 4989 4990 4991 4992 4993 4999 5000	020644 020650 020654 020662 020670 020672 020676 020702 020706 020716 020720 020720 020730	004167 005076 152777 016600 006300 006300 016300 016100 056600 046100 016102 050002 010267 017702 056102	163174 000010 000001 000012 013156 000014 013160 013162 172202 172374 013162 172170 172162	172412	RD.E1:	JSR CLR BISB MOV ASL ASL ASL MOV BIS BIC MOV BIS MOV BIS MOV CMP BEQ MOV	R1,\$SAVE2 a10(SP) #1,aML.REG+120 12(SP),RO RO RO RO RO,R1 ML.REG+2(R1),RO 14(SP),RO ML.REG+6(R1),RO ML.REG+6(R1),RO RO,R2 R2,WT.DATA aML.REG+6(R1),R2 R2,RD.DATA WT.DATA,RD.DATA	1	ERR.FLG INDEX,* TST.PAT,*	3357 3408 3409 3410
4986 4987	020734	010267	172170 172162	172162		CMP	R2,RD.DATA WT.DATA,RD.DATA			3413
4989 4990 4991 4992 4993	020746 020750 020756 020764	001403 012776 142777 000207	000001 000001	000010 172310	1\$:	RTS	1\$ #1.a10(SP) #1.aML.REG+120 PC 41 words depth per invoca	- 1	*,ERR.FLG	3415 3357
4999					; max1/	num staci	depth per invoca	ition: 5 words		

5002 5003 5004	ML4							22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (31)</neale>	
5004 5005 5006 5007		3418 3419 3420	routine beg	WRT_E2	(TST_PAT	, index) : novalue =			
5005 5006 5007 5008 5009 5010 5011 5012 5013		3421 3422 3423	FUNCT	IONAL DE	SCRIPTIO	N:				
5011 5012 5013 5014		3424 3425 3426 3427		LOADS T REGISTE GENERAT WRT_MAS	ED BY TH	RC WORD A DATA E MACRO	PATTERN			
5015 5016 5017		3429	FORMA	L PARAME	TERS:					
5018 5019 5020		3431 3432 3433		TST_PAT	DATA PA	TTERN T	O BE LOADED IN TH	E REGISTER.		
5016 5017 5018 5019 5020 5021 5022 5023 5024 5025 5026 5027 5028 5030 5031 5032 5033 5034 5035		3431 3432 3433 3434 3436 3437 3438 3440 3441 3442 3443 3444 3445		INDEX USED BY TO SELE ADDRESS LO AND INFORMA	THE MAC CT THE C , FORCED DON'T CA TION.	RO WRT J URRENT HI, FO RE MASK	MASK REGISTERS RCED			
5029 5030 5031 5032 5033 5034 5035	:		MLE	DM = ON 2_MASK = 2 = WRT_ 2_MASK = DM = ZE	MASK; %o'0000	00';		SET ECC DIAG MODE MASK OUT DATA DIAG E LOAD MLE2 WITH GENER RESTORE MLE2 MASK CLEAR ECC DIAG MODE	BITS PATED WRT_MASK PATTERN	
5039 5043 5044 5045 5046 5047 5048	020766 020772 021000 021006 021012 021014	004167 152777 012767 016600 006300 006300	163052 000001 177700 000010	172274 172334	WRT.E2:	BISB MOV MOV	R1,\$SAVE2 #1,aML.REG+120 #-100,ML.REG+16 10(SP),R0 R0 R0	i IND	DEX,*	3418 3443 3444 3445
5045 5046 5047 5048 5049 5050 5051 5052 5053 5054	021000 021006 021012 021014 021016 021020 021022 021026 021032 021036 021042	004167 152777 012767 016600 006300 006300 010001 016100 056600 046100 016102 050002	013156 000012 013160 013162			ASL ASL MOV MOV BIS BIC MOV BIS	R1, \$SAVE2 #1, aML.REG+120 #-100, ML.REG+16 10(SP), RO RO RO RO RO, R1 ML.REG+2(R1), RO 12(SP), RO ML.REG+4(R1), RO ML.REG+6(R1), R2 RO, R2	; 751	PAT,*	

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 142

SEQ 0129

SOS7
SOS8
SOS9
SO60 021044 010277 172264
S061 021050 005067 172266
S062 021054 142777 000001 172212 BICB M1.REG+166
S063 021054 142777 000001 172212 BICB M1.REG+120 : 34467
S065
S065
S066
S066
S066
S067
RTS
PC
ROUTINE Size: 31 words
SO66
SO71
SO72

TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML 4.BLI.2 (32)

5074 :ML4 5075 : 5076 : 5077 : 5078 : 5080 : 5081 : 5082 : 5083 : 5085 : 5086 : 5087 : 5088 : 5099 : 5090 : 5091 : 5092 : 5093 : 5094 : 5095 : 5096 : 5097 : 5098 : 5099 : 5100 : 5101 : 5102 : 5103 : 5104 : 5105 : 5106 : 5107 : 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 3355555567890123346678901723475678901233488901233499334990 3455123345567890123346678901723475678901233488901233499334990 routine RD_E2 (TST_PAT, index, ERR_FLG) : novalue = begin ! FUNCTIONAL DESCRIPTION: COMPARES THE CONTENTS OF THE ECC CRC WORD REGISTER 2 WITH THE MASKED DATA PATTERN GENERATED BY THE MACRO 'WRT_MASK' IF THE COMPARE IS NOT EQUAL THEN THE FORMAL PARAMETER 'ERR FLG' IS ASSIGNED A ONE TO INDICATE THE ERROR. FORMAL PARAMETERS: TST_PAT DATA PATTERN TO BE MASKED AND COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST. USED BY THE MACRO WIT MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION. ERR_FLG CONTAINS THE ADDRESS (PASSED BY REF) OF THE CALLERS ERROR FLG TO ENABLE THE CALLER TO EXAMINE THE ERROR STATUS 5108 5109 5110 OF THE ROUTINE CALL. 5110 5111 5112 5113 5114 5115 5116 5117 5118 5120 5121 5122 5123 5123 5126 5127 5128 IMPLICIT INPUTS: WT DATA GETS LOADED WITH THE GENERATED WRT_MASK DATA PATTERN THUS ALLOWING CALTER TO PRINT FAILING GOOD DATA. RD DATA GETS LOADED WITH DATA READ FROM THE REGISTER THUS ALLOWING CALLER TO PRINT FAILING BAD DATA. IMPLICIT OUTPUTS: GLOBAL LOCATION WR_DATA AND RD_DATA LOADED WITH GOOD AND BAD REGISTER DATA

5130 :ML4 5131 :							22-Dec-1980 09 22-Dec-1980 09	2:24:31	TOPS-20 Bliss-16 V2(212) (PA: NEALE>BL3ML4.BL1.2 (32)	
5133 ; 5134 ; 5135 ; 5136 ; 5137 ;	3501 3502 3503 3504 3505 3506 3507 3508 3510 3511 3512 3513	ERI ECC MLE WT_I RD_I	R FLG = DM = ON 2 MASK = DATA = W DATA = .	ZERO; E; %o'1777 RT_MASK; MLE2 or	'00';	CLEAR ERROR FLAG SET ECC DIAG MODE MASK OUT DATA DIAG BITS SAVE THE DATA WRITTEN TO THE REGISTER READ AND SAVE THE REGISTER				
5140 : 5141 :	3508 3509	if .	.WT_DATA	neq .RD	_DATA th	nen .ERR_FLG = ONE	!READ	THE REG F		
5142 5143 5144 5145	3510 3511 3512 3513	MLE: ECC end	2 MASK = DM = ZE	%o'0000	000';		SET ERROR FLO RESTORE MLE2 CLEAR ECC DIA	MASK AG MODE		
5149 5153 021064 00 5154 021070 00 5155 021074 13 5156 021102 01 5157 021110 01 5158 021114 00 5160 021120 00 5161 021122 01 5162 021124 01 5163 021130 03 5164 021134 00 5165 021140 01 5166 021144 01 5167 021146 01 5168 021152 01 5169 021156 03 5170 021162 01 5171 021166 02 5172 021174 00 5173 021176 01	04167 05076 52777 12767 16600 06300 06300 06300 16100 16100 56600 46100 16102 50002 10267 17702 56102 10267 26767 01403	162754 000010 000001 177700 000012	172172 172232	RD.E2:	JSR CLR BISB MOV MOV ASL ASL ASL MOV MOV BIS	R1,\$SAVE2 a10(SP) #1,aML.REG+120 #-100,ML.REG+166 12(SP),R0 R0 R0 R0 R0 R0,R1 ML.REG+2(R1),R0 14(SP),R0		ERR.F	*	3449 3502 3503 3504 3505
5164 021134 04 5165 021140 01 5166 021144 05 5167 021146 01 5168 021152 01	46100 16102 50002 10267 17702	013156 000014 013160 013162 171754 172156 013162 171742			MOV BIS BIC MOV BIS MOV MOV BIS MOV CMP BEQ	ML.REG+2(R1),R0 14(SP),R0 ML.REG+4(R1),R0 ML.REG+6(R1),R2 R0,R2 R2,WT.DATA aML.REG+160,R2 ML.REG+6(R1),R2 R2,RD.DATA WT.DATA,RD.DATA		;		3506
5170 021162 01 5171 021166 02	10267 26767	171742	171734		MOV CMP	R2,RD.DATA WT.DATA,RD.DATA		;		3508
5172 021174 00 5173 021176 01 5174 021204 00 5175 021210 14 5176 021216 00	01403 12776 05067 42777 00207	000001 172132 000001	000010 172056	1\$:	BEQ MOV CLR BICB RTS	1\$ #1.a10(SP) ML.REG+166 #1.aML.REG+120 PC		: *,ERR	R.FLG	3511 3512 3449
5177 5178 5179				; Routi	ne Size:	46 words depth per invoca	tion: 3 words			

DAT_DM = ONE;
MLPD = WRT_MASK;
DAT_CLK = ONE;
PROM_RW = ZERO;
DAT_DM = ZERO;
end; 5220 ;
5224 ;
5228 021220 ;
5229 021224 ;
5230 021232 ;
5231 021240 ;
5232 021244 ;
5233 021246 ;
5234 021250 ;
5235 021252 ;
5236 021254 ;
5237 021260 ;
5238 021264 004167 152777 152777 016600 006300 006300 162620 000100 000010 000010 WRT.PD: JSR BISB BISB R1,\$SAVE2 #100,aML.REG+120 #10,aML.REG+120 10(SP),R0 172042 172034 MOV RO RO RO ASL RO, R1 016100 056600 046100 013156 000012 013160 ML.REG+2(R1),R0 12(SP),R0 ML.REG+4(R1),R0

!DO A DATA CLK !CLEAR PROM READ WRITE !CLEAR DATA DIAG MODE

: INDEX.*

: TST.PAT,*

CZMLABO ML-11 LOGIC TES MISCELLANEOUS CODING SE	T MACRO	M1113 22-DEC-80	12:16 PAGE 146		SEQ	0133
5240 5241		ML4			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
5243 021270 016102 5244 021274 050002 5245 021276 010277 5246 021302 152777 5247 021310 142777 5248 021316 142777 5249 021324 000207	013162 172102 000020 171764 000100 171756 000010 171750	MOV BIS MOV BISB BICB BICB RTS	ML.REG+6(R1),R2 R0,R2 R2,aML.REG+230 #20,aML.REG+120 #100,aML.REG+120 #10,aML.REG+120 PC			3543 3544 3545 3514
5248 021316 142777 5249 021324 000207 5250 5251 5252 5257 5258		; Routine Size: ; Maximum stack	: 35 words k depth per invocation: 3	words		

TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML 4.BLI.2 (34)

5260 :ML4 5261 : 5262 : 5263 : 5264 : 5265 : 5266 : 5267 : 5277 : 5278 : 5277 : 5278 : 5278 : 5279 : 5281 : 5282 : 5283 : 5284 : 5284 : 5285 : 5286 : 5287 : 5290 : 5291 : 5291 : 5292 : 5293 : 5296 : 5296 : 5297 : 5298 : 5308 : 5308 : 5308 : 5311 : 5312 : 5313 : 5314 : 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 3548 3548 35553 35553 35555 35555 35555 35555 35555 35555 35555 355666 356666 35666 35666 35666 35666 35666 35666 35666 35666 35666 35666 366666 36666 routine RD_PD (TST_PAT, index, ERR_FLG) : novalue = begin ! FUNCTIONAL DESCRIPTION: COMPARES THE CONTENTS OF THE PROM DATA REGISTER WITH THE MASKED DATA PATTERN GENERATED BY THE MACRO 'WRT_MASK' IF THE COMPARE IS NOT EQUAL THEN THE FORMAL PARAMETER 'ERR FLG' IS ASSIGNED A ONE TO INDICATE THE FRROR ! FORMAL PARAMETERS: TST_PAT DATA PATTERN TO BE MASKED AND COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST INDEX USED BY THE MACRO WRT MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION. ERR_FLG CONTAINS THE ADDRESS (PASSED BY REF) OF THE CALLERS ERROR FLG TO ENABLE THE CALLER TO EXAMINE THE ERROR STATUS OF THE ROUTINE CALL. IMPLICIT INPUTS: WT_DATA GETS LOADED WITH THE GENERATED WRT_MASK DATA PATTERN THUS ALLOWING CALTER TO PRINT FAILING GOOD DATA. RD_DATA GETS LOADED WITH DATA READ FROM THE REGISTER THUS ALLOWING CALLER TO PRINT FAILING BAD DATA. IMPLICIT OUTPUTS: GLOBAL LOCATION WR DATA AND RD DATA LOADED WITH GOOD AND BAD REGISTER DATA

HISCELLANEOUS CO.	DIMO 25	CITON							3EG 0133
5316 :ML4 5317 :						22-De	ec-1980 09:24:31 ec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (3</neale>	4)
5319 : 5320 : 5321 : 5322 : 5323 : 5324 :	3599 3600 3601 3602 3603 3604 3605 3606 3607 3608 3609 3610 3611	PRODAT WT_RD_	R FLG = MRW = CO DM = CO DATA = CO DATA = CO	ZERO; NE; IE; IRT MASK, MLPD or	.IGNORE	!CLEA! !SET !SET !SAVE !REAL	AR ERROR FLG PROM READ WRITE DATA DIAG MODE THE DATA WRITTEN AND SAVE THE REGI	TO THE REGISTER	
5326	3606	if	.WT_DATA	neq .RI	DATA th	nen .ERR_FLG = ONE;	!READ THE REG F	OR WRT_MASK	
5328 5329 5330 5331	3608 3609 3610 3611	PRO DAT end	M RW = 2 DM = ZE	ERO; RO;		!SET !CLEA !CLEA	ERROR FLAG IF NEQ VR PROM READ WRTE VR DATA DIAG MODE		
5345 021360	004167 005076 152777 152777 016600 006300 006300 006300	162512 000010 000100 000010 000012	171730 171722	RD.PD:	CLR BISB BISB MOV ASL	R1,\$SAVE2 a10(SP) #100,aML.REG+120 #10,aML.REG+120 12(SP),RO RO RO RO RO,R1 ML.REG+2(R1),RO ML.REG+4(R1),RO ML.REG+6(R1),R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R2 RO,R3	ERR.F		3547 3600 3601 3602 3603
5349 021372 5350 021376 5351 021402 5352 021406 5353 021410	010001 016100 056600 046100 016102 050002 010267 017702	013156 000014 013160 013162 171512			ASL ASL MOV MOV BIS MOV MOV BIS MOV CMP	14(SP),R0 ML.REG+4(R1),R0 ML.REG+6(R1),R2 R0,R2 R2,WT.DATA	; TST.F	AT,*	
5355 021420 5356 021424	056102 010267	013162 171500	171472		BIS	ML.REG+6(R1),R2 R2,RD.DATA			3604
5357 021430 5358 021436	026767 001403				CMP BEQ	WT.DATA,RD.DATA	:		3606
5358 021436 5359 021440 5360 021446 5361 021454 5362 021462 5363 5364 5365	026767 001403 012776 142777 142777 000207	000001 000100 000010	000010 171620 171612	1\$:	MOV BICB BICB RTS	#1,a10(SP) #100,aML.REG+120 #10,aML.REG+120 PC	; *,ERR	.FLG	3609 3610 3547
5364 5365				; Rout	ine Size:	: 47 words depth per invocation:	: 3 words		

```
5371 :ML4
5372 :
5373
5374 :
5375 :
5376 :
                                                                                            22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                            TOPS-20 Bliss-16 V2(212)
                                                                                                                            PA: <NEALE>BL3ML4.BL1.2 (35)
                            routine WRT_EL (TST_PAT, index) : novalue =
                    3612
3613
3614
3615
3616
3617
3618
3620
3621
3622
3623
                                 begin
! FUNCTIONAL DESCRIPTION:
                                      DUMMY ROUTINE CALL TO ASSIST IN THE READ WRITE REGISTER ALGORITHEM
                               FORMAL PARAMETERS:
                                      TST PAT DATA PATTERN TO BE MASKED AND
                                       COMPARED AGAINST THE CONTENTS
                                       OF THE REGISTER UNDER TEST
                                       INDEX
                                       USED BY THE MACRO WRT_MASK TO
                                      SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION.
                                ERROR LOCATION REG IS READ ONLY
                                 return;
                                 end;
                                                 WRT.EL: RTS
                                                                       PC
                                                  ; Routine Size: 1 word
                                                  ; Maximum stack depth per invocation: 0 words
```

TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (36)

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 routine RD_EL (TST_PAT, index, ERR_FLG) : novalue = begin ! FUNCTIONAL DESCRIPTION: COMPARES THE CONTENTS OF THE ERROR LOCATION REGISTER WITH THE CONTENTS OF TST_PAT. IF THE COMPARE IS NOT EQUAL THEN THE FORMAL PARAMETER 'ERR FLG' IS ASSIGNED A ONE TO INDICATE THE ERROR FORMAL PARAMETERS: TST PAT DATA PATTERN TO BE MASKED AND COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST INDEX USED BY THE MACRO WRT MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION. 3665 3666 3666 3667 3669 3670 3671 3672 3673 3674 3675 3676 3676 3680 3681 3682 3683 3685 3686 3686 3686 3686 3688 ERR FLG CONTAINS THE ADDRESS (PASSED BY REF) OF THE CALLERS ERROR FLG TO ENABLE THE CALLER TO EXAMINE THE ERROR STATUS OF THE ROUTINE CALL. IMPLICIT INPUTS: WT DATA GETS LOADED WITH THE GENERATED WRT_MASK DATA PATTERN THUS ALLOWING CALTER TO PRINT FAILING GOOD DATA. RD_DATA GETS LOADED WITH DATA READ FROM THE REGISTER THUS ALLOWING CALLER TO PRINT FAILING BAD DATA. IMPLICIT OUTPUTS: GLOBAL LOCATION WR DATA AND RD DATA LOADED WITH GOOD AND BAD REGISTER DATA .ERR_FLG = ZERO;

5474 :ML4 5475 :					22-De 22-De	c-1980 09:24:31 c-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (36)</neale>		
5477 5478 :	3690 3691	WT_DATA = RD_DATA =	.TST_PAT; .MLEC;		SAVE THE DATA WRITTEN TO THE REGISTER READ AND SAVE THE REGISTER				
5475 : 5476 : 5477 : 5478 : 5479 : 5480 : 5481 : 5482 : 5486 :	3690 3691 3692 3693 3694 3695	if .RD_DAT	A neq .WT	_DATA t	then .ERR_FLG = ONE;	!READ REGISTER	FOR WT_DATA		
5491 021472 5492 021500 5493 021506 5494 021514 5495 021516	005076 016667 017767 026767 001403 012776 000207	000002 000006 171426 171670 171422 171416 171412 000001 000002		CLR MOV MOV CMP BEQ MOV RTS	a2(SP) 6(SP),WT.DATA aMI.REG+220,RD.DATA RD.DATA,WT.DATA 1\$ #1,a2(SP) PC	ERR.F	AT.*	3689 3690 3691 3693	
5496 021524 5497 5498 5499 5504 5505			; Routing; Maximu	ne Size um stac	: 16 words k depth per invocation:	0 words			

SCELLANEOUS CODING SE 5507 :ML4 5508 : 5509 : 5510 : 3696 5511 : 3698 5512 : 3698 5513 : 3700 5515 : 3701 5516 : 3702 5517 : 3703 5518 : 3704 5519 : 3705 5520 : 3706 5521 : 3707 5522 : 3708 5523 : 3709 5524 : 3710 5525 : 3711 5526 : 3712 5527 : 3713 5528 : 3714 5529 : 3715 5530 : 3716 5531 : 3717 5532 : 3718 5533 : 3719 5534 : 3720 5544 5545 5550 5551 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (37) routine WRT_EE (TST_PAT, index) : novalue = begin ! FUNCTIONAL DESCRIPTION: DUMMY ROUTINE CALL TO ASSIST IN THE READ WRITE REGISTER ALGORITHEM FORMAL PARAMETERS:
TST_PAT
DATA PATTERN TO BE MASKED AND
COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST INDEX USED BY THE MACRO WRT_MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION. 3715 3716 3717 3718 3719 3720 ! ECC ERROR REGISTER IS READ ONLY return; end; WRT.EE: RTS 3696 ; Routine Size: 1 word ; Maximum stack depth per invocation: 0 words

TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML 4.BL1.2 (38)

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 routine RD_EE (TST_PAT, index, ERR_FLG) : novalue = begin ! FUNCTIONAL DESCRIPTION: COMPARES THE CONTENTS OF THE ECC ERROR REGISTER WITH TST_PAT. IF THE COMPARE IS NOT EQUAL THEN THE FORMAL PARAMETER 'ERR FLG' IS ASSIGNED A ONE TO INDICATE THE ERROR. FORMAL PARAMETERS: TST PAT DATA PATTERN TO BE COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST. INDEX USED BY THE MACRO MLEE TO SELECT THE CURRENT REGISTERS ADDRESS. ERR_FLG CONTAINS THE ADDRESS (PASSED BY REF) OF THE CALLERS ERROR FLG TO ENABLE THE CALLER TO EXAMINE THE ERROR STATUS OF THE ROUTINE CALL. IMPLICIT INPUTS: WT DATA GETS LOADED WITH THE TST PAT THUS ALLOWING CALLER TO PRINT THE FAILING DATA. RD DATA GETS LOADED WITH DATA READ FROM THE REGISTER THUS ALLOWING CALLER TO PRINT FAILING BAD DATA. 3761 3762 3763 3764 3765 3766 3767 3768 3769 IMPLICIT OUTPUTS: GLOBAL LOCATION WR DATA AND RD_DATA LOADED WITH GOOD AND BAD REGISTER DATA .ERR_FLG = ZERO; WT_DATA = .TST_PAT; RD_DATA = .MLEE; 3771

!CLEAR THE ERROR FLAG SAVE DATA WRITTEN TO THE REGISTER PREAD AND SAVE THE REGISTER

5640 :1 5641 :	ML4		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML 4.BLI.2 (39)</neale>
5643 : 5644 :	3777 3778	routine WRT_D1 (TST_PAT, index) : begin	novalue =	
5643 5644 5645 5646 5647 5648 5649 5650 5651 5653 5654 5655 5656 5657 5658	3780 3780 3781 3782 3783 3785 3786 3787 3788 3789 3791 3792 3793	FUNCTIONAL DESCRIPTION: LOADS THE DATA DIAG REG 1 FORMAL PARAMETERS: TST_PAT CURRENT DATA PATTERN TO B INDEX USED BY THE MACRO WRT_MAS FORCED HI, FORCED LO AND	K TO SELECT THE CURRENT REGISTERS ADDITION'T CARE MASK INFORMATION.	RESS,
5658 : 5659 : 5660 :	3792 3793 3794	DAT_DM = ONE; MLDT = WRT_MASK; DAT_DM = ZERO; end;	!SET DATA DIAG MODE !LOAD MLD1 WITH GENERA! !CLEAR DATA DIAG MODE	TED WRT_MASK PATTERN
5668 02 5669 02 5670 02 5671 02 5672 02	21570 004167 21574 152777 21602 016600 21606 006300 21610 006300 21612 006300 21614 010001	162250 000010 171472 WRT.D1: JSR R BISB W 000010 MOV 1 ASL R ASL R	1.\$SAVE2 10.aML.REG+120 0(SP).RO : INDEX	3777 3791 3792
5673 02 5674 02 5675 02 5676 02 5677 02 5678 02 5679 02	21614 010001 21616 016100 21622 056600 21626 046100 21632 016102 21636 050002 21640 010277 21644 142777 21652 000207	013156 MOV M 000012 BIS 1 013160 BIC M	0,R1 L.REG+2(R1),R0 2(SP),R0 ; TST.F L.REG+4(R1),R0 L.REG+6(R1),R2	PAT,*
5680 02 5681 02 5682 02	21640 010277 21644 142777 21652 000207	171500 MOV R 000010 171422 BICB #	0.R2 2.aML.REG+170 10.aML.REG+120 ;	3793 3777
5681 02 5682 02 5683 5684 5685 5690 5691		; Routine Size: ; Maximum stack d	26 words epth per invocation: 3 words	

5693 :ML4 5694 : 5695 : 5696 : 5698 : 5699 : 5700 : 5701 : 5702 : 5703 : 5704 : 5705 : 5706 : 5707 : 5708 : 5709 : 5711 : 5712 : 5713 : 5714 : 5715 : routine RD_D1 (TST_PAT, index, ERR_FLG) : novalue = begin ! FUNCTIONAL DESCRIPTION: COMPARES THE CONTENTS OF THE DATA DIAG REGISTER 1 WITH THE MASKED DATA PATTERN GENERATED BY THE MACRO 'WRT_MASK''. ASSIGNED A ONE TO INDICATE THE ERROR. FORMAL PARAMETERS: TST PAT DATA PATTERN TO BE MASKED AND 5716 5717 5718 5719 5720 5721 5722 5723 5724 5725 5726 5727 5728 5728 5729 5730 5731 5732 5733 5734 5735 5736 5737 5738 5739 5739 5739 5740 5741 5742 5743 5744 5745 COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST. INDEX OF THE ROUTINE CALL. IMPLICIT INPUTS: WT DATA GETS LOADED WITH THE GENERATED RD_DATA REGISTER THUS ALLOWING CALLER TO PRINT FAILING BAD DATA. IMPLICIT OUTPUTS: GLOBAL LOCATION WR_DATA AND RD_DATA LOADED WITH GOOD AND BAD REGISTER DATA

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML 4.BL1.2 (40)

IF THE COMPARE IS NOT EQUAL THEN THE FORMAL PARAMETER 'ERR_FLG' IS

USED BY THE MACRO WRT MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION.

ERR_FLG CONTAINS THE ADDRESS (PASSED BY REF) OF THE CALLERS ERROR FLG TO ENABLE THE CALLER TO EXAMINE THE ERROR STATUS

WRT_MASK DATA PATTERN THUS ALLOWING CALTER TO PRINT FAILING GOOD DATA.

GETS LOADED WITH DATA READ FROM THE

: TST.PAT.*

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 158	SEQ 0145
5804 5805 5806	;ML4	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
\$806 \$807 \$808 022016 050002 \$809 022020 010267 171102 \$810 022024 017767 171314 \$811 022032 026767 171072 \$812 022040 001403 \$813 022042 012776 000001 \$814 022050 152777 000040 \$815 022056 016702 171520 \$816 022062 042702 177770 \$816 022066 142777 000007 \$818 022074 150277 171114 \$819 022100 000207	MOV ML.REG+6(R1),R2 BIS RO,R2 MOV R2,WT.DATA 171076 MOV aML.REG+170,RD.DATA 171066 CMP RD.DATA,WT.DATA BEQ 25 000010 MOV W1.a10(SP) 171136 25: BISB W40.aML.REG+40 MOV ML.DUT.R2 BIC W177770,R2 171120 BICB W7.aML.REG+40 BISB R2.aML.REG+40 RTS PC ROUTINE Size: 75 words Raximum stack depth per invocation: 3 words	3862 3864 ERR.FLG
5822 5827 5828	; Maximum stack depth per invocation: 3 words	

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5830 :ML4
5831 :
5832 :
5833 :
5834 :
5835 :
5836 :
5837 :
5838 :
5840 :
5841 :
5842 :
5843 :
5844 :
5845 :
5846 :
5847 :
5848 :
5849 :
5850 :
5861 0
5861 0
5862 0
5863 0
5864 0
5865 0
5866 0
5867 (
5868 (
5869 )
5871 :
5872 :
5873 :
5874 :
5875 :
5880 :
5880 :
5881 :
                                                                                                                                                                      TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (41)
                                                                                                                             22-Dec-1980 09:24:31 22-Dec-1980 09:21:22
                                            routine WRT_D2 (TST_PAT, index) : novalue =
                                                   begin
                                            ! FUNCTIONAL DESCRIPTION:
                                                         LOADS THE DATA DIAG REG 2 WITH A DATA PATTERN GENERATED BY THE MACRO WRT_MASK
                                               FORMAL PARAMETERS:
                                                          TST_PAT
                                                          CURRENT DATA PATTERN TO BE LOADED IN THE REGISTER.
                                                          INDEX
                                                         USED BY THE MACRO WRT MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION.
                                                                                                                             !SET DATA DIAG MODE
!LOAD MLD2 WITH GENERATED WRT_MASK PATTERN
!CLEAR DATA DIAG MODE
                                                   DAT_DM = ONE;
                                                   MLDZ = WRT_MASK;
                                                   DAT_DM = ZERO;
                                                   end:
                              004167
152777
016600
006300
006300
010001
                                                                       WRT.D2: JSR
BISB
                                            161736
000010
000010
                                                                                                  R1,$SAVE2
#10,aML.REG+120
10(SP),R0
                                                         171160
                                                                                     MOV
                                                                                                                                                         : INDEX.*
                                                                                                  RO
RO
                                                                                     ASL
                                                                                     ASL
                                                                                    ASL
                                                                                     MOV
                                                                                                  RO.R1
                              016100
056600
046100
016102
                                            013156
                                                                                    MOV
                                                                                                  ML.RES+2(R1),R0
12(SP),R0
                                                                                                                                                        : TST.PAT.*
                                            013160
                                                                                    BIC
                                                                                                  ML.REG+4(R1),R0
                                            013162
                                                                                                  ML.REG+6(R1),R2
                                                                                     MOV
                              050002
010277
142777
000207
                                                                                     BIS
                                                                                                  RO, R2
                                            171176
                                                                                     MOV
                                                                                                  R2, 24L. REG+200
                                            000010 171110
                                                                                    BICB
                                                                                                  #10, aML.REG+120
                                                                                                                                                                                                                            3885
3869
                                                                                     RTS
                                                                       ; Routine Size: 26 words
                                                                       ; Maximum stack depth per invocation: 3 words
```

TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (42)

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 3/887 3/888 3/889 3/890 3/800 routine RD_D2 (TST_PAT, index, ERR_FLG) : novalue = begin ! FUNCTIONAL DESCRIPTION: COMPARE THE CONTENTS OF THE DATA DIAG REGISTER 2 WITH THE MASKED DATA PATTERN GENERATED BY THE MACRO 'WRT_MASK''. IF THE COMPARE IS NOT EQUAL THEN THE FORMAL PARAMETER 'ERR_FLG'' IS ASSIGNED A ONE TO INDICATE THE ERROR. FORMAL PARAMETERS: TST PAT
DATA PATTERN TO BE MASKED AND
COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST. INDEX USED BY THE MACRO WRT MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFOMATION. ERR_FLG CONTAINS THE ADDRESS (PASSED BY REF) OF THE CALLERS ERROR FLG TO ENABLE THE CALLER TO EXAMINE THE ERROR STATUS OF THE ROUTINE CALL. IMPLICIT IMPUTS: WT_DATA GETS LOADED WITH THE GENERATED WRT MASK DATA PATTERN THUS ALLOWING CALLER TO PRINT FAILING GOOD DATA. RD_DATA
GETS LOADED WITH DATA READ FROM THE
REGISTER THUS ALLOWING CALLER
TO PRINT FAILING BAD DATA. IMPLICIT OUTPUTS: GLOBAL LOCATION WR_DATA AND RD_DATA LOADED WITH GOOD AND BAD REGISTER DATA

CODING 20	CITUN								SEG O	140
						22-Dec-1980 0 22-Dec-1980 0	9:24:31 9:21:22	TOPS-20 Bliss-16 N PA: <neale>BL3ML4.8</neale>	/2(212) BLI.2 (42)	
3939 3940 3941 3942	DAT ML I DAT	R FLG = DM = ON FUNC = W _CLK = O	ZERO; E; rite; NE;			CLEAR ERROR SET DATA DIA LOAD WRITE F DO A DATA CL	FLAG G MODE UNC TO CS1 OCK			
3944 3945 3946 3947 3948 3949	ther	begin CLR_MBU	s:	S_SET		SEE IF CALLE	R IS REG I O GENERATE	INIT TEST INIT DATA		
3950 3951 3952	WT_I RD_I	DATA = W	RT_MASK; MLD2;			SAVE THE DAT	A WRITTEN E THE REGI	TO THE REGISTER		
3953 3954					then .ERR_FLG = ONE	: !READ	MLD2 FR	RT_MASK PATTERN		
3955 3956 3957 3958						SET ERROR FL	AG IF NEQ			
6 004167 2 005076 6 152777 4 142777	161652 000010 000010 000077	171070 170742	RD.D2:	JSR CLR BISB BICB	R1,\$SAVE2 a10(SP) #10,aML.REG+120 #77,aML.REG		ERR.F	LG		3887 3939 3940 3941
0 152777 6 026727	000020 170704	171046 000001		BISB CMP	#20.aML.REG+120 REG.INIT.FLG.#1		:			3942 3944
0 062701	000040 171332 177770	170750		BISB	#40.aML.REG+40 ML.DUT.R1		;			3946
4 142777 2 150177 6 152777 4 016600	000007 170726 000010 000012	170732 171000	15:	MUV	#7,aML.REG+40 R1,aML.REG+40 #10,aML.REG+120 12(SP),R0		: INDEX	(,*		3948 3951
006300 4 006300 6 010001 0 016100 4 056600 0 046100 4 016102	013156 000014 013160 013162			ASL ASL MOV MOV BIS BIC MOV	RO RO RO,R1 ML.REG+2(R1),RO 14(SP),RO ML.REG+4(R1),RO ML.REG+6(R1),R2		; TST.F	PAT,*		
	3939 3940 3941 3942 3943 3944 3945 3946 3946 3946 3950 3951 3952 3953 3953 3953 3954 3955 3957 3958 6004167 6005076 6152777 64152777 64016701 640701	3940 DAT 3941 ML 3943 if 3943 if 3946 3947 3948 3949 3950 3951 WT 3952 RD 3953 if 3955 3956 3957 CLR 2005076 000010 26 152777 000010 26 152777 000010 26 026727 170704 27 000010 28 026727 170704 29 0152777 000020 20 026727 170704 20 026727 170704 20 026727 170704 20 026727 170704 21 152777 000010 22 000010 23 000010 24 016701 171332 25 026727 170704 26 026727 170704 27 000010 28 026727 170704 29 026727 170726 30 042701 177770 31 150177 170726 32 150177 170726 33 150177 170726 34 016600 000012 35 152777 000010 36 016000 000012 37 150177 170726 38 152777 000010 39 016000 000012 30 006300 30 006300 30 006300 31 156	3939	3939	3939	3939	22-Dec-1980 0 25 EFT DATA DATA DATA DATA DATA CLIPS SET FOR LIPS SE	22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 3939	22-Dec-1980 09:24:31 TOPS-20 BLiss-16 \ 22-Dec-1980 09:24:32 TOPS-20 BLiss-16 \ 22-Dec-1980 09:21:22 TOPS-20 BLISS BLISS TOPS-20 BLISS BLISS TOPS-20 BLISS BLISS BLISS TOPS-20 BLISS BL	22-Dec-1980 09:24:31 TOPS-20 BLiss-16 V2(212) 22-Dec-1980 09:21:22 PA: <meale>BL3ML4.BLI.2 (42) 3939</meale>

CZMLABO ML-11 L MISCELLANEOUS C	OGIC TES	T	MACRO M	1113	22-DEC-80	12:16 PAGE 162		SEG	0149
5994 5995				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
5997 022332 5998 022336 5999 022344 6000 022352	010267 017767 026767 001403	170570 171012 170560	170564 170554		MOV MOV CMP BEQ MOV BISB	R2.WT.DATA BML.REG+200,RD.DATA RD.DATA,WT.DATA 2\$;		3952 3954
6001 022354 6002 022362 6003 022370 6004 022374	012776 152777 016702 042702	000001 000040 171206 177770	000010 170624	2\$:	MDV	#1,a10(SP) #40,aML.REG+40	; *,ERR.FLG		
6005 022400 6006 022406 6007 022412 6008	042702 142777 150277 000207	171206 177770 000007 170602	170606		BIC BICB BISB RTS	ML.DUT.R2 #177770.R2 #7.aML.REG+40 R2.aML.REG+40 PC			3887
5999 022344 6000 022352 6001 022354 6002 022362 6003 022370 6004 022374 6005 022400 6006 022406 6007 022412 6008 6009 6010 6015 6016				; Rou ; Max	tine Size: imum stack	75 words depth per invocation:	3 words		

16018 6019	ML4							22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (43)</neale>	
6020 6021 6022		3959 3960	rout ine	WRT_D3	(TST_PAT	, index) : novalue =			
6024 6025		3961 3962 3963	FUNCT	IONAL DE	SCRIPTIO	N:				
6027 6028 6029 6030		3965 3966 3967 3968		LOADS T REGISTE GENERAT WRT_MAS	HE DATA R 3 WITH ED BY TH K.	DIAG I A DATA IE MACRO	PATTERN			
6032		3969	FORMA	L PARAME	TERS:					
6033 6034 6035 6036		3972 3973 3974 3975		TST_PAT CURRENT LOADED	DATA PA IN THE R	TTERN T	O BE			
6018 6020 6021 6022 6023 6024 6025 6026 6027 6028 6030 6031 6033 6033 6033 6035 6038 6039 6040 6041 6042 6043 6044 6045 6046 6049 6049		3963 3964 3965 3966 3967 3968 3969 3971 3972 3973 3974 3975 3976 3976 3979 3981 3981 3982 3983 3984		INDEX USE BY TO SELE ADDRESS LO AND INFORMA	THE MACR CT THE C FORCED DON'T CA TION.	O WRT M CURRENT HI, FO KRE MASK	ASK REGISTERS RCED			
6044		3982 3983	_							
6046 6047 6048 6049 6050 6051		3986 3987 3988	DAT MLE MLE MLE DAT	DM = ON 2_MASK = 2 = WRT_ 2_MASK = DM = ZE	E; %o'0003 MASK; %o'1003 RO;	377'; 300';		SET DATA DIAG MODE MASK OUT ECC CRC WORD LOAD MLEZ WITH GENERA RESTORE MASK CLEAR DATA DIAG MODE.	BITS ATED WRT_MASK PATTERN	
6052 6056	:	3989 3990	and							
6060 6061 6062 6063 6064	022414 022420 022426 022434 022440	004167 152777 012767 016600 006300 006300 006300 010001	161424 000010 000377 000010	170646 170706	WRT.D3:	BISB	R1.\$SAVE2 #10.aML.REG+120 #377.ML.REG+166 10(SP),R0 R0 R0 R0	INDE	*x.*	3959 3985 3986 3987
6061 6062 6063 6064 6065 6066 6067 6068 6069 6070	022414 022426 022434 022440 022442 022444 022446 022450 022450 022460	006300 010001 016100 056600 046100 016102	013156 000012 013160 013162			MOV MOV ASL ASL MOV MOV BIS BIC MOV	RO RO,R1 ML.REG+2(R1),RO 12(SP),RO ML.REG+4(R1),RO ML.REG+6(R1),R2	; TST.	PAT,*	

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 164	SEQ 0151
6073 6074 6075 6076 022470 050002	:ML4 BIS RO.R2	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
6077 022472 010277 170636 6078 022476 012767 100300 6079 022504 142777 000010	BIS RO.R2 MOV R2.aML.REG+160 170636 MOV #-77500,ML.REG+166 170562 BICB #10.aML.REG+120 RTS PC	3988 3989 3959
6080 022512 000207 6081 6082 6083 6088 6089	; Routine Size: 32 words ; Maximum stack depth per invocation: 3 words	

TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BL1.2 (44)

```
6091 :ML4
6092 :
6093 :
6094 :
6095 :
6096 :
6097 :
6098 :
6100 :
                                                                                                                  22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                   routine RD_D3 (TST_PAT, index, ERR_FLG) : novalue =
                         3991
3992
3993
3994
3995
3996
3997
3998
3999
4000
                                         begin
                                   ! FUNCTIONAL DESCRIPTION:
                                                COMPARES THE CONTENTS OF THE
6100 ;
6101 ;
6102 ;
6103 ;
6104 ;
6105 ;
6106 ;
6107 ;
6108 ;
6109 ;
                                                DATA DIAG REGISTER 3
                                                WITH THE MASKED DATA PATTERN
                                                GENERATED BY THE MACRO 'WRT_MASK''.
                                               IF THE COMPARE IS NOT EQUAL THE FORMAL PARAMETER "ERR_FLG" IS ASSIGNED A ONE TO INDICATE THE
                                                ERROR.
6110
                                      FORMAL PARAMETERS:
6111 :
6112
                                                TST_PAT DATA PATTERN TO BE MASKED AND
6114 :
                         4011
4012
4013
4014
4015
4016
4017
4018
4019
4020
                                                COMPARED AGAINST THE CONTENTS
                                                OF THE REGISTER UNDER TEST.
6116 :
                                                INDEX
6118
6119
6120
6121
6122
6123
6124
6125
6126
6127
6128
6129
6130
6131
6133
6133
6134
6135
6136
6141
6142
6143
6144
6144
                                                USED BY THE MACRO WRT_MASK TO
                                                SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE
                                                MASK INFORMATION
                                               ERR_FLG
CONTAINS THE ADDRESS (PASSED BY REF)
OF THE CALLERS ERROR FLG TO ENABLE THE
CALLER TO EXAMINE THE ERROR STATUS
                                                OF THE ROUTINE CALL.
                                      IMPLICIT INPUTS:
                                                WT_DATA
                                                   GETS LOADED WITH THE GENERATED
                                                   WRT_MASK DATA PATTERN THUS ALLOWING
                                                   CALTER TO PRINT FAILING GOOD DATA.
                                                RD DATA
                                                   GETS LOADED WITH DATA READ FROM THE
                                                   REGISTER THUS ALLOWING CALLER
                         4034
4035
4036
4037
4038
4039
4040
4041
4042
                                                   TO PRINT FAILING BAD DATA.
                                      IMPLICIT OUTPUTS:
                                                GLOBAL LOCATION WR_DATA
                                                AND RD_DATA LOADED WITH GOOD
                                                AND BAD REGISTER DATA
```

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 166
MISCELLANEOUS CODING SECTION

MISCELLANEOUS COD	DING SE	CTION							SEQ 0153
6147 ;ML4 6148 ; 6149							22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (44</neale>	6)
6150 : 6151 : 6152 :	4043 4044 4045	.ER	R_FLG =	ZERO: %0'000:	377';		CLEAR ERROR FLAG		
6154 6155 6155	4048 4048	ML	R_FLG = 2 MASK = DM = ON FUNC = W _CLK = O	rite; NE;			!LOAD WRITE FUNCTION T	O MLCS1	
6157 : 6158 :	4050 4051 4052	if the			S_SET		SEE IF CALLER IS REG	INIT TEST TE INIT DATA	
6148 ; 6149 ; 6150 ; 6151 ; 6152 ; 6153 ; 6154 ; 6155 ; 6156 ; 6157 ; 6160 ; 6161 ; 6162 ; 6163 ; 6164 ; 6165 ; 6166 ; 6167 ; 6168 ; 6169 ; 6170 ; 6171 ;	4043 4044 4045 4046 4047 4048 4049 4050 4051 4052 4053 4054 4057 4058 4059 4060 4061 4062 4063 4064		begin CLR_MBU DAT_DM end;	S; = ONE;					
6165 ; 6165 ;	4056 4057 4058 4059	WT_RD_	DATA = W	RT_MASK; MLE2 or	.1GNORE	;	SAVE THE DATA WRITTEN	TO THE REGISTER	
6167	4060	if	.WT_DATA	neq .RI	DATA 1	then .ERR_FLG = ONE	: !READ THE REG	FOR WRT_MASK	
6169 : 6170 : 6171 :	4062 4063 4064	CLR	2 MASK = MBUS;		300';		!CLEAR THE MASS BUS		
6171; 6175 022514 0 6180 022520 0 6181 022524 0 6182 022532 1 6183 022540 1 6184 022546 1 6185 022554 1 6186 022562 0 6187 022570 0 6188 022572 1 6189 022600 0 6190 022604 0 6191 022610 1 6192 022616 1 6193 022622 1 6194 022630 0	004167 005076 012767	161324 000010 000377	170610	RD.D3:	CLR	R1,\$SAVE2 a10(SP) #377,ML.REG+166 #10,aML.REG+120 #77,aML.REG #61,aML.REG #20,aML.REG+120 REG.INIT.FLG,#1	ERR.	FLG	3991 4044 4045 4046 4047
6183 022540 1	142777	000077	170406		BICB	#77, and . REG			4047
6185 022564 1 6186 022562 0 6187 022570 0	152777 152777 026727 001017	000020 170350	170512 000001		MOV BISB BICB BISB BISB CMP BNE BISB	#20, art. REG+120 REG. INIT. FLG, #1	:		4048 4050
6188 022572 1 6189 022600 (6190 022604 (152777 016701 042701	000040 170776	170414		MOV BISB	#40, aml.REG+40 ML.DUT, R1 #177770 P1			4052
6191 022610 1 6192 022616 1	142777	000007	170376		BICB	#7.24L.REG+40			
6193 022622 1 6194 022630 (6195 022634 (6196 022636 (042701 142777 150177 152777 016600 006300 006300 006300 010001	000010	170444	15:	BICB BISB BISB MOV ASL ASL ASL MOV MOV BIS	ML.DUT.R1 #177770,R1 #7.aML.REG+40 R1.aML.REG+40 #10.aML.REG+120 12(SP),R0 R0 R0 R0 R0 R0,R1	: INDE	x,*	4054 4057
6197 022640 0 6198 022642 0 6199 022644 0	006300 010001 016100	013156 000014			ASL MOV MOV	RO RO,R1 ML.REG+2(R1),RO 14(SP),RO			
6200 022650 0	056600	000014			R12	14(SP),RU	; TST.	PAI,*	

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 167	SEQ 0154	
6202 6203 6204	;ML4	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:	MV
6205 022654 046100 013160 6206 022660 016102 013162 6207 022664 050002 6208 022666 010267 170234 6209 022672 017702 170436 6210 022676 056102 013162 6211 022702 010267 170222 6212 022706 026767 170214 6213 022714 001403 6214 022716 012776 000001 6215 022724 012767 100300 6216 022732 152777 000040 6217 022740 016702 170636	BIC ML.REG+4(R1),R0 MOV ML.REG+6(R1),R2 BIS RO,R2 MOV R2,WT.DATA MOV aML.REG+160,R2 BIS ML.REG+6(R1),R2 MOV R2,RD.DATA CMP WT.DATA,RD.DATA BEQ 2\$ 000010 MOV W1,a10(SP) 170410 2\$: MOV W-77500,ML.REG+166 170254 BISB W40,aML.REG+40	: 4058 : 4060 : *,ERR.FLG 4062	0
6217 022740 016702 170636 6218 022744 042702 177770 6219 022750 142777 000007 6220 022756 150277 170232 6221 022762 000207 6222 6223 6224 6229 6230	170254 BISB #40.amL.REG+40 MOV ML.DUT.R2 BIC #177770.R2 170236 BISB #7.amL.REG+40 BISB R2.amL.REG+40 RTS PC ; Routine Size: 84 words ; Maximum stack depth per invocation: 3 words	; 3991	

```
22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                               TOPS-20 Bliss-16 V2(212)
PA:<NEALE>BL3ML4.BLI.2 (45)
                      routine WRT_DS (TST_PAT, index) : novalue =
                             begin
                       FUNCTIONAL DESCRIPTION:
DUMMY ROUTINE CALL TO ASSIST IN THE READ
WRITE REGISTER ALGORITHEM
                         FORMAL PARAMETERS:
TST_PAT
DATA PATTERN TO BE MASKED AND
                                   COMPARED AGAINST THE CONTENTS
                                   OF THE REGISTER UNDER TEST
                                   INDEX
                                  USED BY THE MACRO WRT_MASK TO
SELECT THE CURRENT REGISTERS ADDRESS,
FORCED HI, FORCED LO AND DON'T CARE
MASK INFORMATION.
                           DRIVE STATUS REG IS READ ONLY
                             return;
                             end:
022764 000207
                                                                                                                                                                              4065
                                              WRT.DS: RTS
                                               ; Routine Size: 1 word
                                               ; Maximum stack depth per invocation: 0 words
```

TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BLI.2 (46)

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 :ML4 4091 4092 4093 4094 4095 4096 4097 4098 4100 4101 4102 4103 routine RD_DS (TST_PAT, index, ERR_FLG) : novalue = begin ! FUNCTIONAL DESCRIPTION: COMPARES THE CONTENTS OF THE DRIVE STATUS REGISTER WITH THE MASKED DATA PATTERN GENERATED BY THE MACRO 'WRT_MASK'. IF THE COMPARE IS NOT EQUAL THEN THE FORMAL PARAMETER 'ERR_FLG' IS 4104 4105 4106 4107 ASSIGNED A ONE TO INDICATE THE ERROR. FORMAL PARAMETERS: 4108 4109 TST_PAT 4110 DATA PATTERN TO BE MASKED AND COMPARED AGAINST THE CONTENTS OF THE REGISTER UNDER TEST. USED BY THE MACRO WRT MASK TO SELECT THE CURRENT REGISTERS ADDRESS, FORCED HI, FORCED LO AND DON'T CARE MASK INFORMATION. 4119 4120 4121 4122 4123 4124 4125 4126 4127 4128 4130 4131 4133 4134 4135 4137 4138 4139 ERR_FLG CONTAINS THE ADDRESS (PASSED BY REF) OF THE CALLERS ERROR FLG TO ENABLE THE CALLER TO EXAMINE THE ERROR STATUS OF THE ROUTINE CALL. IMPLICIT INPUTS: WT DATA GETS LOADED WITH THE GENERATED WRT_MASK DATA PATTERN THUS ALLOWING CALTER TO PRINT FAILING GOOD DATA. RD DATA GETS LOADED WITH DATA READ FROM THE REGISTER THUS ALLOWING CALLER TO PRINT FAILING BAD DATA. IMPLICIT OUTPUTS: GLOBAL LOCATION WR_DATA AND RD_DATA LOADED WITH GOOD AND BAD REGISTER DATA 4140 4141 4142

; Maximum stack depth per invocation: 3 words

```
22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (47)
       routine WRT_REG (TST_PAT, REG_SEL, index) : novalue =
       ! FUNCTIONAL DESCRIPTION:
                 A ROUTINE TO SELECTIVELY
                 CALLED ROUTINES WHICH
                 WRITE TO INDIVIDUAL ML11
4160
4161
4162
4163
4164
4165
                 REGISTERS
       ! FORMAL PARAMETERS:
                REG SEL
CASE SELECT EXPRESSION TO
SELECT THE WRITE REGISTER
ROUTINE TO CALLED
                TST_PAT
DATA PATTERN WHICH THE SELECTED
REGISTER WILL BE TESTED AGAINST
                 INDEX
                 LOADED WITH THE ML REG INDEX
SELECT NUMBER OF THE REGISTER
                 BEING TESTED
       ! SIDE EFFECTS:
                WHEN A WRITE REGISTER ROUTINE IS CALLED THE VARIABLE 'INDEX' FROM THE CALLING
                 TEST IS LOADED WITH THE REGISTERS
                 ML REG INDEX NUMBER.
                 THIS ENABLES THE CALLING TEST JO FIND
                THE FAILING REGISTER ADDRESS.
            case .REG_SEL from 0 to 13 of
                                                                  SELECT THE WRITE REGISTER ROUTINE CALL
                     WRT_CS1 (.TST_PAT, .index = 0);
                                                                 !CALL ROUTINE TO LOAD MLCS1
                [1]: WRT_ER (.TST_PAT, .index = 6);
                                                                !CALL ROUTINE TO LOAD MLER
                     WRT_DA (.TST_PAT, .index = 3);
                                                                !CALL ROUTINE TO LOAD MLDA
                [3] : WRT_MR (.TST_PAT, .index = 10);
                                                                !CALL ROUTINE TO LOAD MILMR
                [4] :
```

SEQ 0		5 PAGE 173	22-DEC-80	MACRO M1113	CTION	ODING SE	LABO ML-11 L CELLANEOUS C
22-Dec-1980 09:24:31				;ML4			
22-Dec-1980 09:24:31 22-Dec-1980 09:21:22							6495 6496 6497
		1\$.WORD			000174	498 023140
		-1\$ -1\$. WORD			000212	5499 023142 5500 023144
		-15	. WORD			000246	5501 023146
		-15	. WORD . WORD . WORD . WORD . WORD . WORD			000302	3502 023150 3503 023152
		-1\$ -(SP)	.WORD MOV	2\$:		000320	5504 023154
			CLR	20.		005010	5498 023140 5499 023142 5500 023144 5501 023146 5502 023150 5503 023152 5504 023154 5505 023156 5507 023160 5508 023164 5509 023170 5510 023172
		P) JRT. CS1	CLR		174272	005046	5507 023162 5508 023164
		RT.CS1	JSR BR	70.		000532	5509 023170
	•	-(SP) (RO)),-(SP) ART.ER	MOV	3\$:	000006	012710	6511 023174
		,-(SP)	MOV JSR		174432	011046	512 023200 513 023202
	:	(60)	BR			000523	5513 023202 5514 023206 5515 023210 6516 023212 5517 023216
	•	(RO)	MOV	4\$:	000003	010146 012710	6516 023212
		-(SP) (RO)),-(SP) #RT.DA	MOV		174572	011046 004767 000514	5517 023216 5518 023220
			JSR BR		114512	000514	519 023224
		-(SP) ,(R0) ,-(SP)	MOV	5\$:	000012	012710	6520 023226 6521 023230
		,-(SP)	MOV JSR		174732	011046 004767 000505	6498 023140 6499 023144 6500 023144 6501 023146 6502 023150 6503 023152 6504 023154 6505 023154 6506 023160 6508 023164 6509 023170 6510 023172 6511 023174 6512 023200 6515 023210 6516 023212 6516 023212 6518 023220 6519 023224 6520 023234 6523 023236 6524 023242 6525 023244
			BR		114132	000505	524 023242
		-(SP) .(R0)	MOV	6\$:	000015	010146	
		,-(SP)	MOV			011046	5527 023252
	;	M(1.E)	BR		175300	000476	5529 023260
		-(SP) (RO)	MOV	7\$:	000016	010146	6530 023262 6531 023264
		(SP)	MOV			011046	532 023270
		MI.EZ	BR		175470	000467	5534 023276
	:	-(SP) (RO)	MOV	8\$:	000010	010146	6535 023300
		(SP)	MOV			011046	5537 023306
		RT.PA	BR BR		175036	000460	5539 023314
		-(SP)	MOV	9\$:	000023	010146	6540 023316
		(SP)	MOV			011046	5542 023324
		aRT.PD	JSR BR		175666	000451	344 023332
		-(SP)	MOV	10\$:	000021	010146	6545 023334
		-(SP) (R0) (R1.E1 -(SP) (R0) (R	MOV JSR MOV MOV JSR BR MOV MOV JSR BR MOV MOV JSR BR MOV MOV JSR BR			011046 004767 000476 010146 012710 011046 004767 010146 012710 011046 012710 011046 012710 011046 012710 011046 012710 011046 012710 011046 012710 011046	5526 023246 5527 023252 5528 023254 5529 023260 5530 023262 5531 023264 5532 023270 5533 023272 5534 023276 5535 023300 5536 023300 5538 023310 5539 023314 5540 023326 5541 023320 5542 023324 5543 023336 5544 023336 5545 023334 5546 023336
		RT.EE	JSR BR		176156	004767	5549 023344 5549 023350

CZMLABO ML-11 LOGIC TO MISCELLANEOUS CODING	SECTION	MITI3 22-DEC-80	12:16 PAGE 174		SEQ	0161
6551 6552 4553		:ML4			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
6554 023352 01014 6555 023354 01271 6556 023360 01104	000022	11\$: MOV MOV MOV	R1,-(SP) #22,(R0) (R0),-(SP)	;		4219
6554 023352 01014 6555 023354 01271 6556 023360 01104 6557 023362 00476 6558 023366 00043 6559 023370 01014 6560 023372 01271 6561 023376 01104 6562 023400 00476 6563 023404 000424 6564 023406 01014 6565 023414 01104 6566 023414 01104 6567 023426 01271 6570 023426 01271 6571 023432 01104	5	JSR BR 12\$: MOV MOV	(RO),-(SP) PC,WRT.EL 16\$ R1,-(SP)	;		4188 4222
6561 023372 01271 6561 023376 01104 6562 023400 00476 6563 023404 00042	0 000005	MOV	R1,-(SP) #5,(R0) (R0),-(SP) PC,WRT.DS			/100
6564 023406 01014 6565 023410 01271 6566 023414 01104	0 000017	JSR BR 13\$: MOV MOV MOV	R1,-(SP) #17,(R0) (R0),-(SP) PC,WRT.D1 16\$;		4188 4225
6566 023414 01104 6567 023416 00476 6568 023422 00041 6569 023424 01014	7 176146	JSR BR 14\$: MOV	PC.WRT.D1 16\$ R1(SP)	:		4188 4228
6570 023426 012710 6571 023432 01104 6572 023434 00476 6573 023440 000400 6574 023442 01014	0 000020 7 176442	MOV MOV JSR	R1,-(SP) #20,(R0) (R0),-(SP) PC,WRT.D2 16\$ R1,-(SP) #16,(R0) (R0),-(SP) PC,WRT.D3 (SP)+,(SP)+	,		/100
6574 023442 01014 6575 023444 01271 6576 023450 01104	0 000016	15\$: BR MOV MOV MOV	R1,-(SP) #16,(R0) (R0),-(SP)	:		4188 4231
6572 023434 00476 6573 023440 00040 6574 023442 01014 6575 023444 01271 6576 023450 01104 6577 023452 00476 6578 023456 02262 6579 023460 00020	7 176736 5	16\$: CMP RTS	PC, WRT.D3 (SP)+, (SP)+ PC	;		4153 4152
6551 6553 6554 023352 01014 6555 023360 01104 6556 023360 01104 6557 023362 00476 6558 023370 01014 6560 023372 01271 6561 023376 01104 6562 023400 00476 6563 023404 00042 6564 023406 01014 6565 023410 01271 6566 023414 01104 6567 023416 00476 6568 023422 00041 6570 023426 01271 6571 023432 01014 6572 023434 00476 6573 023444 01014 6575 023444 01014 6576 023450 01104 6577 023452 00476 6578 023450 01104 6577 023452 00476 6578 023450 01104 6577 023452 00476 6578 023450 01104 6577 023452 00476 6578 023450 010040 6578 023450 010040 6579 023460 00020		; Routine Size ; Maximum stac	: 123 words k depth per invocation: 5 words			

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 6590 : F TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (48) :ML4 routine RD_REG (TST_PAT, REG_SEL, ERR_FLG) : novalue = begin FUNCTIONAL DESCRIPTION: A ROUTINE TO SELECTIVELY CALLED ROUTINES WHICH READ TO INDIVIDUAL ML11 REGISTERS. FORMAL PARAMETERS: ERR_FLG CONTAINS THE ADDRESS (PASSED BY REF) OF THE CALLERS ERROR FLG TO ENABLE THE CALLER TO EXAMINE THE ERROR STATUS OF THE ROUTINE CALL. 6610 REG_SEL CASE SELECT EXPRESSION TO 6611 6612 6613 6614 6615 6616 6617 6618 6619 6620 6621 6623 6623 6624 6623 6623 6633 6631 6633 6633 6634 6635 6636 6637 6638 6637 6638 6638 6638 6640 6641 6642 6643 SELECT THE WRITE REGISTER ROUTINE TO CALLED DATA PATTERN WHICH THE SELECTED REGISTER WILL BE TESTED AGAINST case .REG_SEL from 0 to 13 of SELECT THE READ REGISTER ROUTINE CALL : [0] RD_CS1 (.TST_PAT, 0, .ERR_FLG); !CALL ROOUTINE TO READ MLCS1 [1]: RD_ER (.TST_PAT, 6, .ERR_FLG); !CALL ROUTINE TO READ MLER [2] :
 RD_DA (.TST_PAT, 3, .ERR_FLG); !CALL ROUTINE TO READ MLDA [3]: RD_MR (.TST_PAT, 10, .ERR FLG); !CALL ROUTINE TO READ MLMR RD_E1 (.TST_PAT, 13, .ERR_FLG); !CALL ROUTINE TO READ MLE1 [5] : RD_E2 (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLE2 RD_PA (.TST_PAT, 8, .ERR_FLG); !CALL ROUTINE TO READ MLPA

47		
113 22-DEC-80 12:16 PAGE 176		SEQ 0163
	22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (4)</neale>	8)
D (.TST_PAT, 19, .ERR_FLG);	!CALL ROUTINE TO READ MLPD	
	!CALL ROUTINE TO READ MLEE	
L (.TST_PAT, 18, .ERR_FLG);	!CALL ROUTINE TO READ MLEL	
S (.TST_PAT, 5, .ERR_FLG);	!CALL ROUTINE TO READ MLDS	
1 (.TST_PAT, 15, .ERR_FLG);	!CALL ROUTINE TO READ MLD1	
2 (.TST_PAT, 16, .ERR_FLG);	!CALL ROUTINE TO READ MLD2	
3 (.TST_PAT, 14, .ERR_FLG);	!CALL ROUTINE TO READ MLE2	
MOV 10(SP),R0 MOV 14(SP),R1	ERR.FLG.* TST.PAT.*	4235 4267
ASL R2 ADD 1\$(R2),PC	; REG.SEL,*	4263
1\$: .WORD 2\$-1\$.WORD 3\$-1\$.WORD 4\$-1\$		
.WORD 5\$-1\$.WORD 6\$-1\$.WORD 7\$-1\$		
.WORD 85-15 .WORD 95-15 .WORD 105-15		
.WORD 115-15 .WORD 125-15 .WORD 135-15		
.WORD 145-15 .WORD 155-15 2\$: MOV R1,-(SP)		4267
	PD (.TST_PAT, 19, .ERR_FLG); EE (.TST_PAT, 17, .ERR_FLG); EL (.TST_PAT, 18, .ERR_FLG); EL (.TST_PAT, 18, .ERR_FLG); EL (.TST_PAT, 15, .ERR_FLG); EL (.TST_PAT, 15, .ERR_FLG); ERD_REG: JSR R1_\$SAVE2 MOV 10(SP),R0 MOV 14(SP),R1 MOV 12(SP),R2 ASL R2 ADD 1\$(R2),PC ASL R3 ADD 1\$(R2),PC ASL R3 ADD 1\$(R2),PC ASL R3 ADD 1\$(R2),PC	22-Dec-1980 09:24:31 TOPS-20 BLiss-16 V2(212) 22-Dec-1980 09:21:22 PA: <neale>BLISML4.BLI.2 (4) PD (.TST_PAT, 19, .ERR_FLG); !CALL ROUTINE TO READ MLPD EE (.TST_PAT, 17, .ERR_FLG); !CALL ROUTINE TO READ MLEE EL (.TST_PAT, 18, .ERR_FLG); !CALL ROUTINE TO READ MLDS ES (.TST_PAT, 5, .ERR_FLG); !CALL ROUTINE TO READ MLDS ES (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD1 EX (.TST_PAT, 16, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 16, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 14, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLE2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT, 15, .ERR_FLG); !CALL ROUTINE TO READ MLD2 EX (.TST_PAT,</neale>

	SEQ 0164					CTION	OGIC TES ODING SE	WEOUS C	ISCELL
S	22-Dec-1980 09:24:31 TOF 22-Dec-1980 09:21:22 PA:				:ML4				6701 6702
-			-(SP)	CLR			005046	023546	6703
			RO,-(SP) PC,RD.CS1 16\$	CLR MOV JSR		173754	010046	023550	6705 6706 6707 6708 6709 6710 6712 6713
3	426 427		16 \$ R1(SP)	BR MOV	3\$:		000532 010146	023556 023560	6707
			#6,-(SP)	BR MOV MOV MOV JSR		000006	012746	023562	6709
-	4.24		R1,-(SP) #6,-(SP) R0,-(SP) PC,RD.ER 16\$	JSR		174114	004767	023570	6711
3	426 427	1	R1,-(SP)	BR MOV MOV MOV JSR	48:	000007	010146	023576	6713
1			RO,-(SP)	MOV		000003	010046	023604	6715
3	426		R1(SP) #3(SP) R0(SP) PC.RD.DA 16\$	RR		174254	004767 000514 010146	023606 023612	6716
6	426 427	;	R1 - (SP)	MOV	5\$:	000012	010146	023614	6718
			R1,-(SP) #12,-(SP) R0,-(SP) PC,RD.MR	MOV MOV MOV JSR		174414	010046	023622	6720
3	426 427	:	102	BR		174414	000505	023630	6722
7	421		#15,-(SP)	MOV	6\$:	000015	012746	023634	6724
			R1,-(SP) #15,-(SP) R0,-(SP) PC,RD.E1 16\$	JSR		174776	010046	023642	6726
3	426 428	:	16\$ R1(SP)	BR	7\$:		000476 010146	023600 023604 023606 023612 023614 023616 023622 023624 023630 023632 023640 023640 023650 023650	6718 6720 6721 6722 6723 6724 6725 6726 6727 6728 6729
٦			#16,-(SP)	MOV MOV		000016	012746	023652	6729
-	/2/		PC_RD.E2	MOV JSR		175200	004767	023660	6731
5	426 428	:	R1,-(SP)	BR MOV	8\$:	000010	010146	023666	6733
1			RO,-(SP)	MOV		000010	010046	023674	6735
3	426		PC_RD.PA 16\$	JSR BR		174534	004767	023676 023702	6736
8	426 428	:	R1,-(SP)	MOV	9\$:	000023	010146	023704	6738 6739
1			RO(SP)	MOV		175406	010046	023712	6740
3	426		R1(SP) #16(SP) R0(SP) PC.RD.E2 16\$ R1(SP) #10(SP) PC.RD.PA 16\$ R1(SP) #23(SP) R0(SP) PC.RD.PD 16\$ R1(SP)	BR	100.	17,7400	000451	023720	6742
1	427	•	#21,-(SP)	MOV	10\$:	000021	012746	023724	6744
			RO,-(SP) PC,RD.EE	JSR		175572	010046	023/30	6746
3	426 429		R1,-(SP) #21,-(SP) R0,-(SP) PC,RD.EE 16\$ R1,-(SP)	BR MOV	115:		000442 010146	023736 023740	6747
			#22,-(SP)	MOV		000022	012746	023742	6749
2	124		R1,-(SP) #22,-(SP) R0,-(SP) PC,RD.EL 16\$ R1,-(SP) #5,-(SP) R0,-(SP)	MOV MOV JSR BR MOV MOV JSR BR MOV MOV JSR BR MOV MOV MOV MOV MOV MOV MOV		175512	010146 012746 010046 004767 000460 010146 012746 010046 012746 010046 012746 010146 012746 010146 012746 010146 012746 010146 012746 010146	023676 023676 023676 023702 023704 023706 023714 023720 023722 023724 023736 023736 023740 023740 023740 023740 023740 023740 023740 023740 023756	6733 6734 6735 6736 6737 6738 6739 6740 6743 6744 6745 6746 6747 6748 6749 6751 6752 6753 6754
7	426 429		R1,-(SP)	MOV	12\$:	000005	010146	023756	6753
			RO(SP)	MOV		000005	010046	023764	6755

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 178		SEQ 0165
6757 6758 6759	;ML4		22-Dec-198 22-Dec-198	80 09:24:31 TOPS 80 09:21:22 PA:<
6760 023766 004767 176774 6761 023772 000424 6762 023774 010146 6763 023776 012746 000017 6764 024002 010046	JSR BR 13\$: MOV MOV MOV	PC_RD.DS 16\$ R1(SP) #17(SP) R0(SP) PC_RD.D1 16\$;	4263 4300
6760 023766 004767 176774 6761 023772 000424 6762 023774 010146 6763 023776 012746 000017 6764 024002 010046 6765 024004 004767 175644 6766 024010 000415 6767 024012 010146 6768 024014 012746 000020 6769 024020 010046 6770 024022 004767 176140	MOV JSR BR 14\$: MOV MOV MOV	R1,-(SP) #20,-(SP) R0,-(SP)	;	4263 4303
6771 024026 000406 6772 024030 010146 6773 024032 012746 000016 6774 024036 010046	15\$: BR MOV MOV MOV	PC_RD.D2 16\$ R1(SP) #16(SP) R0(SP)	:	4263 4306
6775 024040 004767 176450 6776 024044 062706 000006 6777 024050 000207 6778 6779 6780 6785 6786 6787 ; 4310 ! <blf 8<="" td=""><td>RTS ; Routine Size:</td><td>PC.RD.D3 M6.SP PC 124 words depth per invocation: 6 words</td><td>;</td><td>4236 4235</td></blf>	RTS ; Routine Size:	PC.RD.D3 M6.SP PC 124 words depth per invocation: 6 words	;	4236 4235
6785 6786 6787 ; 4310 ! <blf <="" td=""><td>PAGE></td><td></td><td></td><td></td></blf>	PAGE>			

	0002.10						324 0107
6844 ; ML4 6845 ; 6846 ; 6847 ; 6851 ; 6855 024062 ; 6856 024062 ; 6858 024072 ; 6859 024072 ; 6860 024102 ; 6861 024102 ; 6863 024112 ; 6864 024112 ; 6865 024112 ; 6866 02412 ; 6867 02412 ; 6868 02412 ; 6869 02412 ; 6869 02412 ; 6871 02414 ; 6872 02414 ; 6873 02415 ; 6874 02415 ; 6875 02416 ; 6876 02416 ; 6877 02417 ; 6878 02417 ; 6878 02417 ; 6879 0241					22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (49</neale>)
6847 :	4343	ENDMSG;					
6855 024062			MSDUMPER:				
6856 024062	012746	007662	MOV	#PHR.14,-(SP)	:		4320
6857 024066	012746 012746	006016 000002	MOV	#PHR.14,-(SP) #ONE.FMT,-(SP) #2,-(SP)			
6859 024076	010600	000002	MOV	SP,RO	; SP,*		
6860 024100	104414	005/50	TRAP	14			
6861 024102	012716	005652 000001	MOV	#FMT.23,(SP) #1,-(SP)			4321
6863 024112	010600	000001	MOV	SP_RO	; SP,*		
6864 024114	104414	4/3030	TRAP	SP,R0			
6865 024116 6866 024126 6867 024126 6868 024132 6869 024136 6870 024142	017716	167032 167026	MOV	aML.REG,(SP)			4322
6867 024126	016746 012746	010256	MOV	#REG. 1 (SP)			
6868 024132	012746	005722 000004	MOV	ML.REG,-(SP) MREG.1,-(SP) MFMT.24,-(SP) M4,-(SP)			
6870 024142	012746 010600	000004	MOV	#4,-(SP)	; SP,*		
6871 024144	104414		TRAP	SP.R0			
6872 024146 6873 024156 6874 024156 6875 024166 6876 024166 6877 024172	017716	167012 167006 010430 005722 000004	MOV	aML.REG+10,(SP) ML.REG+10,-(SP) MREG.18,-(SP) MFMT.24,-(SP) M4,-(SP) SP,R0	:		4323
6874 024156	016746 012746	010430	MOV	ML.REG+10,-(SP)		•	
6875 024162	012746	005722	MOV	WFMT.24,-(SP)			
6876 024166	012746	000004	MOV	#4,-(SP)			
6878 024174	010600 104414		MOV TRAP	14 SP. NO	; SP,*		
6879 024176	017716	166772	MOV	aML.REG+20, (SP)			4324
6880 024202	016746	166772 166766 010436	MOV	ML.REG+20,-(SP)			
6882 024212	012746 012746	005722	MOV	#FMT 24 -(SP)			
6883 024216	012746	005722 000004	MOV	aML.REG+20,(SP) ML.REG+20,-(SP) MREG.19,-(SP) MFMT.24,-(SP) M4,-(SP)			
6884 024222	010600		MOV	SP.R0	; SP,*		
6885 024224 6886 024226	017716	166752	TRAP	aML .REG+30. (SP)			4325
6887 024232	016746	166752 166746 010316 005722 000004	MOV	aML.REG+30,(SP) ML.REG+30,-(SP) MREG.6,-(SP) MFMT.24,-(SP) M4,-(SP) SP,R0			4363
6888 024236	012746	010316	MOV	#REG.6,-(SP)			
6890 024246	012746	000004	MOV	#4 -(SP)			
6891 024252	010600		MOV	SP.RO	; SP,*		
6892 024254	017714	166732	TRAP	14 200 DEC+40 (CD)			1724
6894 024262	016746	166726	MOV MOV MOV MOV MOV TRAP MOV MOV MOV	ML.REG+40(SP)			4326
6895 024266	012746	010420	MOV	#REG. 17,-(SP)			
6886 024226 6887 024232 6888 024236 6889 024242 6890 024246 6891 024252 6892 024254 6893 024256 6894 024262 6895 024272 6897 024276	104414 017716 016746 012746 012746 012746 010600 104414 017716 016746 012746 012746	166732 166726 010420 005722 000004	MOV	aml.REG+40,(SP) ML.REG+40,-(SP) MREG.17,-(SP) MFMT.24,-(SP) M4,-(SP)			
0071 024270	012140	300004	HUY	W4, -(3P)			

22-Dec-1980 09:24:31 TO 22-Dec-1980 09:21:22 PA			:ML4			6899 6900
22-DeC-1900 09:21:22 PA	; SP,*	nv sp. 80	MOV		010600	6901
43		IOV SP.RO RAP 14 IOV AML.REG+50.(SP)	MOV TRAP MOV	166712	104414	6902 024302 6903 024304 6904 024306
		OV ML.REG+50,-(SP) OV WREG.2,-(SP)	MOV	166712 166706 010266	016746	6905 024312 6906 024316 6907 024322
		OV #FMT.24,-(SP)	MOV	005722	012746	6907 024322 6908 024326
	; SP,*	OV SP.RO	MOV		010600	6909 024332 6910 024334
43		OV amREG+60,(SP) OV ML.REG+60,-(SP)	MOV	166672 166666 010274	017716	6911 024336 6912 024342
		OV WREG.3 - (SP) OV WFMT.24, - (SP)	MOV	010274 005722 000004	012746 012746	6913 024346 6914 024352
	; SP,*	OV SP_RO	MOV	000004	010600	6915 024356
433		OV aML.REG+70, (SP)	MOV	166652 166646 010310	017716	6902 024302 6903 024304 6904 024306 6905 024312 6906 024316 6907 024322 6908 024332 6909 024332 6910 024334 6911 024334 6912 024342 6913 024346 6914 024352 6915 024364 6918 024362 6919 024372 6920 024376 6921 024402 6922 024406 6923 024412 6925 024416 6926 024422 6927 024426 6928 024432 6929 024436
		DV WREG.5,-(SP)	MOV MOV MOV	010310	012746	6920 024376
	; SP,*	OV #4,-(SP)	MOV	005722	012746	6922 024406
433		RAP 14 DV aML.REG+120.(SP)	TRAP	166652	104414	6924 024414 6925 024416
		OV ML.REG+120,-(SP) OV WREG.4,-(SP)	MOV	166652 166646 010302	016746 012746	6926 024422 6927 024426
		OV #FMT.24,-(SP) OV #4,-(SP)	MOV	005722 000004	012746 012746	6928 024432 6929 024436
	; SP.*	RAP 14	MOV	144473	104414	6931 024444
433		OV ML.REG+130,-(SP)	MOV MOV MOV	166632 166626 010324 005722 000004	017716 016746 012746 012746 012746 010600 104414	933 024452
		DV #FMT.24,-(SP)	MOV	005722	012746	6935 024462
	; SP,*	OV SP,RO	MOV TRAP		010600	5937 024472 6938 024474
433		DV art.REG+140,(SP)	MOV	166612 166606 010340 005722 000004	UI//ID	6939 024476 6940 024502
		OV	MOV	010340 005722	012746 012746	6941 024506 6942 024512
	; SP,*	OV #4,-(SP) OV SP,RO	MOV	000004	016746 012746 012746 012746 010600 104414 017716	6932 024446 6933 024452 6934 024456 6935 024462 6936 024466 6937 024472 6938 024474 6939 024476 6940 024502 6941 024506 6942 024512 6943 024516 6944 024522 6945 024524 6946 024526 6947 024532 6948 024536 6949 024546 6950 024546 6951 024552
433		0V aML.REG+210, (SP)	MOV TRAP MOV MOV	166632	017716	6932 024446 6933 024452 6934 024456 6935 024462 6936 024466 6937 024472 6938 024474 6939 024476 6940 024502 6941 024506 6942 024512 6943 024516 6944 024522 6945 024524 6946 024526 6947 024532 6948 024536 6949 024536 6950 024546 6950 024556
		OV #REG.14,-(SP)	MOV	166632 166626 010376 005722 000004	012746	6948 024536
	; SP,*	OV aML.REG+210,(SP) OV ML.REG+210,-(SP) OV WREG.14,-(SP) OV WFMT.24,-(SP) OV W4,-(SP) OV SP,R0 RAP 14	MOV MOV TRAP	000004	012746 010600 104414 062706	6950 024546 6951 024552
	, ,	RAP 14 DD #146.SP	TRAP	000146	104414	6952 024554 6953 024556

MISCELLANEOUS CO	D1140 3E			;ML4					22-01090		0169
6956 6957				PILA					22-Dec-1980 22-Dec-1980	09:21:22	TOPS PA:<
6958 024562 6959 024566 6960 024572 6961 024576 6962 024602 6963 024606 6964 024610 6965 024612 6966 024620 6967 024622 6968 024626	017716 016746 012746 012746	166606 166602 010404 005722 000004			MOV MOV MOV MOV	aml.REG+220,(SP) ML.REG+220,-(SP) MREG.15,-(SP) MFMT.24,-(SP) M4,-(SP) SP,R0 14	;				4334
6963 024606	010600	000004			MOV	SP, RO	:	SP,*			
6965 024612	026727	166316	000025		CMP	LST.DUT.REG,#25					4336
6969 024632 6970 024636 6971 024642	017746 016746 012746 012746	166566 166562 010444 005722 000004			MOV MOV MOV MOV	MREG+240,-(SP) ML.REG+240,-(SP) MREG.20,-(SP) MFMT.24,-(SP) M4,-(SP) SP,R0 14	:				4339
6972 0246 6	010600	000004			MOV	SP.RO	;	SP,*			
6972 0246 6 6973 0246 0 6974 0246 2 6975 0246 6 6976 024662 6977 024666 6978 024672	017716 016746 012746 012746 012746	166546 166542 010454 005722 000004			MOV MOV MOV MOV	aML.REG+250, (SP) ML.REG+250, -(SP) MREG.21, -(SP) MFMT.24, -(SP) M4, -(SP) SP,R0 14	,				4340
09/9 0240/0		000001			MOV TRAP	SP.RO	:	SP,*			
6980 024700 6981 024702 6982 024706 6983 024712	104414 062706 062706 000207	000022 000012		1\$:	ADD ADD RTS	#22,SP #12,SP PC	:				4338 4311
6980 024700 6981 024702 6982 024706 6983 024712 6984 6985 6986 6991 6992				; Rou ; Max	itine Size	: 205 words k depth per invocation	: 52 words				
6993 ;	4344	! SLF/P	AGE>								

```
22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                         TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (50)
BGNINIT;
           INITIALIZATION CODE IS EXECUTED AT THE BEGINNING OF EACH PASS, WHEN POWER DOWN/POWER UP HAS OCCURRED, OR WHEN THE OPERATOR HAS ISSUED; A START, RESTART OR CONTINUE COMMAND. DURING INITIALIZATION, THE 'GPHARD' MACRO IS USED TO GET P-TABLE INFORMATION FOR THE LOGICAL UNIT UNDER TEST. THE
           NUMBER OF UNITS AVAILABLE FOR TESTING IS CONTAINED IN A HEADER LOCATION ('LSUNIT').
local
     OFFSET;
external
     LSUNIT:
if not READEF (EF CONTINUE)
                                                                       !SKIP INIT CODE IF CONTINUE
then
     begin
                                                                       !START GPHARDS AT LUN O AND LOAD 'ML_REG'
      if READEF (EF_START)
                                                                      !SEE IF THIS IS THE VERY FIRST PASS
      then
           begin
                                                                      !THIS IS CATEGORY 1 CODE
            ML_LUN = -1;
            do
                 begin
                 ML_LUN = .ML_LUN + 1;
                                                                      !INCREMENT LOGICAL UNIT NUMBER
                 if .ML LUN geg .L$UNIT then DOCLN: !START OVER IF ALL UNITS HAVE BEEN TESTED
                 end
           until (GPHARD (.ML_LUN, PTBL_PTR)) neq 0;
                                                                                  !REPEAT THE GPHARD UNTIL A 0 IS RETURNED
           RH_ADD = .((.PTBL_PTR) + 0);
RH_TYP = .((.PTBL_PTR) + 2);
RH_VEC = .((.PTBL_PTR) + 4);
OFFSET = 0;
                                                                      GET BASE RH ADDRESS FOR THIS UNIT GET RH TYPE FOR THIS UNIT GET RH VECTOR FOR THIS UNIT
                                                                      !INIT OFF SET COUNT
            incr COUNT from 0 to 21 do
                                                                      !LOAD THE REGISTER ADDRESS FOR THIS UNIT INTO ML_REG
                 begin
                 ML_REG [.COUNT, REGISTER_ADD] = .RH_ADD + .OFFSET;
OFFSET = .OFFSET + 2;
           end
      else
                                                                      !IS THIS A NEW PASS
            begin
           if READEF (EF_NEW) then ML_LUN = -1; !IF NEW PASS START GPHARDS AT LUN 0
```

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22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                                                                                                                                                                                                                                                                                 TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (50)
7051 :ML4

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                                                                                                                                                      ML_LUN = .ML_LUN + 1;
                                                                                                                                                                                                                                                                                                                    !IF NOT GET NEXT LUN PTABLE
                                                                                                                                                      if .ML_LUN geg .L$UNIT then DOCLN; !START OVER IF ALL UNITS ARE TESTED
                                                                                                                                   until (GPHARD (.ML_LUN, PTBL_PTR)) neq 0;
                                                                                                                                     end:
                                                                                                                PAR DIS = .((.PTBL PTR) + 12);
ML_DUT = .((.PTBL PTR) + 10);
OP_NUM_ARR = .((.PTBL_PTR) + 6) - 1;
GOOD_BEK = ZEROES;
ARR_T6 = ZEROES;
LST_ARR = ZEROES;
LST_BLK = ZEROES;
                                                                                                                                                                                                                                                                                                                       !GET PARITY DISABLE FLAG
                                                                                                                                                                                                                                                                                                                      GET DRIVE NUMBER
GET OPERATOR NUMBER OF ARRAYS
INIT GOOD BLOCK TO BLOCK ZERO
INIT ARRAY 16 TO ZERO
INIT LAST ARRAY TO ZERO
INIT LAST BLOCK TO ZERO
                                                                                                                  if .((.PTBL_PTR) + 8) IS_SET
                                                                                                                                                                                                                                                                                                                      !CALCULATE ML11 16K MOS RAM PARAMETERS
                                                                                                                                 begin

DRIVE_TYPE = %o'000110';

W C SIZE = %o'140000';

RAS INC = %o'200';

CHIP_SIZ = 16;

ARR_INC = %o'1000';

ARR_16<9, 4> = %o'17';

LST_ARR<9, 4> = .OP_NUM_ARR;

LST_BLK<9, 4> = .OP_NUM_ARR;

LST_BLK = .LST_BLK or %o'777';
                                                                                                                                                                                                                                                                                                                     !EXPECTED DRIVE TYPE VALUE !WORD COUNT SIZE FOR 16K WORD XFER
                                                                                                                                                                                                                                                                                                                       !RAS INCREMENT FOR 16K RAMS
                                                                                                                                                                                                                                                                                                                       ! CHIP SIZE
                                                                                                                                                                                                                                                                                                                        !ARRAY INCREMENT
                                                                                                                                                                                                                                                                                                                        !ARRAY 16
                                                                                                                                                                                                                                                                                                                        !LAST ARRAY
                                                                                                                                                                                                                                                                                                                        !LAST BLOCK
                                                                                                                                    end
                                                                                                                                                                                                                                                                                                                       !CALCULATE ML11 64K MOS RAM PARAMETERS
                                                                                                                                 begin
DRIVE_TYPE = %o'000111';
W_C_SIZE = %o'000000';
RAS_INC = %o'1000';
CHIP_SIZ = 64;
ARR_INC = %o'4000';
ARR_16<11, 4> = %o'74';
LST_ARR<11, 4> = .0P_NUM_ARR;
LST_BLK<11, 4> = .0P_NUM_ARR;
LST_BLK = .LST_BLK or %o'3777';
end:
                                                                                                                                                                                                                                                                                                                     !EXPECTED DRIVE TYPE VALUE
!WORD COUNT SIZE FOR 64K WORD XFER
!RAS INCREMENT FOR 64K RAMS
!CHIP SIZE
!ARRAY INCREMENT
!ARRAY 16
                                                                                                                                                                                                                                                                                                                       LAST ARRAY
                                                                                                                   if .((.PTBL_PTR) + 2) eql %o'70' then LST_DUT_REG = 21 else LST_DUT_REG = 19;
                                                                                                                                                                                                                                                                                                                       !TELL OPERATOR WHICH UNIT IS BEING TESTED !CLEAR MASS BUS
                                                                                                                  PRINTB (FMT_17, .ML_LUN);
                                                                                                                   CLR_MBUS;
                                                                                                                   end:
                                                                                                 ENDINIT:
```

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 185		SEQ 0172
7112 7113 7114	.GLOBL	LSUNIT		
7119 024714 004167 157140 7120 024720 012700 000036 7121 024724 104447 7122 024726 103001	LINIT: JSR MOV TRAP BHIS	R1.\$SAVE3 #36.R0 47 1\$		4343 4361
7123 024730 000207 7124 024732 012700 000040 7125 024736 104447 7126 024740 103061	1\$: RTS MOV TRAP BHIS	PC #40,R0 ; 47 5\$		4365
7128 024742 012767 17777 7128 024750 005267 166624 7129 024754 026767 166620 7130 024762 002401	166630	#-1,ML.LUN ; ML.LUN ; ML.LUN,LSUNIT ; 3\$		4368 4372 4374
7131 024764 104444 7132 024766 016700 166606 7133 024772 104442 7134 024774 010067 164556 7135 025000 005767 164552	3\$: MOV TRAP MOV	ML.LUN,RO ; 42 RO,PTBL.PTR PTBL.PTR		4377
7135 025000 005767 164552 7136 025004 001761 7137 025006 017767 164544 7138 025014 016701 164536 7139 025020 016167 000002 7140 025026 016701 164524 7141 025032 016167 000004	166556 MOV 166546 MOV	2\$ aPTBL.PTR,RH.ADD PTBL.PTR,R1 2(R1),RH.TYP PTBL.PTR,R1 4(R1),RH.VEC		4379 4380
7142 025040 005002 7143 025042 005001	166536 MOV CLR	PTBL.PTR.R1 4(R1),RH.VEC R2 R1	OFFSET COUNT	4381 4382 4384 4386
7144 025044 010100 7145 025046 006300 7146 025050 006300 7147 025052 006300	4\$: MOV ASL ASL ASL	R1,R0 ; R0 R0 R0	COUNT,*	4386
7147 025052 008300 7148 025054 016703 166512 7149 025060 060203 7150 025062 010360 013154 7151 025066 062702 000002 7152 025072 005201 7153 025074 020127 000025 7154 025100 003761 7155 025102 000426 7156 025104 012700 000035 7157 025110 104447 7158 025112 103003 7159 025114 012767 177777 7160 025122 005267 166452	MOV ADD MOV ADD	RH.ADD,R3 R2,R3 R3,ML.REG(R0) #2,R2 R1	OFFSET,* *,OFFSET	4387 4384
7152 025072 005201 7153 025074 020127 000025 7154 025100 003761 7155 035103 000436	INC CMP BLE BR	R1,#25	*,OFFSET COUNT COUNT,*	
7152 025072 005201 7153 025074 020127 000025 7154 025100 003761 7155 025102 000426 7156 025104 012700 000035 7157 025110 104447 7158 025112 103003 7159 025114 012767 177777	5\$: MOV TRAP BHIS	8\$ #35,R0 47 6\$		4365 4394
7159 025114 012767 177777 7160 025122 005267 166452	166456 MOV 6\$: INC	6\$ #-1,ML.LUN ML.LUN ;		4398

6 PAGE 186 SEQ 0173	80 12:16 PAGE 186	13 22-DEC-80	MACRO M111	T	OGIC TES	CZMLABO ML-11 LO
22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<		44	M			7162
	ML.LUN,LSUNIT	CMP BLT	154656	166446	026767 002401	7164 7165 025126 7166 025134
LUN,RO : 4403	ML.LUN,RO	TRAP	7\$	166434	104444	7167 025136 7168 025140
I DTD	RO PTBL PTR	MOV		164404 164400	104442 010067 005767 001761 016701	7165 025126 7166 025134 7167 025136 7168 025140 7169 025144 7170 025146 7171 025152 7172 025156 7173 025160 7174 025172 7176 025176 7177 025204 7178 025210 7179 025214 7180 025216 7181 025222 7182 025226 7183 025232 7184 025236 7185 025242
L.PTR.21 : 4407	PTBL.PTR.R1 14(R1).PAR.DIS	BEQ MOV MOV	164374 8\$	164372	016701	7173 025160
L.PTR.R1 ; 4408	PTBL.PTR.R1 12(R1) ML.DUT	MOV	166376	164360	016167 016701 016167	7175 025172
L.PTR,R1 ; 4409 1),R3	6\$ PTBL.PTR.R1 14(R1),PAR.DIS PTBL.PTR.R1 12(R1),ML.DUT PTBL.PTR,R1 6(R1),R3	MOV MOV DEC		164372 000014 164360 000012 164346 000006	016701 016103	7177 025204 7178 025210 7179 025214
OP.NUM.ARR D.BLK : 4410	R3.OP.NUM.ARR	MOV		164336 164336	010367 005067	7180 025216 7181 025222
.16 .ARR 4411 4412	ARR.16 LST.ARR	CLR		164342	005067	7182 025226
ARR .BLK L.PTR.R1 R1),#1 4412 4413 4415	R3 R3.OP.NUM.ARR GOOD.BLK ARR.16 LST.ARR LST.BLK PTBL.PTR.R1 10(R1),#1	CLR MOV CMP	000001	164336 164336 164342 164340 164330 164310 000010	005303 010367 005067 005067 005067 016701 026127 001054 012767 012767 012767	7186 025246
	9\$ #110,DRIVE.TYPE #-40000,W.C.SIZE	MOV BNE MOV		000110 140000 000200 000020 001000 017000 164232	001054 012767 012767	7187 025254 7188 025256 7189 025264
O.RAS.INC : 4420	#200.RAS.INC #20,CHIP.SIZ	MOV	165624	000200	012767	7190 025272 7191 025300
00.ARR.INC : 4422 000.ARR.16 : 4423	#1000,ARR.INC #17000,ARR.16	MOV	165646 165630 165624 164262 164246 164252	001000	012767 052767	7192 025306 7193 025314
	OP.NUM.ARR,R3	BIS MOV SWAB		164232	016703 000303 006303 042767 050367 016703 000303 042767 050367 052767 005067 012767 012767 012767 012767 012767 012767 012767 012767 012767 012767 012767	7194 025322 7195 025326 7196 025330 7197 025332 7198 025336 7199 025344 7200 025350 7201 025354 7202 025356 7203 025364 7205 025372 7206 025376 7207 025404 7208 025406 7209 025414 7210 025420 7211 025426 7212 025434 7213 025442 7214 025450 7215 025456
0777.R3 000,LST.ARR	#160777,R3 #17000,LST.ARR R3,LST.ARR OP.NUM.ARR,R3 R3 #160777,R3 #17000,LST.BLK R3,LST.BLK	ASL BIC BIS MOV SWAB ASL BIC BIS BIS BIS BR	164232	160777 017000 164226 164204	042703 042767	7197 025332 7198 025336
LST.ARR NUM.ARR,R3 : 4425	R3,LST.ARR OP.NUM.ARR,R3	BIS		164226	050367	7199 025344 7200 025350
0777 P3	R3 #160777_R3	ASL		160777	006303	7202 025356
000 LST.BLK	#17000 LST.BLK	BIC	164200	160777 017000 164174 000777	042767	7204 025364 7205 025372
7.LST.BLK : 4426 4415	#777,LST.BLK	BIS	164166		052767	7206 025376 7207 025404
7, LST.BLK 1, DRIVE. TYPE SIZE 00, RAS. INC 0, CHIP. SIZ 00, ARR. INC 000, ARR. 10C 4436	WITT DRIVE. TYPE	: MOV	165516 9\$	000111 165502	012767 005067	7208 025406 7209 025414
00.RAS.INC : 4432 0.CHIP.SIZ : 4433	#1000.RAS.INC #100.CHIP.SIZ	MOV	165476 164134	001000 000100	012767 012767	7210 025420 7211 025426
00 ARR. INC 000 ARR. 16 4435	#4000 ARR. INC #74000 ARR. 16	BIC	165476 164134 164120 164124 164116	000111 165502 001000 000100 004000 074000 060000 164076	042767	7213 025442
000,ARR.16 NUM.ARR,R3 ; 4436	10\$ #111,DRIVE.TYPE W.C.SIZE #1000,RAS.INC #100,CHIP.SIZ #4000,ARR.INC #74000,ARR.16 #60000,ARR.16 OP.NUM.ARR,R3 R3	CLR MOV MOV MOV BIC BIS MOV SWAB	104110	164076	016703	7215 025456

MLABO ML-11 L SCELLANEOUS C	ODING SE	CTION							0174
7218 7219				;ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
7221 025464 7222 025466 7223 025470 7224 025476 7226 025504 7227 025510 7228 025514 7229 025516 7231 025522 7232 025524 7233 025524 7233 025524 7233 025530 7234 025536 7236 025554 7238 025554 7238 025554 7239 025564 7240 025572 7241 025574 7242 025602 7243 025616 7244 025612 7245 025616 7246 025620 7247 025620 7247 025620 7248 025630 7249 025634	006303 006303 006303 042703 042767 050367 016703 000303 006303	103777 074000 164066 164044	164072	ASL ASL BIC BIC BIS MOV SWAB ASL	R3 R3 R3 W103777,R3 W74000,LST.ARR R3,LST.ARR OP.NUM.ARR,R3 R3 R3 R3 R3 R3				4437
7228 025514 7229 025516 7230 025520 7231 025522 7232 025524 7233 025530 7234 025536 7235 025536 7236 025554 7238 025554 7238 025554 7239 025564 7240 025572 7241 025574 7242 025602 7243 025616 7246 025612 7245 025616 7246 025620 7247 025622 7248 025630	006303 006303 042767 050367 052767 016701 026127 001004 012767 000403	103777 074000 164030 003777 164002 000002	164034 164022 000070	ASL ASL BIC BIC BIS BIS BIS MOV CMP BNE	R3 R3 W103777,R3 W74000,LST.BLK R3,LST.BLK W3777,LST.BLK PTBL.PTR,R1 2(R1),W70	:			4438 4441
7239 025564 7240 025572 7241 025574 7242 025602 7243 025606 7244 025612 7245 025616	012767	000025 000023 165772 005306 000002	165342 165332	11\$: MOV BR 11\$: MOV 12\$: MOV MOV	#25.LST.DUT.REG 128 #23.LST.DUT.REG ML.LUN,-(SP) #FMT.17,-(SP) #2,-(SP) SP.RO		SP,*		4443
7246 025620 7247 025622 7248 025630 7249 025634 7250 025640 7251 025646 7252 025652 7253 025656	012746 010600 104414 152777 016703 042703 142777 150377 062706 000207	000040 165746 177770 000007 165342 000006	165364 165346	MOV TRAP BISB MOV BIC BICB BISB ADD RTS	14 #40,aML.REG+40 ML.DUT,R3 #177770,R3 #7,aML.REG+40 R3,aML.REG+40 #6,SP PC	•			4363 4343
7251 025646 7252 025652 7253 025656 7254 7255 7256 7261 7262 7266 7270 025660				; Routine Size:		7 words			
7270 025660 7271 025664	004767 104411	177030		L\$INIT::JSR TRAP	PC,LINIT	;			4445

MACRO M1113 22-DEC-80 12:16 PAGE 188 CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION

SEQ 0175

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS PA:<

:ML4

RTS PC

7273 7274 7275 7276 025666 000207 7277 7278 7279 7284 7285 7286 ; 4448

; Routine Size: 4 words ; Maximum stack depth per invocation: 0 words

4448 ! <BLF/PAGE>

. .

1	288 288 288	ML4							22-Dec-1980 22-Dec-1980	09:24:31 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (51)</neale>	
7	292 291 291		4449	BGNTST;								
7	295 295		4451 4452 4453	TEST	NUMBER:	TST 1						
2	297		4455	TEST	NAME:		MASS BU	IS READY TEST				
7777	298 299 300 301		4456 4457 4458 4459	TEST	DESCRIPTI TEST THE BY:	ON: RH CON	ITROLLER	FOR EXISTANCE				
7	303 304		4461 4462		1. WRIT	TING ON	IES TO TH	HE RH CS2 REGISTE BUS CLEAR	R			
777	306 307 308		4464 4465 4466	_	2. THEN	READ T	THE CS2 R	REGISTER FOR CLEA	R DATA	•		
7777	310 311 312 313		4468 4469 4470 4471	CLR_MBU MLCS1 = MLCS2 = DELAY (S; ZEROES; %o'17777 ONE_US);	0';			!CLEAR OUT !LOAD CS2 W	CS1 ITH ONES	AND FORCE A MBUS CLEAR	
7	314	:	4473	if ((.M	LCS2) and	(%o'17	7670'))	neg ZERO	!SEE IF CS2	GOT CLEAR	ED	
77	316 317 318 319 320 321	ML4	4455 4456 4457 4458 4461 4462 4463 4464 4465 4466 4467 4468 4467 4472 4473 4476 4476 4476 4476 4476 4476 4476	then beg ERR PRII DOD DOC	in DF (119, NTB (THR U (.ML_LU LN;	RH_ERRO FMT, RE N);	OR, DUMPE G_17, FA	R); IC_23, PHR_4);	!REPORT ERR	OR IF NOT	CLEARED	
7	728		4481 4482	end ENDTST;								
2	328 332	025670	010146			\$T1:	MOV	R1,-(SP)		:		4447
7	335 335	025674 025702	152777 016701	000040 165674	165312		TST BISB MOV	#40,aML.REG+40 ML.DUT,R1		:		4450
7	333 334 335 336 337 338 339 340	025706 025712 025720	142777 150177	000007 165270	165274		BIC BICB BISB	#7///0,R1 #7,aML.REG+40 R1,aML.REG+40				
7	339 340 341	025670 025672 025674 025702 025706 025712 025720 025724 025730 025736	010146 005746 152777 016701 042701 142777 150177 005077 012777	000040 165674 177770 000007 165270 165224 177770 000001	165256		CLR MOV MOV	R1,-(SP) -(SP) #40,aML.REG+40 ML.DUT,R1 #177770,R1 #7,aML.REG+40 R1,aML.REG+40 aML.REG #-10,aML.REG+40		*,\$	\$TMP2	4469 4470 4471

CZMLABO ML-11 LI	ODING 25	CITON								SEQ 0177
7343				:ML4					22-Dec-1980 09:24:3 22-Dec-1980 09:21:2	1 TOPS
7346 025742 7347 025744 7348 025750 7349 025752	001410 016701	154146		1\$:	BEQ MOV	4\$ L\$DLY,R1		; *,\$\$TMP1		
7348 025750	001403			2\$:	BEQ	3\$ (SP)		; SSTMP		
7350 025754	005301				CLR DEC BNE DEC	R1		: SSTMP1		
7346 025742 7347 025744 7348 025750 7349 025752 7350 025754 7351 025756 7352 025760 7353 025762 7354 025764 7355 025772 7356 025774 7357 025776 7358 026000 7359 026002 7360 026004 7361 026010 7362 026014 7363 026020 7364 026030 7366 026030 7366 026030 7367 026034 7368 026040 7369 026042 7369 026042	005300			3\$:	DEC	R1 2\$ R0 1\$; \$\$TMP2		
7353 025762 7354 025764 7355 025772 7356 025774 7357 025776 7358 026000 7359 026002 7360 026004 7361 026010	032777	177670	165222	45:	BR BIT	#-110.aml_RFG+40		:		4473
7355 025774	001426 104455				TRAP	5\$ 55 167		:		4476
7357 025776 7358 026000	000167 011060				BEQ TRAP .WORD .WORD .WORD	167 RH.ERROR				
7359 026002	024052	007454			MINING IV	DUMPER				447
7361 026010	012746	007454 010242 010420			MOV	#FNC.23,-(SP)		•		-
7362 026014 7363 026020	012746	006034			MOV	WFNC.23,-(SP) WREG.17,-(SP) WTHR.FMT,-(SP) W4,-(SP) SP,R0 14				
7364 026024 7365 026030 7366 026032 7367 026034 7368 026040 7369 026042 7370 026044 7371 026050	012746	000004			MOV	SP,RO		; SP,*		
7367 026034	104414	165540			MOV	ML.LUN,RO		:		4478
7368 026040	104451				MOV TRAP TRAP	44				
7371 026050	016700 104451 104444 062706 005726 012601 000207	000012		5\$:	ADD TST	#12.SP (SP)+		:		447
7372 026052 7373 026054	012601				MOV	(SP)+,R1 PC				
7372 026052 7373 026054 7374 7375				· Pour		59 words				
7376				; Max	imum stack	depth per invocation:	7 words			
7382										
7376 7381 7382 7386 7390 026056 7391 026056 7392 026062 7393 026064 7394 026066 7395 026070 7396				T1::						
7390 026056 7391 026056 7392 026062 7393 026064 7394 026066 7395 026070	004767 104466 006000 103773 000207	177606		15:	JSR TRAP	PC,\$T1 66 R0 1\$ PC		:		4480
7393 026064 7394 026066	006000 103773				ROR BLO RTS	R0 1\$				

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 191
MISCELLANEOUS CODING SECTION

;ML4

SEQ 0178

TOPS PA:<

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22

Routine Size: 6 words; Maximum stack depth per invocation: 0 words

7409 : 4483 !<BLF/PAGE>

SEQ 0179

MLABO ML-11 LOGIC SCELLANEOUS CODIN	SECTION					SEQ 0180
7471 7475 026072 004 7476 026076 005 7477 026100 016	167 156020	\$12:	JSR CLR	R1,\$SAVE5	: DODU.FLG	4482 4500 4500
7477 026100 016	704 165030		MOV	LST.DUT.REG,R4		4500
7478 026104 005	002 474		CLR BR	R2	; REG.SEL	
7480 026110 010	200	1\$:	MOV	32.R0	; REG.SEL,*	4512
7482 026114 006	300		ASL	RO RO RO RO,R3		
7483 026116 006 7484 026120 010	500 003		ASL	R0 R0_R3		
7485 026122 104	000040	145042 25:	TRAP BISB	2		450 451
7487 026132 016	700 165444	165062	MOV	#40, aml. REG+40 ML. DUT, RO		4311
7485 026122 104 7486 026124 152 7487 026132 016 7488 026136 042 7489 026142 142 7490 026150 150	700 177770	165044	BIC BICB BISB	#177770,R0 #7.2ML.REG+40		
7490 026150 150	77 165040		BISB	RO, aML. REG+40	. + CAVE	451
7479 026106 000 7480 026110 010 7481 026112 006 7482 026114 006 7483 026116 006 7484 026120 010 7485 026122 104 7486 026124 152 7487 026132 016 7488 026136 042 7489 026142 142 7490 026150 150 7491 026154 017 7492 026160 032 7493 026160 032 7494 026174 011 7495 026174 011 7497 026176 024 7498 026204 012 7500 026210 012 7501 026214 012 7502 026220 012 7503 026224 012 7504 026236 104 7506 026236 104	777 000040 700 165444 700 177770 777 000007 077 165040 305 013154 777 010000	165026	RIT	#177770.R0 #7.aML.REG+40 R0.aML.REG+40 aML.REG(R3),R5 #10000,aML.REG+40	; *,SAVE	4517 4514
7477 026100 016 7478 026104 005 7479 026106 000 7480 026110 010 7481 026112 006 7482 026114 006 7483 026116 006 7485 026120 010 7486 026124 152 7487 026132 016 7488 026124 152 7489 026142 142 7490 026150 150 7491 026150 150 7492 026160 032 7493 026160 032 7493 026160 032 7494 026170 104 7497 026176 024 7498 026200 012	455		BEQ TRAP .WORD .WORD .WORD MOV	3\$ 55 170		451
7495 026172 000 7496 026174 011	170		.WORD	170 RH. ERROR		
497 026176 024	052		WORD	NI MADED		
499 026204 012	746 010004 746 007066		MUV	MRD.52,-(SP)		451
00 026210 012 01 026214 012	746 006426 746 007472		MOV	#URD.12,-(SP)		
502 026220 012	746 007210		MOV	#FNC.6,-(SP) #WRD.52,-(SP) #WRD.12,-(SP) #PHR.5,-(SP) #WRD.62,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0		
2503 026224 012 2504 026230 012			MOV	#6,-(SP)		
505 026234 010 506 026236 104	614		MOV	SP.R0	; SP.*	
2507 026240 016 2508 026244 012 2509 026250 012 2510 026254 010 2511 026256 104 2512 026260 012	316 013154 746 004750 746 000002		MOV	ML.REG(R3),(SP) MFMT.11,-(SP) M2,-(SP) SP,R0 14	:	4519
2509 026250 012	746 000002		MOV	#2,-(SP)		
7511 026256 104	414		MOV TRAP	14. RU	; SP,*	
/213 UZDZD4 UDZ	706 0000022		MOV	#1,R1 #22,SP 67	: *,DODU.FLG	4520 4516 4521
7513 026264 062 7514 026270 104 7515 026272 006	467	3\$:	TRAP	67	:	452
7516 026274 103	712		BLO	2\$		
7509 026250 012 7510 026254 010 7511 026256 104 7512 026260 012 7513 026264 062 7514 026270 104 7515 026272 006 7516 026274 103 7517 026276 005 7518 026300 020	202	45:	INC	R0 2\$ R2 R2,R4	: REG.SEL .*	4508
7519 026302 003 7520 026304 005	702		MOV ADD TRAP ROR BLO INC CMP BLE DEC	1\$ R1		153
1320 020304 003.			DEC	N1	; DODU.FLG	452

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE	M 14 E 194	SEQ 0181
7522 7523	ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
7525 026306 001004 7526 026310 016700 165264 7527 026314 104451	BNE 5\$ MOV ML.LUN,RO TRAP 51 TRAP 44		4529
7529 026320 000207	58: RTS PC	:	4482
7529 026320 000207 7530 7531 7532 7537 7538	; Routine Size: 76 words ; Maximum stack depth per	invocation: 15 words	
7546 026322 7547 026322 004767 177544 7548 026326 104466 7549 026330 006000 7550 026332 103773 7551 026334 000207	T2:: 1\$: JSR PC,\$T2 TRAP 66 ROR RO BLO 1\$ RTS PC		4531
7551 026334 000207 7552 7553 7554 7559 7560 7561 ; 4534 ! <blf f<="" td=""><td>; Routine Size: 6 words ; Maximum stack depth per PAGE></td><td>invocation: 0 words</td><td></td></blf>	; Routine Size: 6 words ; Maximum stack depth per PAGE>	invocation: 0 words	

begin DODU (.ML_LUN);

DOCLN; end;

ENDTST:

CZMLABO ML-11 LOGIC TEST	MACRO M1113 22-DEC-80 12:16 PAGE 196		CEO 0197
7618 7619	;ML4	• .	SEQ 0183 22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
7620 7624 026336 004167 155534 7625 026342 005746 7626 026344 005003 7627 026346 104402 7628 026350 152777 000040 7629 026356 016702 165220 7630 026362 042702 177770 7631 026366 142777 000007 7632 026374 150277 164614 7633 026400 017704 164600 7634 026404 012701 000001 7635 026410 001410 7636 026412 016702 153500 7637 026416 001403 7638 026420 005016 7639 026422 005302 7640 026424 001375 7641 026426 005301 7642 026430 000767 7643 026432 032777 7644 026440 001420 7645 026440 001420 7646 026444 000001 7647 026446 010464 7648 026450 024052 7649 026452 012746 007422 7650 026456 012746 006016 7651 026462 012746 006016 7651 026462 012746 000002	\$T3: JSR R1,\$SAVE4 TST -(SP) CLR R3 18: TRAP 2 BISB #40,aML.REG+40 MOV ML.DUT.R2 BIC #177770,R2 BICB #7,aML.REG+40 BISB R2,aML.RE3+40 MOV aML.REG+30,R4 MOV #1,R1 2\$: BEQ 5\$ MOV L\$DLY,R2 BRQ 4\$ 3\$: CLR (SP) DEC R2 BNE 3\$ 4\$: DEC R1 BR 2\$ BNE 3\$ 4\$: DEC R1 BR 2\$ WORD 1 WORD ASYNC WORD DUMPER MOV #PHR.3,-(SP) MOV #2,-(SP) MOV #2,-(SP) MOV #2,-(SP) MOV SP,R0	: DODU.FLG : *,SAVE : *,SSTMP2 : *,SSTMP1 : SSTMP1 : SSTMP1 : SSTMP2 40	22-Dec-1980 09:24:31 TOPS PA:<4 4533 4558 4559 4564 4567 4568
7653 026470 104414 7654 026472 012703 000001 7655 026476 062706 000006 7656 026502 104467 7657 026504 006000 7658 026506 103717 7659 026510 005303 7660 026512 001004 7661 026514 016700 165060 7662 026520 104451 7663 026522 104444 7664 026524 005726 7666 7667 7666	MOV M1.R3 ADD M6.SP TRAP 67 ROR R0 BLO 1\$ DEC R3 BNE 7\$ MOV ML.LUN,RC TRAP 51 TRAP 44 7\$: TST (SP)+ RTS PC	. *.DODU.FLG . DODU.FLG .	4569 4566 4570 4574 4577 4533
7667 7668	; Routine Size: 61 words ; Maximum stack depth per invoca	ation: 9 words	

CZMLABO ML-11 MISCELLANEOUS	LOGIC TES	T	MACRO M1113	22-DEC-80	12:16 PAGE	C 15 197	SEQ 0184	
7674 :ML4 7675 : 7676 7680						22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (53)</neale>	-
7684 02653 7685 02653 7686 02653 7687 02653	0 004767 4 104466 6 006000 0 103773 2 000207	177602	13:: 1\$:	JSR TRAP ROR BLO RTS	PC,\$T3 66 R0 1\$ PC		4579	9
7689 02654 7690 7691 7692 7697 7698 7699 :	4582	! <blf p<="" td=""><td></td><td>utine Size ximum stac</td><td>: 6 words k depth per i</td><td>invocation: 0 words</td><td></td><td></td></blf>		utine Size ximum stac	: 6 words k depth per i	invocation: 0 words		

```
7701 :ML4
7702 :
7703 :
7704 :
7705 :
7706 :
                                                                                                  22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (54)
                             BGNTST;
! TEST NUMBER: TST 4
                              ! TEST NAME:
                                                    DRIVE SELECTION TEST
                            TEST DESCRIPTION:
THIS TEST TESTS FOR UNIQUE DRIVE SELECTION BY WRITING THE DRIVE
UNDER TEST (DUT) DRIVE NUMBER INTO ITS DSA REG. THEN WRITING THE DRIVE
NUMBERS OF OTHER DRIVES INTO THEIR RESPECTIVE DSA REGISTERS
AND READING THE DUT DSA FOR ITS DRIVE NUMBER.
                   4596
4597
IMPLICIT INPUTS:
4598
4599
                    LOADED DURING THE INITIALIZATION CODE AND CONTAINS THE DRIVE
                                        NUMBER OF THE DRIVE PRESENTLY BEING TESTED.
                              DODU_FLG,
SAVE;
                                                                                                 !DROP UNIT FLAG
!TEMPORARY SAVE LOCATION
                             CLR_MBUS;
DODU_FLG = ZERO;
                            MLDA = .ML_DUT;
SAVE = .MLDA;
                                                                                                !LOAD THIS DRIVES DRIVE NO. INTO ITS DSA REG
                                                                                                 PREAD THE REGISTER BACK
                             if .SAVE neg .ML_DUT
                                                                                                !SEE IF DSA HAS DRIVE NUMBER
                                  begin
ERROF (2, INTER, DUMPER);
PRINTB (SIX_FMT, PHR 4, WRD 12, FNC 3, WRD 37, WRD 13, REG_6);
PRINTB (FMT_2, .ML_DUT, .SAVE, (.ML_DUT xor .SAVE));
                                   EXIT_TST; end;
                            incr DRV_SEL from 0 to 7 do
                                                                                                !WRITE DRV NO OF OTHER DRIVES INTO THEIR RESPECTIVE DSA REG.
                                   if .DRV_SEL neq .ML_DUT
                                                                                               !SKIP IF .DRV_SEL EQL TO THE DRIVE UNDER TEST (DUT)
                                        begin
DRV_NUM = .DRV_SEL;
MLDA = .DRV_SEL;
DELAY (ONE_US);
                                                                                                SELECT DRIVE TO BE WRITTEN TO SWRITE DRIVE SEL NO. INTO ITS DSA REG
                             DRV_NUM = .ML_DUT;
                                                                                                 !SELECT THE DUT
                             SAVE = .MLDA:
                                                                                                 !READ ITS DSA REG
```

1,4

```
7757 :ML4
7758 :
7759
7760 :
7761 :
7762 :
7763 :
7764 :
7765 :
7766 :
7767 :
7768 :
7769 :
                                                                                                                                                        22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                                                            TOPS-20 Bliss-16 V2(212)
                                                                                                                                                                                                            PA: <NEALE>BL3ML4.BL1.2 (54)
                                                                                                                                            !SEE IF WRITTING 10 OTHER DRIVES CHANGE ITS VALUE
                                            if .SAVE neg .ML_DUT
                                              then
                                                      begin

ERRDF (3, ASYNC, DUMPER);

PRINTB (THR_FMT, FNC 3, WRD 37, WRD 14);

PRINTB (FMT 1, .ML_DUT, .SAVE);

DODU_FLG = ONE;
                                                                                                                                                   !ERROR AND SET DODU_FLG IF CHANGED
                                            ENDSUB:
 7770 :
7771:
                                            if .DODU_FLG IS_SET
                                                                                                                                                   !DROP UNIT IF DODU_FLG IS_SET
7772
7773
7774
7775
7776
                                            then
                                                      begin
DODU (.ML_LUN);
                                                  DOCLN;
                                                      end:
7778
7782
7786
026544
7787
026550
7788
026552
7789
026554
7790
026562
7791
026566
7792
026572
7793
026600
7794
026604
7795
026604
7797
026620
7798
026626
7799
026630
7801
026632
7802
026634
7803
026636
7804
026632
7805
026646
7806
026652
7807
026656
7808
026662
7809
026666
                                             ENDTST;
                           004167 155346

005746

104402

152777 000040

016703 165014

042703 177770

142777 000007

150377 164410

005005

016777 164770

017704 164364

020467 164756

001451

104455
                                                                                $T4:
                                                                                                                    R1.$SAVE5
                                                                                                                                                                                                                                                                                   4581
                                                                                                                    -(SP)
                                                                                                   TST
                                                                                                   TRAP
                                                                                                                                                                                                                                                                                   4605
                                                                                                                   #40,aML.REG+40
ML.DUT.R3
#177770,R3
#7,aML.REG+40
R3,aML.REG+40
                                              000040
165014
177770
000007
164410
                                                               164432
                                                                                                   BISB
                                                                                                                                                                                                                                                                                   4607
                                                                                                   MOV
                                                                                                  BIC
BICB
BISB
                                                               164414
                                                                                                   CLR
                                                                                                                                                                                           : DODU.FLG
                                                                                                                                                                                                                                                                                   4609
                                                                                                                    ML.DUT, aML.REG+30
aML.REG+30,R4
                                                               164370
                                                                                                   MOV
                                                                                                                                                                                                                                                                                  4610
                                                                                                   MOV
                                                                                                                                                                                           : * . SAVE
                                                                                                                                                                                                                                                                                   4611
                                                                                                                    R4,ML.DUT
                                                                                                   CMP
                                                                                                                                                                                           : SAVE .*
                                                                                                                                                                                                                                                                                   4613
                                                                                                   BEQ
                                                                                                   TRAP
                                                                                                                                                                                                                                                                                  4616
                                                                                                   . WORD
                                                                                                   . WORD
                                                                                                                     INTER
                                                                                                   . WORD
                                                                                                                    DUMPER
                                                                                                                    MREG.6,-(SP)
MURD.13,-(SP)
MURD.37,-(SP)
MFNC.3,-(SP)
MURD.12,-(SP)
                                                                                                   MOV
                                                                                                                                                                                                                                                                                  4617
                                                                                                   MOV
                                                                                                  MOV
                                                                                                  MOV
                                                                                                  MOV
                                                                                                                   MPHR.4,-(SP)
WSIX.FMT,-(SP)
W7,-(SP)
                                                                                                  MOV
                                                                                                  MOV
```

MLABO ML-11 LI SCELLANEOUS CO	ODTING 25	LITON							EQ 0187
7812 7813				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
7814 7815 026676 7816 026700	010600 104414				MOV	SP.R0	; SP,*		
7817 026702 7818 026706	016716	164674			MOV	ML.DUT, (SP) R4,R3	SAVE,*		4618
7819 026710 7820 026712	041603				MOV BIC BIC BIS MOV	(SP),R3 R4,(SP)	; SAVE,*		
7822 026716 7823 026720 7824 026724	010446 016746 012746	164656 004266 000004			MOV	R4,-(SP) ML.DUT,-(SP) #FMT.2,-(SP)	; SAVE,*		
7825 026730 7826 026734 7827 026736 7828 026740	012746 010600 104414 104463 062706 000523 005003 020367 001425	000004			MOV MOV TRAP TRAP	M4,-(SP) SP,R0 14	; SP,*		
7829 026742 7830 026746	062706	000030			ADD	#30.SP 10\$	1		4613 4613 4624
7831 026750 7832 026752	005003	164624		2 \$: 3 \$:	CLR	R3 R3.ML.DUT	; DRV.SEL,*		462
7815 026676 7816 026700 7817 026702 7818 026706 7819 026710 7820 026712 7821 026714 7822 026716 7823 026720 7824 026724 7825 026730 7826 026734 7827 026736 7828 026740 7829 026742 7831 026750 7832 026752 7833 026756 7834 026760 7835 026766 7837 026767 7838 027000 7839 027004 7840 027010 7841 027012 7842 027016 7843 027020 7844 027022	011423 010302 042702 142777 150277 010377 012701 001410 016702 001403	177770 000007 164214	164220		CLR CMP BEQ MOV BIC BICB BISB	14 ML.DUT, (SP) R4, R3 (SP), R3 R4, (SP) R3, (SP) R4, -(SP) ML.DUT, -(SP) MFMT.2, -(SP) M4, -(SP) SP, R0 14 63 M30, SP 10\$ R3 R3, ML.DUT 7\$ R3, R2 M177770, R2 M7, aML.REG+40 R2, aML.REG+40 R3, aML.REG+40 R3, aML.REG+30 M1, R1 7\$; DRV.SEL,*		4627
7838 027000 7839 027004	010377	164200 000001			PLIV	R3, aML.REG+30 #1,R1	: DRV.SEL .* ; *,\$\$TMP2		4628 4629
7841 027012 7842 027014	001410	153100		4\$:	MOA	LSDLY . KZ	; *,\$\$TMP1		
7843 027020 7844 027022	005016			5\$:	MOV BEQ MOV BEQ CLR DEC	6\$ (SP) R2	; \$\$TMP ; \$\$TMP1		
7845 027024 7846 027026	001375			6\$:	BNE DEC BR	5\$ R1	; SSTMP2		
7848 027032 7849 027034 7850 027040	005203 020327 003744	000007		7\$:	INC CMP BLE MOV	R3 R3,#7	: DRV.SEL .*		4622
7851 027042 7852 027046 7853 027052	016703 042703 142777	164534 177770 000007 164130 164114 164506	164134		BIC	R1 4\$ R3 R3, M7 3\$ ML.DUT, R3 M1777770, R3 M7, aML.REG+40 R3, aML.REG+40 aML.REG+30, R4 R4, ML.DUT 8\$			4632
7855 027064 7856 027070	017704 020467	164114 164506			BICB BISB MOV CMP	aML.REG+30,R4 R4,ML.DUT	: *.SAVE : SAVE,*		4633 4635
7858 027076 7859 027100 7860 027102	104455 000003 010464				BEQ TRAP .WORD .WORD .WORD	SS SSYNC	:		4638
7845 027024 7846 027026 7847 027030 7848 027032 7849 027034 7850 027040 7851 027042 7852 027046 7853 027052 7854 027060 7855 027064 7856 027070 7857 027074 7858 027076 7859 027100 7860 027102 7861 027104 7862 027106 7863 027112 7864 027116 7865 027122 7866 027126	001375 005301 000767 005203 020327 003744 016703 042703 142777 150377 017704 020467 001435 104455 000003 010464 012746 012746 012746 012746	006442 006700 007750 006034 000004			MORD MOV MOV MOV MOV MOV	ASYNC DUMPER WWRD.14,-(SP) WWRD.37,-(SP) WFNC.3,-(SP) WTHR.FMT,-(SP) W4,-(SP)	;		4639

ZMLABO ML-11 L ISCELLANEOUS C	ODING SE	CTION			12:16 PAGE 201				SEQ 0188
7868 7869 7870			;ML4					22-Dec-1980 09:24:3 22-Dec-1980 09:21:2	1 TOPS
7871 027132 7872 027134	010600			MOV TRAP	SP.R0		; SP,*		
7873 027136 7874 027140 7875 027144	010416 016746 012746	164436 004222 000003		MOV MOV MOV	6/ /661		; SAVE,*		4640
7877 027154 7878 027154	010600	000003		MOV MOV TRAP	SP,RO		; SP,*		
7875 027144 7876 027150 7877 027154 7878 027156 7879 027160 7880 027164 7881 027170 7882 027172 7883 027174	012705 062706 104467 006000	000001 000020	8\$:	MOV ADD TRAP ROR	ML.DUT, -(SP) MFMT.1, -(SP) MFMT.1, -(SP) M3, -(SP) SP,R0 14 M1,R5 M20,SP 67 R0 9\$ 1\$ R5 10\$; *,DODU.FLG		4641 4637 4642
7883 027174 7884 027176 7885 027202	103002 000167 005305	177350	95:	BHIS JMP DEC	75 1\$ 05		; DODU.FLG		4646
7886 027204 7887 027206 7888 027212	001004 016700 104451	164366	70.	DEC BNE MOV TRAP	51		;		4649
7887 027206 7888 027212 7889 027214 7890 027216 7891 027220	104451 104444 005726 000207		10\$:	TRAP TST RTS	(SP)+ PC				4581
7871 027132 7872 027134 7873 027136 7874 027140 7875 027144 7876 027150 7877 027154 7878 027156 7879 027160 7880 027164 7881 027170 7882 027172 7883 027174 7884 027176 7885 027202 7886 027204 7887 027206 7888 027212 7889 027214 7890 027216 7891 027220 7892 7893 7894 7899 7900 7904			; Rout ; Maxi	ine Size mum stac	: 151 words c depth per invocation:	19 words			
7908 027222 7909 027222 7910 027226 7911 027230 7912 027232	004767 104466 006000 103773 000207	177316	T4:: 1\$:	JSR TRAP ROR BLO RTS	PC,\$T4 66 RO 1\$ PC		•		4651
7913 027234 7914 7915 7916 7921 ;	4654	! <blf page<="" td=""><td>; Rout ; Maxi</td><td>ine Size mum staci</td><td>6 words depth per invocation:</td><td>0 words</td><td></td><td></td><td></td></blf>	; Rout ; Maxi	ine Size mum staci	6 words depth per invocation:	0 words			

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                                                                                                                                                                                                         22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                                                                                                                           TOPS-20 Bliss-16 V2(212)
                                                                                                                                                                                                                                                                           PA: <NEALE>BL3ML4.BL1.2 (55)
                                                                   BGNTST:
                                                                         TEST NUMBER: TST 5
                                                                         TEST NAME:
                                                                                                               READ WRITE REG ONES/ZEROES TEST
                                                                         TEST DESCRIPTION:
                                                                                          THIS TEST WRITES AND READS A DATA PATTERN OF ALL ONES AND ZEROES TO ALL
                                                                                          OF THE ML11'S READ / WRITE REGISTERS.
                                                                                         ROUTINES WRT REG AND RD REG ACCEPT ARGUMENTS TO FURTHER SELECT ROUTINES WHICH ACTUALLY PERFORMS THE READING AND WRITING OF THE REGISTERS.
                                                    4667
                                                    4669
                                                                                          THE UNIT IS DROPPED ON DETECTED ERRORS.
                                                    4671
                                                                         IMPLICIT INPUTS:
                                                                                          WT_DATA
                                                                                          LOADED BY READ REGISTER ROUTINES AND CONTAINS THE DATA PATTERN WRITTEN
                                                                                          TO THE REGISTERS (REPRESENTS GOOD DATA).
                                                                                          RD DATA
                                                                                          LOADED BY THE READ REGISTER ROUTINES AND CONTAINS THE DATA PATTERN
                                                                                          READ FROM THE REGISTER (REPRESENTS BAD DATA).
                                                   4680
                                                   4681
4682
4683
4684
4685
                                                                   local
                                                                              ERR_FLG.
                                                                                                                                                                                                         !ERROR FLAG PASSED TO ROUTINES
                                                                               TST_PAT,
                                                                                                                                                                                                         !TEST PATTERN
                                                                               index,
                                                                                                                                                                                                          !POINTS TO REGISTER PRESENTLY BEING TESTED
                                                                              DODU FLG:
                                                                                                                                                                                                         !DROP UNIT FLAG
                                                                   DODU_FLG = ZERO;
                                                                   TST_PAT = ONES;
                                                                                                                                                                                                        !LOAD TEST PAT WITH ONES
                                                    4690
                                                    4691
                                                                   incr TWICE from 0 to 1 do
                                                                                                                                                                                                        !REPEAT LOOP TWICE
                                                                              begin
                                                                               incr REG_SEL from 0 to 7 do
                                                                                                                                                                                                       !TEST ELEVEN WRITE/READ REGISTERS
                                                                                         begin
BGNSUB;
                                                                                        CLR_MBUS;
WRT_REG (.TST_PAT, .REG_SEL, index);
RD_REG (.TST_PAT, .REG_SEL, ERR_FLG);
                                                                                                                                                                                                         !WRITE TO THE REGISTER
                                                                                                                                                                                                        !READ THE REGISTER
                                                                                          if .ERR_FLG IS_SET
                                                                                                                                                                                                         !SEE IF READ FOUND AN ERROR
                                                                                          then
                                                                                                    begin
                                                                                                                                                                                                        !IF ERROR FLAG IS_SET THEN ERROR AND SET DODU_FLG
          7976
                                                                                                    selectone .REG_SEL of
                                                                                                                                                                                                        SELECT WHICH MODULE FAILED
          7977 :
                                                                                                                set
```

```
MACRO M1113 22-DEC-80 12:16 PAGE 203
CZMLABO ML-11 LOGIC TEST
MISCELLANEOUS CODING SECTION
                                                                                                                                                                                                  SEQ 0190
                                                                                                                 22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                       TOPS-20 Bliss-16 V2(212)
PA:<NEALE>BL3ML4.BLI.2 (55)
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7981
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7999
8000
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8010
8011
8015
                                                               [0 to 3] :
                                                                     ERROF (4, ASYNC, DUMPER);
                                                                                                                 !ASYNC MODULE FAILURE
                                                              [4 to 5]:
ERRDF (4, SYNC, DUMPER);
                                                                                                                 !SYNC MODULE FAILURE
                                                               [6 to 7] :
                                                                     ERROF (4, ARR_DAT, DUMPER); !ARRAY DATA MODULE FAILURE
                                                        PRINTB (SIX_FMT, PHR_4, WRD_12, FNC_5, FNC_6, WRD_52, WRD_56);
PRINTB (FMT_16, .ML_REG [.:ndex, REGISTER_ADD], .WT_DATA, .RD_DATA);
DODU_FLG = ONE;
                                                        end:
                                                  ENDSUB:
                                                  end:
                                            TST_PAT = not .TST_PAT;
                                                                                                                !REPEAT AGAIN WITH COMPLIMENT DATA
                                            end:
                                     if .DODU_FLG IS_SET
                                                                                                                !DROP THIS UNIT IF THE DODU_FLG IS_SET
                                     then
                                            begin
DODU (.ML_LUN);
                                            DOCLN:
                                            end:
                                     ENDTST:
                                     154634
                                                               $15:
                                                                                        R1,$SAVE4
                                                                                                                                                                                                          4653
                                                                                        -(SP),-(SP)
                                                                                                                                             DODU.FLG
*,TST.PAT
TWICE
                                                                                                                                                                                                          4688
4689
4691
4694
                                                                           MOV
CLR
CLR
TRAP
BISB
                                     177777
                                                                                        #-1,R2
                                                                                        R1
R3
                                                                                                                                              REG. SEL
                                      000040
164310
177770
000007
163704
                                                                                        #40, aML.REG+40
ML.DUT, RO
                                                  163726
                                                                            MOV
                                                                           BICB
                                                                                        #177770,R0
#7,aML.REG+40
                                                  163710
                                                                            BISB
                                                                                        RO, aML. REG+40
                                                                                                                                                                                                         4698
                                                                            MOV
                                                                                                                                           : TST.PAT,*
```

: REG. SEL .*

8035 027514 012746 000010	CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 204	SEQ 0191
8039 02731		:ML4		
8082 027514 104414 8083 027516 016716 163406 MOV RD.DATA, (SP) ; SP,* 8084 027522 016746 163400 MOV WT.DATA, -(SP) ; INDEX,* 8085 027526 016600 000036 MOV 36(SP), RO ; INDEX,*	8034 8035 8037 027314 012746 000010 8038 027320 060616 8039 027322 004767 173546 8040 027336 010216 8041 027330 010346 8042 027332 012746 000012 8043 027336 060616 8044 027340 004767 174116 8045 027344 026627 000012 8046 027352 001106 8047 027354 005703 8048 027356 002410 8049 027360 020327 000003 8050 027364 003005 8051 027366 104455 8052 027370 000004 8053 027372 010464 8054 027374 024052 8055 027376 000425 8056 027400 020327 000004 8057 027404 002410 8058 027406 020327 000004 8059 027412 003005 8060 027414 104455 8061 027416 000004 8062 027420 010526 8063 027422 024052 8064 027424 000412 8065 027426 020327 000006	MOV ADD JSR MOV MOV MOV ADD JSR CMP BNE TST BLT CMP BGT TRAPWORDWORDWORDWORDWORDWORDWORDWORDWORDWORDWORDWORDWORDWORDWORD	R2.(SP) R3(SP) R3(SP) #12(SP) SP.(SP) PC.RD.REG 12(SP).#1 68 R3 R3.#3 REG.SEL R3.#3 REG.SEL	22-Dec-1980 09:24:31 TOPS PA:< 4699 4701 4705 4705 4705 4712
	8082 027514 104414 8083 027516 016716 163406 8084 027522 016746 163400 8085 027526 016600 000036 8086 027532 006300 8087 027534 006300	TRAP MOV MOV MOV ASL ASL	WT.DATA, -(SP)	4719

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION				SEQ 0192
8090 8091 8092	ML4		22-0 22-0	Dec-1980 09:24:31 TOPS Dec-1980 09:21:22 PA:<
8093 027540 016046 0131 8094 027544 012746 0052 8095 027550 012746 0000 8096 027554 010600	MOV	ML.REG(RO),-(SP) #FMT.16,-(SP) #4,-(SP) SP.RO	; SP,*	
8093 027540 016046 0131 8094 027544 012746 0052 8095 027550 012746 0000 8096 027554 010600 8097 027556 104414 8098 027560 012704 0000 8099 027564 062706 0000 8100 027570 062706 0000 8101 027574 104467 8102 027576 006000	TRAP	#1, R4 #30, SP #12, SP 67 R0 2\$ R3 R3, #7 2\$ R2 R1 R1, #1	*,DODU.FLG	4720 4703 4695 4721
8103 027600 103626 8104 027602 005203 8105 027604 020327 0000 8106 027610 003622	O7 CMP	R3 R3,#7	: REG.SEL .*	4694
8107 027612 005102 8108 027614 005201 8109 027616 020127 0000 8110 027622 003614	ROR BLO INC CMP BLE COM INC CMP BLE COM ENC O1 BLE DEC BNE MOV TRAP	R2 R1 R1,#1	: TST.PAT : TWICE : TWICE,*	4726 4691
8111 027624 005304	DEC	1\$ R4 7\$; DODU.FLG	4729
8113 027630 016700 1637 8114 027634 104451	44 MOV TRAP TRAP	ML.LUN.RO		4732
8115 027636 104444 8116 027640 022626 8117 027642 000207 8118	7\$: CMP RTS	(SP)+,(SP)+ PC	:	4653
8119 8120 8125 8126 8130 8134 027644	; Routine Size ; Maximum stac	: 131 words k depth per invocation:	24 words	
8130 8134 027644 8135 027644 004767 17736 8136 027650 104466 8137 027652 006000 8138 027654 103773 8139 027656 000207 8140 8141 8142	15:: JSR TRAP ROR BLO RTS	PC,\$T5 66 R0 1\$ PC		4734
8140 8141 8142		: 6 words k depth per invocation:	0 words	

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 206 MISCELLANEOUS CODING SECTION

SEQ 0193

8151 8152

4737 ! <BLF/PAGE>

SEQ 0194

```
22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                      TOPS-20 Bliss-16 V2(212)
PA:<NEALE>BL3ML4.BLI.2 (56)
8160
8161
8162
8163
                                   ! TEST NUMBER: TST 6
8164
8165
8166
8167
8168
8169
                                     TEST NAME: READ WRITE REG SHIFTING ONES/ZEROES TEST
                                     TEST DESCRIPTION:
                                               THIS TEST WRITES AND READS A
                                              SHIFTING ONE'S AND SHIFTING ZEROE'S PATTERN TO ALL THE ML11'S READ/WRITE REGISTERS
8171
8172
8173
8174
8175
8176
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8179
                                              ROUTINES WRT REG AND RD REG
ACCEPT ARGUMENTS TO FURTHER
SELECT ROUTINES WHICH ACTUALLY
PERFORMS THE READING AND
WRITING OF THE REGISTERS.
                                               THE DRIVE IS DROPPED ON DETECTED
                        4758
4759
4760
4761
4762
4763
4764
4765
4766
4767
4768
                                               ERRORS.
8180
8181
8182
8183
                                     IMPLICIT INPUTS:
                                               WT DATA
                                                  COADED BY READ REGISTER ROUTINES AND
8184
8185
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8209
                                                  CONTAINS THE DATA PATTERN WRITTEN TO THE
                                                  REGISTERS (REPRESENTS GOOD DATA).
                                               RD DATA
                                                  COADED BY THE READ REGISTER ROUTINES AND
                                                  CONTAINS THE DATA PATTERN READ FROM THE
                                                  REGISTER (REPRESENTS BAD DATA).
                         4770
                         4771
4772
4773
                         4774
                         4775
                                  local
                        4776
4777
4778
                                        ERR_FLG.
TST_PAT,
                                                                                                                !ERROR FLAG PASSED TO ROUTINE
                                                                                                                !TEST PATTERN !POINTS TO REG PRESENTLY BEING TESTED !DROP UNIT FLAG
                                        index,
DODU_FLG;
                        4779
4780
                                  DODU_FLG = ZERO;
                                  TST_PAT = ONE;
                                                                                                               !LOAD TST_PAT WITH A 1 IN A FILED OF O'S
                                  incr SHIFT from 0 to 15 do
                                                                                                               !DO SHIFT 16 TIMES
                                        begin
                                         incr TWICE from 0 to 1 do
                                                                                                               !REPEAT LOOP TWICE
                                               begin
```

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CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 208 MISCELLANEOUS CODING SECTION
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PA: <NEALE>BL3ML4.BL1.2 (56)
                                                                                                                                                                                                                                                                               22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                             incr REG_SEL from 0 to 7 do
                                                                                                                                                                                                                                                                               !TEST ELEVEN READ/WRITE REGISTERS
                                                                                                                                           begin
BGNSUB;
                                                                                                                                          CLR_MBUS;
WRT_REG (.TST_PAT, .REG_SEL, index);
RD_REG (.TST_PAT, .REG_SEL, ERR_FLG);
                                                                                                                                                                                                                                                                                                            !WRITE TO THE REGISTER
                                                                                                                                                                                                                                                                                                            !READ THE REGISTER
                                                                                                                                           if .ERR_FLG IS_SET
                                                                                                                                                                                                                                                                               !SEE IF THE READ FOUND AN ERROR
                                                                                                                                           then
                                                                                                                                                         begin
                                                                                                                                                                                                                                                                              !IF THE ERROR FLAG IS_SET THEN ERROR
                                                                                                                                                         selectone .REG_SEL of
                                                                                                                                                                                                                                                                              !FIND WHICH MODULE FAILED
                                                                                                                                                                        set
                                                                                                                                                                        [0 to 3]:
                                                                                                                                                                                      ERROF (5, ASYNC, DUMPER);
                                                                                                                                                                                                                                                                                                            !ASYNC MODULE FAILURE
                                                                                                                                                                        [4 to 5] :
                                                                                                                                                                                      ERROF (5, SYNC, DUMPER);
                                                                                                                                                                                                                                                                                                            !SYNC MODULE FAILURE
                                                                                                                                                                        [6 to 7] :
                                                                                                                                                                                      ERROF (5, ARR_DAT, DUMPER);
                                                                                                                                                                                                                                                                                                           !ARRAY DATA MODULE FAILURE
                                                                                                                                                        PRINTB (SIX_FMT, PHR_4, WRD_12, FNC_5, FNC_6, WRD_52, WRD_56);
PRINTB (FMT_16, .ML_REG [.index, REGISTER_ADD], .WT_DATA, .RD_DATA);
DODU_FLG = ONE;
                                                                                                                                                         end;
                                                                                                                                          ENDSUB;
                                                                                                                                           end:
                                                                                                                            TST_PAT = not .TST_PAT;
                                                                                                                                                                                                                                                                               !REPEAT WITH A O IN A FIELD OF 1'S
                                                                                                                            end:
                                                                                                              TST_PAT = .TST_PAT^ONE;
                                                                                                                                                                                                                                                                               SHIFT THE 1 IN THE FIELD OF O'S
                                                                                                              end:
                                                                                               if .DODU_FLG IS_SET
                                                                                                                                                                                                                                                                               !DROP THIS UNIT IF DODU_FLG IS SET
                                                                                               then
                                                                                                            begin
DODU (.ML_LUN);
                                                                                                              DOCLN:
                                                                                                              end;
                                                                                               ENDTST;
```

Ref	CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-0	EC-80 12:16 PAGE 209		SEQ 0196
82/3 02/600 003003 000001	8271 027660 004167 154	232 \$16:	SR R1, \$SAVES	;	4736
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005 WORD 5 8311 030044 010526 WORD SYNC 8312 030046 024052 WORD DUMPER	8273 027666 005005		R R5	: DODU_FLG	4781
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005 WORD 5 8311 030044 010526 WORD SYNC 8312 030046 024052 WORD DUMPER	8274 027670 012703 000	0001 P	OV #1,R3	: *,TST.PAT	4782
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005 WORD 5 8311 030044 010526 WORD SYNC 8312 030046 024052 WORD DUMPER	8275 027674 005001	18:	R R2	SHIFT	4/84
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005 WORD 5 8311 030044 010526 WORD SYNC 8312 030046 024052 WORD DUMPER	8277 027700 005004	25: (R R4	; REG. SEL	4790
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005 WORD 5 8311 030044 010526 WORD SYNC 8312 030046 024052 WORD DUMPER	8278 027702 104402	1040 163302 3\$: 1	CD #// OM DECT/U		4791 4792
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8280 027712 016700 163	3664 M	OV ML.DUT.RO		41.75
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8281 027716 042700 177	7770	IC #177770,R0		
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8283 027730 150077 163	3260	ISB RO. aML. REG+40		
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8284 027734 010346		OV R3,-(SP)	: TST.PAT.*	4794
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8286 027740 012746 000	0010	OV #10(SP)	; REG.SEL,*	
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8287 027744 060616	A	D SP. (SP)	; INDEX,*	
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8288 027746 004767 173	5122		· TST PAT *	4795
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8290 027754 010446	N	OV R4,-(SP)	; REG.SEL,*	""
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8291 027756 012746 000	2012	0V #12,-(SP)	· FDD FLG +	
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8293 027764 004767 173	3472	SR PC.RD.REG		
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8294 027770 026627 000 8295 027776 001106	0012 000001	#P 12(SP),#1	: ERR.FLG,*	4797
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8296 030000 005704		ST R4	: REG.SEL	4801
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8297 030002 002410	2007	T 48		
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8299 030010 003005	,003 B	T 4\$; REG.SEL,*	
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8300 030012 104455		RAP . 55	:	4805
8304 030022 000425 8305 030024 020427 000004 4\$: CMP R4, M4 ; REG.SEL,* 8306 030030 002410 BLT 5\$ 8307 030032 020427 000005 CMP R4, M5 ; REG.SEL,* 8308 030036 003005 BGT 5\$ 8309 030040 104455 TRAP 55 ; 4808 8310 030042 000005WORD 5 8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER	8302 030016 010464		HORD ASYNC		
8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER 8313 030050 000412	8303 030020 024052		JORD DUMPER		
8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER 8313 030050 000412	8304 030022 000425	0004 48.	8 65 8D 94 #4	PEG SEL +	4801
8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER 8313 030050 000412	8306 030030 002410	45.	T 5\$		
8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER 8313 030050 000412	8307 030032 020427 000	0005	MP R4,#5	; REG.SEL,*	
8311 030044 010526WORD SYNC 8312 030046 024052WORD DUMPER 8313 030050 000412	8309 030040 104455	i	AP 55		4808
8312 030046 024032 .WORD DUMPER 8313 030050 000412 .BD 48	8310 030042 000005	•	ORD 5		
8313 030050 000412 8314 030052 020427 000006 5\$: CMP R4,#6 ; REG.SEL,* 8315 030056 002407 BLT 6\$ 8317 030064 003004 BGT 6\$ 8318 030066 104455 TRAP 55 ; WORD 5 8319 030070 000005 WORD 5WORD 5WORD ARR.DAT	8312 030046 024052		JORD DUMPER		
8314 030052 020427 000006 58: CMP R4,M6 ; REG.SEL,* 8316 030060 020427 000007 CMP R4,M7 ; REG.SEL,* 8317 030064 003004 BGT 6\$ 8318 030066 104455 TRAP 55 ; 4811 8319 030070 000005 WORD 5 8320 030072 010570 WORD ARR.DAT	8313 030050 000412		, A		4801
8316 030060 020427 000007 CMP R4,M7 ; REG.SEL,* 8317 030064 003004 BGT 6\$ 8318 030066 104455 TRAP 55 ; 4811 8319 030070 000005 .WORD 5 8320 030072 010570 .WORD ARR.DAT	8315 030056 002407	0006 5\$: (T 65	; REG.SEL,*	
8317 030084 003004 8318 030066 104455 TRAP 55 8319 030070 000005 .WORD 5 8320 030072 010570 .WORD ARR.DAT	8316 030060 020427 000	0007	P R4.#7	; REG.SEL,*	
8319 030070 000005 8320 030072 010570 .WORD 5 .WORD ARR.DAT	8318 030064 003004		57 6 5		4911
8320 030072 010570 .WORD ARR.DAT	8319 030070 000005		IORD 5		4011
	8320 030072 010570	1	ORD ARR.DAT		

SEQ 0197		. 441 /	MLABO ML-11 LOGIC TEST SCELLANEOUS CODING SECTION
22-Dec-1980 09:24:31 TOP 22-Dec-1980 09:21:22 PA:		;ML4	8322 8323 8324
: 481	DUMPER MURD.56,-(SP) MURD.52,-(SP) MFNC.6,-(SP) MFNC.5,-(SP) MURD.12,-(SP) MURD.12,-(SP) MPHR.4,-(SP) MSIX.FMT,-(SP) M7,-(SP) SP.RO 14	6\$: MOV MOV MOV MOV MOV MOV MOV	8325 030074 024052 8326 030076 012746 007122 8327 030102 012746 007066 8328 030106 012746 010004 8329 030112 012746 007774 8330 030116 012746 006426 8331 030122 012746 007454 8332 030126 012746 006100 8333 030132 012746 006100 8333 030132 012746 000007 8334 030136 010600 8335 030140 104414 8336 030142 016716 162754 8338 030152 016600 000036 8340 030160 006300 8341 030160 006300 8342 030164 016746 162754 8343 030170 012746 005216 8344 030174 012746 005216 8345 030200 010600 8346 030202 104414 8347 030204 012705 000001 8348 030210 062706 000030 8349 030214 062706 000030 8349 030224 103626 8353 030226 005204 8353 030226 005204 8354 030230 020427 000007
; SP,*	#7,-(SP) SP.RO	MOV MOV TRAP	8331 030122 012746 007454 8332 030126 012746 006100 8333 030132 012746 000007 8334 030136 010600 8335 030140 104414
; 481	RD.DATA, (SP) WT.DATA, -(SP) 36(SP), RO	MOV MOV MOV	8336 030142 016716 162762 8337 030146 016746 162754 8338 030152 016600 000036
; INDEX,*	36(SP),R0 R0 R0 R0	MOV ASL ASL	8338 030152 016600 000036 8339 030156 006300 8340 030160 006300
; SP,*	RO ML.REG(RO),-(SP) WFMT.16,-(SP) W4,-(SP) SP,RO 14	ASL ASL ASL MOV MOV MOV	8337 030146 016746 162754 8338 030152 016600 000036 8339 030156 006300 8340 030160 006300 8341 030162 006300 8342 030164 016046 013154 8343 030170 012746 005216 8344 030174 012746 000004
: *,DODU.FLG 481 479 : 479 : 481	#30.SP #12.SP	TRAP MOV ADD 7\$: ADD TRAP ROR	8345 030200 010600 8346 030202 104414 8347 030204 012705 000001 8348 030210 062706 000030 8349 030214 062706 000012 8350 030220 104467 8351 030222 006000
; REG.SEL,*	67 R0 3\$ R4 R4,M7	BLO INC CMP	8355 030234 003622
; TST.PAT 482 ; TWICE 478	R3 R2 R2,#1	INC CMP	8356 030236 005103 8357 030240 005202 8358 030242 020227 000001
; TST.PAT 482 ; SHIFT 478	R4, M7 3\$ R2 R2, M1 2\$ R3 R1 R1, M17 1\$ R5 R5 R5 R5 R1 LUN, R0 51	BLE COM INC CMP BLE ASL INC CMP BLE DEC BNE MOV TRAP TRAP TRAP TRAP RTS	8356 030236 005103 8357 030240 005202 8358 030242 020227 000001 8359 030246 003614 8360 030250 006303 8361 030252 005201 8362 030254 020127 000017 8363 030260 003606 8364 030262 005305 8365 030264 001004 8366 030266 016700 163306 8367 030272 104451 8368 030274 104444 8369 030276 022626 8370 030300 000207
; DODU.FLG 482	1\$ R5	BLE	8363 030260 003606 8364 030262 005305 8365 030264 001004
; 483	ML.LUN,RO	MOV TRAP	8366 030266 016700 163306 8367 030272 104451
: 473	(SP)+,(SP)+ PC	8\$: CMP	8368 030274 104444 8369 030276 022626 8370 030300 000207

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 211

8382
8383
8387
8391 030302 004767 177352 T6::
8392 030302 004767 177352 1\$: JSR PC,\$T6
8393 030306 104466 ROR RO
8395 030312 103773 BLO 1\$
8396 030314 000207 RTS PC

8398
8399
8404
8405
8406: 4836 !
8407: 4837 ! &BLF/PAGE>

SEQ 0198

```
8409 : ML

8411 : 8412 : 8413 : 8414 : 8415 : 8416 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 : 8426 
                                                                                                                                                                                                                                                       22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                                                                                                                                                                                              TOPS-20 Bliss-16 V2(212)
                                                                                                                                                                                                                                                                                                                                             PA: <NEALE>BL3ML4.BL1.2 (57)
                                                                           BGNTST:
                                                                             ! TEST NUMBER: TST 7
                                                                                                                                    REGISTER INITIALIZATION TEST
                                                                                  TEST NAME:
                                                                                  TEST DESCRIPTION:
                                                                                                       THIS TEST TESTS THE ABILITY OF
ALL ACCESSIBLE ML11 REGISTERS
TO CLEAR OUT REGISTER DATA OF
                                                                                                        ONE'S AND ZEROES PATTERN.
                                                                                                       ROUTINE WRT REG WRITES A PATTERN TO THE SELECTED REGISTER.
                                                                                                        A MASS BUS CLEAR IS DONE.
                                                                                                        THEN ROUTINE RD_REG READS THE
                                                                                                        SELECTED REGISTER FOR CLEARED DATA
                                                                                                        THE DRIVE IS DROPPED ON DETECTED ERRORS.
                                                                                                       THIS TEST WILL ALSO READ THE DRIVE TYPE REGISTER FOR ITS
                                                                                                        INITIAL REGISTER VALUE.
                                                                                  IMPLICIT IMPUTS:
                                                                                                        THIS FLAG IS NEEDED DUE TO THE UNIQUE
                                                                                                        MANNER IN WHICH THESE REGISTERS
                                                                                                        MUST BE READ.
                                                                                                        RD DATA
                                                                                                       LOADED BY THE READ REGISTER ROUTINE
                                                                                                        AND CONTAINS THE DATA PATTERN READ FROM
                                                                                                        THE REGISTER (REPRESENTS BAD DATA).
                                                                                                        DRIVE TYPE
                                                                                                        LOADED DURING THE INITIALIZATION CODE AND STORES THE EXPECTED CONTENTS OF THE DRIVE
                                                                                                        TYPE REGISTER.
                                                                           local
                                                                                          TST_PAT.
ERR_FLG.
                                                                                                                                                                                                                                                         !TEST PATTERN
                                                                                                                                                                                                                                                        POINTS TO REG PRESENTLY BEING TESTED STORES CALCULATED REGISTER CLEAR DATA TEMPORARY STORAGE LOCATION
                                                                                           index,
                                                                                          CLR DATA,
                                                                                          DODU_FLG;
                                                                                                                                                                                                                                                         DROP UNIT FLAG
                                                                           ML_REG [19, FORCE_HI] = %0'177777';
                                                                                                                                                                                                                                                      !CLR DATA FOR MLPD IS ONES
```

```
CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 213 MISCELLANEOUS CODING SECTION
```

```
8465 :ML4
8466 :
                                                                                      22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                    TOPS-20 Bliss-16 V2(212)
                                                                                                                    PA: <NEALE>BL3ML4.BL1.2 (57)
                          DODU_FLG = ZERO;
                          TST_PAT = ONES;
                                                                                      !BACKGROUND PATTERN
8471
8472
8473
8474
8475
8476
8477
8478
8487
8481
8483
8483
8484
8485
8486
8487
8488
8489
8491
8492
                          incr TWICE from 0 to 1 do
                                                                                      !REPEAT LOOP TWICE
                               begin
                                                                                    !TEST THIRTEEN REGISTERS
                               incr REG_SEL from 0 to 10 do
                                    begin
                                    BGNSUB;
                                    CLR_MBUS;
WRT_REG (.TST_PAT, .REG_SEL, index);
CLR_DATA = (.AI) or (.IGNORE);
                                                                                      WRITE REGISTER WITH BACKGROUND
                                                                                      CALCULATE THE CLEARED DATA PATTERN
                                                                                      CLEAR THE REGISTER
                                    CLR MBUS:
                                                                                      !READ THE REGISTER FOR THE CLEARED DATA PAT
                                    RD_REG (.CLR_DATA, .REG_SEL, ERR_FLG);
                                    if .ERR_FLG IS_SET
                                                                                      !SEE IF READ FOUND AN ERROR
                                    then
                                         begin
                                                                                      !IF ERROR FLAG IS_SET THEN ERROR AND SET DODU_FLG
                                         selectone .REG_SEL of
                                                                                      !FIND WHICH MODULE FAILED
                                              set
                                              [0, 1, 2, 3, 9, 10]:
ERRDF (6, ASYNC, DUMPER); !ASYNC MODULE FAILURE
                                             [4, 5, 8] :
ERRDF (6, SYNC, DUMPER);
                                                                                   !SYNC MODULE FAILURE
                                              [6 to 7]:
                                                   ERROF (6, ARR_DAT, DUMPER); !ARRAY DATA MODULE
                                        PRINTB (FIV_FMT, PHR_4, WRD_12, WRD_52, FNC_23, WRD_56);
PRINTB (FMT_16, .ML_REG [.index, REGISTER_ADD], .CLR_DATA, .RD_DATA);
DODU_FLG = ONE;
                                         end:
                                    ENDSUB:
                                    end:
                               TST_PAT = not .TST_PAT;
                                                                                      !REPEAT WITH COMPLIMENT BACKGROUND PAT
                               end;
                          ML_REG [19, FORCE_HI] = ZEROES;
                                                                                      !RESTORE MLPD FORCED_HI
                             NOW TEST THE DRIVE TYPE REGISTER
8516
8517
8518
                          if .MLDT neg .DRIVE_TYPE
                                                                                     !COMPARE REG CONTENTS TO CALCULATED VALUE
8519 :
                          then
```

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 214

8521 8522	ML4							22-Dec-1980 0 22-Dec-1980 0	9:24:31 9:21:22	TOPS-20 Bliss-10 PA: <neale>BL3ML</neale>	6 V2(212) 6.BLI.2 (57)	
8524 8525 8526 8526 8527 8528		4942 4943 4944 4945	beg ERR PRI PRI end	in DF (113, NTB (TWO NTB (FMI	ASYNC, FMT, R 2, DR	DUMPER) EG_7, PH IVE_TYPE	Ř_4); , .SAVE);					
8530 8531 8532 8533 8534 8535 8536		4942 4943 4944 4945 4947 4948 4949 4951 4951 4953 4954	then	U_FLG IS In U (.ML_L LN;				!DROP THIS UN	IT IF DOD	U_FLG IS_SET		
8536 8537 8541	:		ENDTST;									
	030316	004167	153574 000006 177777		\$17:	JSR	R1,\$SAVE5		:			4835
8547 8548 8549 8550	030326 030334 030336	004167 162706 012767 005005 012702	177777	163052		MOV CLR MOV CLR CLR TRAP	#6,SP #-1,ML.REG+232 R5 #-1,R2 R1		DODU : *,TS	FLG T.PAT E SEL		4889 4890 4891 4893 4896 4897 4898
8545 8546 8547 8548 8549 8550 8551 8552 8553 8554	030316 030322 030326 030334 030336 030342 030346 030356 030356 030366 030374 030402 030404 030410	005001 005004 104402 152777 016703 042703 142777 150377 010246 010446 012746 060616 004767	000040 163220	162636	1\$: 2\$:	WOA B12B	R4		REG.	SEL		4896 4897 4898
8556 8557	030366 030374 030400	142777 150377 010246	000040 163220 177770 000007 162614	162620		BIC BICB BISB MOV	#40,aML.REG+40 ML.DUT,R3 #177770,R3 #7,aML.REG+40 R3,aML.REG+40 R2,-(SP) R4,-(SP)		; TST.	PAT,*		4900
8559 8560	030402 030404	010446 012746	000012			MOV	# IC - (SP)		; REG.	SEL,*		
8561 8562	030410 030412	060616	172456			JSR	SP. (SP) PC. WRT. REG		; INDE			
8558 8559 8560 8561 8562 8563 8564 8565 8566	030416 030422 030424 030426 030430	016600 006300 006300 006300 010003	000012			MOV ASL ASL ASL MOV	12(SP),R0 R0 R0		; INDE	X.*		4901
8565 8566 8567 8568 8569 8570 8571 8572 8573	030424 030426 030430 030432 030436 030442 030446 030454	006300 006300 010003 016300 056300 010066 152777 016700 042700 142777	013156 013162 000006 000040 163122 177770	162540		MOV BIS MOV BISB MOV	RO,R3 ML.REG+2(R3),RO ML.REG+6(R3),RO RO,6(SP) #40,aML.REG+40 ML.DUT,RO #177770,RO #7,aML.REG+40		; *,CL	R.DATA		
8574	030464	142777	000007	162522		BICB	#177770,R0 #7,aML.REG+40					

	SEQ 0					CITON	ODING SE	ISCELLANEOUS C
TOPS PA:<	22-Dec-1980 09:24:31 22-Dec-1980 09:21:22				;ML4			8576 8577
4903		: CLR.DATA.*	RO, aML. REG+40 6(SP), (SP)	BISB		162516 000006	150077 016616 010446	8579 030472 8580 030476 8581 030502
		: CLR.DATA,* ; REG.SEL,*	R4(SP) #14(SP) SP.(SP) PC.RD.REG 14(SP).#1	MOV		000014	010446	8579 030472 8580 030476 8581 030502 8582 030504 8583 030510 8584 030512 8585 030516 8586 030524 8586 030526 8588 030530 8589 030532 8590 030536 8591 030540 8592 030544 8593 030552 8596 030552 8596 030556 8597 030560 8598 030562 8599 030564 8600 030566 8601 030572 8602 030574 8603 030600 8604 030602 8605 030606 8606 030610 8607 030612
		; ERR.FLG,*	SP.(SP) PC.RD.REG	ADD JSR			060616	8583 030510 8584 030512
4905		; ERR.FLG,*	102	CMP	000001	172744 000014	026627	8585 030516 8586 030524
4909		; REG.SEL	R4 3\$ R4,#3	BNE TST BLT			005704	8587 030526 8588 030530
		; REG.SEL,*	R4,#3	BLT CMP BLE CMP		000003	020427	8589 030532 8590 030536
		; REG.SEL,*	R4.#11 5\$	CMP BLT	3\$:	000011	020427	8591 030540 8592 030544
		; REG.SEL,*	R4.#12	BCT		000012	020427	8593 030546 8594 030552
4913		:	5\$ 55	TRAP	4\$:		104455	8595 030554
			ASYNC DUMPER	TRAP .WORD .WORD .WORD			010464	8597 030560 8598 030562
4909		REG.SEL,*	9\$ R4.#4	BR CMP	5\$:	000004	000430	8599 030564
		; REG.SEL,*	6\$ R4,#5	BLT		000005	002403	8601 030572 8602 030574
			7\$ R4,#10	BLE	6\$:	000010	003403	8603 030600 8604 030602
4916		; REG.SEL,*	8\$	RNF	75:	000010	001005	8605 030606 8606 030610
1710			6	TRAP . WORD . WORD . WORD			000006	
.000			SYNC	WORD			024052	8609 030616
4909		REG.SEL.*	R4.#6	BR CMP	8\$:	000006	020427	8611 030622
		; REG.SEL,*	DUMPER 9\$ R4.#6 9\$ R4.#7 9\$	CMP BLT CMP		000007	010526 024052 000412 020427 002407 020427 003004 104455 000006 010570	8613 030630
4919		:	55	TRAP			104455	8615 030636
			ARR.DAT	. WORD			010570	8617 030642
4922			MRD.56,-(SP)	BGT TRAP .WORD .WORD .WORD MOV MOV MOV MOV MOV MOV MOV TRAP	9\$:	007122	012746	8619 030646
			MRD.52,-(SP)	MOV		007122 010242 007066 006426 007454 006062 000006	012746	8621 030656
			MPHR. 4, -(SP)	MOV		007454	012746	8623 030666
			#6,-(SP)	MOV		000006	012746	8625 030676
		; SP,*	ARR.DAT DUMPER WWRD.56,-(SP) WFNC.23,-(SP) WWRD.52,-(SP) WWRD.12,-(SP) WPHR.4,-(SP) WFIV.FMT,-(SP) W6,-(SP) SP,R0 14	TRAP			010370 024052 012746 012746 012746 012746 012746 012746 012746 010600 104414 016716	8627 030704
1923		CLR.DATA,*	RD.DATA, (SP) 30(SP), -(SP) ML.REG(R3), -(SP)	MOV MOV MOV		162216 000030 013154	016716 016646 016346	8608 030614 8609 030616 8610 030620 8611 030622 8612 030626 8613 030630 8614 030634 8615 030636 8616 030640 8617 030642 8618 030644 8619 030646 8620 030652 8621 030656 8622 030662 8623 030666 8624 030672 8625 030676 8626 030702 8627 030704 8628 030706 8629 030712 8630 030716

3632				:ML4					22-Dec-1980 09:24:31	TOP
632 633 634				:					22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	PA:
635 030722 636 030726 637 030732	012746	005216 000004			MOV	#FMT.16,-(SP)				
1635 030722 1636 030726 1637 030732 1638 030734 1639 030736 1640 030742 1641 030756 1642 030752 1643 030754 1644 030756 1645 030766 1646 030764 1648 030772 1649 030774 1650 030776 1651 031000 1652 031004 1653 031016 1654 031012 1657 031026 1658 031036 1659 031036 1659 031036 1669 031036	010600	000004			MOV	SP.RO	:	SP,*		
638 030734 639 030736	104414	000001			MOV	14 #1.R5		*,DODU.FLG		492
640 030742	062706	000001 000026 000012		10\$:	ADD	#26,SP				492 490 489 492
642 030752	104467	000012		103.	TRAP	#1.R5 #26.SP #12.SP 67	:			492
643 030754 644 030756	103002				ROR	R0 12\$ 2\$ R4				
645 030760 646 030764	000167	177362		11\$:	INC	2\$		DEC SEI		489
647 030766	020427	000012		123.	CMP	R4,#12	:	REG.SEL,*		407
548 030772 549 030774	005/72			1	BLE	11\$ R2 R1		TST.PAT		493 489
650 030776 651 031000	005201	000001			INC	R1 R1,#1		TST.PAT TWICE TWICE,*		489
547 030766 548 030772 549 030774 550 030776 551 031000 552 031004 553 031006	020127 003002 000167 005067 027767 001431			7	BGT	13\$		14202,-		
654 031012	005067	177332 162370 162262		13\$:	CLR	1\$ ML.REG+232 aML.REG+130,DRIVE.TYPE	:			493
555 031016 556 031024	027767	162262	162106		CMP BEQ	aML.REG+130,DRIVE.TYPE	;			494
57 031026 58 031030	104455				TRAP .WORD .WORD .WORD MOV	14 \$ 55 161	:			494
59 031032	010464				WORD	ASYNC				
60 031034 61 031036	024052	007454			MOV MOV	DUMPER MPHR. 4 (SP)				494
62 031042	012746	007454 010324 006024			MOV	MPHR.4,-(SP) MREG.7,-(SP) MTWO.FMT,-(SP) M3,-(SP)				
64 031052	012746	006024			MOV	#3,-(SP)				
	104414				TRAP	SP.R0 14		SP,*		
67 031062 68 031064	010416	162042			MOV	R4,(SP) DRIVE,TYPE,-(SP)	:	SAVE,*		494
69 031070	010416 016746 012746 012746	162042 004266 000003			MOV	WFMT. 2, -(SP)				
666 031060 67 031062 68 031064 69 031070 70 031074 671 031100 672 031102 673 031110 674 031110 675 031112 676 031112 677 031120 678 031122 679 031124	010600	000003			MOV TRAP MOV MOV MOV MOV MOV TRAP	R4,(SP) DRIVE.TYPE,-(SP) WFMT.2,-(SP) W3,-(SP) SP,R0	:	SP,*		
73 031102	062706 005305	000016			ADD					494
574 031110	005305			145:	ADD DEC BNE	R5	:	DODU.FLG		494
676 031114	016700	162460			MOV	R5 15\$ ML.LUN,R0 51	:			495
678 031122	001004 016700 104451 104444 062706 000207				MOV TRAP TRAP	44				
679 031124 680 031130	062706	000006		15\$:	ADD RTS	#6.SP PC	:			483
3666 031060 3667 031062 3668 031064 3669 031070 3670 031074 3671 031100 3672 031102 3673 031104 3674 031110 3675 031112 3676 031114 3677 031120 3680 031122 3680 031130 3681 3682 3683				· Pout		198 words depth per invocation: 2				

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8692
8693
8697
8701 031132 004767 177160 15: JSR PC,\$T7
8702 031132 004767 177160 1\$: JSR PC,\$T7
8704 031140 006000 ROR RO
8705 031142 103773 BLO 1\$
8706 031144 000207 RTS PC
8707
8708 : Routine Size: 6 words
8709
8714
8715
8716 : 4956 !<BLF/PAGE>

SEQ 0204

4953

1

1. 1.

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```
8718 :ML4
8719 :
8720 :
8721 :
8722 :
8723 :
8724 :
8725 :
8726 :
8727 :
8728 :
8729 :
8730 :
8731 :
8732 :
8733 :
                                                                                                                                22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                            TOPS-20 Bliss-16 V2(212)
PA:<NEALE>BL3ML 4.BLI.2 (58)
                            4957
4958
4959
4960
4961
4962
4963
4964
4965
4966
4969
4970
                                       BGNTST:
                                        ! TEST NUMBER: TST 8
                                           TEST NAME:
                                                                    REGISTER SELECTION TEST
                                           TEST DESCRIPTION:
                                                      THIS TEST TESTS FOR UNIQUE REGISTER
                                                      SELECTION BY FIRST WRITING A BACKGROUND
8734
8735
8736
8737
8738
8739
8740
8741
8742
8743
8744
8745
8746
8747
8748
8751
8752
8753
8754
8755
8756
8757
8758
8759
8760
8761
8762
8763
8764
8765
8766
8767
8768
                                                      PATTERN INTO ALL READ/WRITE REGISTERS
                                                      IT THEN WRITES A COMPLIMENT
                                                      BACKGROUND PATTERN INTO ONE REGISTER
                                                      AND READS THE REMAINING UNWRITTEN
REGISTERS FOR AN UNCHANGED BACKGROUND
                                                      THIS PROCEDURE IS REPEATED UNTIL ALL REGISTERS HAVE BEEN WRITTEN WITH A
                                                      COMPLIMENT BACKGROUND PATTERN.
                                       local
                                              DODU_FLG,
TST_PAT,
ERR_FLG,
                                                                                                                                PROP UNIT FLAG
                                                                                                                                 !ERROR FLAG PASSED TO ROUTINE;
                                               index;
                                                                                                                                 !POINTS TO REGISTER PRESENTLY BEING TESTED.
                                       BGNSUB;
                                      BGNSUB;

CLR MBUS;

DODO FLG = ZERO;

TST PAT = %0'125252';

WRT CS1 (.TST PAT, 0);

WRT ER (.TST PAT, 6);

WRT DA (.TST PAT, 3);

WRT PA (.TST PAT, 13);

WRT E1 (.TST PAT, 13);

WRT E2 (.TST PAT, 14);
                                                                                                                                !LOAD TST PAT WITH ALTERNATE 1'S & 0'S !WRITE A BACKGROUND INTO ALL THE DIRECTLY
                                                                                                                                !ACCESSABLE READ WRITE REGISTERS
                                       incr CNT_1 from 0 to 4 do
begin
TST_PAT = not .TST_PAT;
                                                                                                                                !WRITE A COMPLIMENT PATTERN INTO ONE REGISTER
                                                                                                                                !GENERATE THE COMPLIMENT PAT
                                              case .CNT_1 from 0 to 4 of
                                                                                                                                SELECT THE REGISTER TO WRITE INTO
                                                      set
 8771
                                                      [0]:
```

```
TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML 4.BLI.2 (58)
8775
8776
5009
5010
                                  WRT_CS1 (.TST_PAT, 0);
                                                                         !FIRST PASS WRITE COMP PAT TO MLCS1
                              [1] : WRT_ER (.TST_PAT, 6);
                                                                         SECOND PASS WRITE COMP PAT TO MLER
                              [2] : WRT_DA (.TST_PAT, 3);
                                                                         !THIRD PASS WRITE COMP PAT TO MLDA
                              [3] : WRT_PA (.TST_PAT, 8);
                                                                         !FORTH PASS WRITE COMP PAT TO MLPA
                                   WRT_E1 (.TST_PAT, 13);
                                                                         !FIFTH PASS WRITE COMP PAT TO MLE1
                          TST_PAT = not .TST_PAT;
                                                                         !COMPLIMENT TST_PAT BACK TO BACKGROUND
                          incr INT_2 from .CNT_1 + 1 to 5 do
                                                                         !NOW READ THE REMAINING UNWRITTEN REGISTERS FOR AN
                                                                         !UNCHANGED BACKGROUND
                              case .CNT_2 from 0 to 5 of
                                                                         !SELECT THE REGISTER TO READ
                                       RD_CS1 (.TST_PAT, index = 0, ERR_FLG); !READ MLCS1
                                  [1] :
                                       RD_ER (.TST_PAT, index = 6, ERR_FLG);
                                                                                 !READ MLER
                                  [2] :
                                       RD_DA (.TST_PAT, index = 3, ERR_FLG);
                                                                                 !READ MLDA
                                  [3]:
RD_PA (.TST_PAT, index = 8, ERR_FLG);
                                                                                 !READ MLPA
                                       RD_E1 (.TST_PAT, index = 13, ERR_FLG); !READ MLE1
                                       RD_E2 (.TST_PAT, index = 14, ERR_FLG); !READ MLE2
                                   tes:
                              if .ERR_FLG IS_SET
                                                                         !SEE IF READ FOUND AN ERROR
                              then
                                  begin
                                  selectone .CNT_2 of
                                       set
               5058
5059
5060
                                       [0 to 2] :
                                           ERRDF (110, ASYNC, DUMPER); !ASYNC MODULE FAILURE
```

```
22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                         TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML 4.BLI.2 (58)
[3 to 5] :
                                                            ERROF (110, ARR_DAT, DUMPER); !ARRAY DATA MODULE FAILURE
                                                 PRINTB (THR_FMT, WRD_38, WRD_37, WRD_10);
PRINTB (FMT_16, .ML_REG [.index, REGISTER_ADD], .WT_DATA, .RD_DATA);
DODU_FLG = ONE;
                                                 end:
                                           end:
                                     end:
                               ENDSUB;
                               if .DODU_FLG IS_SET
                                                                                                    !DROP THIS UNIT IF DODU FLG IS SET
                               then
                                     begin
DODU (.ML_LUN);
                       5079
                        5080
                                     DOCLN;
                       5081
                                     end:
                       5082
                               ENDTST:
       031146
031152
031154
031156
031164
031170
                    004167
005746
104402
                               152744
                                                       $18:
                                                                              R1,$SAVE5
                                                                                                                                                                                        4955
                                                                   TST
                                                                              -(SP)
                                                                   TRAP
                                                       15:
                                                                              #40,aML.REG+40
ML.DUT.R3
#177770,R3
#7,aML.REG+40
R3,aML.REG+40
                               000040
162412
177770
                                           162030
                                                                   BISB
                                                                                                                                                                                        4990
                                                                   MOV
                                                                   BIC
                                                                   BICB
BISB
CLR
                                000007
                                           162012
                                                                              R5
                                                                                                                             ; DODU.FLG
                                                                              #-52526,R4
R4,-(SP)
-(SP)
                                125252
                                                                                                                             : *,TST.PAT
                                                                   MOV
                                                                   MOV
                                                                                                                              : TST.PAT.*
                                                                   CLR
                                                                              PC.WRT.CS1
                                166236
                                                                   JSR
                                                                              R4.(SP)
#6,-(SP)
                                                                   MOV
                                                                                                                                                                                        4995
                                                                                                                             : TST.PAT.*
                                000006
                                                                   MOV
                                                                  JSR
MOV
MOV
                                                                              PC, WRT. ER
                                                                              R4.(SP)
                                                                                                                                                                                        4996
                                                                                                                             ; TST.PAT,*
                                000003
166546
                                                                              #3,-(SP)
                                                                   JSR
MOV
                                                                              PC, WRT.DA
                    010416
012746
                                                                              R4, (SP)
#10,-(SP)
                                                                                                                             ; TST.PAT,*
                                                                                                                                                                                        4997
                               000010
```

MLABO ML-11 L	ODING SE	CTION			12:16 PAGE 221		SEQ 0208
8885 8886 8887 8888 031256 8889 031262 8890 031264 8891 031270 8892 031274 8893 031276 8894 031302 8896 031310 8896 031316 8990 031322 8900 031322 8901 031324 8902 031336 8902 031336 8903 031336 8904 031332 8905 031336 8906 031336 8907 031346 8908 031344 8909 031346 8910 031350 8911 031354 8909 031366 8911 031354			;ML4			22 - 0 22 - 0	ec-1980 09:24:31 TOPS ec-1980 09:21:22 PA:
8888 031256	004767	167070		JSR	PC.WRT.PA	. 707 047 4	4000
8890 031264	012746	000015 167264		MOV	PC.WRT.PA R4.(SP) #15,-(SP) PC.WRT.E1 R4.(SP) #16,-(SP) PC.WRT.E2	; TST.PAT,*	4998
8892 031274	010416			MOV	R4. (SP)	; TST.PAT,*	4999
8893 031276 8894 031302 8895 031306 8896 031310	012746	000016 167460		JSR	#16,-(SP) PC,WRT.E2		
8895 031306 8896 031310	005001 005104		2\$:	MOV JSR MOV MOV JSR CLR COM MOV	KI	: CNT.1 : TST.PAT : CNT.1,*	5003 5003 5003
8897 031312 8898 031314	010103			MOV	R4 R1 ,R3 R3	; CNT.1,*	5005
8899 031316 8900 031322	066307	031322	3\$:	ADD	3\$ (R3) ,PC		
8901 031324 8902 031326	000024			. WORD	58-38 48-38		
8904 031330	000054			. WORD	78-3\$ 86-76		
8905 031334	010446		45:	ASL ADD .WORD .WORD .WORD .WORD MOV CLR JSR	R3 3\$ (R3) ,PC 4\$-3\$ 5\$-3\$ 6\$-3\$ 7\$-3\$ 8\$-3\$ R4,-(SP) -(SP)	; TST.PAT,*	5009
8907 031340	004767	166116		JSR	PC.WRT.CS1		500
1899 031316 1900 031322 1901 031324 1902 031326 1903 031330 1904 031332 1905 031334 1906 031334 1908 031346 1909 031346	010446	000004	5\$:	BR MOV	R4,-(SP)	: TST.PAT,*	5005 5012
911 031354	004767	000006 166260		MOV JSR	R4,-(SP) #6,-(SP) PC.WRT.ER 9\$		500
911 031354 912 031360 913 031362 914 031364 915 031370	010446	000007	6\$:	BR MOV	R4,-(SP)	TST.PAT,*	5005 5015
915 031370	004767	000003 166422		MOV JSR	PC, WRT.DA		
916 031374 917 031376	010446		7\$:	JSR BR MOV	R4,-(SP) #3,-(SP) PC, WRT.DA 9\$ R4,-(SP) #10,-(SP) PC, WRT.PA 9\$ R4,-(SP) #15,-(SP) PC, WRT.E1 R4 R1,R3 22\$	TST.PAT,*	5005 5018
918 031400 1919 031404	012746 004767 000405 010446 012746 004767 005104	000010 166742		MOV JSR BR MOV MOV JSR COM MOV	PC,WRT.PA		
920 031410 921 031412	000405		8\$:	BR	9\$ R4(SP)	TST.PAT.*	5005 5021
9922 031414 9923 031420	012746	000015 167134		MOV JSR	#15,-(SP) PC,WRT.E1		
8924 031424 8925 031426	005104 010103		9\$:	MOV	R4 R1,R3	: TST.PAT : CNT.1,CNT.2	5024 5026
8926 031430 8927 031432	000576 010300		10\$:	BR MOV	22\$ R3,R0 R0	; CNT.2,*	5029
8928 031434 8929 031436	006300 066007	031442		ASL	RO 11\$(RO) .PC		
8930 031442 8931 031444	000014		115:	. WORD	128-118 138-118		
8932 031446 8933 031450	000062			. WORD	145-115		
8918 031400 8919 031404 8920 031410 8921 031412 8922 031414 8923 031420 8924 031424 8925 031426 8926 031430 8927 031432 8928 031434 8929 031436 8930 031442 8931 031444 8932 031446 8933 031450 8934 031450 8935 031454 8936 031456 8937 031460 8938 031464	006300 066007 000014 000036 000106 000132 000156 010446 005002 005046 012746			ASL ADD .WORD .WORD .WORD .WORD .WORD MOV CLR CLR MOV	RO 11\$(RO),PC 12\$-11\$ 13\$-11\$ 14\$-11\$ 15\$-11\$ 16\$-11\$ 17\$-11\$ R4,-(SP) R2 -(SP) #30,-(SP)		
8936 031456	010446		12\$:	MOV	R4,-(SP)	: TST.PAT.* : INDEX	5033
8938 031462	005046	000030		CLR	-(SP)	; INDEX	

PR.	CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22	-DEC-80 12:16 PAGE 222		SEQ 0209
894.5 031470 060616	8941	:ML4		22-Dec-1	980 09:24:31 TOPS
\$950 031506 010266 000030 PUV #75(SP) ; IMDEX.* \$950 031510 012766 000030 PUV #75(SP) ; IMDEX.* \$951 031510 00476 166166 JSR PC, RD.ER \$952 031511 00476 166166 JSR PC, RD.ER \$953 031512 004477 148: PUV R2, -(SP) ; IMDEX.* \$955 03152 010266 000030 PUV #3.02 : *, IMDEX.* \$955 03152 010266 000030 PUV R2, -(SP) ; IMDEX.* \$950 03152 010266 000030 PUV R2, -(SP) ; IMDEX.* \$950 031540 000015 166320 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 166320 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 166320 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 PUV R2, -(SP) ; IMDEX.* \$960 031550 010446 PUV R2, -(SP) ; IMDEX.* \$960 031550 010446 PUV R2, -(SP) ; IMDEX.* \$964 031550 010446 PUV R2, -(SP) ; IMDEX.* \$964 031560 012746 000030 PUV R2, -(SP) ; IMDEX.* \$965 031560 010466 PUV R2, -(SP) ; IMDEX.* \$965 031560 010466 PUV R2, -(SP) ; IMDEX.* \$965 031560 010466 PUV R2, -(SP) ; IMDEX.* \$967 031570 0000475 PUV R2, -(SP) ; IMDEX.* \$968 031570 010446 PUV R2, -(SP) ; IMDEX.* \$969 031570 010446 PUV R2, -(SP) ; IMDEX.* \$969 031570 010466 PUV R2, -(SP) ; IMDEX.* \$969 031570 010466 PUV R2, -(SP) ; IMDEX.* \$969 031570 010466 PUV R2, -(SP) ; IMDEX.* \$970 031602 010466 PUV R2, -(SP) ; IMDEX.* \$971 031604 0102746 PUV R2, -(SP) ; IMDEX.* \$977 031602 010466 PUV R2, -(SP) ; IMDEX.* \$978 031603 010604 PUV R2, -(SP) ; IMDEX.* \$979 031603 000001 PUV R2, -(SP) ; IMDEX.* \$990 031603 000001 PUV R2, -(SP) ; IMDEX.* \$990 031	8943				900 U9:21:22 PA:4
\$950 031506 010266 000030 PUV #75(SP) ; IMDEX.* \$950 031510 012766 000030 PUV #75(SP) ; IMDEX.* \$951 031510 00476 166166 JSR PC, RD.ER \$952 031511 00476 166166 JSR PC, RD.ER \$953 031512 004477 148: PUV R2, -(SP) ; IMDEX.* \$955 03152 010266 000030 PUV #3.02 : *, IMDEX.* \$955 03152 010266 000030 PUV R2, -(SP) ; IMDEX.* \$950 03152 010266 000030 PUV R2, -(SP) ; IMDEX.* \$950 031540 000015 166320 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 166320 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 166320 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 PUV R2, -(SP) ; IMDEX.* \$960 031550 010446 PUV R2, -(SP) ; IMDEX.* \$960 031550 010446 PUV R2, -(SP) ; IMDEX.* \$964 031550 010446 PUV R2, -(SP) ; IMDEX.* \$964 031560 012746 000030 PUV R2, -(SP) ; IMDEX.* \$965 031560 010466 PUV R2, -(SP) ; IMDEX.* \$965 031560 010466 PUV R2, -(SP) ; IMDEX.* \$965 031560 010466 PUV R2, -(SP) ; IMDEX.* \$967 031570 0000475 PUV R2, -(SP) ; IMDEX.* \$968 031570 010446 PUV R2, -(SP) ; IMDEX.* \$969 031570 010446 PUV R2, -(SP) ; IMDEX.* \$969 031570 010466 PUV R2, -(SP) ; IMDEX.* \$969 031570 010466 PUV R2, -(SP) ; IMDEX.* \$969 031570 010466 PUV R2, -(SP) ; IMDEX.* \$970 031602 010466 PUV R2, -(SP) ; IMDEX.* \$971 031604 0102746 PUV R2, -(SP) ; IMDEX.* \$977 031602 010466 PUV R2, -(SP) ; IMDEX.* \$978 031603 010604 PUV R2, -(SP) ; IMDEX.* \$979 031603 000001 PUV R2, -(SP) ; IMDEX.* \$990 031603 000001 PUV R2, -(SP) ; IMDEX.* \$990 031	8944 031470 060616		ADD SP, (SP)	; ERR.FLG,*	
\$950 031506 010266 000030 PUV #75(SP) ; IMDEX.* \$950 031510 012766 000030 PUV #75(SP) ; IMDEX.* \$951 031510 00476 166166 JSR PC, RD.ER \$952 031511 00476 166166 JSR PC, RD.ER \$953 031512 004477 148: PUV R2, -(SP) ; IMDEX.* \$955 03152 010266 000030 PUV #3.02 : *, IMDEX.* \$955 03152 010266 000030 PUV R2, -(SP) ; IMDEX.* \$950 03152 010266 000030 PUV R2, -(SP) ; IMDEX.* \$950 031540 000015 166320 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 166320 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 166320 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 PUV R2, -(SP) ; IMDEX.* \$959 031540 000015 PUV R2, -(SP) ; IMDEX.* \$960 031550 010446 PUV R2, -(SP) ; IMDEX.* \$960 031550 010446 PUV R2, -(SP) ; IMDEX.* \$964 031550 010446 PUV R2, -(SP) ; IMDEX.* \$964 031560 012746 000030 PUV R2, -(SP) ; IMDEX.* \$965 031560 010466 PUV R2, -(SP) ; IMDEX.* \$965 031560 010466 PUV R2, -(SP) ; IMDEX.* \$965 031560 010466 PUV R2, -(SP) ; IMDEX.* \$967 031570 0000475 PUV R2, -(SP) ; IMDEX.* \$968 031570 010446 PUV R2, -(SP) ; IMDEX.* \$969 031570 010446 PUV R2, -(SP) ; IMDEX.* \$969 031570 010466 PUV R2, -(SP) ; IMDEX.* \$969 031570 010466 PUV R2, -(SP) ; IMDEX.* \$969 031570 010466 PUV R2, -(SP) ; IMDEX.* \$970 031602 010466 PUV R2, -(SP) ; IMDEX.* \$971 031604 0102746 PUV R2, -(SP) ; IMDEX.* \$977 031602 010466 PUV R2, -(SP) ; IMDEX.* \$978 031603 010604 PUV R2, -(SP) ; IMDEX.* \$979 031603 000001 PUV R2, -(SP) ; IMDEX.* \$990 031603 000001 PUV R2, -(SP) ; IMDEX.* \$990 031	8946 031476 000461		BR 18\$;	5029
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031622 012702 000016 8977 031626 010246 8977 031626 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031630 00164 8982 031650 001064 8982 031650 001064 8982 031650 001064 ADD SP, (SP) 17\$: MOV R4, -(SP) 8970 031636 010246 8970 031636 010246 8970 031636 004767 167222 8981 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 ; ERR.FLG,* 8982 031650 001064	8947 031500 010446	13\$:	MOV R4,-(SP)	: TST.PAT.*	5036
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031622 012702 000016 8977 031626 010246 8977 031626 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031630 00164 8982 031650 001064 8982 031650 001064 8982 031650 001064 ADD SP, (SP) 17\$: MOV R4, -(SP) 8970 031636 010246 8970 031636 010246 8970 031636 004767 167222 8981 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 ; ERR.FLG,* 8982 031650 001064	8949 031506 010246		MOV R2,-(SP)	INDEX.*	
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031620 010446 8977 031620 010446 8977 031626 010246 8977 031636 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 8982 031650 001064 ADD SP,(SP) FRR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SO51	8950 031510 012746 000030		MOV #30,-(SP)		
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031622 012702 000016 8977 031626 010246 8977 031626 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031630 00164 8982 031650 001064 8982 031650 001064 8982 031650 001064 ADD SP, (SP) 17\$: MOV R4, -(SP) 8970 031636 010246 8970 031636 010246 8970 031636 004767 167222 8981 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 ; ERR.FLG,* 8982 031650 001064	8952 031516 004767 166166		JSR PC.RD.ER	; ERR.FLG,*	
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031622 012702 000016 8977 031626 010246 8977 031626 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031630 00164 8982 031650 001064 8982 031650 001064 8982 031650 001064 ADD SP, (SP) 17\$: MOV R4, -(SP) 8970 031636 010246 8970 031636 010246 8970 031636 004767 167222 8981 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 ; ERR.FLG,* 8982 031650 001064	8953 031522 000447		BR 18\$:	5029
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031620 010446 8977 031620 010446 8977 031626 010246 8977 031636 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 8982 031650 001064 ADD SP,(SP) FRR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SO51	8955 031526 010446	145:	MOV #3.R2	; TST.PAT.*	5039
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031622 012702 000016 8977 031626 010246 8977 031626 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031630 00164 8982 031650 001064 8982 031650 001064 8982 031650 001064 ADD SP, (SP) 17\$: MOV R4, -(SP) 8970 031636 010246 8970 031636 010246 8970 031636 004767 167222 8981 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 ; ERR.FLG,* 8982 031650 001064	8956 031532 010246		MOV R2,-(SP)	: INDEX.*	
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031622 012702 000016 8977 031626 010246 8977 031626 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031630 00164 8982 031650 001064 8982 031650 001064 8982 031650 001064 ADD SP, (SP) 17\$: MOV R4, -(SP) 8970 031636 010246 8970 031636 010246 8970 031636 004767 167222 8981 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 ; ERR.FLG,* 8982 031650 001064	8957 031534 012746 000030		MOV #30,-(SP)		
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031622 012702 000016 8977 031626 010246 8977 031626 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031630 00164 8982 031650 001064 8982 031650 001064 8982 031650 001064 ADD SP, (SP) 17\$: MOV R4, -(SP) 8970 031636 010246 8970 031636 010246 8970 031636 004767 167222 8981 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 ; ERR.FLG,* 8982 031650 001064	8959 031542 004767 166320		JSR PC.RD.DA	, EM.FLO,-	
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031622 012702 000016 8977 031626 010246 8977 031626 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031630 00164 8982 031650 001064 8982 031650 001064 8982 031650 001064 ADD SP, (SP) 17\$: MOV R4, -(SP) 8970 031636 010246 8970 031636 010246 8970 031636 004767 167222 8981 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 ; ERR.FLG,* 8982 031650 001064	8960 031546 000435		BR 18\$	FOT DAT A	5029
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031620 010446 8977 031620 010446 8977 031626 010246 8977 031636 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 8982 031650 001064 ADD SP,(SP) FRR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SO51	8962 031552 012702 000010	139:	MOV #10_R2	: *.INDEX	5042
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031620 010446 8977 031620 010446 8977 031626 010246 8977 031636 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 8982 031650 001064 ADD SP,(SP) FRR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SO51	8963 031556 010246		MOV R2,-(SP)	: INDEX,*	
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031620 010446 8977 031620 010446 8977 031626 010246 8977 031636 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 8982 031650 001064 ADD SP,(SP) FRR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SO51	8965 031564 060616 000030		MOV #30,-(SP)	· EDD FLG +	
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031620 010446 8977 031620 010446 8977 031626 010246 8977 031636 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 8982 031650 001064 ADD SP,(SP) FRR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SO51	8966 031566 004767 166644		JSR PC.RD.PA	, EM., EG,	
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031620 010446 8977 031620 010446 8977 031626 010246 8977 031636 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 8982 031650 001064 ADD SP,(SP) FRR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SO51	8967 031572 000423 8968 031574 010446	148.	BR 18\$	TOT DAT .	5029
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031620 010446 8977 031620 010446 8977 031626 010246 8977 031636 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 8982 031650 001064 ADD SP,(SP) FRR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SO51	8969 031576 012702 000015	109:	MOV #15.R2	: *.INDEX	3043
8972 031610 060616 8973 031612 004767 167026 8974 031616 000411 8974 031616 000411 8975 031620 010446 8976 031620 010446 8977 031620 010446 8977 031626 010246 8977 031636 010246 8978 031630 012746 000030 8979 031634 060616 8979 031636 004767 167222 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 8982 031650 001064 ADD SP,(SP) FRR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SPR.FLG,* SO51	8970 031602 010246		MOV R2,-(SP)	; INDEX,*	
8974 031616 000411 8975 031620 010446 17\$: MOV R4,-(SP) : TST.PAT,* 5048 8976 031622 012702 000016 MOV W16,R2 :*,INDEX 8977 031626 010246 MOV R2,-(SP) : INDEX,* 8978 031630 012746 000030 MOV W30,-(SP) : ERR.FLG,* 8980 031636 004767 167222 JSR PC,RD.E2 8981 031642 026627 000030 000001 18\$: CMP 30(SP),W1 : ERR.FLG,* 5051	8972 031610 060616		ADD SP. (SP)	· FRR.FLG.*	
8976 031622 012702 000016 MOV #16,R2 ; *,INDEX 8977 031626 010246 MOV #30,-(SP) ; INDEX,* 8978 031636 0012746 000030 MOV #30,-(SP) ; ERR.FLG,* 8980 031636 004767 167222 JSR PC,RD.E2 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 ; ERR.FLG,* 5051 8982 031650 001064	8973 031612 004767 167026		JSR PC.RD.E1	, 2.00.	
8976 031622 012702 000016 MOV #16,R2 ; *,INDEX 8977 031626 010246 MOV #30,-(SP) ; INDEX,* 8978 031636 0012746 000030 MOV #30,-(SP) ; ERR.FLG,* 8980 031636 004767 167222 JSR PC,RD.E2 8981 031642 026627 000030 000001 18\$: CMP 30(SP),#1 ; ERR.FLG,* 5051 8982 031650 001064	8975 031620 010446	178.	BR 18\$	TET DAT .	5029
8977 031626 010246 000030 MOV M30,-(SP) ; INDEX,* 8978 031636 004767 167222 JSR PC,RD,E2 8981 031642 026627 000030 000001 188: CMP 30(SP),#1 ; ERR,FLG,* 8982 031650 001064 TST R3 ; CNT.2 5055 8984 031654 002410 BLT 198 ; CNT.2 5055 8985 031656 020327 000002 BLT 198 ; CNT.2,* 8986 031666 000156 S989 0316670 010464 JGR SP,	8976 031622 012702 000016		MOV #16,R2	*,INDEX	3040
8979 031634 060616 ADD SP.(SP) ; ERR.FLG.* 8980 031636 004767 167222 JSR PC.RD.E2 8981 031642 026627 000030 000001 188: CNP 30(SP),#1 ; ERR.FLG.* 8983 031650 001066 BNE 218 8983 031652 005703 BLT 198 8986 031654 002410 BRT 198 8986 031664 004165 FRAP 55 ; CNT.2.* 8986 031664 104455 FRAP 55 ; CNT.2.* 8989 031667 001066 UNROW ASYNC 8999 031672 024052 BRT 198 STAND BRT 198 STAND ASYNC 8990 031676 000156 BR 20.8 STAND BRT 20.8 STAND	8977 031626 010246 18978 031630 012746 000030		MOV R2,-(SP)	; INDEX,*	
8980 031636 004767 167222 000030 000001 18\$: CIPP 30(SP),#1 ; ERR.FLG,* 5051 8982 031650 0001064 TST R3 ; CNT.2 5055 8984 031652 005703 BLT 19\$; CNT.2,* 8986 031662 003005 BLT 19\$; CNT.2,* 8986 031662 003005 BLT 19\$; CNT.2,* 8988 031664 104455 TRAP 55 ; MORD 156 ; MORD 156 ; MORD 031670 010464 MORD 0412 MORD 041	8979 031634 060616		ADD SP. (SP)		
8982 031650 001064 8983 031652 005703 8984 031654 002410 8985 031656 020327 000002 8986 031666 020327 000002 8986 031666 000156 8988 031666 000156 8989 031670 010464 8990 031672 04052 8990 031674 000412 8991 031674 000412 8992 031676 020327 000003 8992 031676 020327 000003 8993 031700 002407 8994 031704 020327 000005 8995 031710 003004 8995 031710 003004 8995 031710 003004 8995 031710 003004	8980 031636 004767 167222	000001 100	JSR PC.RD.E2		
8983 031652 005703 8984 031654 002410 8985 031656 020327 8986 031666 020327 8987 031664 104455 8988 031666 000156 8988 031670 010464 8999 031672 024052 8991 031674 000412 8992 031676 020327 000003 8993 031702 002407 8994 031704 020327 000005 807 R3,#3 808 R2 20\$ 809 R3,#3 809 R3,#2 809 R3,#3 809 R3,#2 809 R3,#3 809 R3,#2 809 R3,#3 809 R3,#3 809 R3,#2 809 R3,#3 809 R3,#2 809 R3,#2 809 R3,#3 809 R3,#2 809 R3,#3 809 R3,#2	8982 031650 001064	000001 185:	BME 21\$; ERR.FLG,*	5051
8985 031656 020327 000002			TST R3	; CNT.2	5055
8986 031662 003005 8987 031664 104455 8988 031666 000156 8989 031670 010464 8990 031672 024052 8991 031674 000412 8992 031676 020327 000003 8993 031702 002407 8993 031704 020327 000005 807 19\$ 808	8985 031656 020327 000002		BLT 195 CMP 93 #2	· CMT 2 +	
8987 031664 104455 8988 031666 000156 8999 031670 010464 8990 031672 024052 8991 031674 000412 8992 031676 020327 000003 19\$: CMP R3,#3 ; CNT.2,* 8993 031702 002407 8994 031704 020327 000005 CMP R3,#5 ; CNT.2,* 8995 031710 003004 BGT 20\$	8986 031662 003005		BGT 19\$, (141.2)	
8989 031670 010464 8990 031672 024052 8991 031674 000412 8992 031676 020327 000003 19\$: CMP R3,#3 ; CNT.2,* 8993 031702 002407 8994 031704 020327 000005 CMP R3,#5 ; CNT.2,* 8995 031710 003004 BGT 20\$	8987 031664 104455 8988 031664 000156		TRAP 55	:	5059
8990 031672 024052 8991 031674 000412 8992 031676 020327 000003 19\$: CMP R3_M3 ; CNT.2,* 8993 031702 002407 8994 031704 020327 000005 CMP R3_M5 ; CNT.2,* 8995 031710 003004 BGT 20\$; CNT.2,*	8989 031670 010464		WORD ASYNC		
8992 031676 020327 000003 19\$: CNP R3.#3 ; CNT.2,* 8993 031702 002407 BLT 20\$ 8994 031704 020327 000005 CNP R3.#5 ; CNT.2,* 8995 031710 003004 BGT 20\$	8990 031672 024052		WORD DUMPER		
8993 031702 002407 8994 031704 020327 000005 CMP R3.#5 ; CNT.2,* 8995 031710 003004 BGT 20\$	8992 031676 020327 000003	198:	CMP R3.43	CNT.2.*	5055
8995 031710 003004 BGT 20\$; CNT.2,*	8993 031702 002407		BLT 20\$		
	8995 031710 003004		RGT 20\$; CNT.2,*	

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 223	SEC	0210
8997 8998	ML4		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
9000 031712 104455 9001 031714 000156 9002 031716 010570	TRAP .WORD .WORD .WORD	55 156 ARR DAT	•	5062
9000 031712 104455 9001 031714 000156 9002 031716 010570 9003 031720 024052 9004 031722 012746 006406 9005 031732 012746 006710 9007 031736 012746 006034 9008 031742 012746 006034 9009 031746 010600 9010 031750 104414 9011 031752 016716 161152 9012 031756 016746 161152 9013 031764 006300 9014 031764 006300 9015 031766 006300 9016 031770 006300 9017 031772 016046 013154 9018 031776 012746 005216 9019 032002 012746 005216 9019 032002 012746 00004 9020 032006 010600 9021 032010 104414 9022 032012 012705 000001 9023 032016 062706 000022 9024 032022 062706 000006 9025 032026 005203 9026 032030 020327 000005 9027 032034 003002 9028 032036 000167 177370 9029 032042 022626	MOV	ARR.DAT DUMPER NURD.10,-(SP) NURD.37,-(SP) NURD.38,-(SP) NTHR.FMT,-(SP) N4,-(SP) SP,R0		5065
9009 031746 010600 9010 031750 104414	MOV MOV TRAP	SP.RO	; SP,*	
9011 031752 016716 161152 9012 031756 016746 161144 9013 031762 010200 9014 031764 006300 9015 031766 006300	MOV MOV ASL ASL	RD.DATA, (SP) WT.DATA, -(SP) R2,R0 R0 R0 R0	: INDEX,*	5066
9017 031772 016046 013154 9018 031776 012746 005216 9019 032002 012746 000004 9020 032006 010600	ASL MOV MOV MOV TRAP	#FMT.16,-(SP) #4,-(SP) SP,R0	; SP,*	
9022 032012 012705 000001 9023 032016 062706 000022 9024 032022 062706 000006 9025 032026 005203	MOV ADD 21\$: ADD 22\$: INC CMP BGT JMP 23\$: CMP	#1,R5 #22,SP #6,SP R3 R3,#5 23\$ 10\$ (SP)+,(SP)+	: *,DODU.FLG : CNT.2	5067 5053 5027 5026
9026 032030 020327 000005 9027 032034 003002 9028 032036 000167 177370	CMP BGT	R3,#5 23\$: CNT.2 : CNT.2,*	
9029 032042 022626 9030 032044 005201 9031 032046 020127 000004 9032 032052 003002	23\$: CMP INC CMP BGT	R1 .#4	: CNT.1 : CNT.1,*	5002 5001
9033 032054 000167 177230 9034 032060 062706 000016 9035 032064 104467 9036 032066 006000	INC CMP BGT JMP 24\$: ADD TRAP ROR BHIS JMP	2\$ #16,SP 67 R0 25\$ 1\$ R5 26\$ ML.LUN,R0	:	4988 5072
9038 032072 000167 177056 9039 032076 005305	BHIS JMP 25\$: DEC	1\$ R5	; DODU.FLG	5076
9036 032066 006000 9037 032070 103002 9038 032072 000167 177056 9039 032076 005305 9040 032100 001004 9041 032102 016700 161472 9042 032106 104451 9043 032110 104444 9044 032112 005726 9045 032114 000207 9046 9047 9048	25\$: DEC BNE MOV TRAP TRAP TRAP TRAP TRAP TRAP	31	•	5079
9044 032112 005726 9045 032114 000207	26\$: TST RTS	(SP)+ PC		4955
9047 9048		244 words depth per invocation: 28 words		

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 224

9057
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9066 032116 004767 177024 18:: JSR PC.\$T8
9068 032122 104466
9069 032124 006000 8DR RO
9070 032126 103773 8L0 1\$
9071 032130 000207 : Routine Size: 6 words
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9080 : 5084 !<BLF/PAGE>

SEQ 0211

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9083 :ML4

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22-Dec-1980 09:21:22
                                                                                                                                                                                                                                                                                                                                                                                                                                            TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (59)
                                                                                                                  BGNTST;
                                                                                                                    ! TEST NUMBER: TST 9
                                                                                                                          TEST NAME:
                                                                                                                                                                                       PRINT SERIAL NUMBER
                                                                                                                   ! TEST DESCRIPTION:
                                                                                                                                                   UPON A YES RESPONSE TO THE SOFTWARE QUESTION PRINT SERIAL NO?
                                                                                        5096
5097
5098
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5100
5101
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5107
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5111
5112
5113
5114
5115
                                                                                                                                                     PRINT OUT THE DRIVE UNDER TEST
                                                                                                                                                     SERIAL NUMBER.
                                                                                                                 external
                                                                                                                                   PRSN:
                                                                                                                                                                                                                                                                                                                                    !LOCATION WHERE ANSWER TO SW QUESTION IS STORED
                                                                                                                 local
                                                                                                                                                                                                                                                                                                                                    STORES DIGIT 3 OF STORES DIGIT 2 OF STORES DIGIT 1 OF STORES DIGIT 0 OF
                                                                                                                                  D3.
                                                                                                                                                                                                                                                                                                                                                                                                                     SERIAL NUMBER
SERIAL NUMBER
SERIAL NUMBER
                                                                                                                                   D1.
                                                                                                                                   DO:
                                                                                                                                                                                                                                                                                                                                                                                                                      SERIAL NUMBER
                                                                                                                  if .PRSN IS_SET
                                                                                                                                                                                                                                                                                                                                    !SEE IF WE PRINT THE SERIAL NUMBER
                                                                                                                  then
                                                                                                                                                                                                                                                                                                                                    PRINT THE SERIAL NUMBER IF THE REPLY WAS YES LOAD DIGIT 3 OF SN INTO D3 LOAD DIGIT 2 OF SN INTO D2 LOAD DIGIT 1 OF SN INTO D1 LOAD DIGIT 0 OF SN INTO D0
                                                                                                                                  begin

D3 = .SN3;

D2 = .SN2;

D1 = .SN1;
                                                                                                                                   DO = .SNO;
                                                                                                                                   if ((.D3 gtr 9) or (.D2 gtr 9) or (.D1 gtr 9) or (.D0 gtr 9))
!SEE IF DIGITS ARE TO BIG FOR COVERSION
                                                                                                                                   then
                                                                                                                                                    PRINTB (FMT_25, PHR_15, .MLSN)
                                                                                                                                                                                                                                                                                                                                    !PRINT OCTAL SN IF TO BIG
                                                                                                                                                    PRINTB (FMT_26, PHR_15, .D3, .D2, .D1, .D0); !ELSE PRINT DECIMAL SERIAL NUMBER
                                                                                                                                   end:
                                                                                                                 ENDIST:
                                                                                                                                                                                                                           .GLOBL PRSN
```

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 226	SEQ 0213
9138 9139 9140	:ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
9144 032132 004167 151722	000001 \$T9: JSR CMP	R1.\$SAVE3 PRSN,#1	5083 5111
9145 032136 026727 150234 9146 032144 001101 9147 032146 017703 161142 9148 032152 006203 9149 032154 006203 9150 032156 006203 9151 032160 006203 9153 032164 042703 177760 9154 032174 000300 9156 032174 000300 9157 032202 117701 161106 9158 032216 042700 177760 9159 032210 006201 9160 032212 006201 9161 032214 006201 9163 032222 117702 161066 9164 032226 042701 177760 9165 032232 020327 000011 9166 032232 020327 000011 9167 032240 020027 000011 9168 032244 003006 9169 032246 020127 000011	O00001 ST9: JSR CMP BNE MOV ASR ASR ASR ASR SWAB BIC MOV SWAB BIC MOVB	4\$ aML.REG+140,R3 R3	5114
9153 032164 042703 177760 9154 032170 017700 161120 9155 032174 000300	BIC MOV SWAB	#177760,R3 ; *,D3	5115
9156 032176 042700 177760 9157 032202 117701 161106 9158 032206 006201 9159 032210 006201 9160 032212 006201	BIC MOVB ASR ASR ASR BIC MOVB BIC CMP BGT CMP BGT CMP BGT CMP BGT CMP BGT CMP	#1/7/60,R0 ; *,D2 aML.REG+140,R1 ; *,D1 R1 ; D1 R1 ; D1 R1 ; D1	5116
9162 032216 042701 177760 9163 032222 117702 161066 9164 032226 042702 177760 9165 032232 020327 000011	BIC MOVB	R1 #177760,R1 : *,D1 aML.REG+140,R2 : *,D0 #177760,R2 : *,D0	5117
9164 032226 042702 177760 9165 032232 020327 000011 9166 032236 003011 9167 032240 020027 000011 9168 032244 003006 9169 032246 020127 000011 9170 032252 003003	CMP	#177760,R1	5119
9167 032240 020027 000011 9168 032244 003006	CMP	RO.#11 ; D2.*	
9169 032246 020127 000011 9170 032252 003003	CMP BGT	R1,#11 ; D1,*	
9171 032254 020227 000011 9172 032260 003413	CMP	R2,#11 ; D0,*	
9173 032262 017746 161026 9174 032266 012746 007714 9175 032272 012746 005746 9176 032276 012746 000003		R2,#11 ; D0,* 2\$ aML.REG+140,-(SP) ; #PHR.15,-(SP) #FMT.25,-(SP) #3,-(SP) SP,R0 ; SP,* 14	5122
9177 032302 010600 9178 032304 104414	MOV TRAP	SP.RO ; SP.*	
9180 032310 010246 9181 032312 010146	2\$: MOV	R2(SP) R1(SP) R0(SP) R3(SP) D0.* D1.* D2.* D3.*	5119 5124
9174 032266 012746 007714 9175 032272 012746 005746 9176 032276 012746 000003 9177 032302 010600 9178 032304 104414 9179 032306 000416 9180 032310 010246 9181 032312 010146 9182 032314 010046 9183 032316 010346 9184 032320 012746 007714 9185 032334 012746 005764 9186 032330 012746 000006 9187 032334 010600 9188 032336 104414 9189 032340 062706 000006 9190 032344 062706 000010 9191 032350 000207	MOV MOV MOV TRAP BR 2\$: MOV MOV MOV MOV MOV MOV MOV MOV	3\$ R2,-(SP) R1,-(SP) R1,-(SP) R0,-(SP) R3,-(SP) MPHR.15,-(SP) MFMT.26,-(SP) M6,-(SP) SP,R0 14 M6,SP M10,SP PC : D0,* D1,* D2,* D3,* SP,*	
9187 032334 010600 9188 032336 104414	MOV TRAP	SP,R0 ; SP,*	
9189 032340 062706 000006 9190 032344 062706 000010 9191 032350 000207 9192	3\$: ADD 4\$: RTS	#10,SP PC :	5113 5083

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 227	SEQ 0214
9194 9195	;ML4	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
9194 9195 9196 9197 9198 9203 9204 9208	; Routine Size: 72 words ; Maximum stack depth per invocation: 11 words	
9212 032352 9213 032352 004767 177554 9214 032356 104466 9215 032360 006000	T9:: 1\$: JSR PC.\$T9 ; TRAP 66 ROR RO BLO 1\$ RTS PC	5126
9217 032364 000207 9218 9219 9220 9225 9226 9227 ; 5129 ! <blf f<="" td=""><td>; Routine Size: 6 words ; Maximum stack depth per invocation: 0 words PAGE></td><td></td></blf>	; Routine Size: 6 words ; Maximum stack depth per invocation: 0 words PAGE>	

```
9229 :ML4
9230 :
9231
9232 :
                                                                                                            TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML 4.BLI.2 (60)
                                                                                22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                        BGNTST:
                        ! TEST NUMBER: TST 10
                          TEST NAME:
                                          C-BUS PARITY TEST
                          TEST DESCRIPTION:
                                 TEST THE CONTROL BUS PARITY
                                 DETECTION AND GENERATING BY:
                                 1. WRITING BAD PARITY TO DEVICE
                                      AND TEST CPAR SET.
                                 2. WRITING GOOD PARITY TO DEVICE
                                      AND TEST CPAR CLR.
                                 3. READING DEVICE AND TEST GOOD
                                      PARITY GENERATION BY READING
                                      MCPE CLR.
                        local
                            SAVE,
TST_PAT;
                                                                                !TEMPORARY SAVE LOCATION
                                                                                !TEST PATTERN
                        if .PAR_DIS IS_NOT_SET
                                                                                !SEE IF PARITY IS DISABLED
                        then
                            begin
TST_PAT = %o'125252';
                                                                                !BEGIN IF PARITY IS ENABLE
                                                                                !ALTERNATING 1, 0 PATTERN
                             incr TWICE from 1 to 2 do
                                                                                !REPEAT LOOP TWICE
                                 begin
CLR_MBUS;
                                 PAT = ONE;
MLDA = .TST_PAT;
                                                                                !GENERATE EVEN PARITY BY SETTING THE PAT BIT
                                                                                !WRITE BAD PARITY ON CONTROL BUS
                                 if .CPAR IS_NOT_SET
                                                                                !READ CPAR BIT SET
                                 then
                                     begin
ERRDF (7, ASYNC, DUMPER);
PRINTB (THR_FMT, WRD_5, WRD_7, WRD_9);
                                                                                 !ERROR IF NOT SET
                                      end;
                                 CLR_MBUS;
MLDA = .TST_PAT;
                                                                                !CLEAR OUT PAT BIT
                                                                                !WRITE ODD PARITY CONTROL BUS
                                 if .CPAR IS_SET
                                                                                !READ CPAR BIT CLEARED
                                 then
```

MOV

5174

MLABO ML-11 LOG	and Section					SEQ 0217
9340 9341		:	ML4		22-Dec-19 22-Dec-19	980 09:24:31 TOPS 980 09:21:22 PA:
9343 032506 0 9344 032512 0 9345 032516 0	12746 006034 12746 000004		MOV	#THR.FMT,-(SP) #4,-(SP)		
9345 032516 0 9346 032520 1	10600		MOV	SP,RO	; SP,*	
9347 032522 0 9348 032526 1	62706 000012 52777 000040	160460 2	ADD	#12.SP #40.aML.REG+40		5177 5179
9349 032534 0 9350 032540 0	16700 161042 42700 177770		BIC	ML_DUT_RO #177770_RO		
9351 032544 1	62706 000012 52777 000040 16700 161042 42700 177770 42777 000007 50077 160436 10177 160422 32777 000010	160442	BICB	ML.DUT.RO #177770.RO #7, aML.REG+40 RO.aML.REG+40 R1, aML.REG+30 #10, aML.REG+60 3\$		
9354 032562 1	32777 000010	160444	MOV BITB	#10,aML.REG+60	: TST.PAT,*	5170 5180
9356 032572 1	32777 000010 01422 04455 00010 10464		BEQ TRAP .WORD .WORD .WORD MOV	35	:	5183
9358 032576 0 9359 032600 0	26032		. WORD	ASYNC DUMPER		
9355 032570 0 9356 032572 1 9357 032574 0 9358 032576 0 9359 032600 0 9360 032602 0 9361 032606 0 9362 032612 0 9363 032612 0	12746 006374 12746 006344 12746 006336 12746 006034 12746 000004		MUV	ASYNC DUMPER MURD.9,-(SP) MURD.7,-(SP) MURD.6,-(SP) MTHR.FMT,-(SP) M4,-(SP)		5184
9362 032612 0 9363 032616 0	12746 006336 12746 006034		MOV	WWRD.6,-(SP) WTHR.FMT,-(SP)		
9365 032626 0	10600		MOV MOV TRAP	SP,R0	; SP,*	
9353 032556 0 9354 032562 1 9355 032570 0 9356 032572 1 9357 032574 0 9358 032576 0 9358 032600 0 9361 032606 0 9361 032606 0 9362 032612 0 9363 032616 0 9364 032622 0 9365 032636 1 9367 032632 0 9368 032636 1 9369 032644 0	112746 000004 110600 04414 62706 000012 52777 000040 116700 160732 42700 177770	160350 3	ADD BISB	#12.SP	:	5183 5183
9368 032636 1 9369 032644 0 9370 032650 0	62706 000012 52777 000040 16700 160732 42700 177770 42777 000007 50077 160326		MOV	#40, aml. REG+40 ML.DUT, RO #177770, RO		J10.
9377 032653 1	50077 160326	160332	BICB BISB MOV	#177770.R0 #7.aML.REG+40 R0.aML.REG+40 aML.REG+30.R3 #20000.aML.REG		
9373 032666 0 9374 032672 0	17703 160312 32777 020000	160254	RIT	#20000, aml. REG	: *,SAVE	5180 5190
9376 032702 1 9377 032704 0	04455		TRAP	55		5193
9378 032706 0	10464		. WORD	ASYNC DUMPER		
9373 032666 0 9374 032672 0 9375 032700 0 9376 032702 1 9377 032704 0 9378 032706 0 9378 032710 0 9380 032712 0 9381 032716 0 9382 032726 0 9383 032726 0 9384 032726 0 9386 032736 0 9386 032736 0 9388 032746 0 9389 032750 0 9390 032752 0	17703 160312 32777 020000 01422 04455 00011 10464 24052 12746 006344 12746 006344 12746 006034 12746 006034 12746 000004 10600 04414 62706 000012 06301 05202 20227 000002 03614 00207 112746 007074 112746 006344		BEQ TRAP .WORD .WORD .WORD MOV MOV	ASYNC DUMPER MURD.8,-(SP) MURD.7,-(SP) MURD.6,-(SP) MTHR.FMT,-(SP) M4,-(SP) SP,R0		5194
9382 032722 0 9383 032726 0	12746 006336 12746 006034		MOV	MURD.6,-(SP) MTHR.FMT,-(SP)		
9385 032736 0	10600		MOV MOV TRAP	M4(SP) SP.R0	; SP,*	
9387 032742 0 9388 032746 0	62706 000012	4	ADD S: ASL	#12,SP R1	: TST.PAT	5197 5197 5164
9389 032750 0 9390 032752 0	05202 20227 000002		INC	R2 R2,#2	TWICE.*	516
9391 032756 0 9392 032760 0	03614 00207		RTS	15	;	5150
9393 032762 0 9394 032766 0	12746 007074	59	S: MOV	PC MURD.53,-(SP) MURD.7,-(SP)		5159 5202

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 231		SEQ	0218
9396 9397	ML4			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
9398 9399 032772 012746 00602 9400 032776 012746 00000 9401 033002 010600 9402 033004 104414 9403 033006 062706 00001 9404 033012 000207	MOV	#TWO.FMT,-(SP) #3,-(SP) SP,R0 14 #10,SP PC	; SP.*		5128
9404 033012 000207 9405 9406 9407 9412 9413		: 139 words k depth per invocation: 9			
9421 033014 9422 033014 004767 17734 9423 033020 104466 9424 033022 006000 9425 033024 103773 9426 033026 000207 9427	T10:: JSR TRAP ROR BLO RTS	PC .\$T10 66 RO 1\$ PC			5202
9434 9435	; Routine Size ; Maximum stack /PAGE>	: 6 words k depth per invocation: 0	words		

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9438 :ML4
9439 :
9440
9441 :
9442 :
9443 :
9444 :
                                                                                                 22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                   TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML 4.BLI.2 (61)
                              BGNTST:
TEST NUMBER: TST 11
                                TEST NAME: MEMORY SIZING TEST
                                TEST DESCRIPTION:
                                         THIS TESTS THE ML11'S SIZING
                                         LOGIC BY COMPARING THE
                                         OPERATORS INPUTED NUMBER OF ARRAYS PRESENT TO THE ML11 SIZING
                                         NUMBER OF ARRAYS PRESENT
THE DRIVE IS DROPPED ON DETECTED ERRORS.
                                IMPLICIT INPUTS:
                                        OP_NUM_ARR:
LOADED DURING INIT CODE AND
STORES OPERATORS INPUT TO THE
                                         SOFTWARE QUESTION:
NUMBER OF ARRAYS PRESENT?
                              Local
                                   DODU_FLG;
                                                                                                 !DROP UNIT FLAG
                              BGNSUB:
                              CLR_MBUS;
                             DODU_FLG = ZERO;
                              if (.OP_NUM_ARR + 1) neq .ML_NUM_ARR
                                                                                                 !SEE IF DRIVE SIZED SAME NO. OF ARRAYS AS OP INPUTED
                              then
                                   begin
ERROF (10, ASYNC, DUMPER); !IF
PRINTB (TWO_FMT, FNC_1, WRD_14);
PRINTB (FMT_2, (.OP_RUM_ARR + 1), .ML_NUM_ARR);
DODU_FLG = DNE;
                                                                                                 !IF NOT EQL THEN ERROR AND SET DODU_FLG
                                   end:
                             ENDSUB;
                             if .DODU_FLG IS_SET
                                                                                               !DROP THIS UNIT IF DODU_FLG IS_SET
                              then
                                   begin
DODU (.ML_LUN);
                                   DOCLN;
                                   end:
```

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 233 SEQ (0220
94.94 :ML4 94.95 : 94.96 94.97 : 5258 94.98 : 5259 ENDTST	22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 22-Dec-1980 09:21:22 PA: PA: NEALE>BL3PL 4.BL1.2 (61)	
9502 9506 033030 004167 151024 9507 033034 104402 9508 033036 152777 000040 9509 033044 016701 160532 9510 033050 042701 177770 9511 033054 142777 0,0007 9512 033062 150177 160126	\$T11: JSR R1,\$SAVE3 1\$: TRAP 2 160150 BISB #40,aML.REG+40 MOV ML.DUT,R1 BIC #177770,R1 BICB #7,aML.REG+40 BISB R1.aML.REG+40 CLR R2 ; DODU.FLG	5204 5235 5237
9513 033066 005002 9514 033070 016701 156464 9515 033074 005201 9516 033076 017703 160172 9517 033102 006203 9518 033104 006203 9519 033106 006203	MOV OP.NUM.ARR,R1 ;	5239 5241
9520 033110 000303 9521 033112 042703 177740 9522 033116 010100 9523 033120 020003 9524 033122 001444 9525 033124 104455 9526 033126 000012 9527 033130 010464 9528 033132 024052 9529 033134 012746 006442 9530 033140 012746 007726 9531 033144 012746 006024	CMP RO,R3 BEQ 2\$ TRAP 55 .WORD 12 .WORD ASYNC .WORD DUMPER MOV MARD.14,-(SP) ;	5244
9529 033134 012746 006442 9530 033140 012746 007726 9531 033144 012746 006024 9532 033150 012746 000003 9533 033154 010600	MORD DUMPER MOV	5245
9534 033156 104414 9535 033160 017703 160110 9536 033164 006203 9537 033166 006203 9538 033170 006203	MOV SP,RO ; SP,* TRAP 14 MOV aML.REG+120,R3 ; ASR R3 ASR R3 ASR R3 SWAB R3 BIC #177740,R3 MOV R3,(SP) MOV OP.NUM.ARR,-(SP) INC (SP)	5246
9532 033150 012746 000003 9533 033154 010600 9534 033156 104414 9535 033160 017703 160110 9536 033164 006203 9537 033166 646203 9538 033170 006203 9539 033172 000303 9540 033174 042703 177740 9541 033200 010316 9542 033202 016746 156352 9543 033206 005216 9544 033210 012746 004266 9545 033214 012746 004266 9547 033222 104414	MOV aML.REG+120,R3 ASR R3 ASR R3 ASR R3 SIMAB R3 BIC #177740,R3 BI	

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 234	SEQ 0221
9549 9550	;ML4	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
9552 033224 012702 00000 9553 033230 062706 00001 9554 033234 104467 9555 033236 006000	MOV #1,R2 ADD #16,SP TRAP 67 ROR RO BLO 1\$ DEC R2 BNE 3\$ MOV ML.LUN,RO	; *,DODU.FLG 5247 5243 ; 5248
9556 033240 103675 9557 033242 005302 9558 033344 001004	BLO 15 DEC R2 DAG 38	; DODU.FLG 5252
9559 033246 016700 16032 9560 033252 104451	IRAP 31	: 5255
9561 033254 104444 9562 033256 000207	38: RTS PC	5204
9561 033254 104444 9562 033256 000207 9563 9564 9565 9570	; Routine Size: 76 words ; Maximum stack depth per invocation:	11 words
9575 9579 033260 9580 033260 004767 17754 9581 033264 104466 9582 033266 006000 9583 033270 103773 9584 033272 000207 9585 9586 9587 9592	T11:: JSR PC,\$T11 TRAP 66 ROR RO BLO 1\$ RTS PC	5257
9593	; Routine Size: 6 words ; Maximum stack depth per invocation: /PAGE>	0 words

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22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BL1.2 (62)
                     BGNTST:
                              ! TEST NUMBER: TST 12
                              ! TEST NAME:
                                                    NO-OP FUNCTION TEST
                              ! TEST DESCRIPTION:
                                        TEST IF THE DRIVE CAN PERFORM A NO OP FUNCTION WITH OUT HANGING THE DRIVE.
                                         A NO_OP FUNCTION IS WRITTEN
                                        INTO MLCS1.
THEN GO AND ERROR BITS ARE CHECKED FOR CORRECT STATES.
                                         THIS UNIT IS DROPPED ON DETECTED ERRORS.
                              ! IMPLICIT INPUTS:
                                                               NONE
                              local
                               DODU_FLG;
                                                                                                !DROP UNIT FLAG
                             BGNSUB;
                              CLR MBUS;
DODO_FLG = ZERO;
                             MLCST = NOOP:
                                                                                                !DO A NOOP FUNCTION !DELAY 1 US
                              DELAY (ONE_US);
                              if .GO IS_SET
                                                                                                 !SEE IF GO STILL SET
                              then
                                  begin
ERRDF (11, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_1, PHR_2, WRD_11, FNC_2, WRD_19);
DODU_FLG = ONE;
!ERROR AND SET DODU_FLG IF STILL SET
                             if .ILF IS_SET then
                                                                                                !SEE ILF SET
                                   begin
ERROF (12, ASYNC, DUMPER); !ERROR AND
PRINTB (FIV_FMT, WRD_3, PHR_5, WRD_11, FNC_2, WRD_19);
                                                                                                !ERROR AND SET DODU_FLG IF SET
                                   DODU_FLG = ONE;
                                    end:
```

1120000	744.000	CODING SE									SEA OFFS	1
9652 9653 9654	PL4							22-Dec-1980 09:24 22-Dec-1980 09:21	:31 TO	PS-20 Bliss-16 V2(212) .2 (62)	
9655 9656 9657		5313 5314 5315	if .OPI	IS_SET				SEE IF CPI SET				
9658 9659 9660 9661 9662		5316 5317 5318 5319 5320	hea	in DF (13, NTB (FI) U_FLG = I;	ASYNC, FMT, W ONE;	DUMPER); IRD_4, PH	R_5, WRD_11, FNC_	!ERROR AND SET DOE 2. WRD_19);	DU_FLG IF	SET		
9664		5322	ENDSUB;									
9665 9666 9667		5323 5324 5325	if .DOD	U_FLG IS	S_SET			!DROP THIS UNIT I	F DODU_FL	G IS_SET		
9669 9670 9671		5326 5327 5328 5329	DOD DOC end	in U (.ML_L LN; I;	LUN);							
9673		5331	ENDTST;									
9677 9681 9682	033274	004167 005746	150544		\$T12:	JSR TST	R1,\$SAVE2 -(SP)	;			525	
9683 9684 9685	033304 033304 033304 033316 033336 033336 033336 033336	104402 152777 016701	000040 160264	157702	15:	JSR TST TRAP BISB MOV BIC BICB BISB CLR MOV MOV BEQ MOV	#40,aML.REG+40	:			529 529	NO
9687 9688	02222 02225 02225	142777	000007 157660	157664		BICB	#177770,R1 #7,aML.REG+40 R1,aML.REG+40		20011 51 6			
9690 9691	03334	012777	000001 000001	157610	20.	MOV	R2 #1,aML.REG #1,R0 5\$ L\$DLY,R1		*,SSTMP2		529 529 529	900
9693 9694	033352	016701	146540		2\$:	MOV	LSDLY,R1	:	*,SSTMP1			1
9695 9696	033356 033366 033366 033376 033376 033406 033406 033406 033406	005016 005301			3\$:	CLR DEC	(SP) R1	:	SSTMP1			
9698	033366	005300			45:	DEC	RO	:	SSTMP2			1
9700	03337	132777	000001	157554	5\$:	BITB	M1. AML. REG	:			529	8
9702 9703	033402	005301 001375 005300 000767 132777 001430 104455 000013 010464 024052				BNE DEC BR BITB BEQ TRAP .WORD .WORD	RO 28 W1, aml.REG 68 55 13 ASYNC DUMPER	;			530	n
9705	033410	024052		H		. WORD	DUMPER					

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 237	SEQ 0224
9707 9708	ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
9710 033412 012746 006506 9711 033416 012746 007742 9712 033422 012746 006416 9713 033426 012746 007404 9714 033432 012746 006276 9715 033436 012746 006062	MOV MOV MOV MOV MOV MOV	#WRD.19,-(SP) #FNC.2,-(SP) #WRD.11,-(SP) #PHR.2,-(SP) #WRD.1,-(SP) #FIV.FMT,-(SP) #6,-(SP)	5302
9717 033446 010600 9718 033450 104414 9719 033452 012702 000001 9720 033456 062706 000016 9721 033462 132777 000001	MOV TRAP MOV ADD	3P. NO : SP	,* DODU.FLG 5303 5300 5306
9722 033470 001430 9723 033472 104455 9724 033474 000014 9725 033476 010464	BEQ TRAP .WORD .WORD	75 55 14 ASYNC	5309
9726 033500 024052 9727 033502 012746 006506 9728 033506 012746 007742 9729 033512 012746 006416 9730 033516 012746 007472 9731 033522 012746 006314 9732 033526 012746 006062	MOV MOV MOV MOV MOV	ASYNC DUMPER NURD.19,-(SP) NFNC.2,-(SP) NURD.11,-(SP) NURD.3,-(SP) NURD.3,-(SP) NFIV.FMT,-(SP) N6,-(SP) SP,R0 ; SP	5310
9734 033536 010600 9735 033540 104414 9736 033542 012702 000001	MOV MOV TRAP MOV	19	
9737 033546 062706 000016 9738 033552 032777 020000	157454 7\$: ADD BIT	#16.SP #20000, aML.REG+60	DODU.FLG 5311 5308 5314
9740 033562 104455 9741 033564 000015 9742 033566 010464	TRAP .WORD .WORD	SS 55 15 ASYNC DUMPER	5317
9743 033570 024052 9744 033572 012746 006506 9745 033576 012746 007742 9746 033602 012746 006416 9747 033606 012746 007472	BEQ TRAP .WORD .WORD MOV MOV MOV MOV MOV TRAP MOV TRAP ROR BLO DEC BNE MOV TRAP	DUMPER MURD.19,-(SP) MFNC.2,-(SP) MURD.11,-(SP) MPHR.5,-(SP) MURD.4,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0 11, P2	5318
9751 033626 010600 9752 033630 104414	MOV	SP.RO ; SP.	
9748 033612 012746 006322 9749 033616 012746 006062 9750 033622 012746 000006 9751 033626 010600 9752 033630 104414 9753 033632 012702 000001 9754 033636 062706 000016 9755 033642 104467 9756 033644 006000 9757 033646 103615 9758 033650 005302 9759 033652 001004 9760 033654 016700 157720 9761 033660 104451	8\$: TRAP	#1,R2 #16,SP 67 R0 1\$ R2 9\$ ML.LUN,R0	5319 5316 5320
9758 033650 005302	DEC BNE	R2 9\$; DOI	DU.FLG 5324
9759 033652 001004 9760 033654 016700 157720 9761 033660 104451	TRAP	51	5327

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 238	SEQ 0225
9763 9764	:ML4	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
9765 9766 033662 104444 9767 033664 005726 9768 033666 000207	98: TRAP 44 TST (SP)+	5259
9768 033666 000207 9769 9770 9771 9776 9777 9781	; Routine Size: 126 words ; Maximum stack depth per invocation: 11 words	
9785 033670 9786 033670 004767 177400 9787 033674 104466 9788 033676 006000 9789 033700 103773	T12:: 1\$: JSR PC.\$T12 TRAP 66 ROR RO BLO 1\$ RTS PC	5329
9790 033702 000207 9791 9792 9793 9798 9799 9800 ; 5332 ! <blf f<="" td=""><td>; Routine Size: 6 words ; Maximum stack depth per invocation: 0 words PAGE></td><td></td></blf>	; Routine Size: 6 words ; Maximum stack depth per invocation: 0 words PAGE>	

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9802 :ML4
9803 :
                                                                                                22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                 TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML 4.BLI.2 (63)
                             BGNTST:
                              TEST NUMBER: TST 13
                               TEST NAME:
                                                   WRITE CHECK FUNCTION TEST
                               TEST DESCRIPTION:
                                        TEST IF THE DRIVE CAN PERFORM A WRITE CHECK FUNCTION WITHOUT HANGING THE DRIVE.
                                        A WRITE CHECK FUNCTION IS WRITTEN
                                        INTO MLCS1.
THEN GO AND ERROR BITS ARE CHECKED FOR CORRECT STATES
                            local
                                  DODU_FLG;
                                                                                                !DROP UNIT FLAG
                            BGNSUB:
                            CLR MBUS;
DODU FLG = ZERO;
FIRST_BLK_XFER ();
                                                                                                SET UP A FIRST BLK XFERR DO A WRITE CHECK FUNCTION
                            MLCS1 = WRT_CHK;
                            if .GO IS_NOT_SET
                                                                                                !SEE IF THE GO BIT GOT SET
                             then
                                  begin
ERRDF (14, ASYNC, DUMPER); !ERROR IF N
PRINTB (FIV_FMT, WRD_1, PHR_1, WRD_12, FNC_4, WRD_19);
                                                                                                !ERROR IF NOT SET
                                  if .DRY IS_NOT_SET
                                                                                                !SEE IF THE DRY IS SET WHILE GO IS CLEARED
                                  then
                                       begin
ERROF (15, ASYNC, DUMPER); !ERROR IF CLE/
PRINTB (FIV_FMT, WRD_2, PHR_1, WRD_43, WRD_1, PHR_6);
                                                                                                !ERROR IF CLEARED
                                  DODU_FLG = ONE;
                                                                                                !SET DODU_FLG
                                  end
                            else
                                  if .DRY IS_SET
                                                                                                !THE GO IS SET SO SEE IF DRY IS CLEARED
                                       begin
ERRDF (16, ASYNC, DUMPER);
                                                                                              !ERROR IF DRY IS SET
```

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22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 
22-Dec-1980 09:21:22 PA:<NEALE>BL3ML4.BLI.2 (63)
          PRINTB (FIV_FMT, WRD_2, PHR_2, WRD_43, WRD_1, PHR_5);
          DODU_FLG = ONE;
if .ILF IS_SET
                                                          !SEE IF ILF IS SET
 then
     begin
ERRDF (17, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_3, PHR_5, WRD_12, FNC_4, WRD_19);
DODU_FLG = ONE;
                                                           !ERROR IF SET
if .OPI IS_SET
                                                          !SEE IF OPI IS SET
then
     begin
ERRDF (18, ASYNC, DUMPER); !ERROR IF !
PRINTB (FIV_FMT, WRD_4, PHR_5, WRD_12, FNC_4, WRD_19);
DODU_FLG = ONE;
                                                          !ERROR IF SET
DELAY (FRTY_US);
                                                          !WAIT FO XFERR TO COMPLTE
if .DRY IS_NOT_SET
                                                          !SEE IF DRY IS SETS AFTER XFERR
then
     begin
     if .GO IS_SET
                                                         !IF DRY IS NOT SET THEN SEE IF GO IS SET
     then
         begin
CLR_MBUS;
                                                          !IF THE GO IS SET THEN TRY TO CLR GO
         if .GO IS_SET then ERRDF (19, ASYNC, DUMPER) else ERRDF (20, SYNC, DUMPER);
          PRINTB (FIV_FMT, WRD_1, PHR_2, WRD_11, FNC_4, WRD_19); !ELSE SYNC MODLE FAILURE
          end
     else
          begin
ERROF (21, ASYNC, DUMPER); !ERROR GO AND I
PRINTB (FIV_FMT, WRD_2, PHR_1, WRD_11, FNC_4, WRD_19);
                                                           !ERPOR GO AND DRY BOTH CLEARED
     DODU_FLG = ONE;
                                                          !SET THE DODU_FLG
     end;
if .GO IS_SET
                                                          !SEE IF THE GO IS STILL SET
then
     CLR MBUS:
                                                          !TRY TO CLR GO IF STILL SET
     if .GO IS_SET then ERRDF (22, ASYNC, DUMPER) else ERRDF (23, SYNC, DUMPER);
```

HISCELLANEOUS	conting 20	CITON								2EM 0550
9914 :ML4 9915 :							22-Dec-1980 09:2 22-Dec-1980 09:2		TOPS-20 BLiss-16 V2(212) PA: <neale>BL3ML 4.BLI.2 (63)</neale>	
9917 : 9918 : 9919 : 9920 : 9921 :	5437 5438 5439 5440 5441	PRI DOD end	:	FMT, W	IRD_1, PHI	R_2, WRD_11, FNC_	!IF GO IS STILL 4, WRD_19);	SET THE	ASYNC FAILURE YNC MODULE FAILURE	
9916 9917 9918 9919 9920 9921 9921 9923 9924 9925 9926 9927 9928 9929 9930	5440 5442 5443 5444 5445 5446 5447 5450 5450	if .DOD	U_FLG IS U (.ML_L LN;	S_SET			!DROP THIS UNIT	IF DODU	_FLG IS_SET	
9939 03370	4 004167 10 005746	150134		\$T13:	JSR TST	R1,\$SAVE2 -(SP)		:		5331
9939 0337 9940 0337 9941 0337 9942 0337 9943 0337 9944 0337 9946 0337 9946 0337 9948 0337 9949 0337 9950 0337 9951 0337 9952 0337	12 104402 14 152777 22 016701 26 042701 27 142777	000040 157654 177770 000007 157250	157272 157254	15:	TRAP BISB MOV BIC BICB BISB	2 #40.aML.REG+40 ML.DUT.R1 #177770.R1		;		5356 5358
9946 03374 9947 03374	60 150177	157250	137234		BISB	#7.aML.REG+40 R1.aML.REG+40 R2		; DODU.	FIG	5340
9948 0337 9949 0337 9950 0337 9951 0337	66 004767 52 012777	162066 000051 000001	157174 157166		JSR MOV BITB	PC.FIRST.BLK.XF #51,aML.REG #1.aML.REG	ER	;		5360 5361 5362 5364
9953 0337	70 104455				CLR JSR MOV BITB BNE TRAP . WORD	2\$ 55 16 ASYNC		:		5367
9955 0337 9956 0340 9957 0340 9958 0340 9959 0340 9961 0340 9963 0340 9963 0340 9964 0340 9966 0340 9966 0340	76 024052 00 012746 04 012746 10 012746 14 012746 20 012746 24 012746 30 012746	006506 007756 006426 007366 006276 006062 000006			MOV MOV MOV MOV MOV MOV MOV	DUMPER MWRD.19,-(SP) MFNC.4,-(SP) MWRD.12,-(SP) MPHR.1,-(SP) MWRD.1,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0 14		:		5368
9963 0340 9964 0340	4 010600 6 104414	***************************************			MOV	SP RO		; SP,*		
9965 0340 9966 0340	94 010600 96 104414 90 132777 96 001056 90 104455	000200	157156		MOV TRAP BITB BNE TRAP	WZUU, CTL. KEG+DU		:		5370
9967 0340	50 104455				TRAP	3 \$ 55		:		5373

	OGIC TES			:ML4					0229
9969 9970 9971				; ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
9972 034052 9973 034054	000017				. WORD . WORD . WORD	17 ASYNC			
9974 034056	024052	007504			MORD	DUMPER			5374
975 034060 976 034064 977 034070	012746	006276			MOV	MMRD.1,-(SP) MMRD.43,-(SP)			
978 034074 979 034100 980 034104 981 034110 982 034114 983 034126 985 034124 986 034124 986 034134 989 034136 991 034140 991 034140 991 034140 992 034144 993 034160 996 034164 997 034164	012746	007366			MOV	#WRD.1,-(SP) #WRD.43,-(SP) #PHR.1,-(SP) #WRD.2,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0			
80 034104 81 034110	012746	000006			MOV	#FIV.FMT,-(SP) #6,-(SP)			
82 034114 83 034116	104414				MOV	17	; SP,*		
85 034124	062706	000016			ADD BR	#16,SP 3\$:		5372 5377 5381
7 034132	105777	157072		2\$:	TSTB BPL	aML.REG+50 4\$			
84 034120 85 034124 86 034126 87 034132 88 034134 89 034136 90 034140 91 034142	000020				TRAP .WORD .WORD .WORD	4\$ 55 20 ASYNC			5384
7 034142	024052	007472			. WORD	DUPPER			£70£
034150 034154 034160	012746	007472			MOV MOV	#PHR.5,-(SP) #MRD.1,-(SP)	•		5385
034160	012746	007404			MOV	#PHR.2,-(SP)			
034170	012746	006062			MOV	#WRD.1,-(SP) #WRD.43,-(SP) #PHR.2,-(SP) #WRD.2,-(SP) #FIV.FMT,-(SP) #6,-(SP)			
034200	010600				MOV	SP.RO	; SP,*		
034204 034210	104414 012702 062706	000001 000016 000001		3\$:	MOV ADD BITB	14 #1,R2 #16,SP #1,aML.REG+60 5\$ 55 21 ASYNC DUMPER #URD.19,-(SP) #ENC.4(SP)	: *,DODU.FLG		5386 5383 5389
034214 034222	132777 001430 104455 000021 010464	000001	157012	45:	BITB	#1.aML.REG+60 5\$			
034224	104455				BEQ TRAP .WORD .WORD	55 21			5392
8 034232	024052	00/50/			. WORD	ASYNC DUMPER			
0 034540	012746	006506			MOV MOV MOV	##RD.19,-(SP) #FNC.4,-(SP)	•		5393
2 034250	012746	007472			MOV	#PHR.5,-(SP)			
03 034214 04 034222 05 034224 07 034230 08 034232 09 034234 11 034244 112 034254 114 034260 115 034274 116 034272 118 034274 119 034304 121 034314 122 034314	012746 012746 012746 012746 012746 012746 010600 104414 012702 062706 032777 001430 104455 000022	006506 007756 006426 007472 006314 006062 000006			MOV MOV MOV MOV TRAP MOV ADD BIT BEQ TRAP . WORD	#FNC.4,-(SP) #WRD.12,-(SP) #PHR.5,-(SP) #WRD.3,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14			
16 034270	010600	00000			MOV	SP.R0	; SP,*		
18 034274	012702	000001 000016 020000			MOV	#1.R2 #16.SP #20000,aML.REG+60	: *,DODU.FLG		5394
20 034304	032777	020000	156722	5\$:	BIT	#20000, aML.REG+60	1		5394 5391 5397
000 034204 001 034204 002 034210 003 034214 004 034222 005 034224 006 034236 007 034230 008 034234 010 034240 011 034244 012 034250 013 034254 014 034264 015 034264 016 034270 017 034272 018 034272 019 034304 021 034314	104455				TRAP	6 \$ 55 22	:		5400

10025 10026 10027				ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
10028 034320 10029 034322 10030 034324 10031 034330 10032 034334 10033 034340 10036 034354 10036 034354 10037 034360 10038 034364 10040 034370 10041 034374 10042 034400 10043 034402 10046 034412 10047 034414 10048 034416 10049 034420 10050 034422 10051 034430 10052 034432 10053 034440	010464 024052 012746 012746 012746 012746	006506 007756 006426 007472 006322			MORD MOV MOV MOV MOV MOV	ASYNC DUMPER MURD.19,-(SP) MFNC.4,-(SP) MURD.12,-(SP) MPHR.5,-(SP) MURD.4,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0 14			5401
10035 034350 10036 034354 10037 034360	012746 012746 010600	000006			MOV MOV TRAP	#FIV.FMT,-(SP) #6,-(SP) SP.RO	; SP.*		
10036 034354 10037 034360 10038 034362 10039 034364 10040 034370 10041 034374 10042 034400 10043 034406 10045 034410	012702 062706 012700	000001 000016 000050			MOV	#1 .R2 #16.SP	. *,DODU.FLG		5402 5399 5405
10041 034374 10042 034400	012700 001410 016701			6\$: 7\$:	MOV ADD MOV BEQ MOV BEQ CLR DEC BNE DEC BR BITB	#1.R2 #16.SP #50.R0 10\$; *,\$\$TMP2		5405
10043 034402	016701	145510		00.	MOV	LSDLY,R1 9S	; *,\$\$TMP1		
10046 034412	005301			8\$:	DEC	(SP) R1	; SSTMP1		
10046 034412 10047 034414 10048 034416 10049 034420	005300			9\$:	DEC	R1 8\$ R0 7\$; \$\$TMP2		
10050 034422 10051 034430	132777 001106 132777 001452 152777 016701	000200	156574	10\$:	BITB	#200,aML.REG+50 15\$:		5407
10052 034432	001452	000001	156514		BNE BITB BEQ BISB	#1, a4L.REG	•		5411
10055 034450	016701 042701	157126	156544			#40, aml. REG+40 ML. DUT, R1 #177770, R1	•		5413
10056 034454 10057 034460 10058 034466	142777 150177	000040 157126 177770 000007 156522 000001	156526		BICB	#7.aML.REG+40 R1.aML.REG+40			
10059 034472 10060 034500 10061 034502 10062 034504 10063 034506 10064 034510 10065 034512	142777 150177 132777 001405 104455 000023 010464 024052 000404	000001	156454		BIC BICB BISB BITB BEQ TRAP .WORD .WORD	#1.aml.REG 11\$ 55 23 ASYNC DUMPER 12\$			5416
10066 034514 10067 034516 10068 034520	104455 000024 010526			11\$:	TRAP . WORD . WORD	55 24 SYNC			
10059 034472 10060 034500 10061 034502 10062 034504 10063 034506 10064 034510 10065 034512 10066 034514 10067 034516 10069 034522 10070 034524 10071 034530 10072 034534 10073 034540 10074 034544 10075 034560 10076 034560 10078 034560	024052 012746 012746 012746 012746 012746 012746 012746 010600 104414 000424	006506 007756 006416 007404 006276 006062 000006		128:	BR TRAP .WORD .WORD .WORD MOV MOV MOV MOV MOV MOV TRAP	#177770,R1 #7,aML.REG+40 #1,aML.REG #1.aML.REG 11\$ 55 23 ASYNC DUMPER #WRD.19,-(SP) #FNC.4,-(SP) #WRD.11,-(SP) #WRD.1,-(SP) #WRD.1,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14\$			5419
10077 034560	010600	000006			MOV TRAP	SP, RO	; SP,*		
10079 034564	000424				BR	148	;		5411

MLABO ML-11 LI SCELLANEOUS CI				.00 /					0231
10081 10082 10083				ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
10084 034566 10085 034570 10086 034572 10087 034574 10088 034576 10089 034602	104455 000025 010464			13\$:	TRAP .WORD .WORD .WORD MOV	55 25 ASYNC	•		5423
10084 034566 10085 034570 10086 034572 10087 034574 10088 034576 10089 034602 10090 034606 10091 034612 10092 034616 10093 034622	012746 012746 012746 012746 012746 012746	006506 007756 006416 007366 006302 006062 000006			MOV MOV MOV MOV MOV MOV	DUMPER #WRD.19,-(SP) #FNC.4,-(SP) #WRD.11,-(SP) #PHR.1,-(SP) #WRD.2,-(SP) #FIV.FMT,-(SP)			5424
10095 034632	010600	00000			MOV TRAP	SP,RO	; SP.*		
10097 034636	012702	000001 000016 000001		145:	MOV	#1.R2 #16.SP	: *,DODU.FLG		5427
10098 034642 10099 034646 10100 034654	132777 001455	000001	156300	15\$:	BITB	#1, #4L.REG			5427 5409 5430
10101 034656 10102 034664 10103 034670 10104 034674	152777 016701 042701 142777	000040 156712 177770 000007 156306 000001	156330 156312		BEQ BISB MOV BIC BICB	#40,aML.REG+40 ML.DUT.R1 #177770,R1 #7,aML.REG+40 R1,aML.REG+40	•		5432
0105 034702 0106 034706 10107 034714	152777 016701 042701 142777 150177 132777 001405 104455	156306 000001	156240		BICB BISB BITB BEQ TRAP .WORD .WORD	R1, aML.REG+40 #1, aML.REG 16\$ 55 26 ASYNC	:		5435
0110 034722 10111 034724 10112 034726	010464 024052 000404 104455 000027 010526			16\$:	. WORD . WORD BR TRAP . WORD . WORD . WORD . WORD	DUTER			
0114 034732 0115 034734 0116 034736 0117 034740 0118 034744 0119 034750 0120 034754 0121 034760 0122 034764 0123 034776 0124 034776 0125 035000 0127 035004 0128 035010 0129 035012 0130 035012 0131 035022 0133 035024 0134 035026	012746 012746 012746 012746 012746 012746 012746	006506 007756 006416 007404 006276 006062 000006		17\$:	MOV MOV MOV	175 55 27 SYNC DUMPER MURD.19,-(SP) MFNC.4,-(SP) MURD.11,-(SP) MPHR.2,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0 14 M1.R2			5438
10124 034774	010600				MOV	SP.RO	; SP,*		
10125 034776 10126 035000 10127 035004 10128 035010 10129 035012 10130 035014	012746 010600 104414 012702 062706 104467 006000 103002 000167 005302 001004 016700 104451	000001 000016		18\$:	MOV TRAP MOV ADD TRAP ROR BHIS JMP DEC BNE MOV TRAP	#1,R2 #16,SP 67 R0 19\$ 1\$ R2 20\$ ML.LUN,R0	; *,DODU.FLG		5439 5432 5440
10130 035014 10131 035016	000167	176670			JMP	198 18			
10132 035022 10133 035024 10134 035026 10135 035032	005302			19\$:	DEC	R2 20\$; DODU.FLG		5444
10134 035026 10135 035032	016700	156546			MOV	ML.LUN,RO	:		5447

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 2452		SEQ	0232
10137 10138	ML4			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
10140 035034 104444 10141 035036 005726 10142 035040 000207	208: TRAP TST RTS	44 (SP)+ PC			5331
10144 10145 10150 10151	; Routine Size ; Maximum stack	: 303 words k depth per invocation:	18 words		
10155 10159 035042 10160 035042 004767 17663 10161 035046 104466 10162 035050 006000 10163 035052 103773 10164 035054 000207	T13:: JSR TRAP ROR BLO RTS	PC,\$T13 66 R0 1\$ PC			5449
10165 10166 10167 10172 10173 10174; 5452 ! <bl< td=""><td></td><td>: 6 words k depth per invocation:</td><td>0 words</td><td></td><td></td></bl<>		: 6 words k depth per invocation:	0 words		

```
10176 :ML4
10177 :
10178
                                                                                                    22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                      TOPS-20 Bliss-16 V2(212)
                                                                                                                                      PA: <NEALE>BL3ML4.BL1.2 (64)
10179 :
10180 ;
                               BGNTST:
10181 :
10182
10183
10184
10185
10186
                                  TEST NUMBER: TST 14
                                  TEST NAME: WRITE FUNCTION TEST
10187 :
                                  TEST DESCRIPTION:
                                          TEST IF THE DRIVE CAN PERFORM A WRITE FUNCTION WITHOUT HANGING THE DRIVE.
10190 :
10191 :
                                          A WRITE FUNCTION IS WRITTEN INTO MLCS1. THEN GO AND ERROR BITS ARE CHECKED FOR CORRECT STATUS. THIS UNIT IS DROPPED ON DETECTED ERRORS.
10192
10193
10194
10195
                               local
10196 :
10197 :
                                     DODU_FLG;
                                                                                                    !DROP UNIT FLAG
10198
10199
10200
10201
10202
10203
                               BGNSUB:
                               CLR MBUS;
DODU FLG = ZERO;
FIRST_BLK_XFER ();
                                                                                                    SET UP A FIRST BLOCK XFERR
                               MLCS1 = write:
                                                                                                    !DO A WRITE FUNCTION
                               if .GO IS_NOT_SET
                                                                                                   !SEE IF THE GO IS SET
                               then
10205
10206
10207
10208
10209
10210
10211
10212
10213
                                    begin
ERRDF (24, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_1, PHR_1, WRD_12, FNC_5, WRD_19);
                                                                                                    !ERROR IF NOT SET
                                     if .DRY IS_NOT_SET
                                                                                                    !SEE IF DRY SET WITH GO CLEAR
                                     then
                                          begin
ERRDF (25, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_2, PHR_1, WRD_43, WRD_1, PHR_6);
                                     DODU_FLG = ONE;
                                                                                                   !SET DODU_FLG
                                     end
                               else
                                                                                                   !GO IS SET DURING FUNCTION
                                     if .DRY IS_SET
                                                                                                   !SEE IF DRY CLEAR WITH GO SET
                                          begin !ERROR IF SET
ERROF (26, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_2, PHR_2, WRD_43, WRD_1, PHR_5);
DODU_FLG = ONE;
                                          end:
                               if .ILF IS_SET
                                                                                                 !DID FUNCTION CAUSE ILF
```

```
MISCELLANEOUS CODING SECTION
                                                                                                                                                                                                                    22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                                                                                                                                         TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BL1.2 (64)
      10232 :ML4
10233 :
                                                                                 begin !ERROR IF 
                                                                                                                                                                                                                    !ERROR IF YES
                                                                                  DODU_FLG = ONE:
                                                                                 end:
                                                                     if .OPI IS_SET
                                                                                                                                                                                                                    !DID FUNCTION CAUSE OPI
                                                                      then
                                                                                 begin !ERROR IF \
ERROF (28, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_4, PHR_5, WRD_12, FNC_5, WRD_19);
DODU_FLG = ONE;
                                                                                                                                                                                                                    !ERROR IF YES
                                                                     DELAY (FRTY_US);
                                                                                                                                                                                                                   !WAIT FOR XFERR TO COMPLETE
                                                                      if .DRY IS_NOT_SET
                                                                                                                                                                                                                 SEE IF DRY CLEARED AFTER XFERR
                                                                      then
                                                                                  begin
                                                                                  if .GO IS_SET
                                                                                                                                                                                                               !TST GO CLR IF DRY NOT SET
                                                                                 then
                                                                                              CLR MBUS:
                                                                                                                                                                                                                   !CLEAR GO IF STILL SET
                                                                                            if .GO IS_SET then ERRDF (29, ASYNC, DUMPER) else ERRDF (30, SYNC, DUMPER);
                                                                                                                                                                                                                    !TST GO TO DETERMINE FAILING MOD
                                                                                            PRINTB (FIV_FMT, WRD_1, PHR_2, WRD_11, FNC_5, WRD_19);
                                                                                             end
                                                                                 else
                                                                                                                                                                                                                     DRY NOT SET AND GO CLEARED
                                                                                             begin
ERRDF (31, ASYNC, DUMPER);
                                                                                                                                                                                                                     !REPORT ERROR
                                                                                             PRINTB (FIV_FMT, WRD_2, PHR_1, WRD_11, FNC_5, WRD_19);
                                                                                 DODU_FLG = ONE;
                                                                                                                                                                                                                   !SET DODU_FLG
                                                                                 end:
                                                                     if .GO IS_SET
                                                                                                                                                                                                                  !SEE IF GO CLEARED AFTER XFERR.
                                                                      then
                                                                                 begin
CLR_MBUS;
                                                                                                                                                                                                              !CLEAR GO IF STILL SET
                                                                                 if .GO IS_SET then ERRDF (32, ASYNC, DUMPER) else ERRDF (33, SYNC, DUMPER);
                                                                                                                                                                                                                    !TST GO TO DETERMINE FAILING MOD
                                                                                 PRINTB (FIV_FMT, WRD_1, PHR_2, WRD_11, FNC_5, WRD_19);
                                                                                 DODU_FLG = ONE;
                                                                                  end:
                                                                     ENDSUB:
```

000200 156004

5484

```
22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                       TOPS-20 BL1ss-16 V2(212)
PA: <NEALE>BL3ML 4.BLI.2 (64)
if .TRE IS_SET
                                                                               !SEE IF XFERR CAUSED A TRANSFER ERROR
then
      begin !REPORT ERROR IF SET

ERROF (115, INTER, DUMPER);

PRINTB (SIX_FMT, WRD_61, WRD_20, PHR_5, WRD_12, FNC_5, WRD_19);

DODU_FLG = ONE;
                                                                                !REPORT ERROR IF SET AND CONTINUE TESTING
       end:
if .DODU_FLG IS_SET
                                                             !DROP THIS UNIT IF DODU_FLG SET
      begin
DODU (.ML_LUN);
    DOCLN;
       end:
ENDTST:
                                                     R1,$SAVE2
-(SP)
                          $T14:
146762
                                                                                                                                                                            5451
                                         TST
                                        TRAP
000040 156120
156502
177770
000007 156102
156076
                                                     #40,aML.REG+40
ML.DUT.R1
#177770,R1
#7,aML.REG+40
                                        BISB
                                        MOV
                                        BICB
                                        BISB
                                                     R1. aML. REG+40
                                                    R2
PC.FIRST.BLK.XFER
#61.aML.REG
                                        CLR
                                                                                                             DODU.FLG
160714
000061
000001
                                         JSR
            156022
156014
                                        MOV
                                        BITB
                                        BNE
                                                                                                                                                                            5481
                                       . WORD
. WORD
. WORD
MOV
MOV
                                                     ASYNC
                                                     DUMPER
                                                     DUMPER
#MRD.19,-(SP)
#FNC.5,-(SP)
#MRD.12,-(SP)
#PHR.1,-(SP)
#MRD.1,-(SP)
#FIV.FMT,-(SP)
SP,R0
14
                                                                                                                                                                            5482
                                        MOV
MOV
MOV
MOV
                                        MOV
                                                                                                          : SP, *
```

#200, aML. REG+50

ILABO ML-11 LI CCELLANEOUS C									EQ 0236
0344				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
0346 035220 0347 035222 0348 035224 0349 035226	001056 104455 000031 010464				BNE TRAP . WORD . WORD . WORD MOV MOV	3\$ 55 31 ASYNC	;		5487
0351 03525 0352 03525 0353 03524 0354 03534	024052 012746 012746 012746 012746	007504 006276 006754 007366			MOV	DUMPER MPHR.6,-(SP) MURD.1,-(SP) MURD.43,-(SP) MPHR.1,-(SP)	•		5488
0352 035256 0357 035256 0358 035266 0359 035270	012746 012746 012746 010600 104414	006302 006062 000006			MOV MOV MOV TRAP	#FIV.FMT,-(SP) #6,-(SP) SP,R0 14	; SP,*		
0360 035272	062706	000016			ADD BR	#16,SP 3\$:		5486 5491 5495
0362 035300	100030	155720		2\$:	TSTB BPL	aml.REG+50	•	`	
365 035310	000032				.WORD	32 ASVAIC	•		5498
1367 035314 1368 035316 1369 035322 1370 035326	024052 012746 012746 012746	007472 006276 006754 007404			TRAP .WORD .WORD .WORD MOV MOV MOV	4\$ 55 32 ASYNC DUMPER MPHR.5,-(SP) MURD.1,-(SP) MURD.43,-(SP)	:		5499
1572 035336 1373 035342 1374 035346 1375 035352 1376 035354	012746 012746 012746 010600 104414	006302 006062 000006			MOV MOV MOV MOV TRAD	MPHR.2,-(SP) MURD.2,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0 14	; SP,*		
1377 035356 1378 035362	012702	000001 000016 000001		3\$:	MOV	#1.R2 #16.SP	: *,DODU.FLG		5500 5497 5503
579 035366 580 035374	062706 132777 001430 104455 000033	000001	155640	45:	BITB	#1.aML.REG+60	:		5503
SET 033376 382 035406 383 035402 384 035404	104455 000033 010464 024052				WORD	#1,R2 #16,SP #1, aML.REG+60 5\$ 55 33 ASYNC DUMPER			5506
376 035354 377 035356 378 035362 379 035366 380 035376 381 035376 382 035406 383 035402 384 035404 388 035412 388 035426 389 035426 399 035436 399 035436 399 035436 397 035464	012746 012746 012746 012746 012746 012746 012746 010600 104414 012702 062706 032777 001430	006506 007774 006426 007472 006314 006062 000006			MOV TRAP MOV ADD BITB BEQ TRAP .WORD .WORD MOV MOV MOV MOV MOV MOV MOV MOV MOV MOV	DUMPER #WRD.19,-(SP) #FNC.5,-(SP) #WRD.12,-(SP) #PHR.5,-(SP) #WRD.3,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14 #1,R2 #16,SP #20000,aML.REG+60			5507
1391 035436 1392 035442	012746	000006			MOV	#6,-(SP) SP,R0	; SP,*		
0394 035446	012702	000001 000016 020000			MOV	#1 R2	: *,DODU.FLG		5508
0396 035456 0397 035464	032777	020000	155550	5\$:	BIT	#20000, aml . REG+60			5508 5505 5511

ZMLABO ML-11 L	OUTING 25	CIIUN							0237
10399				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	PA:
10402 035466 10403 035470 10404 035472	104455 000034 010464				TRAP .WORD .WORD .WORD MOV MOV	55 34 ASYNC			5514
10405 035474 10406 035476 10407 035502 10408 035506 10409 035512 10410 035516 10411 035522	024052 012746 012746 012746 012746 012746	006506 007774 006426 007472 006322 006062			MOV MOV MOV MOV MOV MOV	DUMPER MURD.19,-(SP) MFNC.5,-(SP) MURD.12,-(SP) MPHR.5,-(SP) MURD.4,-(SP) MF [V.FMT,-(SP) M6,-(SP)	•		5515
10412 035526 10413 035532	012746 010600	000006			MOV MOV TRAP	#6,-(SP) SP,R0	; SP,*		
10414 035534	012702	000001			MOV	#1 R2	; *,DODU.FLG		5516
10417 035546	012700	000001 000016 000050		6\$: 7\$:	MOV ADD MOV BEQ MOV	#1.R2 #16.SP #50.R0 10\$	*,\$\$TMP2		5516 5513 5519
10419 035554	016701	144336		73:	MOV	LSDLY,R1	; *,\$\$TMP1		
10421 035562	005016			8\$:	CLR	(SP)	: SSTMP : SSTMP1		
10422 035564 10423 035566 10424 035570	001375			98:	BNE	R1 8\$ R0 7\$; SSTMP2		
10425 035572 10426 035574	000767	000200	155422	10\$:	BEG CLR DEC BNE DEC BR BITB	7\$ #200,aML.REG+50	;		5521
10427 035602 10428 035604	001106	000001	155342		BITB	15\$ #1,aML.REG	:		5525
10430 035614 10431 035622 10432 035626 10433 035632	001452 152777 016701 042701 142777	000040 155754 177770 000007 155350 000001	155372 155354		BEQ BISB MOV BIC BICB	135 #40,aML.REG+40 ML.DUT.R1 #177770,R1 #7,aML.REG+40 R1,aML.REG+40 #1,aML.REG	•		5527
10435 035644 10436 035652 10437 035654 10438 035656 10439 035660	142777 150177 132777 001405 104455 000035 010464	000001	155302		BICB BISB BITB BEQ TRAP .WORD .WORD .WORD .WORD .WORD .WORD MOV MOV MOV	RT. JML. REG+40 #1. JML. REG 11\$ 55 35 ASYNC DUMPER 12\$	•		5530
10440 035662 10441 035664 10442 035666 10443 035670 10444 035672	024052 000404 104455 000036 010526			11\$:	BR TRAP WORD	DUMPER 128 55 36 SYNC			
10432 035626 10433 035632 10434 035640 10435 035644 10436 035652 10437 035654 10438 035656 10439 035660 10440 035662 10441 035664 10442 035670 10444 035672 10445 035676 10446 035676 10447 035702 10448 035716 10450 035716 10451 035722 10452 035726 10453 035732	024052 012746 012746 012746 012746	006506 007774 006416 007404 006276 006062 000006		12\$:	MOV	55 36 SYNC DUMPER MURD. 19, -(SP) MFNC. 5, -(SP) MURD. 11, -(SP) MURD. 11, -(SP) MURD. 1, -(SP) MFIV. FMT, -(SP) M6, -(SP) SP, RO	:		5533
10452 035726	012746 012746 012746 010600	000006			MOV MOV MOV	#6,-(SP)	; SP,*		

10455				:ML4			22-Dec -1980 22-Dec -1980	09:24:31 TOPS 09:21:22 PA:
10456	10//1/			:	7040	.,	22-Dec-1980	09:21:22 PA:
10459 035736 10460 035740 10461 035742 10462 035744	000424 104455 000037 010464			13\$:	TRAP BR TRAP .WORD	14 14 \$ 55 37 ASYNC	;	5525 5537
0463 035746 0464 035750 0465 035754 0466 0357() 0467 035764 0468 035770	024052 012746 012746 012746 012746 012746	006506 007774 006416 007366 006302			TRAP .WORD .WORD .WORD MOV MOV MOV MOV	ASYNC DUMPER MURD.19,-(SP) MFNC.5,-(SP) MURD.11,-(SP) MPMR.1,-(SP) MURD.2,-(SP)		5530
0469 035774 0470 036000 0471 036004	012746 012746 010600	000006			MOV MOV MOV TRAP	MURD.2,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0	; SP,*	
0473 036010 0474 036014	012702 062706	000001 000016 000001	155134	145:	ADD	#1.R2 #16.SP	*,DODU.FLG	554° 552° 554
0476 036026	001455		155126	15\$:	BEQ	#1. AML.REG 18\$		
0478 036036 0479 036042 0480 036046	016701 042701 142777	000040 155540 177770 000007 155134 000001	155156 155140		BISB MOV BIC BICB	#40,aML.REG+40 ML.DUT.R1 #177770.R1 #7,aML.REG+40		5546
0482 036060 0483 036066 0484 036070	150177 132777 001405 104455	000001	155066		BICB BISB BITB BEQ TRAP	#7.aML.REG+40 R1.aML.REG+40 #1.aML.REG 16\$	•	5549
0486 036074 0487 036076 0488 036100	010464 024052 000404				BEQ TRAP .WORD .WORD .WORD BR	ASYNC DUMPER 178		
0489 036102 0490 036104 0491 036106	000041 010526			16\$:	. WORD	55 41 SYNC		
0488 036100 0489 036102 0490 036104 0491 036106 0492 036110 0493 036112 0494 036116 0495 036122 0496 036123 0498 036132 0498 036132 0498 036136 0501 036130 0502 036150 0502 036152 0503 036164 0504 036162 0505 036164 0506 036164 0507 036170 0508 036174 0509 036202	024052 012746 012746 012746 012746	006506 007774 006416 007404 006276 006062 000006		17\$:	BR TRAP .WORD .WORD .WORD MOV MOV MOV MOV MOV TRAP MOV ADD TRAP ROR BHIS JMP BIT BEQ	16\$ 55 40 ASYNC DUMPER 17\$ 55 41 SYNC DUMPER MARD.19,-(SP) MFNC.5,-(SP) MFNC.5,-(SP) MPHR.2,-(SP) MPHR.2,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0 14 M1,R2		5552
10497 036136 10498 036136 10499 036142	012746 012746	006062			MOV MOV	MRD.1,-(SP) MFIV.FMT,-(SP) M6,-(SP)		
10500 036146 10501 036150	010500 104414				TRAP	SP.R0	; SP,*	
0502 036152 10503 036156 10504 036162 10505 036164	012702 062706 104467 006000	000001 000016		18\$:	MOV ADD TRAP ROR	#1.R2 #16.SP 67 R0 198	: *,DODU.FLG	5553 5544 5554
0506 036166 0507 036170	103002	176670			BHIS	198 18		
0508 036174 10509 036202	000167 032777 001432	176670 040000	154752	19\$:	BIT	#40000, aML.REG	:	55

ZMLABO ML-11 L IISCELLANEOUS C	ON THAT SE	CITON							EQ 0239
10511 10512 10513			ML4					22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	PA:
10514 036204 10515 036206 10516 036210	104455 000163 010672			TRAP .WORD .WORD .WORD MOV MOV	55 163 INTER DUMPER		•		556
10518 036214 10519 036220 10520 036224 10521 036230 10522 036234 10523 036240 10524 036244	012746 012746 012746 012746 012746 012746 012746	006506 007774 006426 007472 006514 007174 006100		MOV MOV MOV MOV MOV MOV MOV MOV	#WRD.19,-(SP) #FNC.5,-(SP) #WRD.12,-(SP) #PHR.5,-(SP) #WRD.20,-(SP) #WRD.61,-(SP) #SIX.FMT,-(SP) #7,-(SP)				556
10526 036254	010600	000007		MOV	SP .RO		: SP,*		
10528 036260	012702	000001 000020		MOV TRAP MOV ADD DEC BNE	#1 R2		. *,DODU.FLG		556
10529 036264	062/06	000020	20\$:	DEC	#1,R2 #20,SP R2 21\$		DODU.FLG		556. 556. 556.
10531 036272 10532 036274 10533 036300 10534 036302	001004 016700 104451 104444	155300		BNE MOV TRAP TRAP	ML.LUN,RO 51		:		556
10535 036304 10536 036306 10537	005726 000207		21\$:	TST	(SP)+ PC				545
10538 10539 10544 10545			; Routi ; Maxim	ne Size: um stack	333 words depth per invocation:	18 words			
10549 10553 036310 10554 036310 10555 036314 10556 036316 10557 036320 10558 036322 10559 10560 10561	004767 104466 006000 103773 000207	176542	T14:: 1\$:	JSR TRAP ROR BLO RTS	PC,\$T14 66 RO 1\$ PC				557
10559			; Routi	ne Size:	6 words depth per invocation:	0			

10570 10571 10572 ;

5574 ! BLF/PAGE>

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10574 :ML4
10575 :
10576
                                                                                       22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                     TOPS-20 Bliss-16 V2(212)
                                                                                                                     PA: <NEALE>BL3ML4.BL1.2 (65)
                           BGNTST:
                             TEST NUMBER: TST 15
                             TEST NAME:
                                              READ FUNCTION TEST
                             TEST DESCRIPTION:
                                    TEST IF THE DRIVE CAN PERFORM A READ FUNCTION WITHOUT
                                     HANGING THE DRIVE.
                                     A READ FUNCTION IS WRITTEN
                                     INTO MLCS1
                                    THEN GO AND ERROR BITS ARE CHECKED FOR CORRECT STATES.
                          local
                               DODU_FLG;
                                                                                       !DROP UNIT FLAG
                          BGNSUB:
                           CLR_MBUS;
                          DODO_FLG = ZERO;
                          FIRST_BLK_XFER ();
                                                                                      SET UP A FIRST BLK XFERR
                          MLCS1 = read:
                          if .GO IS_NOT_SET
                                                                                       !SEE IF GO GOT SET
                          then
                               begin !ERROR IF (
ERROF (34, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_1, PHR_1, WRD_12, FNC_6, WRD_19);
                                                                                       !ERROR IF CLEAR
                               if .DRY IS_NOT_SET
                                                                                       !TST DRY SET WITH GO CLEAR
                                then
                                    begin !ERROR IF NOT ERROF (35, ASYNC, DUMPER); PRINTB (FIV_FMT, WRD_2, PHR_1, WRD_43, WRD_1, PHR_6);
                                                                                       !ERROR IF NOT SET
                               DODU_FLG = ONE;
                                                                                       !SET DODU_FLG
                               end
                          else
                                                                                       !GO BIT GOT SET
                                if .DRY IS_SET
                                                                                       SEE IF DRY IS CLEAR
                               then
                                    begin
                                                                                       !ERROR IF SET
```

begin :REPURT ENRUR
ERROF (41, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_2, PHR_1, WRD_11, FNC_5, WRD_19);

if .GO IS_SET then ERRDF (42, ASYNC, DUMPER) else ERRDF (43, SYNC, DUMPER);

!GO BIT CLEAR WITH DRY NOT SET !REPORT ERROR

!SEE IF GO CLEAR AFTER XFERR

!CLEAR GO IF STILL SET

!SET DODU_FLG

end

DODU_FLG = ONE;

end:

if .GO IS_SET

begin CLR_MBUS;

then

```
CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 256
MISCELLANEOUS CODING SECTION
```

```
10686 :ML4
10687
10688
10689
10690
10691
10693
10694
10695
10696
10697
10698
10699
10700
10701
10702
10703
10704
10705
10706
10707
                                                                                                                                             22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 
22-Dec-1980 09:21:22 PA:<NEALE>BL3ML4.BLI.2 (6)
                                                                                                                                                                                           PA: (NEALE>BL3ML4.BL1.2 (65)
                                                                                                                                             !TST GO TO DETERMINE FAILING MOD
                                                       PRINTB (FIV_FMT, WRD_1, PHR_2, WRD_11, FNC_5, WRD_19);
                                                       DODU_FLG = ONE;
                                                       end:
                                               ENDSUB:
                                               If .TRE IS_SET
                                                                                                                                         !SEE IF XFERR CAUSED A TRANSFER ERROR
                                                       begin !REPORT ERROR IF SET ERROF (116, INTER, DUMPER); PRINTB (SIX_FMT, WRD_61, WRD_20, PHR_5, WRD_12, FNC_6, WRD_19); DODU_FLG = ONE;
                                                                                                                                          !REPORT ERROR IF SET AND CONTINUE TESTING
                                              if .DODU_FLG IS_SET
                                                                                                                                        !DROP THIS UNIT IF DODU_FLG SET
     10707 :
                                                       begin
DODU (.ML_LUN);
     10708 :
10709 :
                                                       DOCLN:
     10710 :
                                                       end:
                                    5701
      10711 ;
     10712 :
                                               ENDTST:
     10716
10720 036324
10721 036330
10722 036332
10723 036334
10724 036342
10725 036346
10726 036360
10728 036364
10729 036366
10730 036372
10731 036400
10732 036406
10733 036416
10735 036414
10736 036416
10737 036420
10738 036424
10739 036430
                                004167
005746
104402
152777
016701
042701
142777
150177
005002
004767
012777
132777
                                               145514
                                                                $T15:
                                                                                                             R1, $SAVE2
                                                                                                                                                                                                                                                         5573
                                                                                               TST
                                                                                                              -(SP)
                                                                                              TRAP
                                                                                                            #40, aml. REG+40
ML. DUT, R1
#177770, R1
#7, aml. REG+40
R1, aml. REG+40
R2
                                               000040
155234
177770
                                                                                             BISB
MOV
BIC
BICB
BISB
                                                              154652
                                                              154634
                                               000007
154630
                                                                                                                                                                                                                                                         5603
5604
5605
                                                                                              CLR
                                                                                                                                                                            ; DODU.FLG
                                               157446
000071
000001
                                                                                                             PC.FIRST.BLK.XFER
                                                                                              JSR
                                                                                              MOV
                                                                                                             #1, aML.REG
28
55
42
                                                                                              BITB
                                                                                                                                                                                                                                                          5607
                                                                                              BNE
                                                                                              TRAP
                                                                                                                                                                                                                                                         5610
                                                                                              . WORD
                                                                                              . WORD
                                                                                                              ASYNC
                                                                                              . WORD
                                                                                                             DUMPER
                                               006506
010004
006426
                                                                                                             MURD.19,-(SP)
MFNC.6,-(SP)
MURD.12,-(SP)
                                                                                              MOV
                                                                                                                                                                                                                                                         5611
                                                                                              MOV
                                                                                             MOV
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10741				:ML4				22-0-4-1000	00.2/.31 70
10742				-				22-Dec-1980 22-Dec-1980	09:24:31 TOF 09:21:22 PA
10744 036434	012746	007366 006276 006062 000006			MOV	#PHR.1,-(SP) #WRD.1,-(SP) #FIV.FMT,-(SP) #6,-(SP)			
0746 036444	012746	006062			MOV	WFIV.FMT,-(SP)			
10748 036454	010600	000006			MOV	SP,RO	; SP,1		
10749 036456 10750 036460	104414	000200	154536		BITB	14 #200,aml.REG+50			561
0751 036466	001056				BNE	76			
0753 036472	000043				. WORD	55 43 ASYNC			561
0754 036474 0755 036476	010464				TRAP BITB BNE TRAP .WORD .WORD .WORD	ASYNC DUMPER			
0756 036500	012746	007504			MOV	(Q2)- A QUQM	:		561
757 036504 758 036519	012746	007504 006276 006754 007366			MOV	MIRD.43,-(SP)			
0759 036514 0760 036520	012746	006302			MOV	MARD.2,-(SP)			
0761 036524 0762 036530	012746	006302 006062 000006			MOV	MARD.1,-(SP) MARD.43,-(SP) MPHR.1,-(SP) MARD.2,-(SP) MFIV.FMT,-(SP) M6,-(SP)			
)763 036534)764 036536	010600 104414				MOV TRAP	SP.R0	; SP,1		
765 036540	062706	000016			ADD	#16.SP	:		561
766 036544 767 036546	062706 000427 105777 100030	154452		25:	BR TSTB	3\$ aML.REG+50			561 562 563
768 036552 769 036554	100030				BPL	4\$ 55 44			562
70 036556	000044				. WORD	44			~
	024052				WORD	ASYNC DUMPER			
72 036562 73 036564 74 036570	012746	007472			. WORD . WORD . WORD MOV MOV	#PHR.5,-(SP)	;		562
775 036574 776 036600	012746	006754			MOV	MURD .43,-(SP)			
773 036564 774 036570 775 036574 776 036604 777 036604 778 036614 779 036614 7780 036620 7781 036622 7782 036624	012746 012746 012746 012746 012746 012746 012746 012746 012702 062706 132777 001430 104455 010464 024052 012746	007472 006276 006754 007404 006302 006062 000006			MOV	DUMPER MPHR.5,-(SP) MURD.1,-(SP) MURD.43,-(SP) MPHR.2,-(SP) MURD.2,-(SP) MFIV.FMT,-(SP) SP,R0			
779 036614	012746	000006			MOV	#6,-(SP)			
0776 036600 0777 036604 0778 036610 0779 036614 0780 036620 0781 036622	104414				MOV TRAP MOV	SP.R0	; SP,*		
0782 036624	012702	000001 000016 000001		3\$:	MOV	#1.R2 #16.SP #1.aML.REG+60 58 55 45 ASYNC DUMPER	: *,00	DU.FLG	562
0784 036634	132777	000001	154372	45:	BITB	#1.aML.REG+60	:		562 563 563
0786 036644	104455				TRAP	55	:		563
0787 036646 0788 036650	000045				. WORD	45 ASYNC			
0783 036630 0784 036634 0785 036642 0786 036644 0787 036646 0788 036650 0789 036652 0790 036654 0791 036664 0793 036670 0794 036674	024052	004504			BEQ TRAP .WORD .WORD .WORD MOV MOV	DUMPER			643
0791 036660	012746	006506 007774 006426 007472 006314 006062			MOV	#URD.19,-(SP) #FNC.5,-(SP) #URD.12,-(SP) #PHR.5,-(SP) #URD.3,-(SP) #FIV.FMT,-(SP)			563
0792 036664 0793 036670	012746 012746 012746 012746 012746	006426			MOV	#WRD.12,-(SP)			
10782 036624 10783 036630 10784 036634 10785 036642 10786 036644 10787 036646 10788 036650 10789 036652 10790 036654 10791 036660 10792 036664 10793 036670 10794 036674	012746	006314			MOV	MURD.3,-(SP)			

10797			:ML4				22-Dec-1980 09:24:31 TO
10798 10799			:		******		22-Dec-1980 09:24:31 TO 22-Dec-1980 09:21:22 P/
10800 036704 012 10801 036710 010 10802 036712 104 10803 036714 012 10804 036720 062	746 000006			MOV	#6(SP) SP.RO 14	; SP,*	
10802 036712 104 10803 036714 012	702 000001			MOV	14 #1.R2	: *,DODU.FLG	5/
10804 036720 06 10805 036724 03	702 000001 706 000016 777 020000	154302	5\$:	ADD	#1.R2 #16.SP #20000.aML.REG+60		56 56 56
10806 036732 001	430			BEQ TRAP .WORD .WORD .WORD	6\$		56
10808 036736 000	046			. WORD	6\$ 55 46 ASYNC		
10810 036742 024	052			WORD	DLIMPER		56
10812 036750 01	746 006506 746 007774			MUV	MARD.19,-(SP) MFNC.5,-(SP)		×
10814 036760 01	746 007472			MOV	MURD.12,-(SP) MPHR.5,-(SP)		
10816 036770 01	746 006062 746 000006			MOV	WFIV.FMT,-(SP)		
10818 037000 010 10819 037002 104	600			MOV	#PHR.5,-(SP) #MRD.4,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14	; SP,*	
10820 037004 012	702 000001			MOV	#1,R2 #16,SP	: *,DODU.FLG	50
10821 037010 06; 10822 037014 01;	702 000001 706 000016 700 000050		6\$: 7\$:	MOV	#50.R0	*,\$\$TMP2	56 56 56
10823 037020 001 10824 037022 016	701 143070		/5:	MOV	10\$ L\$DLY,R1 9\$; *,\$\$TMP1	
10825 037026 001 10826 037030 005	405 016		85:	BEQ CLR	(SP)	; SSTMP	
10827 037032 005 10828 037034 005	301 375			DEC	R1 8\$ R0	; \$\$TMP1	
	300 767		9\$:	DEC BR	74	; SSTMP2	
10830 037040 000 10831 037042 133 10832 037050 001 10833 037052 133 10834 037060 001 10835 037062 153 10836 037070 010 10837 037074 043 10838 037100 143 10840 037112 133 10841 037120 001 10842 037122 104 10843 037124 000 10844 037124 000 10845 037134 000 10846 037134 000 10847 037134 104 10848 037136 000 10849 037140 010 10849 037140 010	767 106 777 000001 452 777 000040 771 154506 771 177770 777 000007 177 154102 777 000001 405 455 047 464 052 404 455 050 050 050 050	154154	10\$:	BITB	#200, aML.REG+50 15\$ #1, aML.REG 13\$ #40, aML.REG+40 ML.DUT, R1 #177770, R1 #7, aML.REG+40 R1, aML.REG+40 #1, aML.REG	:	56
10833 037052 132 10834 037060 001	777 000001 452	154074		BNE BITB BEQ BISB	#1.aML.REG	:	56
10835 037062 152 10836 037070 016	777 000040	154124		BISB	#40,aML.REG+40	:	56
10837 037074 04. 10838 037100 14	701 177770	154106		DIC	#177770,R1		
10839 037106 150	7777 000040 701 154506 701 177770 7777 000007 1777 154102 7777 000001	154034		BICB BISB BITB BEQ TRAP .WORD .WORD	R1, aML.REG+40		56
10841 037120 001	405	134034		BEQ	11\$		*
10843 037124 000	047			. WORD	27		
10845 037130 024	052			WORD	ASYNC DUMPER 12\$ 55 50 SYNC DUMPER		
10847 037134 104	455		115:	TRAP	55		
10848 037136 000 10849 037140 010	1050 1526			. WORD	SYNC		
10850 037142 024 10851 037144 012	052 746 006506		12\$:	BR TRAP .WORD .WORD .WORD MOV	DUMPER WARD.19,-(SP)	;	56

LABO ML-11 LOGIO CELLANEOUS CODIO 0853			·m 4			22-0	SEQ 0246
0854 0855			ML4			22-De c	-1980 09:24:31 TOP: -1980 09:21:22 PA:
0856 037150 01: 0857 037154 01: 0858 037160 01: 0859 037164 01:	746 007774			MOV	WFNC.5,-(SP) WWRD.11,-(SP)		
0858 037160 01	746 007404			MOV	#PHR.2,-(SP)		
0860 037170 01 0861 037174 01	746 006062			MOV	#PHR.2,-(SP) #MRD.1,-(SP) #FIV.FMT,-(SP) #6,-(SP)		
862 037200 010	600			MOV	SP,RO	; SP.*	
864 037204 00	7.27		13\$:	BR	145	:	5654 5666
866 037210 00	051		130:	BR TRAP . WORD . WORD . WORD	55 51 45 Wale		2000
867 037212 010 868 037214 029	052			WORD	ASYNC DUMPER		
70 037222 01	746 006506 746 007774			MUA	MARD.19,-(SP) MFNC.5,-(SP)		566
72 037232 01	746 007366			MOV	#PHR.1,-(SP)		
69 037216 01 370 037222 01 371 037226 01 372 037232 01 373 037236 01 374 037242 01	746 006302 746 006062 746 000006			MOV	WFIV.FMT,-(SP)		
174 037242 01 175 037246 01 176 037252 01 177 037254 10	600			MOV	#FNC.5,-(SP) #WRD.11,-(SP) #PHR.1,-(SP) #WRD.2,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14	; SP.*	
175 037246 01 176 037252 01 177 037254 10 178 037256 01 179 037262 06 180 037266 13	702 000001		145:	MOV	#1 .R2 #16,SP	: *,DODU.FLG	5670
0 037266 13	702 000001 2706 000016 2777 000001	153660	15\$:	BITB	#1, art.REG		5676 5657 5673
2 037276 15 3 037304 01	7777 000040 5701 154272	153710		BISB	18\$ #40, aml .REG+40	:	5675
037310 04	701 177770	153672		BIC	#177770,R1		
4 037333 45	153666	153620		BIC BICB BISB BITB	#177770,R1 #7,3ML.REG+40 R1,3ML.REG+40 #1,3ML.REG		547
7 037326 13 8 037334 00 9 037336 10 0 037340 00 1 037342 01 2 037344 02 2 037346 00 3 037350 10	2777 000001 1405	133020		BEQ	16\$		5670
90 037340 00	052			BEQ TRAP .WORD .WORD	16\$ 55 52 ASYNC DUMPER 17\$		
92 037344 02	052			. WORD	DUMPER		
94 037350 10	455		16\$:	TRAP	55		
% 03/354 010	526 053			. WORD	55 53 SYNC DUMPER		
995 037352 000 996 037354 010 997 037356 020 998 037360 010 999 037364 010 900 037370 010 901 037374 010 902 037400 010 903 037410 010 905 037414 010 906 037416 100 907 037420 010	746 006506		175:	BR TRAP .WORD .WORD .WORD		:	5681
900 037370 01	746 006506 746 007774 746 006416 746 006276 746 006062 746 000006			MOV	#FNC.5,-(SP) #MRD.11,-(SP) #MRD.1,-(SP) #MRD.1,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14 #1,R2		
902 037400 01	746 006276	- 4	1	MOV	#URD 1, -(SP)		
904 037410 01	746 000006			MOV	#6,-(SP)		
0906 037416 10	0600 414 2702 000001			MOV MOV TRAP MOV	14. RO	; SP,* ; *,DODU.FLG	5682

MLABO ML-11 LO SCELLAMEOUS CO	ODING SE	CTION								0247
10909 10910 10911				:ML4					22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOP PA:
10912 037424 10913 037430 10914 037432	062706 104467 006000 103002	000016		18\$:	ADD TRAP ROR BHIS	#16.SP 67 R0 19\$:			567 568
10916 037436 10917 037442	000167	176670 040000	153504	198:	BIT	1\$ #40000 aml .REG	;			568
10918 037450 10919 037452 10920 037454 10921 037456	104455 000164 010672				BEQ TRAP .WORD .WORD .WORD	20\$ 55 164 INTER DUMPER	;			569
10923 037462 10924 037466 10925 037472	012746 012746 012746 012746	006506 010004 006426			MOV	#GRD.19,-(SP) #FNC.6,-(SP) #GRD.12,-(SP)	:			569
10927 037502 10928 037506 10929 037512 10930 037516	012746 012746 012746	006514 007174 006100 000007			MOV MOV MOV	#PHR.5,-(SP) #URD.20,-(SP) #URD.61,-(SP) #SIX.FMT,-(SP) #7,-(SP)				
10931 037522 10932 037524	010600	000007			MOV MOV TRAP	SP,R0	:	SP,*		
10933 037526 10934 037532 10935 037536	012702 062706 005302	000001 000020		20\$:	ADD DEC	#1,R2 #20,SP R2	:::	*.DODU.FLG		569 568 569
10936 037540 10937 037542 10938 037546	001004 016700 104451	154032			MOV TRAP	21\$ ML.LUN,RO 51	:			5690
10940 037552 10941 037554 10942	005726 000207			21\$:	TRAP TST RTS	(SP)+ PC	:			557
10943 10944 10949 10950				; Rout ; Max	tine Size: imum stack	333 words depth per invocation: 18 words				
10954 10958 037556 10959 037556 10960 037562 10961 037564 10962 037566	004767 104466 006000 103773	176542		T15::	JSR TRAP ROR BLO	PC.\$T15 66 R0 1\$:			5700

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 261

10964
10965
10966
10967 037570 000207
RTS PC
10968
10969
10970
10975
10976
10977; 5703 !<BLF/PAGE>

SEQ 0248

22-Dec-1980 09:24:31 TOP 22-Dec-1980 09:21:22 PA:

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22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BLI.2 (66)
10979 :ML4
                            BGNTST:
                             TEST NUMBER: TST 16
                                                 CLEAR FUNCTION TEST
                            ! TEST NAME:
                            TEST DESCRIPTION:
                                      TEST IF THE DRIVE CAN PERFORM A CLEAR FUNCTION WITHOUT HANGING THE DRIVE.
                                      A CLEAR FUNCTION IS WRITTEN INTO MLCS1.
                                      THEN GO AND ERROR BITS ARE CHECKED FOR CORRECT STATUS. THIS DRIVE IS DROPPED ON DETECTED ERRORS.
                           local
                              DODU_FLG;
                                                                                          !DROP UNIT FLAG
                           BGNSUB:
                            CLR_MBUS;
                            DODU_FLG = ZERO;
                            MLER = ONES;
                                                                                           SET BITS IN ERROR REGISTER DO A CLEAR FUNCTION
                           MLCS1 = DRV CLR;
DELAY (ONE_US);
                                                                                            !DELAY
                            if .GO IS_SET
                                                                                           !SEE IF GO CLEARED AFTER FUNCTION
                                 begin :ERRUR IF SET ERRUR IF SET ERRUF (44, ASYNC, DUMPER); PRINTB (FIV_FMT, WRD_1, PHR_5, WRD_11, FNC_3, FNC_7, WRD_19);
                                 if .DRY IS_SET
                                                                                           !TST DRY CLEAR WITH GO SET
                                      begin !ERRUR IF SET ERROF (45, ASYNC, DUMPER); PRINTB (FIV_FMT, WRD_2, PHR_5, WRD_43, WRD_1, PHR_5);
                                 DODU_FLG = ONE;
                                                                                           !SET DODU_FLG
                                 end
                           else
                                                                                           !GO CLEARED AFTER FUNCTION
                                 if .DRY IS_NOT_SET
                                                                                           !TST DRY SET WITH GO CLEAR
                                      begin !ERROR IF NOT
ERROF (46, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_2, PHR_1, WRD_43, WRD_1, PHR_6);
DODU_FLG = ONE;
                                                                                           !ERROR IF NOT SET
```

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CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 263 MISCELLANEOUS CODING SECTION
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```
22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BL1.2 (66)
 if .ILF IS_SET
                                                               !DID FUNCTION CAUSE ILF
      begin !ERROR IF YES
ERROF (47, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_3, PHR_5, WRD_12, FNC_3, FNC_7, WRD_19);
DODU_FLG = ONE;
                                                                 !ERROR IF YES
if .OPI IS_SET
                                                              !DID FUNCTION CAUSE OPI
 then
      begin !ERROR IF YES ERROF (48, ASYNC, DUMPER); PRINTB (FIV_FMT, WRD_4, PHR_5, WRD_12, FNC_3, FNC_7, WRD_19); DODU_FLG = ONE;
                                                                !ERROR IF YES
      end:
if .MLER neg ZERO
                                                          !TEST ERROR REGISTER FOR CLEAR
 then
      begin :ERRUR IF NUT CLEAR
ERROF (49, ASYNC, DUMPER);
PRINTB (SIX_FMT, FNC_3, FNC_7, WRD_19, WRD_14, WRD_13, REG_3);
                                                               !ERROR IF NOT CLEAR
ENDSUB:
 if .DODU_FLG IS_SET !DROP THIS UNIT IF DODU_FLG SET
  begin
DODU (.ML_LUN);
DOCLN;
    end:
ENDTST:
144246
                 $T16:
                                           R1,$SAVE2
                                                                                                                                            5702
                                 TST
                                            -(SP)
                                 TRAP
 000040 153404
153766
177770
                                           #40, aML.REG+40
ML.DUT,R1
#177770,R1
#7, aML.REG+40
R1, aML.REG+40
                                 BISB
                                 MOV
BIC
BICB
            153366
                                                                                      ; DODU.FLG
 177777 153372
                                            #-1.2ML.REG+60
```

MLABO ML-11 L SCELLANEOUS C									0 0251
11091				ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
11093 037642	012777	000001	153304		MOV MOV BEQ	#11.AML.REG #1.RO 5\$: *,\$\$TMP2		5729 5730
11095 037654	001410	142234		2\$:	BEQ MOV	5\$ L\$DLY,R1	; *,\$\$TMP1		,,,,
11097 037662	001403			3\$:	MOV BEQ CLR	4\$ (SP)			
11099 037666 11100 037670	005301				DEC	R1 3\$: SSTMP1		
11101 037672 11102 037674	005300			45:	CLR DEC BNE DEC BR	R1 38 R0 28	; \$\$TMP2		
1103 037676 11104 037704	132777	000001	153250	5\$:	RITE	#1,aML.REG	:		5732
11105 037706 11106 037710	104455				BEQ TRAP .WORD .WORD .WORD	7\$ 55 54 ASYNC DUMPER			5735
11107 037712	024052	******			. WORD	ASYNC DUMPER			
1110 0377722	012746	006506			MUY	MARD.19,-(SP) MFNC.7,-(SP)	•		5736
1112 037732	012746	006416			MOV	#FNC.3,-(SP) #MRD.11,-(SP)			
1114 037742	012746	006276			MOV	MPHR.5,-(SP) MARD.1,-(SP) MFIV.FMT,-(SP) M7,-(SP)			
1116 037752	012746	000007			MOV MOV	#7,-(SP) SP,R0	; SP,*		
1118 037760 1119 037762	104414	153236			TRAP	14 200 PEG+50	, sr,-		5738
120 037766	100026	175250			901	6\$			5741
122 037772	000055				TRAP .WORD .WORD .WORD MOV MOV MOV	6\$ 55 55 ASYNC DUMPER MPHR.5,-(SP)			2.4
123 037774 124 037776 125 040000 126 040004 127 040010 128 040014	024052	007472			, WORD	DUMPER MPHR.5(SP)			5742
126 040004 127 040010	012746	006276 006754			MOV	#W(I) - 1 (SP)			
1128 040014 1129 040020	012746	007472 006302			HUV	#PHR.5,-(SP) #MRD.2,-(SP)			
1130 040024 1131 040030 1132 040034 1133 040036 1134 040040 1135 040044 1136 040050 1137 040054 1138 040056 1139 040064	012746	007472 006276 006754 007472 006302 006062 000006			MOV	#WRD.43,-(SP) #PHR.5,-(SP) #WRD.2,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14			
1133 6665	104414				TRAP	SP.R0	; SP,*		
1135 040040 11135 040044 11136 040050	012702	000016 000001 000020		6\$:	MOV	#16,SP #1,R2 #26,SP 8\$	*.DODU.FLG		5740 5745 5734 5732 5749
1137 040054	000434	000020	153140	7\$:	BR	#20,5P #200,aML.REG+50			5732
1139 070027	132777	000200	133140	7.	BNE	8\$			
1141 040070	000056				. WORD	56 ASYNC			5752
1138 040056 1139 040064 1140 040066 1141 040070 1142 040072 1143 040074 1144 040076	000056 010464 024052 012746	007504			MOV MOV TRAP ADD MOV ADD BR BITB BNE TRAP . WORD . WORD . WORD	8\$ 55 56 ASYNC DUMPER MPHR.6,-(SP)			5753

ML4 040102 012746 006276 MOV MURD.1 ₄ -(SP)	22-Dec-198 22-Dec-198	00 07.64.31
040102 012746 006276 MOV #WRD.1(SP)		80 09:24:31 TOPS 80 09:21:22 PA:<
0 040106 012746 006754 MOV WHRD.43,-(SP) 040112 012746 007366 MOV WPHR.1,-(SP) 040116 012746 006302 MOV WHRD.2,-(SP) 040122 012746 006062 MOV WF.IV.FMT,-(SP) 040126 012746 000006 MOV W6,-(SP)		
040132 010600 MOV SP,R0 040134 104414 TRAP 14	; SP,*	
040136 012702 000001 MOV #1,R2	: *,DODU.FLG	5754 5751 5757
3 040142 062706 000016 ADD #15,SP 040146 132777 000001 153060 8\$: BITB #1,aML.REG+60		5757
040146 132777 000001 153060 8\$: BITB #1, aml. reg+60 040156 104455 040160 000057 040162 010464 040164 024052 040166 012746 006506 040172 012746 010012 MOV #FNC.7,-(SP)		5760
040162 010464 024052 .WORD DUMPER 040166 012746 006506 MOV WIRD.19,-(SP) MOV WFNC.7,-(SP) 040176 012746 007750 MOV WFNC.3,-(SP) 040202 012746 006426 MOV WFNC.3,-(SP) 040206 012746 007472 MOV WFNC.3,-(SP) 040212 012746 006314 MOV WHRD.12,-(SP) 040212 012746 006062 MOV WHRD.3,-(SP) 040212 012746 006062 MOV WFIV.FMT,-(SP) 040222 012746 006062 MOV WFIV.FMT,-(SP) 040222 012746 006062 MOV WFIV.FMT,-(SP)		5761
040226 010600 MOV SP.RO 040230 104414 TRAP 14	; SP,*	
040232 012702 000001 MOV #1 R2 040236 062706 000020 ADD #20,SP	: *,DODU.FLG	5762 5759 5765
040242 032777 020000 152764 9\$: BIT #20000, aML.REG+60		5765
040250 001432 BEQ 108 040252 104455 TRAP 55 040254 000060 .WORD 60 040256 010464 .WORD ASYNC 040260 024052 .WORD DUMPER 040262 012746 006506 MOV MURD.19,-(SP) 040266 012746 010012 MOV MFNC.7,-(SP)		5768
040276 012746 006426 MOV MURD.12,-(SP) 040302 012746 007472 MOV MPHR.5,-(SP) 040306 012746 006322 MOV MURD.4,-(SP) 040312 012746 006062 MOV MFIV.FMT,-(SP)		5769
040322 010600 MOV SP,R0	; SP,*	
040316 012746 000007 MOV #7,-(SP) 040322 010600 TRAP 14 040326 012702 000001 MOV #1,R2 040332 062706 000020 ADD #20,SP 040336 005777 152672 10\$: TST aml.REG+60 040342 001430 TRAP 55 040344 104455 TRAP 55 040346 000061 .WORD 61 040352 024052 .WORD DUMPER	; *,DODU.FLG	5770
040326 012702 000001 MOV #1 R2 040332 062706 000020 ADD #20.SP 040336 005777 152672 10\$: TST aML.REG+60 040342 001430 BEQ 11\$		5770 5767 5773
040342 001430 BEQ 118 040344 104455 TRAP 55 040346 000061 .WORD 61		5776

1202								22-01090 00-	SEQ 0253
203			iML4					22-Dec-1980 09: 22-Dec-1980 09:	24:31 TOP 21:22 PA:
205 040354 1206 040360 1207 040364 1208 040370 1209 040374 1210 040400 1211 040404	012746 012746 012746 012746 012746 012746	010274 006436 006442 006506 010012 007750 006100 000007		MOV MOV MOV MOV MOV	#REG.3,-(SP) #WRD.13,-(SP) #WRD.14,-(SP) #WRD.19,-(SP) #FNC.7,-(SP) #FNC.3,-(SP) #SIX.FMT,-(SP) #7,-(SP) SP,R0		•		577
213 040414	010600	000007		MOV TRAP	SP.RO		; SP,*		
215 040420 1216 040424 1217 040426 1218 040430	062706 104467 006000 103002	000020	11\$:	ADD TRAP ROR BHIS	14 #20,SP 67 R0 12\$:		577 577
219 040432 220 040436	000167	177142	128:	JMP DEC BNE	R2		; DODU.FLG		578
221 040440 222 040442 223 040446 224 060650	001004 016700 104451	153132		BNE MOV TRAP TRAP	138 ML.LUN,RO 51 44		:		578
225 040452 226 040454	005726 000207		13\$:	TST	(SP)+ PC		:		570
227 228 229 234 235			; Rout ; Maxi	ine Size: mum stack	218 words depth per invocation:	19 words			
239 243 040456 244 040456 245 040462 246 040464 247 040466 248 040470 249 250	004767 104466 006000 103773 000207	177110	T16:: 1\$:	JSR TRAP ROR BLO RTS	PC,\$T16 66 R0 1\$ PC		:		578
249 250 251			; Rout		6 words depth per invocation:	0 words			

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION

MACRO M1113 22-DEC-80 12:16 PAGE 267

SEQ 0254

11257 :ML4 11258 : 11259 11260 :

5790 ! <BLF / PAGE>

22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 22-Dec-1980 09:21:22 PA:<NEALE>BL3ML4.BL1.2 (66)

```
11262 :ML4
11263 :
11264
11265 :
11266 :
11267 :
11268 :
11269 :
                                                                                                                                                TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BL1.2 (67)
                                                                                                           22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                 BGNTST
                                    TEST NUMBER: TST 17
                                    TEST NAME: DIAGNOSTIC REGISTER TESTS
                                    TEST DESCRIPTION:
                                             TEST THE DATA DIAG REGISTERS MLD1, MLD2, MLE2 FOR 1'S/0'S, SHIFTING 1'S/0'S AND INITIALIZATION
                                 Local
                                                                                                           CLEAR DATA FOR INIT TEST TEMPORARY SAVE LOCATION LERROR FLAG
                                 CLR_DATA .
                                 SAVE
                                 ERR_FLG .
TST_PAT ,
index ,
                                                                                                           TEST PATTERN
POINTS TO REGISTER PRESENTLY BEING TESTED
PROP UNIT FLAG
                                 DODU_FLG;
DODU_FLG = ZERO;
                                             FIRST TEST THE REGISTERS FOR ONES AND ZEROES
                                 TST_PAT = ONES:
                                                                                                           !LOAD TEST PAT WITH ONES
                                 incr TWICE from 0 to 1 do
                                                                                                         !REPEAT LOOP TWICE
                                       begin
                                       incr REG_SEL from 11 to 13 do !TEST ELEVEN WRITE/READ REGISTERS
                                             begin
BGNSUB;
                                             CLR_MBUS;
WRT_REG (.TST_PAT, .REG_SEL, index);
RD_REG (.TST_PAT, .REG_SEL, ERR_FLG);
                                                                                                          WRITE TO THE REGISTER
                                             if .ERR_FLG IS_SET
                                                                                                           !SEE IF READ FOUND AN ERROR
                                             then
                                                   !IF ERROR FLAG IS SET THEN ERROR AND SET DODU_FLG
ERROF (121, ARR_DAT, DUMPER); !ARRAY DATA MODULE FAILURE
PRINTB (SIX_FMT, PHR 4, WRD 12, FNC 5, FNC 6, WRD 52, WRD 56);
PRINTB (FMT 16, .ML_REG [.index, REGISTER_ADD], .DT_DATA, .RD_DATA);
DODU_FLG = ONE;
                                                    end:
                                             ENDSUB;
11314
                                             end;
11315
11316 :
                                       TST_PAT = not .TST_PAT;
                                                                                                        !REPEAT AGAIN WITH COMPLIMENT DATA
```

SEQ 0256

REG_INIT_FLG = ONE;
TST_PAT = ONES;

Incr TWICE from 0 to 1 do
begin

!REPEAT LOOP TWICE

incr REG_SEL from 11 to 13 do !TEST THIRTEEN REGISTERS

ML.DUT.R5 #177770,R5

#7, 24L. REG+40

MOV BIC

152452

000007

SEQ 025		12:16 PAGE 271	22-DEC-80	RO M1113	TION	OGIC TEST	CELLANEOUS C
22-Dec-1980 09:24:31 TO 22-Dec-1980 09:21:22 PA				ML4			1429 1430
		R5.aML.REG+40	BISB		152446	150577	1431
58	: TST.PAT.* : REG.SEL.*	R3(SP)	MOV		136440	010346	1433 040546
		R2,-(SP) #12,-(SP)	MOV		000012	012746	1435 040552
	; INDEX,*	SP, (SP) PC, WRT.REG	ADD JSR		162310	060616	1437 040556
58	: TST.PAT.* : REG.SEL.*	SP, (SP) PC, MRT.REG R3, (SP) R2,-(SP) M14,-(SP) SP, (SP) PC,RD.REG 14(SP),M1	MOV			010316	1438 040564 1439 040566
		#14,-(SP)	MOV		000014	012746	1440 040570
	; ERR.FLG,*	PC.RD.REG	ADD JSR		162660 000014 00	004767	1442 040576
58	; ERR.FLG,*	38	BNE	001	000014 00	001054	1444 040610
58	:	55 171	TRAP			104455	445 040612
		ARR.DAT	. WORD			010570	1447 040616
58	:	ARR.DAT DUMPER MURD.56,-(SP) MURD.52,-(SP) MURD.52,-(SP) MURD.52,-(SP) MURD.12,-(SP) MURD.12,-(SP) MURD.12,-(SP) MURD.12,-(SP) MURD.12,-(SP) MURD.12,-(SP) MURD.12,-(SP) MURD.12,-(SP) MURD.12,-(SP)	CMP BNE TRAP . WORD . WORD . WORD MOV MOV		007122	012746	1441 040574 1442 040576 1443 040602 1444 040610 1445 040612 1446 040614 1447 040616 1448 040620 1449 040622 1450 040626 1451 040632 1452 040636 1453 040646 1455 040656 1457 040662 1458 040666
		###0.52,-(SP) #FNC.6,-(SP)	MUV		007122 007066 010004	012746	1451 040632
		#FNC.5,-(SP)	MOV		007774 006426	012746	452 040636 1453 040642
		MPHR.4,-(SP)	MOV		007454	012746	454 040646
		#7,-(SP)	MOV		006100 000007	012746	456 040656
	; SP,*	14	MOV			104414	458 040664
58	:	RD.DATA, (SP) WT.DATA, -(SP) 40(SP), RO	MOV		152236 152230	016716	460 040672
	; INDEX,*	40(SP),R0	MOV		152230 000040	016746 016600 006300	60 040672 61 040676 63 040702
		RO RO RO	ASL			006300	63 040704
		ML.REG(RO),-(SP)	MOV MOV ASL ASL ASL MOV		013154	006300 006300 006300 016046 012746	462 040702 463 040704 464 040706 465 040710 466 040714 467 040726 469 040726 470 040730 471 040736 472 040742 473 040750 475 040752 476 040754 477 040756 478 040764 479 040764 481 040770 482 040774 483 040776
		#FMT.16,-(SP) #4,-(SP)	MOV		013154 005216 000004	012746	467 040714
	; SP,*	ML.REG(RO),-(SP) #FMT.16,-(SP) #4,-(SP) SP,RO 14	MOV			010600 104414 012766 062706 062706 104467	1468 040724 1469 040726
58	: *,DODU.FLG	#1,42(SP)	MOV	042	000001 00 000030 000012	012766	1462 040702 1463 040704 1464 040706 1465 040710 1466 040714 1467 040720 1469 040724 1470 040730
58 58 58 58	:	#12.SP	ADD	3\$:	000012	062706	472 040743
>8	•	RO	ROR			006000	1474 040750
58	: REG. SEL	2\$ R2	BLO			103660	1472 040742 1473 040746 1474 040750 1475 040752 1476 040754 1477 040756 1478 040762 1479 040764 1480 040766 1481 040770 1482 040774
	; REG.SEL,*	R2,#15	CMP		000015	020227	1477 040756
58 58	: TST.PAT	RS	COM			005103	1479 040764
58	TST.PAT TWICE	14 #1,42(SP) #30,SP #12,SP 67 R0 2\$ R2 R2,#15 2\$ R3 R1 R1,#1	CMP		000001	020127	1481 040770
58	; *,TST.PAT	1\$ #1,R3	MOV ADD TRAP ROR BLO INC CMP BLE COM INC CMP BLE MOV		000001	003654 005103 005201 020127 003645 012703	1482 040774 1483 040776

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 272		SEQ 0259
11485	:ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
11487 11488 041002 005004 11489 041004 005001 11490 041006 012702 000013 11491 041012 104402 11492 041014 152777 000040 11493 041022 016705 152554 11494 041026 042705 177770	4\$: CLR R4 5\$: MOV #13,R2 6\$: TRAP 2 152172 BISB #40,aML.REG+40 MOV ML.DUT,R5 BIC #177770,R5 BICB #7,aML.REG+40	SHIFT TWICE *,REG.SEL	5852 5855 5858 5859 5860
11494 041026 042705 1777770 11495 041032 142777 000007 11496 041040 150577 152150 11497 041044 010346 11498 041046 010246 11499 041050 012746 000012 11500 041054 060616	BISB R5.aML.REG+40 MOV R3(SP) MOV R2(SP)	: TST.PAT.* : REG.SEL.*	5862
11501 041056 004767 162012 11502 041062 010316 11503 041064 010246 11504 041066 012746 000014 11505 041072 060616	MOV #12,-(SP) ADD SP,(SP) JSR PC,WRT.REG MOV R3,(SP) MOV R2,-(SP) MOV #14,-(SP) ADD SP,(SP) JSR PC,RD,REG CMP 14(SP),#1 BNE 78 TRAP 55 .WORD 172 .WORD DUMPER	: TST.PAT.* : REG.SEL.* : ERR.FLG.*	5863
11506 041074 004767 162362 11507 041100 026627 000014	000001 JSR PC.RD.REG 14(SP),#1	: ERR.FLG,*	5865
11509 041110 104455 11510 041112 000172 11511 041114 010570	TRAP 55 .WORD 172 .WORD ARR.DAT .WORD DUMPER	10 12 1	5868
11512 041116 024052 11513 041120 012746 007122 11514 041124 012746 007066 11515 041130 012746 010004 11516 041134 012746 007774 11517 041140 012746 006426 11518 041144 012746 006426 11519 041150 012746 006100 11520 041154 012746 000007	MOV MARD.52,-(SP) MOV MFNC.6,-(SP) MOV MFNC.5,-(SP) MOV MFNC.5,-(SP) MOV MARD.12,-(SP)	: SP.**	5869
11518 041144 012746 007454 11519 041150 012746 006100 11520 041154 012746 000007 11521 041160 010600 11522 041162 104414 11523 041164 016716 151740 11524 041170 016746 151732 11525 041174 016600 000040 11526 041200 006300 11527 041202 006300 11528 041204 006300	TRAP 14 MOV RD.DATA, (SP) MOV WT.DATA, -(SP) MOV 40(SP), RO ASL RO ASL RO ASL RO MOV ML.REG(RO), -(SP) MOV MFMT.16, -(SP) MOV M4, -(SP) MOV SP, RO TRAP 14 000042 MOV M1,42(SP)	: INDEX.*	5870
11528 041204 006300 11529 041206 016046 013154 11530 041212 012746 005216 11531 041216 012746 000004 11532 041222 010600	ASL RO MOV ML.REG(RO),-(SP) MOV MFMT.16,-(SP) MOV M4,-(SP) MOV SP,RO	; SP.*	
11518 041144 012746 007454 11519 041150 012746 006100 11520 041154 012746 000007 11521 041160 010600 11522 041162 104414 11523 041164 016716 151732 11524 041170 016746 151732 11525 041174 016600 000040 11526 041200 006300 11527 041202 006300 11528 041204 006300 11529 041206 016046 013154 11530 041212 012746 005216 11531 041216 012746 005216 11533 041224 104414 11534 041226 012766 000004 11535 041234 062706 000030 11536 041240 062706 000030 11537 041244 104467 11538 041246 006000 11539 041250 103660	MOV	*,DODU.FLG	5871 5867 5859 5872

1541 1542		:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOP:
543 544 041252 005202 545 041254 020227 000	015		INC	R2 R2,#15	: REG.SEL .*		585
546 041260 003654 547 041262 005103			BLE	6\$ R3 R1		4.	587
548 041264 005201 1549 041266 020127 000	001		COM INC CMP BLE ASL INC CMP BLE MOV	R1 R1,#1	: TST.PAT : TWICE : TWICE,*	10	587 585
550 041272 003645 551 041274 006303			BLE	5\$ R3			5886 5856
552 041276 005204 553 041300 020427 000	017		CMP	R1 .#1 5\$ R3 R4 R4 .#17	: TST.PAT : SHIFT : SHIFT,*		585
555 041304 003637 555 041306 012767 000	001 151622 777		MOV	#1 REG. INIT.FLG			588
557 041320 005004	013	ge.	CLR	R4 #13,R2	*,TST.PAT : TWICE : *,REG.SEL		589
559 041326 104402		8\$: 9\$:	MOV TRAP BISB	2 #40,24L.REG+40	-, REG. SEL		5886 5889 5899 5899 5899 5899
561 041336 016701 152 562 041342 042701 177	770		BIC	ML.DUT_R1 #177770_R1			201
663 041346 142777 000 664 041354 150177 151	007 151640 634		BICB	#7,34L.REG+40 R1,34L.REG+40			
65 041360 010346 66 041362 010246			MOV	R3,-(SP) R2,-(SP)	: TST.PAT.* : REG.SEL.*		589
68 041370 060616	012		ADD	#12,-(SP) SP,(SP)	; INDEX,*		
70 041376 016600 000 71 041402 006300	476 012		JSR MOV	PC, WRT.REG 12(SP), RO	; INDEX,*		5899
72 041404 006300 73 041406 006300			ASL ASL	RO RO			
574 041410 010001 575 041412 016105 013	156		MOV	RO,R1 ML.REG+2(R1),R5	: *,CLR.DATA		
673 041400 000300 674 041410 010001 675 041412 016105 013 676 041416 056105 013 677 041422 012767 000 678 041430 010516 679 041432 010246 680 041434 012746 000 681 041440 060616 682 041442 004767 162 683 041446 026627 000 684 041454 001044 685 041456 104455	156 162 001 151506		MOV	RO,R1 ML.REG+2(R1),R5 ML.REG+6(R1),R5 M1,REG.INIT.FLG R5,(SP) R2,-(SP) W14,-(SP) SP,(SP) PC,RD.REG 14(SP),W1	*,CLR.DATA		5900 5901
78 041430 010516 79 041432 010246	014		MOV	RS, (SP) R2, -(SP)	: CLR.DATA,*		5901
580 041434 012746 000 581 041440 060616 582 041442 004767 162 583 041446 026627 000			MOV ADD JSR	SP, (SP)	; ERR.FLG,*		
583 041446 026627 000 584 041454 001044 585 041456 104455	014 000001		CMP	14(SP),#1 10\$; ERR.FLG,*		5903
585 041456 104455 586 041460 000166			TRAP . WORD	10\$ 55 166			5906
586 041460 000166 587 041462 010570 588 041464 024052 589 041466 012746 007 590 041472 012746 010 591 041476 012746 007 592 041502 012746 006 593 041506 012746 007 594 041512 012746 006 595 041516 012746 000	122		ADD JSR CMP BME TRAP .WORD .WORD .WORD MOV	166 ARR.DAT DUMPER #WRD.56,-(SP) #FNC.23,-(SP) #WRD.52,-(SP) #WRD.12,-(SP) #PHR.4,-(SP) #FIV.FMT,-(SP) #6,-(SP)			500
1590 041472 012746 010 1591 041472 012746 010	122 242 066 426 454 062 006		MOV	#FNC.23,-(SP)	•		5907
592 041502 012746 006 593 041506 012746 007	426		MOV	#WRD.12,-(SP)			

E tak

MISCELLANEOUS C	ODING SE	CTION							0261
11598				ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
11600 041522	010600			**	MOV	SP.R0	; SP.*		
11602 041526 11603 041532 11604 041534 11605 041540	016716 010546 016146 012746	151376 013154 005216 000004			MOV MOV MOV	RD.DATA, (SP) R5,-(SP) ML.REG(R1),-(SP) WFMT.16,-(SP) W4,-(SP) SP.R0	: CLR.DATA		5908
11607 041550	010600	000004			MOV	SP.RO	; SP,*		
11609 041554 11610 041562 11611 041566 11612 041572 11613 041574	012766 062706 062706 104467 006000	000001 000026 000012	000040	10\$:	MOV TRAP MOV ADD ADD TRAP ROR BLO INC CMP BLE COMP BLE CLR CMP BNE MOV TRAP TRAP ADD	14 #1,40(SP) #26,SP #12,SP 67 R0 9\$ R2 R2,#15 9\$ R3 R4 R4,#1 8\$ REG.INIT.FLG (SP),#1 11\$ ML.LUN.R0	*,DODU.FI	.G	5909 5905 5895 5910
11614 041576 11615 041600 11616 041602 11617 041606	103653 005202 020227	000015			INC CMP	9\$ R2 R2,#15	; REG.SEL ;		5894
11618 041610 11619 041612 11620 041614	005103 005204 020427	000001			COM INC CMP	R3 R4 R4,#1	: TST.PAT : TWICE,*		5915 5891
11622 041622 11623 041626	005067 021627	151310 000001			CLR	REG.INIT.FLG	DODU.FLG		5918 5920
11625 041634 11626 041640	016700 104451	151740			MOV TRAP	ML.LUN,R0 51 44 #6,SP	;		5923
11627 041642 11628 041644 11629 041650 11630	062706 000207	000006		115:	ADD RTS	#6,SP PC	:		5789
11631 11632 11637 11638				; Rout ; Maxi	ine Size: imum stack	312 words depth per invocation:	26 words		
11642 11646 041652 11647 041652 11648 041656 11649 041660 11650 041662	004767 104466 006000 103773	176614		T17:: 1\$:	JSR TRAP ROR BLO	PC,\$T17 66 R0 1\$			5925

SEQ 0262

22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<

```
11667 :ML4
11668 :
11669
11670 :
11671 :
11672 :
11673 :
11674 :
11675 :
11676 :
                                                                                                                                      22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 
22-Dec-1980 09:21:22 PA:<NEALE>BL3ML4.BLI.2 (68)
                                          BGNTST:
                                          ! TEST NUMBER: TST 18
                                          ! TEST NAME:
                                                                         COMPOSIT ERROR TEST
11678
11679
11680
11681
11682
11683
11684
11685
11686
11687
11689
11690
11691
11693
11694
11695
11696
11696
11697
11700
11701
11702
11703
11704
11705
11706
                                         TEST DESCRIPTION:
TEST TO SEE IF SETTING EACH
BIT IN THE ERROR REGISTER
CAUSES A COMPOSIT ERROR BY:
                                                        WRITING A SHIFTING ONE THROUGH
THE ERROR REGISTER (SKIPPING THE
READ ONLY BITS) AND TESTING THE
COMPOSIT ERROR BIT IN MLDS
                                                         FOR BEING SET AFTER EACH
                                                         WRITE.
                                         local
                                                DODU FLG,
DAT PAT,
SKIP_MASK;
                                                                                                                                      !DROP UNIT FLAG
                                                                                                                                      !POINTS TO MLER READ ONLY BITS
                                         CLR MBUS;

DODO_FLG = ZERO;

SKIP_MASK = %o'163157';

DAT_PAT = ONE;

DODO_FLG = ZERO;
                                                                                                                                  !LOAD SKIP MASK
!DATA PATTERN SET BIT O IN MLER
                                          incr COUNT from 0 to 15 do
                                                                                                                                    !WRITE AND SHIFT DATA PAT TO MLER 16 TIMES
                                                 if (.DAT_PAT and .SKIP_MASK) neg ZERO
                                                                                                                                  !SKIP IF DAT_PAT FALLS ON READ ONLY BIT
                                                         begin
BGNSUB;
11710
11711 ;
                                                        MLER = .DAT_PAT;
                                                                                                                                   !WRITE DATA_PAT TO MLER
11712
11713
11714
11715
11716
11716
11717
11718
11719
11720
                                                         if .COMP_ERR IS_NOT_SET
                                                                                                                                 !SEE IF DAT_PAT CAUSED A COMP ERROR
                                                               begin

ERRDF (50, ASYNC, DUMPER); !ERROR IF NO COMP ERROR

PRINTB (FOR FMT, FNC 8, PHR 1, WRD 12, FNC 8);

PRINTB (FMT 4, .DAT PAT);

DODU_FLG = ONE;
```

(212)
5927 5955
5958 5959 5960 5963 5966
5963 5966
5968 5970 5972
5975
5976
5977

LABO ML-11 LO	W1140 3C									0265
1778			ML4						22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
1781 042032 1782 042036	012746	000002		MOV MOV TRAP	#2,-(SP) SP,R0 14			; SP,*		
1787 042042 1785 042046 1786 042052 1787 042054	012701 062706 104467 006000	000001 000020	38:	MOV ADD TRAP ROR	#1,R1 #20,SP 67 R0 2\$ R2 R3,#17 1\$ R1 5\$ ML.LUN,R0			: *,DODU.FLG		5978 5974 5979
11789 042060 11789 042060 11790 042062 11791 042064 11792 042070	006302 005203 020327	000017	4\$:	MOV ADD TRAP ROR BLO ASL INC CMP BLE DEC BNE MOV TRAP	R2 R3 R3,#17			; DAT.PAT ; COUNT ; COUNT,*		5984 5963
1793 042072	005301			DEC	R1			; DODU.FLG		5987
11795 042076 11796 042102 11797 042104 11798 042106	016700 104451 104444 000207	151476		MOV TRAP TRAP RTS	ML.LUN,RO 51 44 PC			:		5990
11799	000207		5\$:					•		5927
11800 11801 11806 11807			; Rout ; Maxi	ine Size: mum stack	73 words depth per	invocation:	13 words			
11815 042110 11816 042110 11817 042114 11818 042116 11819 042120 11820 042122	004767 104466 006000 103773 000207	177552	T18:: 1\$:	JSR TRAP ROR BLO RTS	PC.\$T18 66 R0 1\$ PC			;		5992
1821 1822 1823 1828			; Rout ; Maxi	ine Size: mum stack	6 words depth per	invocation:	0 words			
1830 ;	5995	! <blf page=""></blf>								

```
22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (69)
                             BGNTST:
                              ! TEST NUMBER: TST 19
                                TEST NAME:
                                                  ATA BIT TEST
                                TEST DESCRIPTION:
                                        TEST THE ATA BIT FOR SETTING
                                        1. SETTING A BIT IN THE ERROR
                                             REGISTER.
                                             TEST THE ATA BIT FOR CLEARING
AFTER BEING SET BY:
1. WRITING A FUNCTION TO MLCS1.
2. WRITING A ONE INTO THIS
UNITS ATA BIT
                                        TEST THE ATA BIT FOR NOT CLEARING AFTER BEING SET BY:
                                       1. WRITING A ONE INTO THE ATA BIT OF THE OTHER
                                             UNITS.
                             local
                              ATA_SAVE : bitvector [8],
DAT_PAT;
                                                                                           STORES ALL 8 ATA BITS ON READS AND WRITES
                            CLR_MBUS;
MLER = CNE;
MLER = ZERO;
ATA_SAVE = .MLAS;
                                                                                             SET THE ATA BIT
                                                                                             !READ THE ATTN REGISTER
                             If .ATA_SAVE [.ML_DUT] IS_NOT_SET
                                                                                             SEE IF THIS DRIVES ATA BIT IS SET
                             then
                                  begin
ERROF (51, ASYNC, DUMPER);
PRINTB (FOR_FMT, WRD_15, PHR_1, WRD_11, FNC_8);
                                                                                              !ERROR AND EXIT_TST IF NOT SET
                                  EXIT_TST;
11884
11885
11886
                             if .ATTN IS_NOT_SET
                                                                                           SEE IF THE ATTN BIT IS SET
                             then
                                  begin
```

```
11888 :ML4
11889 :
11890
11891 :
                                                                                           22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (69)
                                 ERROF (52, ASYNC, DUMPER);
PRINTB (FIV_FMT, REG_2, WRD_16, PHR_1, WRD_11, FNC_8);
                                 EXIT_TST;
                                 end:
                           MLCS1 = NOOP;
                                                                                        !TRY TO CLEAR THE ATA BIT WITH NOOP FUNC
                            if .ATTN IS_SET
                                                                                         !SEE IF ATA GOT CLEARED
                                 begin
ERROF (53, ASYNC, DUMPER);
PRINTB (FOR_FMT, WRD_15, PHR_2, WRD_11, FNC_2, WRD_19);
                                 EXIT_TST;
                                 end:
                        3 ATA_SAVE = .MLAS;
                                                                                         !READ THE ATTENTION REGISTER
                        5 If .ATA_SAVE [.ML_DUT] IS_SET
                                                                                        !SEE IF THE ATA REG GOT CLEARED BY NO-OP
                                begin
ERRDF (58, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_15, PHR_2, WRD_11, FNC_2, WRD_19);
                    6071

6072 BGNSUB;

6073 MLER = ONE;

6074 MLER = ZERO;

6075 ATA_SAVE = ZEROES;

6076 ATA_SAVE [.ML_DUT] = ONE;

6077 MLAS = .ATA_SAVE;

6078
                                                                                   SET THE ATA BIT
                                                                                      CLEAR ATA SAVE
SET ATA SAVE FOR THIS DRIVE
TRY TO CLEAR THE ATA BY WRITING TO IT.
                           if .ATTN IS_SET
                                                                                        SEE IF THE ATA GOT CLEARED
                            then
                                 begin
ERROF (54, ASYNC, DUMPER); !ERROR IF NO
PRINTB (FIV_FMT, WRD_15, PHR_2, WRD_11, WRD_17, REG_5);
                                                                                           !ERROR IF NOT CLEARED
                            ENDSUB;
                            BGNSUB;
                            MLER = ONE;
                                                                                      SET THE ATA BIT
                            MLER = ZERO:
                            DAT_PAT = ONE;
                                                                                          !DATA PATTERN OF ONE IN FIELD OF ZEROES
                            incr ATA_SEL from 0 to 7 do
                                                                                          !REPEAT LOOP 8 TIMES
11936
11937
11938
11939
11940
11941
11942
                                 if .ATA_SEL neg .ML_DUT
                                                                                         !SKIP IF ATA_SEL EQLS THIS DRIVE NO.
                                    begin
MLAS = .DAT_PAT;
ATA_SAVE = .MLAS;
                                                                                        WRITE DAT PAT TO ATA REGISTER
```

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MACRO M1113 22-DEC-80 12:16 PAGE 281
CZMLABO ML-11 LOGIC TEST
MISCELLANEOUS CODING SECTION
                                                                                                                                                                                                          SEQ 0268
                                                                                                                     22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                             TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BL1.2 (69)
                                                    if .ATA_SAVE [.ML_DUT] IS_NOT_SET !SEE IF THIS DRIVE ATA IS CLEARED
                                                          begin
ERROF (55, ASYNC, DUMPER); !ERROR AND EXIT LOOP IF CLEARED
PRINTB (SIX_FMT, WRD_15, PHR_6, WRD_11, WRD_17, REG_5, PHR_7);
PRINTB (FMT_7, .DAT_PAT);
                                                           end:
                                                    end:
                                             DAT_PAT = .DAT_PAT^ONE;
end;
                                                                                                                  !SHIFT DAT_PAT AND REPEAT
                                       ENDSUB;
ENDTST;
                                                                                           R1,$SAVE3
#40,aML.REG+40
ML.DUT,R3
                                                                                                                                                                                                                  5994
                                                                 $T19:
                                                    151056
                                                                              BISB
                                                                              MOV
                                       177770
000007
151034
000001
151042
151046
151400
                                                                                            #177770,R3
                                                    151040
                                                                              BICB
                                                                              BISB
                                                                                           #1, aml.REG+60
aml.REG+60
aml.REG+70,-(SP)
                                                                              MOV
CLR
MOV
MOV
                                                    151046
                                                                                                                                                   *,ATA.SAVE
                                                                                           ML.DUT,R1
                                                                              ASR
                                                                              ASR
                                                                              ASR
MOV
ADD
MOV
MOV
BIC
                                                                                           SP,R0
R0,R1
R1,-(SP)
ML,DUT,-(SP)
#177770,(SP)
                                                                                                                                                ; ATA. SAVE, *
                                       151360
177770
000001
                                                                                           #1,-(SP)
-(SP)
                                                                              MOV
                                                                              CLR
                                                                                           PC.BLSGT2
                                                                               JSR
                                                                              ADD
                                                                                           R0
1$
55
63
                                                                              BNE
                                                                                                                                                                                                                 6040
                                                                              . WORD
```

ASYNC

ILABO ML-11 LI SCELLANEOUS C									0269
2000				ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	PA:
2002 042256 2003 042260 2004 042264 2005 042270 2006 042274 2007 042300	024052 012746 012746 012746 012746 012746	010022 006416 007366 006454 006046			MORD MOV MOV MOV MOV	DUMPER #FNC.8,-(SP) #WRD.11,-(SP) #PHR.1,-(SP) #WRD.15,-(SP) #FOR.FMT,-(SP) #5,-(SP)	•		6041
2009 042310 2009 042313 2010 042313 2011 042314	012746 010600 104414 104463	000005			MOV MOV TRAP TRAP	14 63	; SP,*		
2012 042316	062706	000014			ADD	#14.SP			6037
2014 042324	032777	100000	150672	15:	BR	#100000,aML.REG+50			6037 6039 6045
2015 042332 2016 042334 2017 042336 2018 042340	001026 104455 000064 010464				TRAP .WORD .WORD .WORD	28 55 64 ASYNC	,		6048
2019 04.2342 2020 04.2350 12021 04.2350 2022 04.2354 2023 04.2364 2024 04.2364 2025 04.2370	024052 012746 012746 012746 012746 012746 012746	010022 006416 007366 006462 010266 006062			MOV MOV MOV MOV	DUMPER #FNC.8,-(SP) #MRD.11,-(SP) #PHR.1,-(SP) #MRD.16,-(SP) #REG.2,-(SP) #FIV.FMT,-(SP) #6,-(SP)			6049
026 042374 027 042400 028 042402 029 042404	012746 010600 104414 104463	000006			MOV MOV TRAP TRAP	14. NO	; SP,*		
2030 042406 2031 042410	000433	000001 150602	150536	2\$:	BR MOV	63 38 #1,aML.REG	:		6045 6053 6055
033 042422	100031	150602			BPL	SS SPL.REG+50			6055
034 042424 035 042426 036 042430	005777 100031 104455 000065 010464 024052				TRAP .WORD .WORD	#1, aml.REG aml.REG+50 5\$ 65 65 ASYNC DUMPER #URD.19,-(SP)	•		6058
2032 042416 2033 042424 2034 042424 2035 042426 2036 042430 2037 042432 2038 042434 2040 042444 2041 042430 2042 042446 2043 042436 2044 042436 2045 042476 2046 042476 2047 042474 2048 042476 2049 042516 2052 042516 2053 042520	012746 012746 012746 012746 012746 012746	006506 007742 006416 007404 006454 006046 000006			BR MOV TST BPL TRAP .WORD .WORD MOV MOV MOV MOV MOV MOV MOV TRAP TRAP ADD JMP MOV ASR ASR	##RD.19,-(SP) #FNC.2,-(SP) ##RD.11,-(SP) #PHR.2,-(SP) ##RD.15,-(SP) #FOR.FMT,-(SP) #6,-(SP) SP,R0 14			6059
2045 042470 2046 042472 2047 042474	010600 104414 104463 062706 000167 017716 016701 006201 006201			70	MOV TRAP TRAP	SP. RO 14 63	; SP,*		
12049 042502	000167	000546		3\$: 4\$: 5\$:	JMP	12\$			6055
12050 042506 12051 042512 12052 042516 12053 042520	017716 016701 006201 006201	000016 000546 150532 151064		5\$:	MOV MOV ASR	63 #16,SP 12\$ aML.REG+70,(SP) ML.DUT,R1 R1 R1	*,ATA.SAVE		6055 6057 6063 6065

MLABO ML-11 LOGIC 1 SCELLANEOUS CODING							22.0 1000 4	SEQ 0	
12056			:ML4				22-Dec-1980 0 22-Dec-1980 0	9:21:22	TOPS PA:
12058 042522 00620 12059 042524 01060 12060 042526 06000 12061 042530 01014 12062 042532 01674 12063 042532 01674 12064 042542 01274	1 10 11 6 6 151044 6 177770 6 000001			ASR MOV ADD MOV MOV BIC MOV	R1 SP,R0 R0,R1 R1,-(SP) ML,DUT,-(SP) #177770,(SP) #1,-(SP) -(SP)	; ATA.SAVE,*			
12066 042550 00476 12067 042554 06270 12068 042560 00530 12069 042562 00102 12070 042564 10443 12071 042566 0000	7 140364 6 000010 6 5			JSR ADD DEC BNE TRAP .WORD .WORD .WORD	PC_BL\$GT2 #10,SP R0 6\$ 55 72 ASYNC DUMPER				6068
12072 042570 01046 12073 042572 02405 12074 042574 01274 12075 042600 01274 12076 042604 01274 12077 042610 01274	6 006506 6 007742 6 006416 6 007404			MOV MOV	#FNC.2,-(SP) #MRD.11(SP)	•			6069
12079 04320 01274 12080 04324 01274 12081 043330 01060 12082 04332 1044 12083 04334 06270	6 006062 6 000006 6 000016		6\$:	MOV MOV MOV TRAP ADD TRAP	MPHR.2,-(SP) MHRD.15,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0 14 M16,SP	; SP.*			6067
12085 042642 01277 12086 042650 00507 12087 042654 00507 12088 042656 01670 12089 042662 00620	7 000001 7 150360 6 150720	150364		MOV	#1.aML.REG+60 aML.REG+60 (SP) ML.DUT,R1 R1	ATA. SAVE			6073 6074 6075 6076
12090 042666 00620 12091 042666 00620 12092 042670 01060 12093 042674 01014 12095 042676 01674 12096 042702 04271 12096 042706 01274 12099 042714 00476 12101 042726 00577 12102 042732 10002 12103 042734 10443 12104 042736 00000 12105 042744 01274 12108 042734 01274 12108 042734 01274	11 6 6 6 150700 6 177770 6 000001			CLR CLR MOV ASR ASR ASR MOV ADD MOV BIC MOV	R1 R1 SP,R0 R0,R1 R1,-(SP) ML,DUT,-(SP) #177770,(SP)	; ATA.SAVE,*			
12096 042702 04271 12097 042706 01274 12098 042712 01164 12099 042714 00476 12100 042720 01667 12101 042726 00577 12102 042732 10002 12103 042734 10443 12104 042736 0006 12105 042742 02405 12106 042742 02405 12108 042754 01274	7 140456 7 000010 7 150272	150316		MOV MOV JSR MOV TST BPL TRAP . WORD . WORD . WORD MOV MOV	RO,R1 R1,-(SP) ML,DUT,-(SP) M1,7770,(SP) M1,-(SP) (SP),-(SP) PC,BL\$PU2 10(SP), aml.REG+70 aml.REG+50 7\$ 55 66 ASYNC DUMPER MREG.5,-(SP) MURD.17,-(SP) MURD.11,-(SP)	; ATA.SAVE,*			6077 6079
12103 042734 10443 12104 042736 00006 12105 042740 01046 12106 042742 02405 12107 042744 01274 12108 042750 01274	6			WORD WORD WORD MORD	55 66 ASYNC DUMPER MREG. 5, -(SP)	•			6082

12111	OGIC TEST			:ML4				22-000-1080 00-24-31	EQ 0271
12112				:				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
12114 042760 12115 042764 12116 042770 12117 042774 12118 043000	012746 012746 012746 012746 010600	007404 006454 006062 000006			MOV MOV MOV MOV TRAP	#PHR.2,-(SP) #MRD.15,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14	; SP,*		
12119 043002 12120 043004	104414	000016			TRAP	14 #16.SP			6081
12121 043010 12122 043014 12123 043016 12124 043020	062706 104467 006000 103707	000016 000010		7\$:	ADD ADD TRAP ROR BLO	#16.SP #10.SP 67 R0 68	1		6081 6070 6084
2125 043022 12126 043024	104402	000001	150202	8\$:	TRAP	•	;		6086
2127 043032 2128 043036	005077 012703	000001 150176 000001			MOV	#1,aML.REG+60 aML.REG+60 #1,R3	*,DAT.PAT		6086 6086 6096 6096 6096
2130 043042	020267	150532		9\$:	CLR	R2 R2,ML.DUT	*,DAT.PAT ATA.SEL ATA.SEL,*		6093
5135 073055 5131 073055	001471 010377 017716	150166 150162 150514			MOV	R3, aML. REG+70	: DAT.PAT.* : *,ATA.SAVE		6096 6099 6101
2134 043062 2135 043066 2136 043070	016701 006201 006201	150514			MOV ASR ASR	R2 R2,ML.DUT 10\$ R3,aML.REG+70 aML.REG+70,(SP) ML.DUT,R1 R1	; ", AIA. SAVE		6101
2137 043072 12138 043074 12139 043076 12140 043100	006201 010600 060001 010146				ROR BLO TRAP MOV CLR MOV BEQ MOV MOV ASR ASR ASR ASR MOV ADD MOV BIC	R1 SP.R0 R0,R1 R1,-(SP) ML.DUT,-(SP) #177770,(SP)	; ATA.SAVE,*		
2141 043102 2142 043106	016746 042716 012746	150474 177770 000001				ML_DUT, -(SP) #177770, (SP) #1,-(SP)			
2145 043120 12146 043124 12147 043130	012746 005046 004767 062706 005700 001040	140014 000010			JSR ADD TST	PC.BL\$GT2 #10,SP R0			
2148 043132 2149 043134 2150 043136	001040 104455 000067				TRAP .WORD	10 s 55 67			6104
2121 023140	024052	007514			. WORD	ASYNC DUMPER			/100
12143 043112 12144 043116 12145 043120 12146 043124 12147 043130 12148 043132 12149 043134 12150 043140 12151 043140 12152 043142 12153 043144 12154 043150 12155 043154 12156 043160 12157 043164 12158 043170 12169 043206 12163 043210 12164 043212 12165 043216	012746 012746 012746	007516 010310 006470 006416 007504 006454 006100 000007			MOV CLR JSR ADD TST BNE TRAP .WORD .WORD MOV MOV MOV MOV MOV MOV MOV MOV MOV MOV	#177770, (SP) #1,-(SP) -(SP) PC,BL\$GT2 #10,SP R0 10\$ 55 67 ASYNC DUMPER #PHR.7,-(SP) #REG.5,-(SP) #WRD.17,-(SP) #WRD.11,-(SP) #WRD.11,-(SP) #WRD.15,-(SP) #SIX.FMT,-(SP) #7,-(SP) SP,R0 14 R3,(SP)	•		6105
12158 043170	012746	00/504			MOV	#PHR.6,-(SP) #URD.15,-(SP)			
12160 043200	012746	000007			MOV	#7,-(SP) SP 80	; SP,*		
12162 043206 12163 043210	104414				TRAP	14 R3.(SP)	; DAT.PAT,*		6106
12164 043212 12165 043216	010316 012746 012746	004562 000002			MOV	R3,(SP) WFMT.7,-(SP) W2,-(SP)			0.00

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 285	SEQ 0272
12167 12168 12169	PML4	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
12170 043222 010600 12171 043224 104414 12172 043226 062706 000024	MOV SP.RO ; SP.* TRAP 14 ADD #24.SP ; BR 11\$	6107
12175 043234 006303 12175 043236 005202 12176 043240 020227 000007	MOV SP,RO ; SP,* TRAP 14 ADD #24,SP ; BR 11\$ 10\$: ASL R3 ; DAT.P INC R2 CMP R2,#7 ; ATA.S BLE 9\$ 11\$: TRAP 67 ROR RO BLO 8\$ 12\$: TST (SP)+ ; RTS PC	AT 6112 EL 6092
12177 043244 003677 12178 043246 104467 12179 043250 006000	10\$: ASL R3 ; DAT.P INC R2 ; ATA.S CMP R2,M7 ; ATA.S BLE 9\$ 11\$: TRAP 67 ROR R0 BLO 8\$ 12\$: TST (SP)+ ; RTS PC	6113
12181 043254 005726 12182 043256 000207	12\$: TST (SP)+ ;	5994
12184 12185 12190 12191	; Routine Size: 302 words ; Maximum stack depth per invocation: 16 words	
12195 12199 043260 12200 043260 004767 176640 12201 043264 104466 12202 043266 006000 12203 043270 103773 12204 043272 000207	T19:: 1\$: JSR PC,ST19 TRAP 66 ROR RO BLO 1\$ RTS PC	6115
12206 12207 12212 12213 12214 ; 6117 ! <blf f<="" td=""><td>Routine Size: 6 words : Maximum stack depth per invocation: 0 words</td><td></td></blf>	Routine Size: 6 words : Maximum stack depth per invocation: 0 words	

```
12216 :ML4
12217
12218
                                                                                                              22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                   TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (70)
                                 BGNTST:
                                    TEST NUMBER: TST 20
                                     TEST NAME:
                                                           SEARCH FUNCTION TEST
                                     TEST DESCRIPTION:
                                              TEST THE SEARCH FUNCTION BY:
                                              1. DOING A SEARCH FUNCTION AT ARRAY ZERO AND TEST GO, ERROR BITS AND ATTN FOR SETTING/NOT SETTING.
                                      2. DOING SEARCH FUNCTIONS AT ALL PRESENT ARRAYS' AND TEST ATTN SET
                                       3. DOING SEARCH FUNCTIONS AT ALL NOT PRESENT ARRAYS AND TEST
                                                    ATTN CLEARED.
                                 CLR_MBUS;
MLDA = ZEROES;
MLCS1 = SEARCH;
                       6148
6149
6150
6151
6152
6153
6154
6155
6156
6157
6160
6161
6165
6166
6167
6168
6169
                                                                                                            !DO A SEARCH FUNCTION
                                 if .GO IS_SET
                                                                                                              !SEE IF GO IS SET
                                       begin !ERROR IF NOT SET PRINTE (FIV_FMT, WRD_1, PHR_2, WRD_11, FNC_10, WRD_19);
                                 if .ILF IS_SET
                                                                                                              !SEE IF ILF IS SET
                                  then
                                       begin
ERROF (57, ASYNC, DUMPER); !ERROR IF SET
PRINTB (FIV_FMT, WRD_3, PHR_5, WRD_11, FNC_11, FNC_10, WRD_19);
                                 if .ATTN IS_NOT_SET
                                                                                                             !SEE IF ATTN IS SET
                                       begin
ERROF (59, ASYNC, DUMPER); !ERROR IF NOT
PRINTB (FIV_FMT, WRD_16, PHR_1, WRD_12, FNC_10, WRD_19);
                                                                                                             !ERROR IF NOT SET
```

```
22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                          TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (70)
else
       begin
       incr ARR_SEL from 0 to .LST_ARR by .ARR_INC do !DO SEARCH AT ALL PRESENT ARRAYS
            begin
BGNSUB;
CLR_MBUS;
MLDA = .ARR_SEL;
                                                                       !LOAD DSA REG WITH ARR_SEL !DO A SEARCH FUNCTION
             MLCS1 = SEARCH:
             if .OPI IS_SET
                                                                       !READ ATTN
             then
                 begin
ERRDF (60, ASYNC, DUMPER); !ERROR IF NOT SET
PRINTB (FIV_FMT, WRD_4, PHR_5, WRD_12, FNC_10, WRD_19);
PRINTB (FMT_9, .ARR_SEL);
            ENDSUB:
            end:
                                                    SEE IF LSS 17 ARRAYS ARE PRESENT
       if .OP_NUM_ARR Lss %0'000017'
            incr ARR_SEL from .LST_ARR + .ARR_INC to .ARR_16 by .ARR_INC do !DO A SEARCH AT ALL NOT PRESENT !ARRAYS IF LSS 17
                  begin
BGNSUB;
                  CLR_MBUS;
MLDA = .ARR_SEL;
MLCS1 = SEARCH;
                                                                       !LOAD DSA REG WITH ARR_SEL
                                                                       !DO A SEARCH FUNCTION
                  if .OPI IS_NOT_SET
                                                                       !SEE IF OPI IS SET
                   then
                       begin
ERRDF (61, ASYNC, DUMPER); !ERROR IF NOT SET
PRINTB (FIV_FMT, WRD_4, PHR_1, WRD_12, FNC_10, WRD_19);
PRINTB (FMT_9, .ARR_SEL);
                  ENDSUB:
                  end:
       end:
 ENDIST:
```

CZMLABO ML-11 LOGIC MISCELLANEOUS CODING			11113 22-DEC-8			SEQ 0275
12332 043274 0041 12334 043300 1527 12334 043306 0167 12335 043312 0427	03 150270 03 177770	147706 147670	\$T20: JSR BISB MOV BIC BICB BISB	R1.\$SAVE3 #40.aML.REG+40 ML.DUT.R3 #177770.R3	•	6116 6120
12337 043324 1503 12338 043330 0050	77 000007 77 147664 77 147650 77 000031 77 000001		BISB CLR MOV	R3, aML.REG+40 aML.REG+30 #31, aML.REG	:	6147 6148 6150
12340 043342 1327	77 000001	147612 147604	RITR	#1 2M RFG		6150
12342 043352 1044 12343 043354 0000 12344 043356 0104	55 70 64		BEQ TRAP .WORD .WORD MOV MOV	1\$ 55 70 ASYNC DUMPER	•	6153
12345 043360 0240 12346 043362 0127 12347 043366 0127 12348 043376 0127 12349 043376 0127 12350 043402 0127	46 006506 46 010050 46 006416 46 007404 46 006276		MOV MOV MOV MOV MOV MOV	#WRD.19,-(SP) #FNC.10,-(SP) #WRD.11,-(SP) #PHR.2,-(SP) #WRD.1,-(SP) #FIV.FMT,-(SP) #6,-(SP)		6154
12352 043412 0127	46 006062 46 000006		MOV	#6,-(SP) SP,R0 14	; SP.*	
12354 043420 1044 12355 043422 0627 12356 043426 1327	14 06 000016 77 000001	147600	MOV TRAP ADD 18: BITB	#16,SP #1, aML_REG+60		6152 6157
12357 043434 0014 12358 043436 1044 12359 043440 0000	30 55 71		BEQ TRAP .WORD .WORD	#16.SP #1.aML.REG+60 28 55 71		6160
12359 043440 0000 12360 043442 0104 12361 043444 0240 12362 043446 0127 12363 043452 0127 12364 043456 0127 12366 043466 0127 12367 043472 0127 12368 043476 0127 12370 043506 0106 12371 043510 1044 12372 043512 0627 12373 043516 0327 12374 043524 0010 12377 043530 0000 12377 043534 0240 12378 043534 0240 12379 043536 0127 12380 043546 0127	52 46 006506 46 010050 46 010060 46 006416 46 007472 46 006314 46 006062 46 000007		MOV MOV MOV MOV MOV	ASYNC DUMPER WURD.19,-(SP) WFNC.10,-(SP) WFNC.11,-(SP) WHRD.11,-(SP) WHR.5,-(SP) WARD.3,-(SP)		6161
12369 043502 0127 12370 043506 0106	46 000007		MOV	#7(SP) SP.R0	; SP,*	
12373 043516 0327 12373 043516 0327	06 000020 77 100000	147500	2\$: BIT	#100000 am PEG+50	:	6159 6164
12374 043524 0010 12375 043526 1044 12376 043530 0000	55 73		TRAP .WORD	55 73	,	6167
12378 043534 0240 12379 043536 0127 12380 043542 0127 12381 043546 0127	52 46 006506 46 010050 46 006426		MOV MOV TRAP ADD BIT BNE TRAP . WORD . WORD . WORD MOV MOV MOV	3\$ 55 73 ASYNC DUMPER MURD. 19,-(SP) MFNC. 10,-(SP) MURD. 12,-(SP)		6168

TOP	22-0-0-1090 00-24-71				:ML4				585
TOPS PA:	22-Dec-1980 09:24:31 22-Dec-1980 09:21:22				1				\$8% \$8\$
			#PHR.1,-(SP) #WRD.16,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14	MOV MOV MOV MOV TRAP			007366 006462 006062 000006	012746 012746 012746 012746	86 043552 87 043556 88 043566 89 043566
		; SP,*	SP.R0	TRAP			******	104414	390 043372 391 043574
6166 6164 6173			#16,SP PC	RTS	70.		000016	000207	363 643608
61/3		, ADD CEI	LST.ARR,R2 ARR.INC,R3	MOV	3\$:		145766 145746	016703	6135.W 555
417		; ARR.SEL	81 65	BR	48:			000467	97 843818
6179		:	#40,aML.REG+40 ML.DUT.RO #177770,RO #7,aML.REG+40	ADD RTS MOV CLR BR TRAP BISB MOV BIC BICB BISB	45:	147364	000040 147746	152777 016700	00 00 00 00 00 00 00 00 00 00 00 00 00
			#7.aML.REG+40	BICB		147346	177770 000007 147342	142777 150077 010177	05 07.35.40 07.35.40
6177 6178 6180		: ARR.SEL,*	RO, aML.REG+40 R1, aML.REG+30 #31, aML.REG #20000, aML.REG+60	MOV		147270	147326 000031 020000	010177	05 043652
6180		1	#20000, aML.REG+60	BIT		147270 147342	020000	032777	06 043664
6183		;	5\$ 55 74	MOV BIT BEQ TRAP .WORD .WORD MOV MOV MOV				104455	9 043674
			ASYNC	. WORD				010464	0 043700
6184			DUMPER MARD. 19, -(SP) MFNC. 10, -(SP) MARD. 12, -(SP)	MOV			006506 010050	012746 012746	2 043704 3 043710
			MRD.12,-(SP) MPHR.5,-(SP)	****			006426 007472 006322	012746 012746 012746	4 043714 5 043720 6 043724
			WRD.4,-(SP) WFIV.FMT,-(SP)	MOV			006062 000006	012746	7 043730
		; SP,*	SP,R0	MOV			000006	010600	17 043730 18 043734 19 043740 20 043742
6185		; ARR.SEL,*	MURD.12,-(SP) MPHR.5,-(SP) MURD.4,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0 14 R1,(SP) MFMT.9,-(SP) M2,-(SP) SP,R0 14 M22,SP 67 R0 48 R3,R1 R1,R2 48 OP.NUM.ARR,M17	MOV			004644	010116	7 023744 2 043746
		; SP,*	#2,-(SP) SP.RO	MOV			000002	012746	23 043752
6182		:	14 #22.SP	TRAP			000022	104414	5 043760
6186			67 R0	TRAP	5\$:			104467	27 043766 28 043770
6173		: *.ARR.SEL	4\$ R3,R1	BLO				103712	29 043772 30 043774
		; *,ARR.SEL ; ARR.SEL,*	R1.R2	BLE	6\$:			020102	51 043776 52 044000
6191		,	OP.NUM.ARR,#17 10\$	BGE		000017	145552	026727	33 044002 34 044010
6194		,	OP.NUM.ARR,#17 10\$ LST.ARR,R1 ARR.INC,R1 ARR.16,R3	MOV MOV MOV MOV MOV MOV MOV TRAP ADD TRAP ROR BLO ADD CMP BLE CMP BGE MOV ADD MOV			145560 145540 145546	026727 002102 016701 066701 016703	26 043762 27 043766 28 043772 30 043774 31 043776 32 044000 33 044010 34 044010 35 044012 36 044016 37 044022

12430				:ML4				22-0-0-1980 00-24-31	TO
2440				:				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	PA
2442 044026	016702	145530			MOV	ARR.INC.R2			
2444 044034	104402	0000/0	4/7450	75:	BR TRAP BISB	2			61
2446 044044	016700	000040 147532	147150		MOV	#40, aML.REG+40 ML.DUT, RO	•		61
2447 044050 2448 044054	042700	177770 000007 147126	147132		BIC BICB BISB	#177770,R0 #7,24L.REG+40			
2449 044062 2450 044066	150077	147126			BISB	RO, aML.REG+40 R1.aML.REG+30	; ARR.SEL,*		61
2451 044072 2452 044100	012777	147112 000031 020000	147054 147126		MOV	#31.aML.REG #20000,aML.REG+60			61 62 62
2453 044106	001035	020000	147.120		BNE	85			62
2455 044112	000075				. WORD	8\$ 55 75 ASYNC			OZ
2457 044116	024052	004504			WORD				
2459 044124	012746	006506 010050			MOV BIT BNE TRAP .WORD .WORD MOV MOV MOV MOV	#FNC.10,-(SP)			62
2457 044116 2458 044120 2459 044124 2460 044130 2461 044134 2462 044140 2463 044144 2464 044150 2465 044154 2466 044156 2467 044160 2468 044162 2469 044162	012746	007366			MOV	MARD.19,-(SP) #FNC.10,-(SP) #URD.12,-(SP) #PHR.1,-(SP) #URD.4,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14			
2463 044144	012746	006062			MOV	WFIV.FMT,-(SP)			
2464 044150 2465 044154	012746	000006			MOV	#6,-(SP) SP,R0	; SP,*		
2466 044156 2467 044160	010116				MOV	14 R1, (SP)	; ARR.SEL,*		62
2468 044162 2469 044166	012746	000644			MOV	R1,(SP) #FMT.9,-(SP) #2,-(SP) SP,R0			
2470 044172 2471 044174	010600				MOV TRAP MOV MOV MOV TRAP	SP.R0	; SP,*		
2472 044176	062706	000022		8\$:	ADD TRAP ROR BLO ADD CMP	#22.SP 67	:		62
2474 044204	006000				ROR	RO 7\$ R2.R1 R1.R3 7\$ PC			-
2476 044210	060201			98:	ADD	R2,R1	; *,ARR.SEL ; ARR.SEL,*		61
2478 044214	003707			10\$:	BLE	7\$, ANN. SEL, "		41
12480	000207					: 234 words k depth per invocation: 13	•		61

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22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (71)
BGNTST:
   TEST NUMBER: TST 21
   TEST NAME:
                    READ IN PRESET TEST
   TEST DESCRIPTION:
          TEST THE READ IN PRESET FUNCTION BY:
          1. PERFORMING A READ-IN-PRESET FUNCTION AND TESTING GO, ERROR BITS
                AND VV FOR SET / NOT SET.
CLR_MBUS;
                                                       !DO A READ IN PRESET FUNCTION
MLCS1 = RD_IN_PRE;
if .GO IS_SET
                                                           !SEE IF GO IS NOT SET
     begin
ERRDF (62, ASYNC, DUMPER); !ERROR IF SE
PRINTB (FIV_FMT, WRD_1, PHR_2, WRD_11, FNC_11, WRD_19);
if .ILF IS_SET
                                                            !SEE IF ILF IS NOT SET
     begin
ERRDF (63, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_3, PHR_5, WRD_11, FNC_11, WRD_19);
if .OPI IS_SET
                                                           !SEE IF OPI IS NOT SET
then
     begin
ERROF (64, ASYNC, DUMPER); !ERROR IF SE
PRINTB (FIV_FMT, WRD_4, PHR_5, WRD_11, FNC_11, WRD_19);
If .VV IS_NOT_SET
                                                          !SEE IF VV IS SET
then
     begin
ERROF (65, ASYNC, DUMPER); !ERROR IF NOT
PRINTB (FIV_FMT, WRD_18, PHR_1, WRD_11, FNC_11, WRD_19);
ENDTST:
```

CELLANEOUS C	ODTIMO 25	CIION							0280
2573				ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
2579 044234 2580 044242 2581 044246	152777 016700 042700	000040 147334 177770	146752	\$T21:	BISB MOV BIC	#40.aML.REG+40 ML.DUT.RO #177770,RO			6218
	150077	000007 146730 000021 000001	146734		BICB BISB MOV	#7,aML.REG+40 RO,aML.REG+40 #21,aML.REG			623/
85 044272	132777	000001	146662 146654		BITB	#1.aML.REG	:		6234
7 044302 8 044304 9 044306	104455 000076 010464				TRAP .WORD .WORD .WORD MOV	55 76 ASYNC DUMPER	;		6239
07310 107316 12 04316 13 04328	012746 012746 012746 012746	006506 010060 006416 007404			MOV MOV MOV		;		6240
95 044335 96 044336 97 044342 98 044346 99 044350 00 044352	012746 012746 012746	006276 006062 000006			MOV MOV	#FNC.11,-(SP) #WRD.11,-(SP) #PHR.2,-(SP) #WRD.1,-(SP) #FIV.FMT,-(SP) #6,-(SP)			
	104414	000016			MOV TRAP ADD	SP.RO 14 #16,SP	; SP,*		4239
D LIKE WA	132777	000016 000001	146650	1\$:	BITB	#1,aML.REG+60	:		6238 6243
3 044366 34 044370 95 044372	104455 000077 010464				TRAP .WORD .WORD .WORD	25 55 77 ASYNC DUMPER	,		6246
06 044374 07 044376 08 044402 09 044406	024052 012746 012746 012746	006506 010060 006416 007472 006314 006062 000006			MORD MOV MOV MOV	DUMPER MARD.19,-(SP) MFNC.11,-(SP) MURD.11,-(SP) MPHR.5,-(SP) MRD.3,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0 14			6247
09 044406 10 044412 11 044416 12 04422 13 04423 14 04432 15 04433 16 04436 17 04443 18 0445 19 0445 21 0445 21 0446 22 0446 23 0446 24 0446 25 0447	012746 012746 012746 012746 012746 010600 104414 062706 032777 001426 104455 000100 010464	006314 006062 000006			MUV	MARD.3,-(SP) MFIV.FMT,-(SP) M6,-(SP)			
515 044434	104414	000016			TRAP	14 #16 SP	; SP,*		6265
617 04442	032777	000016 020000	146564	2\$:	BIT	#16.SP #20000,aML.REG+60	:		6245 6250
19 044452 20 044454 21 044456	104455 000100 010464				TRAP .WORD .WORD	55 100 ASYNC			6253
610 044416 611 044416 612 044426 613 044426 614 044436 615 044436 616 044436 617 044432 618 044450 619 044454 621 044456 622 044460 623 044462 624 044466	012746 012746 012746 012746 012746	006506 010060 006416 007472			MOV MOV TRAP ADD BIT BEQ TRAP . WORD . WORD . WORD MOV MOV MOV	3\$ 55 100 ASYNC DUMPER MURD.19,-(SP) MFNC.11,-(SP) MURD.11,-(SP) MPHR.5,-(SP)			6254

ZMLABO ML-11 LI	- 1140 JE	C110H								SEQ 02	
12628				ML4					22-Dec-1980 09 22-Dec-1980 09	:24:31 :21:22	TOP PA:
12631 044502 12632 044506 12633 044512	012746 012746 012746 010600	006322 006062 000006			MOV MOV MOV	#WRD.4,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP.RO 14		; SP,*			
1205 04520 1205 04523 1205 04523	104414 062706 132777	000016 000100	146470	3\$:	MOV TRAP ADD BITB	#10,5P		:			625 625
1200 0433	001026 104455 000101 010464				TRAP .WORD	55 101 ASYNC		:		•	626
12642 044546 12644 044552 12645 044556 12646 044562 12647 044566 12648 044572	024052 012746 012746 012746 012746 012746 012746	006506 010060 006416 007366 006502 006062 000006			ADD BITB BNE TRAP .WORD .WORD MOV MOV MOV MOV MOV MOV MOV MOV MOV MOV	#100, aml. REG+50 4\$ 55 101 ASYNC DUMPER #WRD.19,-(SP) #FNC.11,-(SP) #WRD.11,-(SP) #WRD.18,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14		;			626
12650 044602	010600	00000			MOV	SP.R0		; SP,*			
12652 044606	062706 000207	000016		48:	ADD RTS	#16,SP PC		:		8	625 621
12655 12656 12661 12662				; Rout ; Maxi	ine Size: mum stack	120 words depth per invocation:	7 words				
12666 12670 044614 12671 044614 12672 044620 12673 044622 12674 044624 12675 044626 12676 12677	004767 104466 006000 103773 000207	177414		T21:: 1\$:	JSR TRAP ROR BLO RTS	PC,\$T21 66 R0 1\$ PC		:		6	626
12676 12677				; Rout		6 words depth per invocation:					

6265 ! <BLF/PAGE>

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22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                              TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BL1.2 (72)
BGNTST:
  TEST NUMBER: TST 22
  TEST NAME:
                    ILLEGAL FUNCTION TEST
  TEST DESCRIPTION:
          TEST THE DETECTION OF ILLEGAL
          FUNCTIONS WRITTEN TO MLCS1
          WRITING ALL POSSIBLE ILLEGAL FUNCTIONS TO MLCS1. THEN TEST GO AND ERROR BITS CLEARED.
local
     BAD BITS,
BAD FUNC;
                                                               ISTORES A COUNT TO GENERATE BAD FUNCTIONS
                                                               !STORES GENERATED BAD FUNCTION
BAD BITS = ZEROES:
                                                               !CLEAR BAD BITS
incr CNT_1 from 0 to 2 do
                                                              !REPEAT LOOP 3 TIMES
     begin
BAD_BITS = .BAD_BITS + %0'2';
                                                            !ADD 2 TO BAD_BITS
     incr CNT_2 from %0'1' to %0'71' by %0'10' do !REPEAT LOOP GENERATING 'GOOD' FUNCTIONS begin BGNSUB;
          CLR_MBUS;
          BAD_FUNC = .CNT_2 + .BAD_BITS;
FIRST_BLK_XFER ();
MLCS1 = .BAD_FUNC;
                                                               ADD BAD BITS TO CNT 2 GENERATING BAD FUNCTIONS SET UP & FIRST BLOCK XFERR
                                                               !LOAD MLCST WITH TWO BAD FUNCTIONS
          if .ILF IS_SET
                                                               !SEE IF ILF IS SET
          then
                begin
               if .GO IS_SET
                                                             !IF ILF IS SET THEN SEE IF GO IS SET
                then
                    begin
ERROF (66, ASYNC, DUMPER); !ERROR IF GO SET WITH BA
PRINTB (FIV_FMT, WRD_1, PHR_2, WRD_11, FNC_12, WRD_19);
PRINTB (FMT_12, .BAD_FUNC);
                                                               !ERROR IF GO SET WITH BAD FUNCTION
                end
```

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MACRO M1113 22-DEC-80 12:16 PAGE 297
CZMLABO ML-11 LOGIC TEST
MISCELLANEOUS CODING SECTION
                                                                                                                                                                                                                                                               SEQ 0284
                                                                                                                                                     22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                                                      TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (72)
                                                                  else
                                                                         begin
ERRDF (67, ASYNC, DUMPER); !ERROR BAD FUNCTION DON'T CAUSE ILF
PRINTB (FIV_FMT, WRD_3, PHR_1, WRD_11, FNC_12, WRD_19);
PRINTB (FMT_12, .BAD_FUNC);
                                                                  if .OPI IS_SET
                                                                                                                                                    !SEE IF OPI IS SET
                                                                  then
                                                                         begin
ERROF (68, ASYNC, DUMPER); !ERROR IF SET
PRINTB (FIV_FMT, WRD_4, PHR_5, WRD_12, FNC_12, WRD_19);
PRINTB (FMT_12, .BAD_FUNC);
                                                                  ENDSUB:
                                                                  end:
                                                         end:
                                                 ENDIST:
                                                                                                                    R1,$SAVE5
                                                 137262
                                                                                  $T22:
                                                                                                   CLR
CLR
ADD
MOV
MOV
ADD
TRAP
                                                                                                                   R1
R1
#2.R4
#1.R2
R4.R5
R2.R5
                                                                                                                                                                                          BAD.BITS
                                                                                                                                                                                          CNT.1
                                                 000002
                                                                                   15:
                                                                                                                                                                                          *,BAD.BITS
                                                                                                                                                                                          *, CNT.2
                                                                                                                                                                                         BAD.BITS,*
                                                                                   25:
                                                                                   35:
                                                                                                                   2
#40,aML.REG+40
ML.DUT.RO
#177770,RO
#7,aML.REG+40
RO,aML.REG+40
R5,R3
PC,FIRST.BLK.XFER
R3,aML.REG
#1,aML.REG+60
48
                                                 000040
146712
177770
000007
146306
                                                                  146330
                                                                                                   BISB
                                                                                                   MOV
BIC
BICB
BISB
MOV
JSR
MOV
BITB
    12788
12789
12790
12791
12792
12793
12794
12795
12796
12798
12798
                                                                  146312
                044674
044702
044716
044714
044720
044726
044730
044736
044742
044744
                                                                                                                                                                                          * ,BAD . FUNC
                                                 151124
146234
000001
                                                                                                                                                                                          BAD. FUNC, *
                                                                  146306
                                                                                                   BEQ
                                                                                                                   #1,aML.REG
6$
55
102
                                                 000001
                                                                 146216
                                                                                                   BITB
                                                                                                                                                                                                                                                                         6309
                                                                                                   BEQ
                                                                                                   TRAP
                                                                                                                                                                                                                                                                         6312
                                                                                                   . WORD
```

. WORD

ASYNC

ZMLABO ML-11 L IISCELLANEOUS C	ODING SE	CTION				12:16 PAGE 298			0285
12802 12803 12804				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOP:
12805 044746 12806 044750 12807 044754 12808 044760 12809 044770 12810 044770 12811 044774	024052 012746 012746 012746 012746 012746 012746	006506 010100 006416 007404 006276 006062			MORD MOV MOV MOV MOV MOV	DUMPER MURD.19,-(SP) MFNC.12,-(SP) MURD.11,-(SP) MURD.1,-(SP) MURD.1,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0 14			631
12813 05000	010600	000006			MOV MOV TRAP	SP.RO	; SP,*		
12815 045010 12816 045012	010316 012746	005004 000002			MOV	R3, (SP) #FMT.12,-(SP)	; BAD.FUNC,*		6314
12818 045022 12818 045022	010600	000002			MOV MOV TRAP	SP, RO	; SP.*		
12820 045026 12821 045030 12822 045032 12822 045032	000433 104455 000103			48:	BR TRAP .WORD	R3, (SP) WFMT.12, -(SP) W2, -(SP) SP,R0 14 5\$ 55 103 ASYNC DUMPER	;		631 632
1282X 045036 12825 045040 12826 045044 12827 045050 12828 045054 12829 045060 12830 045064	024052 012746 012746 012746 012746 012746	006506 010100 006416 007366 006314 006062			BR TRAP .WORD .WORD .WORD MOV MOV MOV MOV MOV MOV	DUMPER #URD.19,-(SP) #FNC.12,-(SP) #URD.11,-(SP) #PHR.1,-(SP) #URD.3,-(SP) #FIV.FMT,-(SP) #6,-(SP)			632
12832 045074	010600	000006			MOV	CD DIT	; SP,*		
12835 045100 12835 045102 12836 045102	104414 010316 012746 012746	005004 000002			MOV MOV	14 R3,(SP) WFMT.12,-(SP) W2,-(SP) SP,R0 14	; BAD.FUNC,*		632
12837 045112	010600				MOV	SP,RO	; SP,*		
12839 045116 12840 045122 13841 045123	010600 104414 062706 032777 001435	020000	146104	5\$: 6\$:	ADD BIT	#22,5P	:		6319
12836 045106 12837 045112 12838 045114 12839 045116 12840 045122 12841 045130 12842 045132 12843 045134 12844 045136 12845 045140	104455 000104 010464				TRAP .WORD .WORD	55 104 ASYNC	;		6320
12835 045102 12836 045106 12837 045112 12838 045114 12839 045116 12840 045122 12841 045130 12842 045132 12843 045134 12844 045134 12845 045140 12846 045142 12847 045146 12848 045152 12849 045156 12850 045162 12851 045166 12852 045172 12853 045176 12854 045204	012746 012746 012746 012746 012746 012746	006506 010100 006426 007472 006322 006062 000006			MOV MOV MOV TRAP ADD BIT BEQ TRAP WORD WORD MOV MOV MOV MOV MOV MOV MOV MOV MOV MOV	78 55 104 ASYNC DUMPER MURD. 19, -(SP) MFNC. 12, -(SP) MURD. 12, -(SP) MURD. 4, -(SP) MFIV.FMT, -(SP) MF, -(SP) SP, R0 14 R3, (SP) MFMT. 12, -(SP)			6325
12853 045176	010600	00000			MOV	SP.RO	; SP.*		
12855 045202 12856 045204	104414 010316 012746	005004			MOV	R3,(SP) #FMT,12,-(SP)	; BAD.FUNC,*		6330

ZMLABO ML-11 LO ISCELLANEOUS CO	DING SE	CTION	ALRU MIII3 Z	2-021-00	12:16 PAGE 299			SEC	0286
12858 12859 12860			:ML4					22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
12861 045210 12862 045214	012746	000002		MOV	#2(SP) SP.R0 14		; SP,*		
12864 045220 12864 045220 12865 045224 12866 045224	104414 062706 104467 006000	000022	78:	TRAP ADD TRAP ROR	14 #22,SP 67 R0 38 #10,R2 R2,#71 28 R1 R1,#2 88 18		1		6327 633
12864 0X 5234 12867 0X 5234	062702 020227	000010 000071		ROR BLO ADD CMP BLE INC CMP BGT	#10.R2 R2.#71		: *,CNT.2 : CNT.2,*		6297
12872 045244	005201 020127	000002		INC	R1 R1 .#2		: CNT.1		6293
12873 045252 12874 045254 12875 045260	003002 000167 000207	177360	85:	BGT JMP RTS	8\$ 1\$ PC				6264
12877 12878 12883 12884			; Rout ; Maxi		141 words depth per invocation:	15 words			
12888 12892 045262 12893 045262 12894 045266 12895 045270 12896 045272 12897 045274	004767 104466 006000 103773 000207	177342	T22:: 1\$:	JSR TRAP ROR BLO RTS	PC,\$T22 66 R0 1\$ PC		:		6336
12898 12899 12900 12905 12906			; Rout ; Maxin		6 words depth per invocation:	0 words			

TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BL1.2 (73) 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 BGNTST: TEST NUMBER: TST 23 TEST NAME: REGISTER MODIFICATION REFUSED TEST TEST DESCRIPTION: TEST THE DETECTION OF A REGISTER MODIFICATION REFUSED BY: 1. WRITTING TO MLCS1, MLDA AND MLER WHILE THE DRIVE IS BUSY AND TEST RMR BIT SET. ALSO SEE IF THE DRIVE ASSERTED EXCEPTION BY TESTING THE TRE BIT SET. incr CNT from 0 to 2 do !REPEAT LOOP 3 TIMES begin BGNSUB; CLR_MBUS; MLCS1 = write; !DO A WRITE FUNCTION case .CNT from 0 to 2 of !WRITE TO SELECTED REGISTERS FORCING RMR set MLCS1 = %0'000000'; [1] : MLDA = ONES; [2] : MLER = ONES DELAY (FRTY_US); if .RMR IS_NOT_SET !SEE IF RMR GOT SET begin ERRDF (69, ASYNC, DUMPER); !ERROR IF NOT SET PRINTB (FOR_FMT, WRD_21, PHR_1, WRD_11, WRD_21);

2965 2966	ML4							22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (73)</neale>	
2968 2970 2971 2971 2973 2973		6392 6393 6394 6395 6396 6397 6398	if	begin ERRDF PRINTB end:	NOT_SET	NC, DUMP T, WRD_2	ER); 0, PHR_1, WRD_11,		EXCEPTION BY TESTING TRE	
975 976 977 978		6399 6400 6401 6402 6403	END end	SUB;						
983 987	045276	004167	136542		\$123:	JSR TST	R1.\$SAVE2			6338
989	045304	005002				CLR	R2	; CNT		6365
25 25 26 26 26 26 26 26	045306 045310 045316	104402 152777 016701	000040 146260	145676	1\$:	CLR TRAP BISB MOV	2 #40,aML.REG+40 ML.DUT.R1	. :		6365 6366 6367
×	045326	142777	000007	145660		MOV BIC BICB BISB	#7, aML.REG+40			
8	045340	012777	000007 145654 000061	145606		MOV	R1.aML.REG+40 #61.aML.REG R2.R1	:		6369 6371
8	045350	006301				MOV MOV ASL	R1	; CNT,		63/1
901	045352 045356 045360	066107 000006 000014	045356		2\$:	ADD .WORD .WORD .WORD CLR BR MOV	2\$(R1),PC 3\$-2\$ 4\$-2\$			
3	045364	005077	145564		3\$:	CLR	58-28 aml.Reg	:		6375
3	045372	012777	177777	145604	45:	MOV	6\$ #-1,aML.REG+30			6378
06 07	045400 045402	000403	177777	145624	5\$:	BR MOV BEQ MOV BEQ CLR DEC BNE DEC BR BITB	6\$ #-1.aML.REG+60 #50,R0 10\$ L\$DLY,R1 9\$ (SP)			6375 6371 6378 6371 6381 6384
8	045410 045412	012700	177777 000050		5\$: 6\$: 7\$:	MOV	#50,R0	; *,\$\$	TMP2	6384
Ó	045416	001410	134474			MOV	LSDLY,R1	; *,\$\$	TMP1	
11	045454	001403 005016			85:	CLR	(SP)	; SSTM ; SSTM	2	
14	6 45430	001375				BNE	R1 8\$ R0 7\$, 35174		
15	0454 <u>32</u>	005300			9\$:	DEC	RO 78	; SSTM	P2	
17	045416 045416 045426 045426 045426 045430 045434 045434	001375 005300 000767 132777 001024	000004	145570	10\$:	BITB	#4.aML.REG+60	;		6386

CZMLABO ML-11 L MISCELLANEOUS C	ODING SE	CTION								EQ 0289
13020				:ML4					22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
13023 045446 13024 045450 13025 045452	104455 000105 010464				TRAP .WORD .WORD	55 105 ASYNC	;			6389
13026 045456 13026 045462 13026 045466 13030 045472 13031 045476 13032 045502	024052 012746 012746 012746 012746 012746	006522 006416 007366 006522 006046 000005			TRAP .WORD .WORD .WORD MOV MOV MOV MOV MOV MOV	DUMPER WWRD.21,-(SP) WWRD.11,-(SP) WPHR.1,-(SP) WWRD.21,-(SP) WFOR.FMT,-(SP) W5,-(SP)	•			6390
13033 045506 13034 045510	010600				MOV MOV TRAP	SP.R0 14	; SF	*		
13035 045512 13036 045516	062706 032777	000014 040000	145430	115:	BIT	#14,SP #40000,aML.REG				6388 6393
1303/ 045526 13038 045526 13039 045530 13040 045532	104455 000165 010526				TRAP .WORD .WORD	12\$ 55 165 SYNC DUMPER	:			6390
13042 045536 13043 045542 13044 045546 13045 045552 13046 045556	012746 012746 012746 012746 012746 012746	006522 006416 007366 006514 006046 000005			ADD BIT BNE TRAP .WORD .WORD .WORD MOV MOV MOV MOV MOV MOV	MURD.21,-(SP) MURD.11,-(SP) MPHR.1,-(SP) MURD.20,-(SP) MFOR.FMT,-(SP) MS,-(SP) SP,R0	,			6397
13048 045566	010600	000005			MOV TRAP	SP .RO	; SF	P,*		
13050 045572 13051 045576 13052 045600 13053 045602	062706 104467 006000 103641	000014		12\$:	TRAP	#14,SP 67 R0 15	;			6395 6396
13054 045604 13055 045606	005202	000002			INC	R2 .#2	; CA	NT,*		6365
13056 045612 13057 045614 13058 045616	003635 005726 000207				ROR BLO INC CMP BLE TST RTS	R2 R2,#2 1\$ (SP)+ PC	:			6338
13060 13061				; Rout		: 105 words depth per invocation:	10 words			

SEQ 0290

6401

3096 :ML4		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (74)</neale>
3099 : 3100 : 3101 :	6405 ! 6406 ! 6407 BGNTST;		
\$103 \$104	6409 !++ 6410 ! TEST NUMBER: TST 24		
3106	6412 TEST NAME : Initial PROM TEST		
5107 : 5108 :	6413 6414 TEST DESCRIPTION:		
5109 : 5110 : 5111 : 5112 :	6415 6416 TEST THE MEMORY ARRAYS' PROM 6417 TIMING AND CONTROL LOGIC FOR 6418 INITIAL PROM READS BY:		
\$114 \$115	1. READING 14 PROM LOCATIONS AND TESTING FOR:		
3117 3118	6423 A. CHECK SUM ERRORS AT EACH ROW COLUMN ADRS		
3120 3121	6426 B. SUM OF EACH PROM BIT SET <9,0> GTR 14.		
3123	6429 -		
5124 : 5125 :	6430 6431 local		
\$126 : 3127 :	6432 OFF SET_CNT : vector [10, byte], 6433 ROW_ORED_COL : bitvector [16], 6434 R C SAV : bitvector [16], 6435 PROM_ADRS, 6436 CHK_SUM,	COUNTS EACH NIBBLE OF SAVES ROW DATA OR ED TEMP LOCATION FOR ROW PROM ADDRESS CHECK SUM DATA CHECK SUM ERROR COUNTS BAD NIBBLES	FSET DATA
3128	6434 R C SAV : bitvector [16],	TEMP LOCATION FOR ROL	COL DATA
3130	6436 CHK_SUM,	CHECK SUM DATA	
5151	6437 CMK_SUM_ERR,	COUNTS RAD MIRRIES	
3133	6439 CNT 14 BAD, 6440 DODU FEG, 6441 ERR_FLG;	COUNTS BAD NIBBLE POS DROP UNIT FLAG ERROR FLAG	SITION EQL 14
3135	6441 ERR_FLG;	ERROR FLAG	
5136 5137	6442 6443 CLB MBUS.		
5138 :	6444 DODU FLG = ZERO;		
3140	6443 CLR MBUS; 6444 DODU FLG = ZERO; 6445 PROMUDIS = ONE; 6446 CHK_SUM_ERR = ZEROES;	SET PROM DISABLE MODE	
5141 :	6447	ICLEAD OFFCET COUNTS	
3143	6448 incr CNT from 0 to 9 do 6449 OFF_SET_CNT [.CNT] = ZEROES;	!CLEAR OFFSET COUNTS	
3145	6451 incr ADRS_CNT from 0 to 14 do	!READ PROM DATA FROM 1	5 ARRAY WORDS
3135 3136 3137 3138 3139 3140 3141 3142 3143 3144 3145 3145 3146 3147 3148	6439 CNT 14 BAD, 6440 DODO FEG, 6441 ERR_FLG; 6442 6443 CLR MBUS; 6444 DODO FLG = ZERO; 6445 PROM DIS = ONE; 6446 CHK_SUM_ERR = ZEROES; 6447 6448 incr CNT from 0 to 9 do 6449 OFF_SET_CNT [.CNT] = ZEROES; 6450 incr ADRS_CNT from 0 to 14 do 6452 begin 6453 ROW ORED_COL = ZEROES; 6454 PROM_ADRS = .ADRS_CNT; 6455 incr TWICE from 0 to 1 do	CLEAR ROW ORED COL SA	IVE LOCATION
3150	6455 6456 incr TWICE from 0 to 1 do	!READ ROW AND COL DATA	

```
TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (74)
        NIB CNT = ZEROES:
    ERR FLG = ZERO;
    MLPA = .PROM_ADRS;
                                                 !LOADING MLPA INITIATES PROM READ
    DELAY (ONE_US)
                                                 !GET THE ROW OR COL DATA
    R C SAV = .MLPD:
    incr BIT_CNT from 0 to 9 do
                                                !COUNT NUMBER BITS SET IN <9:0>
        if .R_C_SAV [.BIT_CNT] IS_SET then BAD_NIB_CNT = .BAD_NIB_CNT + 1;
    CHK_SUM = .R_C_SAV<10, 3>;
                                                !GET THE CHECK SUM BITS
    if .R_C_SAV [13] IS_SET then CHK_SUM = .CHK_SUM + 1; !ADD IN BIT 13
    if .R_C_SAV [14] IS_SET then CHK_SUM = .CHK_SUM + 1; !ADD IN BIT 14
    if .R_C_SAV [15] IS_SET then CHK_SUM = .CHK_SUM + 1; !ADD IN BIT 15
    if .R_C_SAV [15] IS_SET
                                                !IS BIT 15 SET
    then
        begin
         if .BAD_NIB_CNT Lss .CHK_SUM then ERR_FLG = ONE; !SET ERROR FLG IF CHECK SUM ERROR
        end
    else
        begin
         if .BAD_NIB_CNT neg .CHK_SUM then ERR_FLG = ONE: !SET ERROR FLG IF CHECK SUM ERROR
         end:
    if .ERR_FLG IS_SET
                                                !WAS THERE A CHECK SUM ERROR
    then
        !REPORT INTERMEDIATE ERROR IF
ERROF (70, INTER, DUMPER);
PRINTB (SIX_FMT, FNC_21, WRD_10, WRD_12, WRD_45, WRD_35, FNC_6);
PRINTB (FMT_10, .CHK_SUM, .R_C_SAV);
DODU_FLG = ONE;
                                                  !REPORT INTERMEDIATE ERROR IF YES
    ROW_ORED_COL = (.ROW_ORED_COL) or (.R_C_SAV); !OR ROW AND COLUMN DATA PROM_ADRS = (.PROM_ADRS) or (%o'2000'); !GET COLUMN DATA
incr index from 0 to 9 do
                                                 !GET TOTAL OFF SET COUNTS FROM THE 15 ARRAY WORDS
    if .ROW_ORED_COL [.index] IS_SET then OFF_SET_CNT [.index] = .OFF_SET_CNT [.index] + 1;
end;
```

```
22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (74)
CNT_14_BAD = ZEROES;
incr CNT from 0 to 9 do
                                                 !COUNT HOW MANY OFFSET COUNTS GEQ 14
   if .OFF_SET_CNT [.CNT] geq 14 then CNT_14_BAD = .CNT_14_BAD + 1;
if .CNT_14_BAD neg ZERO
                                                !WAS ANY OFF_SET COUNTS GTR ZERO
    begin
ERRDF (71, ARR_DAT, DUMPER);
                                               !ERROR IF YES
    if .CNT_14_BAD eqt 10
                                               !WERE ALL 10 NIBBLE OFFSETS GEQ 14
       PRINTB (SIX_FMT, WRD_55, WRD_54, WRD_46, WRD_51, WRD_42, WRD_40) !E
                                                                                  !ERROR IF YES
        begin
PRINTB (FOR_FMT, WRD_46, WRD_47, WRD_42, WRD_40); !PRINT MESSAGE
        incr CNT from 0 to 9 do
                                               !FIND OFFSETS GEQ 14
          if .OFF_SET_CNT [.CNT] geq 14 then PRINTB (FMT_13, .CNT, (.OFF_SET_CNT [.CNT]));
                                                 !PRINT NIBBLE POSITION AND COUNT
        end:
    DODU_FLG = ONE;
    end;
if .DODU_FLG IS_SET
                                           !DROP THIS UNIT IF DODU_FLG SET
    begin
DODU (.ML_LUN);
ENDIST:
                $T24:
                                                                                                           6403
        145342
                        BISB
                        MOV
BIC
BICB
BISB
        145324
                                                                 : DODU.FLG
```

MLABO ML-11 LI SCELLANEOUS C	DOING SE	CTION	MACRO M	1115 2	22-DEC-80	12:16 PAGE 307	SEQ	0294
13263 13264				ML4			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOP PA:
13266 045700	152777	000040	145366		BISB	#40, aML.REG+120	: CIW CIM FOO	644
155% 045710 155% 045714 1579 045716	012701 060601 060201 105011	000016		1\$:	MOV ADD ADD CLRB	#16,R1 SP,R1 R2,R1 (R1) R2 R2,#11	: CHK.SUM.ERR : OFF.SET.CNT,* : CNT,*	644
13272 045722 13273 045724 13274 045730	005202 020227 003767	000011			INC	R2 R2,#11 1\$ R5	CNT,*	644
1576 045754 1577 045740 1578 045744	005066 010566 005004	000010 000006		2\$:	CMP BLE CLR CLR MOV CLR CLR	10(SP) R5,6(SP) R4	; ADRS.CNT ; ROW.ORED.COL ; ADRS.CNT,PROM.ADRS ; TWICE	645 645
13279 045746 13280 045752	005066	000004	1/5272	3\$:		4(SP) (SP)	ADRS.CNT.PROM.ADRS TWICE BAD.NIB.CNT ERR.FLG PROM.ADRS.*	645
15082 045762 15283 045766	012701 001411	000006 000001	145272	48:	MOV MOV BEQ MOV BEQ	(SP) 6(SP), aML.REG+100 #1,R1 75	; *,\$\$TMP2	646
13284 045770 13285 045774	016702	134122		5\$:	BEQ	7\$ L\$DLY,R2 6\$ 14(SP)	; *,SSTMP1	
15287 046002 15288 046004	005302 001374	000014			DEC	R2 5\$ R1	: SSTMP1	
13289 046006 13290 046010	005301 000766	145366	000012	6\$: 7\$:	CLR DEC BNE DEC BR MOV	R1 4\$ aml.REG+230,12(SP)	; \$\$TMP2	44.6
13292 046020 13293 046024 13294 046024 13295 046026 13296 046030	005002 010201 006201 006201 006201 012700		000012	85:	CLR MOV ASR ASR ASR ASR ADD ADD	R2 R2,R1 R1 R1	: *,R.C.SAV : BIT.CNT : BIT.CNT,*	646
13298 046036	060600 060001	000012			ADD ADD	SP,R0 R0,R1	; R.C.SAV,*	
13300 046042 13301 046044 13302 046046 13303 046052 13304 046056 13305 046060 13306 046070 13308 046072 13309 046074 13310 046100 13311 046102 13312 046116 13314 046114 13315 046116 13316 046120 13317 046122	010246 042716 012746 005046	177770 000001			MOV MOV BIC MOV CLR	R1 #12,R0 SP,R0 R0,R1 R1,-(SP) R2,-(SP) #177770,(SP) #1,-(SP) -(SP) PC,BL\$GT2 #10,SP R0 9\$ 4(SP)	; BIT.CNT,*	
13305 046060 13306 046064 13307 046070 13308 046072 13309 046074	062706 005300	135054 000010			ADD DEC	#10,SP		
13309 046074 13310 046100	005202 005202	000004 000011		9\$:	INC INC	4(SP) R2 R2,#11 8\$; BAD.NIB.CNT ; BIT.CNT,*	646
13312 046106 13313 046110 13314 046114 13315 046116	003745 016603 006203 006203	000012			MOV BIC MOV CLR JSR ADD DEC BNE INC INC CMP BLE MOV ASR ASR SWAB BIC	8\$ 12(SP),R3 R3 R3 R3 W177770,R3	; R.C.SAV, CHK.SUM ; CHK.SUM ; CHK.SUM ; CHK.SUM ; CHK.SUM	646
13316 046120 13317 046122	000303 042703	177770			SWAB	R3 #177770,R3	; CHK.SUM ; *,CHK.SUM	

c-1980 09:24:31 T c-1980 09:21:22 P	22-Dec-198				:ML4				
c-1980 09:21:22 P	22-Dec-198				:				
6	*,RSAV+1	; *,R	#40,13(SP)	BITB		000013	000040	132766	046126
	CHK.SUM *,R.C.SAV+1	; CHK.	#40,13(SP) 10\$ R3 #100,13(SP)	INC	105:	000013	000100	005203	046136
6			112	BEQ	103:	000013	000100	001401	046146
6	CHK.SUM	:	R3 R2	INC CLR TSTB	115:			005005	046152
	R.C.SAV+1	; R.C.	13(SP) 12\$	BPL			000013	100002	046160
	CHK.SUM	: CHK.	R2 R3 R2	INC				005202	046162 046164
6		;	R2 13\$	ROR	12\$:			103006	046166
1 6	BAD.NIB.CNT,CHK.SUM	; BAD	4(SP),R3	ROR BCC CMP BGE BR CMP BEQ MOV CMP BNE TRAP .WORD .WORD			000004	026603	046172
1 6	BAD.NIB.CNT,CHK.SUM	. DAD	14\$ 4(\$P),R3	BR	13\$:		000004	000403	046200
			15\$	BEQ				001402	046206
6	*,ERR.FLG ERR.FLG,*	ERR	#1 (SP) (SP) #1	CMD	14\$: 15\$:		000001 000001	021627	046214
6		;	55	TRAP				104455	046222
			16S 55 106 INTER DUMPER	. WORD				010672	046226
6		:	UPPER UFNC.6,-(SP)	MOV			010004	024052	046230 046232
			MRD.35,-(SP) MRD.45,-(SP)	MOV			006666	012746	245
			MARD.12,-(SP) MARD.10,-(SP)	MOV			006426 006406	012746 012746	046246 046252
			#FNC.21,-(SP) #SIX.FMT(SP)				010216 006100 000007	012746 012746	46256 46263
	SP +	; SP,	#7,-(SP)	MOV			000007	012746	046266
6			14 32(SD) (SD)	TRAP			000032	104414	046274
•	R.C.SAV,* CHK.SUM,*	CHK	R3,-(SP)	MOV				010346	046302
	CD 4	. 60	#FNC.21,-(SP) #SIX.FMT,-(SP) #7,-(SP) SP,R0 14 32(SP),(SP) R3,-(SP) #FMT.10,-(SP) #3,-(SP) SP,R0 14 #1,30(SP) #26 SP	MOV			004676 000003	012746 012746 012746 010600 104414 016616 010346 012746 012746	046310
		; SP,1	14	TRAP		000070	000001		046316
6	*,DODU.FLG	•	#26.SP	ADD		000030	000056	062706	046326
L 6	R.C.SAV, ROW. ORED. COL *, PROM. ADRS	; R.C.	#2000,6(SP)	BIS	16\$:	000010 000006	000001 000026 000012 002000	052766	046340
6	R.C.SAV, ROW. ORED. COL *, PROM. ADRS TWICE TWICE, *	: TWIC	R4 R4_#1	INC			000001	020427	046350
			17\$ 3\$	BGT			177364	003002	046354 046356
6	INDEX.*	: INDE	#1,30(SP) #26,SP 12(SP),10(SP) #2000,6(SP) R4 R4,#1 17\$ 3\$ R2 R2,R1 R1	MOV MOV MOV MOV MOV MOV MOV TRAP MOV ADD BIS BIS INC CMP BGT JMP CLR MOV ASR	17\$: 18\$:			012766 062706 056666 052766 005204 020427 003002 000167 005002 010201 006201	04076 04076

MLABO ML-11 LI SCELLANEOUS CI				:ML4					22-Dec-1 980		029
13.32				:					22-Dec-1980 22-Dec-1980	09:21:22	PA
13434 046612 13435 046614 13436 046616	010600 104414 022626				MOV TRAP CMP BR	SP,R0 14 (SP)+,(SP)+	;	SP,*			
13437 046620 13439 046626 13440 046636 13441 046636 13442 046642	000445 012746 012746 012746 012746 012746	006726 006746 007016 007004 006046 000005		23\$:	MOV MOV MOV MOV MOV	26\$ MMRD.40,-(SP) MMRD.42,-(SP) MMRD.47,-(SP) MMRD.46,-(SP) MFOR.FMT,-(SP) M5,-(SP) SP,R0 14	. :				656
13443 046646 13444 046652 13445 046654	012746 010600 104414	000005			MOV MOV TRAP	#5(SP) SP.R0 14	:	SP,*			
13446 046656 13447 046660 13448 046664	005002 012701 060601	000032		24\$:	MOV TRAP CLR MOV ADD ADD CMPB	R2 #32,R1 SP,R1 R2,R1 (R1),#16	:	OFF.SET.CNT,*			65
13450 046670 13451 046674 13452 046676 13453 046700	060201 121127 103413 005046 1111116	000016			ADD CMPB BLO CLR MOVB MOV	=(SP)	•	CNT,*			
13454 046702 13455 046704 13456 046710	010246 012746 012746	005034 000003			MOV MOV	(R1) (SP) R2,-(SP) MFMT.13,-(SP) M3,-(SP) SP,R0		CNT,*			
13458 046716	104414	000010			TRAP	19		SP.*			
13460 046724	005202	000010		25\$:	MOV MOV TRAP ADD INC CMP BLE MOV ADD	#10,SP R2 R2,#11	:	CNT CNT,*			65
13462 046732 13463 046734	003752		000016	26\$:	BLE	24\$ #1,16(SP) #14,SP 2(SP),#1 28\$ ML.LUN,RO		*,DODU.FLG			65
13464 046742 13465 046746	062706 026627 001004	000001 000014 000002	000001	27\$:	ADD CMP BNE	#14,SP 2(SP),#1		DODU.FLG,*			65
13467 046756 13468 046762	016700 104451	144616			MOV TRAP TRAP	ML.LUN,RO	:				65
13470 046766 13471 046772	062706 000207	000030		28\$:	ADD RTS	51 44 #30,SP PC	. :				64
13473				; Rou	tine Size:	304 words depth per invocation:	29 words				

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 311

SEQ 0298

13486
13487
13488
13492 046774
13493 046774
13494 047000 104466
13495 047002 006000
13496 047004 103773
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22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (75) BGNTST: TEST NUMBER: TST 25 TEST NAME: PROM OR FUNCTION TEST TEST DESCRIPTION: TEST THE HARDWARE ORING OF THE MEMORY ARRAYS' PROM ROW COLUMN DATA BY: 1. READING AND STORING 128
HARDWARE ORED ROW COLUMN 2. THEN IN PROM DISABLE MODE AND VIA SOFTWARE CONTROL, READ AND OR PROM ROW COLUMN DATA AND COMPARE AGAINST THE RESPECTIVE STORED HARDWARE ORED DATA. local PROM ROW DATA
PROM COL DATA
SOFTWARE CALCULATED PROM ORED DATA
SOFTWARE PROM ORED DATA
DROP UNIT FLAG SW ORED. HW SAVE. DODU_FLG; CLR_MBUS; DODU_FLG = ZERO; DAT_DM = ONE; FIRST_BLK_XFER (); ML_FUNC = write; SET UP A FIRST BLOCK XFER DO A WRITE FUNCTION incr PROM_ADRS from 0 to 127 do !READ AND STORE 128 HARDWARE PROM ORED DATA begin DELAY (ONE_US); HW_OR_TBL [.PROM_ADRS] = .MLPD; DAT_CEK = ONE; !READ HARDWARE PROM ORED DATA CLOCK NEXT ONE OUT CLR_MBUS; PROM DIS = ONE: !SET PROM DISABLE MODE 6596 6597 incr PROM_ADRS from 0 to 127 do !CALCULATE 128 SW ORED DATA & COMPARE TO HW TABLE

```
22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                                                                               TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (75)
                                                             begin
MLPA = .PROM_ADRS;
DELAY (ONE_US);
                                                                                                                                                                      !LOADING MLPA INITIATES A PROM READ
                                                            R BITS = .MLPD;
R BITS = .PROM_ADRS or %0'2000';
DELAY (ONE US);
C BITS = .MLPD;
SD ORED = .R BITS or .C BITS;
HW_SAVE = .HD_OR_TBL [.PROM_ADRS];
                                     6601
6602
6603
6604
6605
6606
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6608
6609
6611
6612
6613
6614
6615
6616
6617
6618
6622
6623
6624
6625
6626
                                                                                                                                                                      SAVE ROW DATA SENABLE COLUMN DATA ADRS
                                                                                                                                                                      SAVE COL DATA
CALCULATE SOFTWARE ORED
GET RESPECTIVE HARDWARE ORED
                                                             if (.HW_SAVE<0, 9>) neg (.SW_ORED<0, 9>)
                                                                                                                                                                      ! COMPARE SW & HW ORED
                                                             then
                                                                      !IF NEQ THEN ERROR

ERROF (76, ARR_DAT, DUMPER);

PRINTB (FOR_FMT, WRD_35, WRD_36, WRD_19, PHR_4);

PRINTB (FMT_2, .SW_ORED<0, 9>, .HW_SAVE<0, 9>, (.SW_ORED<0, 9> xor .HW_SAVE<0, 9>));

DODU_FLG = ONE;
                                                                      end:
                                                             end:
                                                                                                                                             !DROP THIS UNIT IF DODU FLG IS_SET
                                                    if .DODU_FLG IS_SET
13590 : 13591 : 13592 : 13593 : 13594 : 13595 : 13596 : 13600 13604 047010 13605 047014 13606 047020 13610 047044 13611 047050 13612 047054 13613 047062 13614 047066 13615 047074 13616 047104 13618 047110
                                                    then
                                                             begin
DODU (.ML_LUN);
                                                             DOCLN:
                                                             end:
                                                   ENDTST:
                                                                                                                               R1.$SAVE5
#10,SP
#40,aML.REG+40
ML.DUT.R5
#177770,R5
#7,aML.REG+40
R5,aML.REG+40
4(SP)
#10,aML.REG+120
PC,FIRST.BLK.XFER
#77,aML.REG
                                                   135102
000010
000040
144550
177770
000007
144144
000004
000010
146752
000077
000061
                                004167
162706
152777
                                                                                         $T25:
                                                                                                             .ISR
                                                                                                                                                                                                                                                                                                            6544
                                                                                                             SUB
BISB
                                                                      144166
                                                                                                                                                                                                                                                                                                            6579
                                                                                                             MOV
                                                                       144150
                                                                                                             BICB
                                                                                                             BISB
                                                                                                                                                                                                                                                                                                           6582
6583
6584
6585
                                                                                                             CLR
                                                                                                                                                                                                             : DODU.FLG
                                                                      144212
                                                                                                             BISB
                                                                                                            JSR
BICB
BISB
CLR
                                                                      144060
144052
                                                                                                                               #61, aML.REG
R2
#1,R1
5$
                                                                                                                                                                                                             : PROM. ADRS
                                                    000001
                                                                                          15:
                                                                                                             MOV
                                                                                                                                                                                                             : * . $$TMP2
                                                                                                             BEQ
```

SEQ 03									ML-11 L
22-Dec-1980 09:24:31 1 22-Dec-1980 09:21:22 F					ML4				
	*,\$\$TMP1	;	LSDLY,R3	MOV			133000	016703	047112
	SSTMP1	;	4\$ 6(SP)	CLR	3\$:		000006	005066	047120
			R3 3\$	BNE				005303	8778 8778
	SSTMP2	:	R1 2\$	DEC BR	48:			005301 000766	047130 047132
6	PROM.ADRS,*	;	4\$ 6(SP) R3 3\$ R1 2\$ R2,R3 R3 amREG+230,HW.0	MOV BEQ CLR DEC BNE DEC BR MOV ASL MOV BISB INC	5\$:			010203	% 73
		R3)	am .REG+230 .HW.0	MOV		011160 144120	144240	017763	047140
	PROM.ADRS PROM.ADRS *	:	R2 R2,#177	INC		144120		005202	047154
	PROM.ADRS,*	:	16	CMP			000177	020227	047156
6		;	#40, aML.REG+40 ML.DUT.R5 #177770,R5 #7, aML.REG+40 R5, aML.REG+40 #40, aML.REG+120	BLE		144022	000040 144404	152777	82764
			#177770,R5	MOV BIC BICB BISB BISB			177770	042705	047176
			#7, am. REG+40	BICB		144004	177770 000007 144000 000040	150577	047202
•		:	#40, aML.REG+120	BISB		144052	000040	150577 152777 005001	047214
	PROM.ADRS PROM.ADRS,* *,\$\$TMP2		KI	MOV	6\$:		144024	010177	047224
6	*,\$\$TMP2	:	R1 . APL . REG+100 #1 . R2	MOV	75:		000001	012702	047230
	*,SSTMP1	:	10\$ LSDLY,R3	CLR MOV BEQ MOV BEQ CLR DEC BNE DEC			132654	016703	047236
	SSTMP		9\$ 6(SP)	CLR	8\$:		000006	005066	047244
	SSTMP1	:	R3 8\$ R2	DEC				005303	047250
	SSTMP2	:		DEC	9\$:			005302	047254
	*.R.BITS	:	aml.REG+230,2(SP	BR MOV	10\$:	000002	144120	017766	047260
6	*,R.BITS PROM.ADRS,*	;	R1.R3	MOV				010103	047266
			R3.aML.REG+100	MOV			002000 143754 000001	010377	047274
6	*,\$\$TMP2	:	#1_R2	MOV BEQ	115:			001411	04/300
	*,SSTMP1	:	R1,R3 #2000,R3 R3,aML.REG+100 #1,R2 14\$ L\$DLY,R3 13\$ 6(SP)	MOV			132604	000766 017766 010103 052703 010377 012702 001411 016703 001404 005066 005303	047256 047260 047266 047274 047300 047304 047306 047312 047314
	SSTMP1	;	6(SP)	CLR	12\$:		000006	005066	047314
	SSTMP1	:	R3 12\$	DEC				005303	04/322
	SSTMP2	:	R3 12\$ R2 11\$	DEC	13\$:			UUDJUZ	047324
ED 6	*.C.BITS	:	am . REG+250. (SP)	MOV	145:		144050 000002	000766 017716 016605 051605	047330
D 6	*.C.BITS R.BITS,SW.ORED C.BITS,SW.ORED PROM.ADRS,*		2(SP),R5 (SP),R5 R1,R3	MOV			000002	016605	047340
6	PROM.ADRS,*	:	R1.R3	MOV				111111111	047342
			R3 HW.OR.TBL(R3),R4 R5,R2 #177000,R2	MOV BIS MOV BEQ MOV BEQ CLR DEC BNE DEC BR MOV MOV BIS MOV MOV BIS			011160	006303 016304 010502 042702	047270 047274 047300 047304 047312 047314 047324 047324 047324 047324 047324 047324 047324 047334 047340 047352 047352
6	*,HW.SAVE SW.ORED,*	;	R5_R2	MOV			177000	010502	047352

22-0 1000 00-24-74			- 100 /			13474
22-Dec-1980 09:24:31 1 22-Dec-1980 09:21:22 F			ML4			3677
; HW.SAVE,*	R4,R3 #177000,R3 R3,R2 15\$	MOV BIC CMP		177000	010403 042703 020302	3679 047360 3680 047362 3681 047366
:	155 55 114 ARR.DAT DUMPER MPHR.4(SP)	TRAP .WORD .WORD			104455 000114 010570	\$685 047376 \$685 047376
;	MPHR.4, -(SP) MURD.19, -(SP) MURD.36, -(SP) MURD.35, -(SP) MFOR.FMT, -(SP) M5, -(SP) SP,R0	BEQ TRAP .WORD .WORD .WORD MOV MOV MOV MOV MOV MOV MOV BIC BIC BIC BIC BIC BIC BIC BIC BIC BIC		007454 006506 006674 006666 006046	012746 012746 012746 012746 012746	3687 047402 13688 047406 13689 047412 13690 047416 13691 047422
; SP.*	#5(SP) SP.RO	MOV		000005	010600	3003 047433
: HW.SAVE.* : SW.ORED.*	R4,R3 R5,(SP) R3,R2 R5,R2	MOV MOV MOV BIC BIC			010403 010516 010302 040502 040316	3695 047436 13696 047440 13697 047442 13698 047444 3699 047446
; HW.SAVE,*	R2,(SP) R4,-(SP)	BIS		477000	050216 010446	3700 047450 3701 047452
; SW.ORED,*	R2,(SP) R4,-(SP) R4,-(SP) W177000,(SP) W177000,(SP) WFMT.2,-(SP) W4,-(SP) SP,R0	MOV BIC MOV		177000 177000 004266	042716 010546 042716 012746	13702 067434 13703 047460 13704 047462 13705 047466
; SP,*	SP,R0	MOV		000004	010600	13707 047476 13708 047500
; *,DODU.FLG 66 ; PROM.ADRS 66 ; PROM.ADRS,*	#1,30(SP) #24,SP R1 R1,#177	MOV ADD INC CMP BLE CMP BNE MOV TRAP TRAP ADD RTS	000030 15\$:	000001 000024 000177	012766 062706 005201 020127	13709 047502 13710 047510 13711 047514
; DODU.FLG.*	6\$ 4(SP)_#1 16\$	BLE	000001	000004	003640	3713 047522 3714 047524
;	ML.LUN,RO 51	BNE MOV TRAP		144040	001004 016700 104451	13715 047532 13716 047534 13717 047540
;	#10,SP PC	ADD RTS	16\$:	000010	062706 000207	13718 047342 13719 047544 13720 047550 13721

SEQ 0303

6624

```
22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (76)
          BGNTST:
! TEST NUMBER: TST 26
          ! TEST NAME:
                                  UV ADRS ERROR TEST
            TEST DESCRIPTION:
                      TEST THE DETECTION OF UV ADRS
                      ERRORS BY:
                      1. GENERATING PROM DATA PATTERN FROM 0 TO %0'177777' AND
                            DETERMINE WHETHER RESPECTIVE PATTERN IS GOOD/OR BAD
                            PROM DATA.
                     2. VIA DAT DM AND PROM R/W MODES PRESENT GENERATED PROM DATA TO THE UV ADRS ERR PROM.

    TEST ERROR CONDITIONS FOR
CORRECT RESPONCE TO GOOD/
OR BAD PROM DATA.

         local
                                                                                    !DROP UNIT FLAG
!STORES PROM DATA CHECK SUM BITS
!STORES PROM DATA
!STORES SUM OF PROM DATA BITS <9:0>
!TEMPORARY STORAGE
!ERROR FLAG
!SETS WHEN PROM DATA BIT 15 IS A ONE
                DODU_FLG.
                HIGH_CNT.
                PROM_DATA : bitvector [16],
                LOW CNT,
                ERR_FLG.
                GTR_FLG;
         PROM_DATA = -1;
                                                                                   !TEST ALL POSSIBLE PROM DATA COMBINATIONS
                begin
PROM_DATA = .PROM_DATA + 1;
                                                                                   !INCREMENT PROM_DATA
                BGNSUB;
               CLR_MBUS;
ERR_FLG = ZERO;
LOW_CNT = ZEROES;
HIGR_CNT = ZEROES;
                GTR_FLG = ZERO;
```

```
22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                       TOPS-20 Bliss-16 V2(212)
                                                                       PA: <NEALE>BL3ML4.BL1.2 (76)
incr BIT_CNT from 0 to 9 do
                                            !COUNT PROM DATA BITS <9:0>
    if .PROM_DATA [.BIT_CNT] IS_SET then LOW_CNT = .LOW_CNT + 1;
HIGH_CNT = .PROM_DATA<10, 3>;
                                            !GET PROM DATA CHECK SUM BITS
if .PROM_DATA [13] IS_SET then HIGH_CNT = .HIGH_CNT + 1; !ADD IN BIT 13 IF SET
if .PROM_DATA [14] IS_SET then HIGH_CNT = .HIGH_CNT + 1; !ADD IN BIT 14 IF SET
if .PROM_DATA [15] IS_SET then HIGH_CNT = .HIGH_CNT + 1; !ADD IN BIT 15 IF SET
if .PROM_DATA [15] IS_SET then GTR_FLG = ONE; !SET FLAG IF BIT 15 IS SET
DAT DM XFER ();
PROM_RW = ONE;
MLPD = .PROM_DATA;
                                              SET UP A DATA DIAG MODE XFERR
                                              LOAD MLPD WITH PROM DATA
MLCS1 = write:
                                              DO A WRITE FUNCTION
DAT_CLK = ONE;
                                              !CLOCK PROM DATA INTO UV PROM
if .UNS IS_SET
                                              !SEE IF PROM DATA CAUSED A UV ERROR
then &
    beuin
    if .GTR_FLG IS_SET
                                            !UNS IS_SET. SEE IF GTR FLG IS SET
    then
        begin

TEMP = .PROM_DATA;

TEMP = .TEMP and %o'162000';
                                              !LOAD TEMP WITH PROM DATA
                                              SEE IF THESE BITS ARE SET IN PROM DATA
        if .TEMP eqt %o'162000'
                                              !THESE BITS SET AUTOMATICALLY CAUSE A UNS
        then
            begin
            if .LOW_CNT geq .HIGH_CNT
                                              !LOW<9:0> SHOULD BE ISS THAN THE HIGH<15:10> IF GTR FLG IS S
                                              !ERROR IF LOW<9:0> IS GEQ HIGH<15:10>
                 ERROF (72, ARR_DAT, DUMPER);
                PRINTB (SIX_FMT, WRD_34, PHR_5, WRD_32, WRD_6, WRD_33, WRD_24);
                ERR_FLG = ONE:
            end:
                                              !GTR FLG IS NOT SET
        if .LOW_CNT eql .HIGH_CNT
                                            !LOW<9:0> SHOULD BE NEQ HIGH<15:10>
            begin
                                              !ERROR IF EQL
```

begin ERROF (108, ARR_DAT, DUMPER); PRINTB (SIX_FMT, WRD_34, PHR_1, ERR_FLG = ONE;

end

end

end:

!ERROR IF NEQ

WRD_32, WRD_5, WRD_33, WRD_24);

SEQ 0306

MISCELL	ANEOUS C	ODING SE	CTION							SEQ 0307
13926 13927 13928	ML4							22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (76</neale>	,
13929 13930 13931 13932 13933 13934 13935 13936		6784 6785 6786 6787 6788 6789 6790 6791	the	.ERR_FLG n begin PRINTB DODU_FL end;	IS_SET (FMT_7, G = ONÉ;	.PROM_D/	ATA);	!SEE IF ERROR FLG GOT !PRINT FAILING PROM_DA		
13938		6793	end		A eal %	177777		TRY ALL BIT COMBINAT	IONS	
13940		6795		_				PROP THIS UNIT IF DO		
13942 13943 13944 13945 13946		6796 6797 6798 6799 6800 6801	then beg DOD DOC end	U_FLG IS U (.ML_L LN;	UN);			. DAGE THIS GALL IF DO	DO_FEG 13 SET	
13948	:	6802 6803	ENDTST;							
13956	047566	004167	134324		\$T26:	JSR	R1. SSAVES			6626
13958 13959 13960 13961	047576 047602 047610	005066 012766 005266	134324 000012 000006 177777 000010	000010	1\$: 2\$:	JSR SUB CLR MOV INC TRAP BISB	R1,\$SAVE5 #12,SP 6(SP) #-1,10(SP) 10(SP)	; DODI ; *,PI ; PROI	U.FLG ROM.DATA M.DATA	6668 6669 6673
13962 13963 13964	047616 047624 047630	152777 016705 042705	000040 143752 177770 000007 143346	143370 143352	20.	BISB MOV BIC BICB BISB	#40.aML.REG+40 ML.DUT.R5 #177770.R5 #7.aML.REG+40			6674
13966	047642	150577	143346	143372		BISB CLR	R5, aML. REG+40	; ERR	FIG	6676
13968	047650	005066	000002				2(SP)	LOW	CNT	6677
13970	047656	005066	000004			CLR	4(SP)	GTR	CNT H. CNT FLG CNT CNT,*	6679
13971 13972 13973 13974 13975 13976	047656 047662 047664 047666 047670	010504 006204 006204			3\$:	MOV ASR ASR	R5,R4 R4 R4	; Bit.	CNT,*	6679 6681 6683
13976 13977 13978 13979	047674 047700 047702 047704	012702 060602 060204 010446	000010			CLR CLR CLR MOV ASR ASR ASR MOV ADD MOV	2(SP) R3 4(SP) R5 R5,R4 R4 R4 R4 R4 R4 R4 R4,-(SP)	; PROF	4.DATA,*	

0.00.24.31	22-000-1000			;ML4			3081
0 09:24:31 TO 0 09:21:22 PA	22-Dec-1980 22-Dec-1980			1			3002
	; BIT.CNT,*	R5,-(SP) #177770,(SP) #1,-(SP)	MOV BIC MOV		177770 000001	010546 042716 012746	3984 047706 3985 047710 3985 047710
		#1,-(SP) -(SP) PC,BL\$GT2 #10,SP R0 4\$	JSR ADD DEC BNE INC INC CMP BLE MOV		133212 000010	004767 062706 005300	500 0.772 300 0.772 300 0.772
	; LOW. CNT ; BIT. CNT	2(SP)	INC	45:	000002	005266	3992 047734
66	BIT.CNT,*	R5 R5,#11	CMP	43:	000011	020527	399 8774
66	; PROM.DATA,HIGH.CNT ; HIGH.CNT ; HIGH.CNT	10(SP),R3 R3 R3	MOV ASR ASR SWAB		000010	016603 006203 006203	3776 02/752 3997 04/756 3998 04/760
66	; HIGH. CNT ; HIGH. CNT ; *, HIGH. CNT ; *, PROM. DATA+1	R3 R3 R3 W177770,R3 W40,11(SP) 58 R3 W100,11(SP)	BIC BITB BEQ	0011	177770 000040 000	042703 132766 001401	4000 0477764 4001 047770 4002 047776
66	; HIGH.CNT ; *,PROM.DATA+1 ; HIGH.CNT	R3 #100,11(SP) 6\$ R3	INC BITB BEQ INC	0011 58:	000100 000	005203 132766 001401 005203	4003 050000 4004 050002 4005 050010 4006 050012
66	PROM.DATA+1	6\$ R3 R5 11 (SP) 7\$	CLR TSTB BPL	6\$:	000011	005005 105766 100002 005205	4007 050014 4008 050016 4009 050022 4010 050024
66	; HIGH.CNT	75 R5 R3 R5 85	INC INC ROR BCC	75:		005203 006005 103003	4011 050026 4012 050030 4013 050032
66 66	; *,GTR.FLG ; PROM.DATA,*	#1.4(SP) PC.DAT.DM.XFER #100.aML.REG+120	JSR BISB	0004 8\$:	144114 000100 143	004767 152777	4014 050034 4015 050042 4016 050046 4017 050054 4018 050060
66 66 67	:	#1.4(SP) PC.DAT.DM.XFER #100.aML.REG+120 10(SP).R5 R5.aML.REG+230 #61.aML.REG #20.aML.REG+120 #40000.aML.REG+60 123 4(SP).#1 118	MOV MOV BISB BIT BEQ CMP BNE MOV BIC CMP BNE CMP BNE CMP BOE JMP TRAP WORD WORD	8062 5174 5126	000001 000 144114 000100 143 000010 143320 000061 143 040000 143	012766 004767 152777 016605 0105777 012777 032777 001500 026627	4014 050034 4015 050042 4016 050046 4017 050054 4018 050060 4019 050064 4021 050100 4021 050100 4022 050106 4023 050116 4023 050116 4025 050120 4026 050122 4027 050134 4029 050134 4030 050140 4031 050146 4032 050152 4033 050150 4034 050152
		128	BEQ			001500	4022 050106
67	; GTR.FLG,*	4(SP),#1 11\$	BNE	0001	000004 000	001042	4024 050116
67 67 67	: *,TEMP : *,TEMP : TEMP,*	R5 (SP) #15777, (SP) (SP) ,#-16000 9\$ 2(SP) ,R3 10\$ 16\$ 55 110 ARR.DAT DUMPER	BIC CMP		015777 162000	010516 042716 021627	4019 050064 4020 050072 4021 050100 4022 050106 4023 050110 4024 050116 4025 050120 4026 050122 4027 050126 4029 050132 4030 050140 4031 050142 4032 050146 4033 050150
67	; LOW.CNT,HIGH.CNT	2(SP),R3 10\$	CMP BGE	08.	000002 000422	026603 002002 000162	4029 0501 5 4 4030 050140 4031 050143
67		55 110 ARR. DAT	TRAP . WORD	9\$: 10\$:	000425	000167 104455 000110 010570 024052	4032 050146 4033 050150 4034 050152

14037	OGIC TES ODING SE			.00 4				22-01090 00-24-	SEQ 0309
14038				:ML4				22-Dec-1980 09:24: 22-Dec-1980 09:21:	31 TOPS 22 PA:
14040 050156 14041 050162 14042 050166 14043 050172 14044 050176 14045 050202 14046 050203	012746 012746 012746 012746 012746 012746 012746	006552 006654 006336 006646 007472 006660 006100			MOV MOV MOV MOV MOV MOV	#WRD.24,-(SP) #WRD.33,-(SP) #WRD.6,-(SP) #WRD.32,-(SP) #PHR.5,-(SP) #WRD.34,-(SP) #SIX.FMT,-(SP) #7,-(SP)			6719
14048 050216	010600	000007			MOV TRAP	SP,RO	;	SP,*	
14050 050222	000556	000002		115:	BR	15\$ 2(SP),R3	:	LOW. CNT , HIGH. CNT	6720 6725
14053 050232 14054 050234 14055 050236	104455 000111 010570				BR CMP BNE TRAP . WORD . WORD . WORD	55 111 ARR.DAT DUMPER			6732
14057 050242 14058 050246 14059 050252 14060 050256 14061 050262 14062 050266 14063 050272 14064 050276	012746 012746 012746 012746 012746 012746 012746 012746	006552 006654 006336 006646 007472 006660 006100 000007			MOV MOV MOV MOV MOV	MURD.24,-(SP) MURD.33,-(SP) MURD.6,-(SP) MURD.32,-(SP) MPHR.5,-(SP) MURD.34,-(SP) MSIX.FMT,-(SP) M7,-(SP)	:		6733
14065 050302 14066 050304	104414				TRAP	SP.R0		SP,*	
14068 050310 14069 050316	026627	000004	000001	12\$:	BR CMP BMF	15\$ 4(SP),#1 14\$;	GTR.FLG,*	6734 6743
14070 050320 14071 050322 14072 050326	001067 010516 042716 021627 001427 104455 000112 010570	015777 162000			MOV BIC CMP	R5 (SP) #15777 (SP) (SP) #-16000	:	*,TEMP *,TEMP,*	6746 6747 6749
14074 050334 14075 050336 14076 050340	104455 000112 010570				TRAP . MORD . MORD	55 112 ARR.DAT	:		6752
14070 050320 14071 050322 14072 050326 14073 050332 14074 050334 14075 050336 14076 050340 14077 050342 14078 050344 14079 050350 14080 050354 14081 050364 14082 050364 14083 050370 14084 050374 14085 050400 14086 050404 14088 050410 14089 050412 14090 050416 14091 050420	012746 012746 012746 012746 012746 012746 012746	006552 006654 006330 006646 007366 006600 006100			CMP BNE MOV BIC CMP BEQ TRAP .WORD .WORD .WORD MOV MOV MOV MOV MOV MOV MOV MOV MOV TRAP BR CMP BGE TRAP	14\$ R5.(SP) #15777,(SP) (SP).#-16000 13\$ 55 112 ARR.DAT DUMPER #MRD.24,-(SP) #MRD.33,-(SP) #MRD.32,-(SP) #MRD.32,-(SP) #MRD.34,-(SP) #MRD.34,-(SP) #SIX.FMT,-(SP) #7,-(SP) SP,R0 14			6753
14086 050404	010600				MOV	SP.R0	:	SP,*	
14088 050410 14089 050412 14090 050414	104414 000463 026603 002064 104455	000002		13\$:	BR CMP BGE	15\$ 2(SP),R3 16\$ 55	:	LOW. CNT, HIGH. CNT	6754 6759
14091 050420	104455				TRAP	55	:		6762

1/007							22-01000 00-	2/ . 71 700
14094				ML4			22-Dec-1980 09: 22-Dec-1980 09:	24:31 TOP 21:22 PA:
14096 050422	000113				- WORD	113 ARR.DAT		
14098 050426	024052	006552			. WORD . WORD . WORD MOV	DUMPER MARD. 24, -(SP)		676
14100 050434	012746	006654			MOV	MMRD.33,-(SP)		0.0
14102 050444 14103 050450	012746	006646 007366			MOV	MARD.32,-(SP)		
14104 050454 14105 050460	012746 012746	006660 006100			MOV	MMRD.34,-(SP) WSIX.FMT,-(SP) W7,-(SP)		
14106 050464 14107 050470	012746	000007			MOV MOV TRAP	#7,-(SP) SP,R0	; SP,*	
14108 050472 14109 050474	104414 000431				DD	14 15\$		676 677
14110 050476 14111 050502	026603	000002		14\$:	BEQ	2(SP) ,R3 16\$; LOW. CNT, HIGH. CNT	
14112 050504 14113 050506	000154				CMP BEQ TRAP . WORD . WORD . WORD MOV MOV	16\$ 55 154 ARR.DAT	•	677
14114 050510 14115 050512	024052	00/550			. WORD	DUPPER		
14117 050520	012746	006654			MOV	MARD. 24, -(SP) MARD. 33, -(SP) MARD. 5, -(SP)		677
14119 050530	012746	006646			MOV MOV			
14121 050540	012746	006660			MOV	#PHR.1,-(SP) #WRD.34,-(SP) #SIX.FMT,-(SP) #7,-(SP)		
14123 050550 14124 050554	012746	000007			MOV	#7,-(SP) SP,R0	; SP,*	
14125 050556 14126 050560	012701	000001		15\$:	MOV	14 #1,R1	; *,ERR.FLG	677
14127 050564 14128 050570	062706	000001 000020 000001		16\$:	ADD	#1 .R1 #20 .SP R1 .#1	: ERR.FLG.*	677 677 678
14130 050576	010546	00/542			MOV TRAP MOV ADD CMP BNE MOV	17\$ R5,-(SP)		678
14132 050604	012746	004562 000002			MUA	R5,-(SP) #FMT.7,-(SP) #2,-(SP) SP,R0 14		
14134 050612	104414	000001	000014		MOV TRAP MOV ADD TRAP ROR BHIS JMP INC BEQ JMP CMP BNE MOV TRAP	14 #1 14(SP)	; SP,* ; *,DODU.FLG	479
14136 050622 14137 050626	062706	000001 000006		175:	ADD TRAP	#6.SP	:	678 678 679
14138 050630 14139 050632	103002				ROR	RO 18\$		
14140 050634 14141 050640 14142 050642 14143 050644 14144 050650 14145 050656 14146 050660 14147 050664	000167 005205	176754		18\$:	INC	#1,14(SP) #6,SP 67 R0 18\$ 2\$ R5 19\$		679
14143 050644	000167	176740 000006	000004	***	JMP	198		
14144 050650 14145 050656 14146 050660 14147 050664	001004		000001	19\$:	BNE	6(SP),#1 20\$ ML.LUN,R0 51	; DODU.FLG,*	679
14147 050664	104451	142714			TRAP	SI SI		679

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 1	2:16 PAGE 324	SEQ 0311
14149 14150	ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
14152 050666 104444 14153 050670 062706 00001 14154 050674 000207	2 208: TRAP ADD RTS	44 #12,SP PC	; 6626
14156 14157 14162 14163	; Routine Size: ; Maximum stack	292 words depth per invocation: 19 words	
14171 050676 14172 050676 004767 17666 14173 050702 104466 14174 050704 006000 14175 050706 103773 14176 050710 000207	T26:: JSR TRAP ROR BLO RTS	PC,\$T26 66 R0 1\$ PC	; 6801
14178 14179 14184 14185 14186 ; 6804 ! <blf< td=""><td>; Routine Size: ; Maximum stack /PAGE></td><td>6 words depth per invocation: 0 words</td><td></td></blf<>	; Routine Size: ; Maximum stack /PAGE>	6 words depth per invocation: 0 words	

14188 :ML4 14189 :		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (77)</neale>
14190	4905 1		
14192	6806		
14193 :	6807 BGNTST;		
14195	6809 !++		
14196 :	6810 TEST NUMBER: TST 27		
14197 :	6811 !		
14198 : 14199 :	6812 TEST NAME: INITIAL ARRAY TEST		
14200 :	6814 ! TEST DESCRIPTION:		
14201 :	6815 !		
14203 :	6816 DUE TO THE NATURE OF THE DEVICE 6817 THERE EXISTS KNOWN BAD ARRAY 6818 DATA LOCATIONS. 6819		
14204 :	6818 ! DATA LOCATIONS.		
14206	ARON I THEREFORE TO INITIALLY TEST THE ADDAYS		
14207 :	6821 6822 NIBBLE THRESHOLD OF 36 BAD NIBBLES 6823 OUT OF 100 NIBBLES TESTED WILL BE 6824 TOLERATED BEFORE DETERMINING CONTROL 6825 LOGIC TO BE IN ERROR.		
14208 :	6822 ! NIBBLE THRESHOLD OF 36 BAD NIBBLES		
14210 :	6824 ! TOLERATED BEFORE DETERMINING CONTROL		
14211 :	6825 LOGIC TO BE IN ERROR.		
14213	TIMING AND CONTROL LOGIC A BAD NIBBLE THRESHOLD OF 36 BAD NIBBLES OUT OF 100 NIBBLES TESTED WILL BE TOLERATED BEFORE DETERMINING CONTROL LOGIC TO BE IN ERROR. THE ARRAYS' ARE INITIALLY TESTED BY: THE ARRAYS' ARE INITIALY		
14214 :	6828 !		
14215 ; 14216 ;	6829 1. VIA DAT DM MOD WRITE DATA PATTERNS 6830 0F 1'S AND 0'S TO 5 ARRAY		
14217 :	6831 ! WORDS.		
14218 :	6832		
14219 :	6833 2. TEST EACH NIBBLE (4 BITS) FOR 1'S AND 0'S AND COUNT EACH BAD NIBBLE ENCOUNTERED.		
14221 :	6835 ! NIBBLE ENCOUNTERED.		
14222 :	6836 ! 7 IF ACCIDENTATED DAD AUTOU FC		
14223 :	6837 : 3. IF ACCUMULATED BAD NIBBLES EXCEED 36 THEN REPORT AN ERROR.		
14225 ;	6839 !		
14226 :	6840 !		
14228 :	6842 local		
14229 :	6843 TST_PAT, 6844 BAD_NIB_CNT,	!TEST PATTERN !NUMBER OF BAD NIBBLES !ERROR FLAG	
14231	6845 ERR_FLG;	INUMBER OF BAD NIBBLES	FOUND
14232	6846	.EMON PENO	
14233	6847 TST_PAT = ONES; 6848 BAD_NIB_CNT = ZEROES;		
14235	0849		
14236 :	6850 incr TWICE from 0 to 1 do 6851 begin 6852 BGNSUB;	!REPEAT LOOP TWICE	
14238	6851 begin 6852 BGNSUB;		
14239 :	6853 CLR MBUS:		
14240 :	6854 MLDT = .TST_PAT;	!LOAD TEST PATTERN INT	D DIAG REGISTERS
14242 ;	6853 CLR_MBUS; 6854 MLDT = .TST_PAT; 6855 MLD2 = .TST_PAT; 6856 MLE2 = .TST_PAT;		

```
22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BLI.2 (77)
     DAT_DM = ONE;
FIRST_BLK_XFER ();
MLCS1 = write;
                                                                  SET DATA DIAG MODE
SET UP A FIRST BLK XFERR
DO A MBUS WRITE FUNCTION
     incr CNT from 0 to 4 do
                                                                 !CLOCK 5 WORDS INTO MEMORY
          begin
DELAY (ONE_US);
DAT_CLK = ONE;
end;
     CLR_MBUS;
DAT_DM = ONE;
FIRST_BLK_XFER ();
MLCS1 = read;
DELAY (ONE_US);
                                                                 SET DATA DIAG MODE
SET UP A FIRST BLK XFERR
DO A READ FUNCTION
     incr ARR_WRD from 0 to 4 do
                                                                 !READ THE 5 WORD IN MEMORY
          begin
DAT_CLK = ONE;
DELAY (ONE_US);
RD_LNG_WRD;
                                                                 !CLOCK OUT A WORD INTO THE DIAG REGS
                                                                 !READ THE DIAG REGISTERS
           incr NIB_PTR from 0 to 9 do
                                                                 !READ THE 10 NIBBLES IN THE WORD
                begin
TST_LNG_WRD (.NIB_PTR, .TST_PAT, ERR_FLG); !COMPARE TST PAT TO NIBBLE UNDER TEST
                if .ERR_FLG IS_SET then BAD_NIB_CNT = .BAD_NIB_CNT + 1;
                                                                  !INCREMENT BAD_NIBBLE COUNT IFERR_FLG SET
                end:
          end:
     TST_PAT = not .TST_PAT;
                                                                 !REPEAT WITH COMPLIMENT TST PAT
     ENDSUB:
     end:
if .BAD_NIB_CNT gtr 36
                                                                 !SEE IF 36 OUT OF THE 100 XFERRED WHERE BAD
    begin
ERRDF (77, ASYNC, DUMPER);
PRINTB (FIV FMT, WRD_22, PHR_4, WRD_12, WRD_45, FNC_14);
DODU (.ML_LUN);
                                                                 !ERROR IF GTR 36
     end:
ENDIST:
```

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 327	SEQ 0314
14299 14300	ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
14305 050712 004167 133200 14306 050716 024646 14307 050720 012701 177777 14308 050724 005046 14309 050726 005005 14310 050730 104402 14311 050732 152777 000040 14312 050740 016704 142636 14313 050744 042704 1777770 14314 050750 142777 000007	\$T27: JSR CMP MOV CLR CLR CLR TRAP BISB MOV BIC BICB BICB BISB	R1.\$SAVE5 -(SP)(SP) W-1.R1 -(SP) R5 2 #40.aml.REG+40 ML.DUT.R4 #177770.R4 #7.aml.REG+40	6803
14315 050756 150477 142232 14316 050762 010177 142356 14317 050766 010177 142336 14318 050772 010177 142336 14319 050776 152777 000010 14320 051004 004767 145030 14321 051010 012777 000061 14322 051016 005002 14323 051024 001411 14325 051026 016704 131064	BISB MOV MOV MOV MOV SISB JSR 142136 28: MOV CLR CLR MOV BEQ MOV BEQ CLR DEC BNE SS: DEC BR	R4.aML.REG+40 R1.aML.REG+170 R1.aML.REG+200 R1.aML.REG+160 #10.aML.REG+120 PC.FIRST.BLK.XFER #61.aML.REG R2 #1.R3 6\$ CNT *,\$\$TMP2	6854 6855 6856 6857 6858 6859 6861 6863
14326 051032 001404 14327 051034 005066 000004 14328 051040 005304 14329 051042 001374 14330 051044 005303 14331 051046 000766 14332 051050 152777 000020	142216 6\$: BISB	L\$DLY,R4 ; *,\$\$TMP1 5\$ 4(SP) R4 4\$ 8\$TMP1 4\$ 8\$TMP2 3\$ #20,aML.REG+120 ; \$\$TMP2	6864 6861
14334 051060 020227 000004 14335 051064 003755 14336 051066 152777 000040 14337 051074 016704 142502 14338 051100 042704 1777770 14339 051104 142777 000007 14340 051112 150477 142076 14341 051116 152777 000010 14342 051124 004767 144710 14343 051130 012777 000071 14344 051136 012703 000001	INC CMP BLE BISB MOV BIC BICB	R2 R2,#4 ; CNT,* 2\$ #40,aML.REG+40 ML.DUT,R4 #177770,R4 #7,aML.REG+40	6865
14346 051144 016704 130746	142120 BLE BISB MOV BIC BICB BISB BISB BISB BISB BISB BISB	#40.aml.REG+40 ML.DUT.R4 #177770,R4 #7.aml.REG+40 R4.aml.REG+40 #10.aml.REG+120 PC.FIRST.BLK.XFER #71.aml.REG #1.R3 10\$ L\$DLY.R4 9\$ 4(SP) R4 **.\$\$TMP1	6868 6869 6870 6871
14348 051152 005066 000004 14349 051156 005304 14350 051160 001374 14351 051162 005303 14352 051164 000766 14353 051166 005002	8\$: CLR DEC BNE 9\$: DEC BR 10\$: CLR	4(SP) R4 ; \$\$TMP1 8\$ R3 ; \$\$TMP2 7\$ R2 ; ARR. WRD	6873

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 328	SEQ 0315
14355 14356	:ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
14357 14358 051170 152777 000020 14359 051176 012703 000001 14360 051202 001411 14361 051204 016704 130706 14362 051210 001404 14363 051212 005066 000004 14364 051216 005304 14366 051222 005303 14367 051224 000766	142076 11\$: BISB MOV 12\$: BEQ MOV BEQ 13\$: CLR DEC BNE 14\$: DEC BNE 15\$: MOV MOV CLR 157712 137706 MOV MOV ADD JSR CMP BNE INC CMP BLE INC CMP BLE INC CMP BLE COM	#20,aML.REG+120 #1,R3 : *,SSTMP2 15\$ LSDLY,R4 : *,SSTMP1 14\$ 4(SP) : SSTMP R4 : SSTMP1 13\$ R3 : SSTMP2	6875
14368 051226 017767 142112 14369 051234 017767 142114 14370 051242 017767 142066 14371 051250 005004 14372 051252 010446 14373 051254 010146 14373 051256 012746 000010 14375 051262 060616	137712 MOV 137706 MOV CLR 16\$: MOV MOV MOV ADD	aML.REG+170,D1.TEMP aML.REG+200,D2.TEMP aML.REG+160,E2.TEMP R4 R4,-(SP) ; NIB.PTR R1,-(SP) ; NIB.PTR,* R1,-(SP) ; TST.PAT,* #10,-(SP) ; ERR.FLG,*	6879 6881
14376 051264 004767 144656 14377 051270 026627 000010 14378 051276 001002 14379 051300 005266 000006 14380 051304 062706 000006	000001 CMP BNE INC	10(SP),#1 ; ERR.FLG,* 17\$ 6(SP) ; BAD.NIB.CNT	6883
14380 051304 062706 000006 14381 051310 005204 14382 051312 020427 000011 14383 051316 003755	178: ADD INC CMP BLE	SP, (SP) PC, TST, LNG, WRD 10(SP), #1 17\$ 6(SP) #6, SP R4 R4, #11 16\$ R2 R2, #4 11\$ R1 R1 ; ERR, FLG, * ; ARR, FLG, * ; ARR, FLG, * ; ARR, FLG, * ; ARR, WRD, * ; ARR, WRD, * ; TST, PAT	6880 6879
14384 051320 005202 14385 051322 020227 000004 14386 051326 003720 14387 051330 005101	INC CMP BLE	R2 R2,#4 11\$; ARR. WRD, *	6873
14388 051332 104467 14389 051334 006000 14390 051336 103002 14391 051340 000167 177364		R0 19\$	6890
14393 051346 020527 000001 14394 051352 003772 14395 051354 021627 000044	CMP BLE CMP	R5 R5,#1 ; TWICE,* 18\$ (SP),#44 ; BAD.NIB.CNT,*	
14396 051360 003432 14397 051362 104455 14398 051364 000115	BLE TRAP .WORD .WORD	R5 R5,#1 ; TWICE,* 18\$ (SP),#44 ; BAD.NIB.CNT,* 20\$ 55 115 ASYNC DUMPER	6897
14400 051370 024052 14401 051372 012746 010122 14402 051376 012746 006772 14403 051402 012746 006426 14404 051406 012746 006530 14406 051412 012746 006530 14407 051422 012746 00006	TRAP ROR BHIS 18\$: JMP 19\$: INC CMP BLE CMP BLE TRAP .WORD .WORD .WORD .WORD MOV MOV MOV MOV MOV MOV MOV MOV MOV TRAP	#FNC.14,-(SP) #WRD.45,-(SP) #WRD.12,-(SP) #PHR.4,-(SP) #WRD.22,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 ; SP,*	6898
14408 051426 010600 14409 051430 104414	MOV	SP.RO ; SP.*	

CZMLABO ML-11 LI MISCELLANEOUS C	OD ING SE	CTION		. 520 00	12:16 PAGE 329			SEQ 0316
14411			:ML4				22-Dec-1980 09 22-Dec-1980 09	:24:31 TOPS :21:22 PA:<
14414 051432 14415 051436	016700 104451	142142		MOV TRAP TRAP	ML.LUN,RO	:		6899
14417 051442 14417 051442 14418 051446 14419 051452	062706 062706 062706 000207	000016 000006	20\$:	ADD ADD RTS	#16.SP #6.SP PC	;		6896 6803
14421 14422 14427 14428			; Rout	ine Size: num stack	177 words depth per invocation:	16 words		
14436 051454 14437 051454 14438 051460 14439 051462 14440 051464 14441 051466	004767 104466 006000 103773 000207	177232	T27:: 1\$:	JSR TRAP ROR BLO RTS	PC,\$T27 66 R0 1\$ PC	•		6901
14443 14444 14449 14450 14451 ;	6904	! <blf page=""></blf>	; Routi	ine Size: num stack	6 words depth per invocation:	0 words		

```
14453 :ML4
14454 :
14455
14456 :
14457 :
14458 :
14459 :
                                                                                                      22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                          TOPS-20 Bliss-16 V2(212)
                                                                                                                                         PA: <NEALE>BL3ML4.BL1.2 (78)
                               BGNTST:
                                  TEST NUMBER: TST 28
                                  TEST NAME:
                                                       PROM SELECTION TEST
                                  TEST DESCRIPTION:
                                           DUE TO THE NATURE OF THE DEVICE
AND OF THE ARRAY MODULES' UV
PROMS, ONLY PROM READS ARE
ALLOWED DURING DIAG TESTING.
                                           THEREFORE THE ARRAY MODULE UV PROMS ARE TESTED FOR UNIQUE SELECTION BY:
                                           1. AT EACH PRESENT ARRAY MODULE WRITE 127 ARRAY WORDS WITH 1'S/O'S PATTERN.
                                                READ THE UV PROMS AT THEIR RESPECTIVE ARRAY WORD LOCATION AND SEE IF THE PROMS MASK BAD NIBBLE LOCATIONS (ENCOUNTERED BAD NIBBLES INDICATES INCORRECT MASKING). COUNT EACH BAD NIBBLE ENCOUNTERED AT AN
                                                 ARRAY MODULE.
                                           3. ALLOW A THRESHOLD OF 5 BAD NIBBLES AT ANY ARRAY MODULE.
                                                 REPORT PROM SEL ERRORS AT RESPECTIVE ARRAY MODULE IF THE
                                                 THRESHOLD IS EXCEEDED.
                              IMPLICIT IMPUTS:
PD TEMP:
A BIT VECTOR OF 16 BITS WHERE
                                           THE READ PROM DATA IS STORED
                                           AND ACCESSED FROM.
                                           IO BUF
A VECTOR OF 256 WORDS WHERE
DATA FOR MBUS READS AND WRITE
                                           FUNCTION ARE FOUND.
                               local
                                     DODU_FLG,
ERR_FLG,
TST_PAT,
                                                                                                      !DROP UNIT FLG
                                                                                                       ERROR FLG
                                     ERR_CNT;
                                                                                                       !ERROR COUNT
                               DODU_FLG = ZERO:
                               TST_PAT = ONES;
                               incr ARR_SEL from 0 to .LST_ARR by .ARR_INC do !TEST ALL PRESENT ARRAYS
```

```
TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (78)
begin
ERR_CNT = ZERO;
BGNSUB:
incr TWICE from 0 to 1 do
                                              !REPEAT LOOP TWICE
                                                !LOAD DATA DIAG REGISTERS WITH TST_PAT
                                                !SET DATA DIAG MODE
                                                LOAD WORD COUNT
LOAD UBUS ADRS
LOAD SECTOR
    MLDA = .ARR_SEL;
    MLCS1 = write:
                                                 !DO A WRITE FUNCTION
    incr CNT from 0 to 127 do
                                                !CLOCK IN 127 WORDS
        begin
DELAY (ONE_US);
DAT_CLK = ONE;
end;
    CLR_MBUS;
DAT_DM = ONE;
                                                SET DATA DIAG MODE
    MLWC = not 255:
                                                 !LOAD UBUS ADRS
    MLBA = IO BUF:
                                                 LOAD SECTOR
    MLDA = .ARR_SEL;
    MLCS1 = read;
                                                 !DO A READ FUNCTION
    DELAY (ONE_US);
    incr WD_CNT from 0 to 127 do
                                                !READ THE 10 WORDS
       begin
PD_TEMP = .MLPD;
DAT_CLK = ONE;
DELAY (ONE_US);
                                                GET PROM DATA FOR THIS WORD CLOCK THIS WORD INTO DIAG REG
        RD_LNG_WRD;
                                                !READ DIAG REG FOR THIS WORD
        incr NIB_PTR from 0 to 9 do
                                                !LOOK AT ALL 10 NIBBLE
             then
                 begin TST_LNG_WRD (.NIB_PTR, .TST_PAT, ERR_FLG); !COMPARE NIBBLE TO TST_PAT
                 if .ERR_FLG IS_SET then ERR_CNT = .ERR_CNT + 1;
                                                !INCREMENT ERROR COUNT IF ERROR FLG IS SET
                 end;
        end:
   TST_PAT = not .TST_PAT;
                                             !REPEAT WITH COMPLIMENT DATA
```

TST.PAT.* TST.PAT.*

MOV

MOV MOV BISB

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 333	SEQ 0320
14620 14621	;ML4	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
14623 051622 012777 011600 14624 051630 010177 141350 14625 051634 012777 000061 14626 051642 005003 14627 051644 012704 000001 14628 051650 001411 14629 051652 016705 130240 14631 051660 005066 000014 14632 051664 005305 14633 051666 001374 14634 051670 005304	141344 MOV #10.BUF, aML, REG+20 MOV R1, aML.REG+30 : ARR.SEL, * 141312 MOV #61, aML.REG CLR R3 : MOV #1, R4 5\$: BEQ 8\$ MOV L\$DLY, R5 6\$: CLR 14(SP) DEC R5 BNE 6\$ 7\$: DEC R4 BR 5\$ 141372 8\$: BISB #20, aML.REG+120 : ARR.SEL, * CNT *, \$\$TMP2 : *,\$\$TMP1 : \$\$TMP1 : \$\$TMP2	6969 6970 6971 6973 6975
14636 051674 152777 000020 14637 051702 005203 14638 051704 020327 000177	INC R3 CNT CMP R3.#177 : CNT.*	6976 6973
14649 051710 003755 14640 051712 152777 000040 14641 051720 016705 141656 14642 051724 042705 177770 14643 051730 142777 000007 14644 051736 150577 141252 14646 051730 012777 177400	141274 BISB #40.aML.REG+40 ; MOV ML.DUT.RS BIC #177770.RS 141256 BICB #7.aML.REG+40 BISB R5.aML.REG+40 141324 BISB #10.aML.REG+120 ;	6977
14648 051764 010177 141214 14649 051770 012777 000071 14650 051776 012704 000001 14651 052002 001411 14652 052004 016705 130106	141210 MOV #10.BUF, aML.REG+10 141210 MOV #10.BUF, aML.REG+20 MOV R1, aML.REG+30 ARR.SEL,* 141156 MOV #71, aML.REG MOV #1, R4 **, \$\$TMP2 9\$: BEQ 12\$ MOV L\$DLY,R5 ;*,\$\$TMP1	6980 6981 6982 6983 6984 6985
14654 052012 005066 000014 14655 052016 005305 14656 052020 001374	10\$: CLR 14(SP) ; \$\$TMP DEC R5 ; \$\$TMP1 BNE 10\$ 11\$: DEC R4 ; \$\$TMP2	
14657 052022 005304 14658 052024 000766 14659 052026 005003 14660 052030 017767 141350 14661 052036 152777 000020 14662 052044 012704 000001 14663 052050 001411 14664 052052 016705 130040 14665 052056 001404 14667 052064 005305 14668 052066 001374 14669 052070 005304 14671 052074 017767 141244 14672 052102 017767 141246 14673 052110 017767 141220	108: CLR 14(SP) DEC R5 BNE 10S 118: DEC R4 BR 9\$ 128: CLR R3 141062 138: MOV aML.REG+230,PD.TEMP BISB #20,aML.REG+120 MOV #1,R4 148: BEQ 17\$ MOV LSDLY,R5 158: CLR 14(SP) DEC R5 BNE 158 158: SSTMP1 168: DEC R4 BR 14S 137050 178: MOV aML.REG+170,D1.TEMP 137040 NOV aML.REG+100,E2.TEMP 137040 NOV aML.REG+160,E2.TEMP CLR R4 SSTMP1 18. SSTMP2 18. SSTMP2 18. SSTMP2 18. SSTMP2 18. SSTMP2	6987 6989 6990 6991
14665 052056 001404 14666 052060 005066 000014 14667 052064 005305 14668 052066 001374 14669 052070 005304 14670 052072 000766	15\$: CLR 14(SP) ; \$\$TMP DEC R5 ; \$\$TMP1 BNE 15\$ 16\$: DEC R4 ; \$\$TMP2	
14667 052064 005305 14668 052066 001374 14669 052070 005304 14670 052072 000766 14671 052074 017767 141244 14672 052102 017767 141246 14673 052110 017767 141220 14674 052116 005004	16\$: DEC R4 ; \$\$TMP2 137050 17\$: MOV aML.REG+170.D1.TEMP 137044 MOV aML.REG+200.D2.TEMP 137040 MOV aML.REG+160.E2.TEMP CLR R4 ; NIB.PTR	6994

SCELLANEOUS C									0321
14677				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
14679 052120 14680 052122 14681 052124 14682 052124	010405 006205 006205 006205	013120		18\$:	MOV ASR ASR ASR ADD	R4,R5 R5 R5 R5	; NIB.PTR,*		6996
4684 052134	010546	013120			MOV	MPD.TEMP,R5 R5,-(SP) R4,-(SP) M177770,(SP)	; NIB.PTR,*		
4686 052140 4687 052144	042716	177770 000001			BIC	#177770, (SP) #1,-(SP) -(SP)	, N.O. , IN,		
	005046 004767 062706 005700	130762 000010			MOV CLR JSR ADD TST BNE MOV MOV	PC_BL\$GT2 #10,SP R0 20\$			
4695 052166 14695 052170	010446 010246				MOV	R4,-(SP) R2,-(SP)	: NIB.PTR.* : TST.PAT.*		6999
46% 052176	060616	000020			ADD	#20,-(SP) SP,(SP)	: ERR.FLG,*		
4698 052204	026627	143742 000020	000001		JSR	SP, (SP) PC, TST.LNG.WRD 20(SP),#1	: ERR.FLG,*		7001
700 052214	001002	000010 000006		100.	INC	19\$ 10(SP)	: ERR.CNT		4000
702 052224	005204	0000011		19\$: 20\$:	INC	#6,SP R4 R4,#11	NIB.PTR		6998
704 052222	003732	000011			BLE	18\$; NIB.PTR,*		6987
706 052236	020327	000177			CMP	R3,#177	; WD.CNT,*		0701
08 052244 09 052246	005102				CMP BNE INC ADD INC CMP BLE INC CMP BLE COM INC	R2 (SP)	: TST.PAT : TWICE : TWICE,*		7008 6961
710 052250	021627	000001			CMP	(SP) ,#1	; TWICE,*		0,01
708 052244 709 052246 710 052250 711 052254 712 052256 713 052270 715 052270 716 052276	005102 005216 021627 003002 000167 026627 003434 104455 000116 010570	177260 000002	000005	21\$:	CMb	3\$ 2(SP) #5	; ERR.CNT,*		7011
714 052270	104455				BLE	22 s 55			7014
6716 052274 6717 052276	010570				. WORD	ARR.DAT			
4718 052300 4719 052302	024052	006406			CMP BGT JMP CMP BLE TRAP . WORD . WORD . WORD MOV MOV	NUMPER	:		7015
4717 052276 4718 052300 4719 052306 4721 052312 4721 052312 4722 052332 4724 052332 4724 052332 4726 052332 4728 052334 4728 052334 4729 052334	012746	006406 006700 006666 006034 000004			MOV	R4,#11 18\$ R3 R3,#177 13\$ R2 (SP) (SP),#1 21\$ 3\$ 2(SP),#5 22\$ 55 116 ARR.DAT DUMPER #WRD.10,-(SP) #WRD.37,-(SP) #WRD.35,-(SP) #WRD.35,-(SP) #YHR.FMT,-(SP) 8P,R0 14 R1,(SP)			
4722 052316 4723 052322 4724 052326 4725 052330 4726 052332 4727 052334 4728 052340 4729 052344	012746	000004			MOV	#4,-(SP)			
4725 052330	104414				TRAP	14 91 (SP)	; SP,* ; ARR.SEL,*		7016
4727 052334	012746	004644			MOV	#FMT.9 -(SP)	, ANN. SEL, "		7010
14729 052344 14730 052346	012746 012746 010600 104414	000002			MOV MOV TRAP MOV MOV MOV TRAP	R1,(SP) #FMT.9,-(SP) #2,-(SP) SP,R0 14	; SP,*		

OGIC TEST	ON MACRO M	1113 22-DEC	80 12:16 PAGE 335		SEQ 0322
		ML4			22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
012766 000 062706 000 104467 006000	0001 000026 0016	22\$: MOV ADD TRA ROR	#1.26(SP) #16.SP 67 R0	*,DODU.FLG	7017 7013 7018
000167 177	7142 0010 000001	23\$: CMP	2\$ 10(SP),#1	; DODU.FLG,*	7022
016700 141 104451	1170	MOV TRA	243	:	7025
104444 066601 000 020166 000	0004 0006	24\$: ADD 25\$: CMP	44	; *,ARR.SEL ; ARR.SEL,*	6956
000167 177 062706 000 000207	7100 0016	26\$: ADD RTS	1\$ #16,SP PC	:	6903
		; Routine S ; Maximum s	ze: 244 words ack depth per invocation:	20 words	
004767 177 104466 006000 103773 000207	7024	TRA	PC.\$T28 66 R0 1\$ PC	•	7029
7072	DI F /DACES	; Routine S ; Maximum s	ze: 6 words ack depth per invocation:	0 words	
	012766 00 062706 00 104467 006000 103002 000167 17 026627 00 001004 016700 14 104451 104444 066601 00 003002 000167 17 062706 00 000207	012766 000001 000026 062706 000016 104467 006000 103002 000167 177142 026627 000010 000001 016700 141170 104444 066601 000004 020166 000006 003002 000167 177100 062706 000016	012766 000001 000026 MOV 062706 000016 22\$: TRAP 006000 BHIS 000167 177142 006627 000010 000001 23\$: CMP 001004 141170 TRAP 104451 104444 066601 000004 24\$: ADD 020166 000006 25\$: CMP 003002 BGT 000167 177100 062706 000016 26\$: ADD 00270 FTS : Routine Si : Maximum st 128:: JSR TRAP 006000 103773 000207 : Routine Si : Maximum st : Routine Si : Maximum st : Routine Si : Maximum st : Routine Si	012766 000001 000026	Mov

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22

TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BL1.2 (79)

BGNTST:

TEST NUMBER: TST 29

TEST NAME: READ WRITE ARRAYS WITH PROM DATA

TEST DESCRIPTION:
COMBINE THE READING OF ARRAY
MODULE DATA WITH ARRAY MODULE UV PROM DATA AND
FIND A GOOD BLOCK WHERE FURTHER
TESTING WILL BE PERFORMED BY:

1. STARTING AT BLOCK O WRITE THE BLOCK WITH SELECTED DATA PATTERNS AND READ THE BLOCK AVOIDING ANY BAD NIBBLES POINTED TO BY THE PROM DATA.

SET ERROR FLAG IF ANY BAD NIBBLES ARE ENCOUNTERED IN BLOCK.

- 2. REPEAT WRITING/READING THIS BLOCK UNTIL ALL PATTERNS ARE TESTED OR THE ERROR FLAG IS SET.
- 3. IF ALL PATTERN HAVE BEEN TESTED AND THE ERROR FLAG IS NOT SET THEN SAVE THIS BLOCK ADDRESS AS THE GOOD BLOCK ADRS AND EXIT TEST.
- 4. ELSE IF THE ERROR FLG HAS SET THEN REPEAT TEST AT THE NEXT ROW. REPEAT UNTIL A GOOD BLOCK IS FOUND OR LAST ROW IS REACHED.
- 5. IF NO GOOD BLOCK IS FOUND BY LAST ROW THEN REPORT ERROR AND EXIT TEST.

IMPLICIT INPUTS:

RAS INC LOADED DURING THE INITIALIZATION CODE AND CONTAINS THE ROW ADDRESS

PD TEMP: A BITVECTOR OF 16 BITS WHERE THE READ PROM DATA IS STORED AND ACCESSED FROM.

IO BUF: A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITE FUNCTION ARE FOUND.

local WRD_CNT, NIB_PTR,

!WORD COUNT INIBBLE POINTER CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION

```
14841 :ML4
14842 :
14843
14844 :
14845 :
14846 :
14847 :
14848 :
                                                                                                   22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                     TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (79)
                                                                                                   PASS COUNT
NIBBLE PATTERN
DONE FLAG
                                    PASS_CNT.
                                    NIB PAT,
DONE FLG,
ERR FLG,
SECTOR_NO;
                                                                                                    !ERROR FLAG
                                                                                                    SECTOR NUMBER
                              BGNSUB;
                              PASS_CNT = -1;
SECTOR_NO = ZEROES;
DONE_FEG = ZERO;
                                                                                                   !THIS LOOP DETERMINES WHEN TO STOP
                              do
                                    begin
                                                                                                   !THIS LOOP RUNS THE PATTERNS
                                          begin
PASS_CNT = .PASS_CNT + 1;
CLR_MBUS;
                                                                                                   !INCREMENT THE PASS COUNT
                                                                                                   SET DATA DIAG MODE
LOAD SECTOR NUMBER IN DSA
LOAD WORD COUNT
LOAD UBUS ADRS
                                          DAT DM = ONE;
                                          MLDA = .SECTOR NO:
                                          MLWC = not 255;
                                          MLBA = IO_BUF;
                                          case .PASS_CNT from 0 to 3 of
                                                                                                   !SELECT A NIBBLE PATTERN
                                               set
                                                [0]:
                                                     NIB_PAT = %0'000000';
                                                                                                   !ZEROES
                                                [1] :
                                                     NIB_PAT = %0'17';
                                                                                                   !ONES
                                                [2] :
                                                     NIB_PAT = %0'12';
                                                                                                   !ALTERNATING ONE'S, ZEROES
                                                     NIB_PAT = %0'15'
                                                                                                   !COMPLIMENT ONE'S, ZEROES
                                                tes;
                                          incr LD_CNT from 0 to 9 do LD_ENG_WRD (.LD_CNT, .NIB_PAT);
                                                                                                   !LOAD NIBBLE PATTERN INTO NIBBLE SAVE
                                          WRT_LNG_WRD;
MLCS1 = write;
                                                                                                   !LOAD THE DATA DIAG REGISTERS WITH NIBBLE SAVE
                                                                                                   !DO A WRITE FUNCTION
                                          incr WRT_CNT from 0 to 127 do
begin
DELAY (ONE_US);
DAT_CLK = UNE;
end;
                                                                                                   !WRITE PATTERN INTO THIS BLOCK
                                          CLR_MBUS;
```

ENDSUB:

!REPEAT UNTIL GOOD BLK FOUND OR AT LST ROW

HIZCELTANEOUS (ODING SE	CTION						SEQ 0326
14953 :ML4 14954 : 14955						22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(21) PA: <neale>BL3ML4.BLI.2</neale>	(79)
14956 :	7189	if .SECTOR_	NO eql .LST	_ARR +	ARR_INC	!SEE IF WE'RE AT THE L	AST BLOCK	
14957 14958 14959 14960 14961 14962 14963	7189 7190 7191 7192 7193 7194 7195 7196 7197 7198	begin ERRDF (PRINTB DODU (. DOCLN; end;	79, INTER, (THR_FMT, W ML_LUN);	DUMPER); RD_14, P	HR_10, FNC_15);	!A GOOD BLK NOT FOUND !ERROR AND DROP UNIT		
14965	7198	ENDTST;						
14969 14973 052454		131436 000014	\$129:	JSR	R1.\$SAVE5	:		7031
14973 052454 14974 052460 14975 052464	004167 162706 104402	000014	18:	SUB	#14,SP			7089
14976 052466	012766	177777 000	0002	MOV	#-1,2(SP)	; *,PA	SS.CNT	7092
14978 052476	005066	000004		CLR	4(SP)	: SECT	SS.CNT OR.NO .FLG .CNT	7089 7092 7093 7094 7101
14979 052502	005266	000002	28:	INC	2(SP)	; PASS	CNT	7101
14981 052514	016705	141062	500	BISB	ML.DUT, R5			
14982 052520	042705	000007 140	462	BIC	#177770,R5			
14984 052532	150577	140456		BISB	R5, aML . REG+40			
14986 052544	005066 005266 152777 016705 042705 142777 150577 010277 010277	140434)530	BIC BICB BISB BISB MOV	2 #-1,2(SP) R2 4(SP) 2(SP) #40,aML.REG+40 ML.DUT,R5 #177770,R5 #7,aML.REG+40 R5,aML.REG+40 #10,aML.REG+120 R2,aML.REG+30 #-400,aML.REG+1(#10.BUF,aML.REG+1(#10.BUF,aML.REG+1(\$10.BUF,aML.REG+1(\$10.BUF,aML.REG+1(SECT	OR.NO,*	7103 7104 7105 7106 7108
14987 052550 14988 052554	012777	177400 140	406 410	MUV	#-400, aML.REG+10	20		7105
14989 052564	016605	000002	410	MOV	2(SP), R5	: PASS	.CNT,*	7108
14990 052570 14991 052572 14992 052576 14993 052600	006305 066507 000010 000014	052576		ASL ADD . WORD . WORD	R5 3\$ (R5) ,PC 4\$-3\$ 5\$-3\$ 6\$-3\$ 7\$-3\$			
14992 052576	000010		3\$:	. WORD	48-38			
14994 052602	000022			WORD	6\$-3\$			
14995 052604	000030		45:	. WURD	7\$-3\$ P1	; NIB.I	DAT	7112
14997 052610	000410	000017		BR	R1 8\$ #17,R1	:		7108
14999 052616	000405	000017	5\$:	BR	#17,R1 8\$: *,NII	B.PAT	7115 7108
14995 052604 14996 052606 14997 052610 14998 052612 14999 052616 15000 052620 15001 052624 15002 052626 15003 052632 15004 052634 15005 052636 15006 052640	000022 000030 005001 000410 012701 000405 012701 005005 010546 010146 004767	000012	6\$:	CLR BR MOV BR MOV CLR MOV MOV JSR	8\$ #12,R1	*,NII	B.PAT	7112 7108 7115 7108 7118 7108 7121 7124 7125
15002 052626	012701	000015	7\$:	MOV	8\$ #15,R1	*,NII	B.PAT	7108
15003 052632	005005		7\$: 8\$: 9\$:	CLR	R5 R5 -(SP)	LD.CI LD.CI : NIB.I	VT •	7124
15005 052636	010146	1//2/0	,	MOV	R5 R5,-(SP) R1,-(SP) PC,LD.LNG.WRD	; NIB.	PAT,*	1123
12000 022040	004/0/	144260		JSK	PC, LD. LNG. WRD			

:31 TOPS	22-Dec-1980 09:24:3 22-Dec-1980 09:21:2				:ML4				15008
:22 PA:4	22-060-1980 09:21:2		(SP)+,(SP)+	CMD	•			022626	15010
7124		: LD.CNT	R5 R5,#11 9\$	CMP INC CMP BLE MOV MOV MOV CLR MOV BEQ MOV BEQ CLR DEC BNE DEC BR			000011	005205 020527	15012 052646
7125			D1.TEMP.amL.REG+170	BLE		140460	136270	003767 016777	15014 052654 15015 052656
7128			D1.TEMP, aML.REG+170 D2.TEMP, aML.REG+200 E2.TEMP, aML.REG+160 M61, aML.REG	MOV		140434	136260 000061	016777	15017 052672 15018 052700
7128 7130 7132		WRT.CNT *,\$\$TMP2	#61,aML.REG R3 #1,R4	CLR	10\$:	140240	000001	005003	15019 052706
		; *,\$\$TMP1	145	BEQ	10\$: 11\$:		127174	001411	15021 052714 15022 052716
		: SSTMP : SSTMP1	L\$DLY,R5 13\$ 12(SP)	CLR	12\$:		000012	001404 005066	15023 052722 15024 052724
		; SSTMP1 ; SSTMP2	R5 12\$ R4	BNE	13\$:			001374	15026 052732
7133		, 331172	118 #20,24L.REG+120	BR	148:	140326	000020	000766	15028 052736 15029 052740
7133 7130		WRT.CNT WRT.CNT,*	R3 R3,#177	INC		140320	000177	005203	15030 052746 15031 052750
7134		:	10\$	BLE		140230	000040 140612	003755 152777	15032 052754 15033 052756
			#40,aML.REG+40 ML.DUT.R5 #177770,R5 #7,aML.REG+40	MOV BIC		140212	140612	016705	15035 052770
7137			R5.aML.REG+40 #10.aML.REG+120	81CB 81SB 81SB		140260	177770 000007 140206 000010 140164	150577	15037 053002 15038 053006
7137 7138 7139		SECTOR.NO,*	R2, aML.REG+30	MOV		140136	177400	010277	15039 053014 15040 053020
7140 7141 7142			#10.8UF, aML.REG+20 #71, aML.REG #1, R4 18\$ L\$DLY,R5 17\$ 12(SP)	MOV MOV MOV BEQ		140140 140112	011600 000071 000001	012777 012777 012704 001411 016705 001404	15041 053026 15042 053034
7142		; *,\$\$TMP2	#1 .R4 18\$	BEQ	15\$:			001411	15044 053042 15044 053046
		; *,\$\$TMP1	17\$ 12(sp)	BEQ	16\$:		127042	001404	15046 053054
		SSTMP1	144	MOV BEQ CLR DEC BNE DEC BR MOV INC	100.		000012	005305	15041 053026 15042 053034 15043 053046 15044 053046 15045 053050 15046 053054 15047 053056 15048 053062 15049 053064 15051 053070 15052 053072 15053 053100 15054 053104 15056 053120 15057 053124 15058 053124 15059 053132 15060 053134 15061 053140 15062 053142
		; \$\$TMP2	15\$ #-1,6(SP) 6(SP) aml.REG+230,PD.TEMP #20,aml.REG+120 #1,R4 23\$ L\$DLY,R5 22\$ 12(SP)	DEC	17\$:			005304	15050 053066 15051 053070
7143 7147		; *,WRD.CNT ; WRD.CNT	M-1,6(SP) 6(SP)	INC	18\$: 19\$:	000006	177777	012766 005266	15052 053072 15053 053100
7143 7147 7148 7149 7150		*,\$\$TMP2	#20,aML.REG+120	BISB		140006 140154	177777 000006 140274 000020 000001	152777	15055 053112
7150		; *,\$\$TMP1	23\$ L\$DLY.R5	MOV BISB MOV BEQ MOV BEQ CLR DEC BNE	20\$:		126764	000766 012766 005266 017767 152777 012704 001411 016705 001404 005066 005305 001374	15057 053124 15058 053126
			22\$ 12(SP)	BEQ CLR	21\$:		000012	001404	15059 053132 15060 053134
		: SSTMP1	R5 21\$	DEC				005305 001374	15061 053140 15062 053142

22-Dec-1980 09:24:31 TO 22-Dec-1980 09:21:22 PA				:ML4				5064
22-Dec-1980 09:21:22 PA	; SSTMP2	R4	DEC	22\$:			005304	5066 5067 053144
	,	R4 20\$ aML.REG+170,D1.TEMP aML.REG+200,D2.TEMP aML.REG+160,E2.TEMP #-1,(SP)	DEC BR MOV MOV		135774 135770 135764	140170 140172 140144 177777	000766 017767 017767	5068 053146 5069 053150 5070 053156
71	: *,NIB.PTR	#-1, (SP)	MOV	24\$:	133764	177777	012716	
71	; *,NIB.PTR ; NIB.PTR ; NIB.PTR,*	(SP) "R5 R5 R5	MOV INC MOV ASR ASR ASR ADD MOV MOV BIC MOV	249.			011605 006205 006205	5074 053200 5075 053202 5076 053204
		MPD.TEMP.R5	ADD			013120	062705	5078 053210
	; NIB.PTR,*	##L.REG+160,E2.TEMP #-1,(SP) (SP) (SP),R5 R5 R5 R5 MPD.TEMP,R5 R5,-(SP) 2(SP),-(SP) #177770,(SP) #1,-(SP) -(SP) PC,BL\$GT2 #10,SP R0	MOV BIC MOV			000002 177770 000001	016646 042716 012746	5060 053516 5061 053522 5062 053528
		PC_BL\$GT2 #10,SP RO	CLR JSR ADD TST BNE			127700 000010	004767 062706 005700	5084 053234 5085 053240 5086 053244
71	: NIB.PTR.*	(SP) ,-(SP)	MOV				011646	5088 053250 5088 053250
		#16,-(SP)	MOV			000016	012746	5090 053254
	; ERR.FLG,*	PC.TST.LNG.WRD	JSR			142660	004767	5092 053575
71	; ERR.FLG,*	#10.5P R0 25\$ (SP),-(SP) R1,-(SP) #16,-(SP) SP,(SP) PC,TST.LNG.WRD #6.SP 10(SP),R5 #1,R5 26\$	MOV MOV ADD JSR ADD MOV BIT BNE	25\$:		142660 000006 000010 000001	016605 032705 001012	5094 053272 5095 053276 5096 053302
	; NIB.PTR,*	(SP) ,#11 24\$	ENE			000011	021627	5097 053304 5098 053310
71	:	(SP) #11 24\$ #1 #5 26\$ 6(SP) #177	CMP BNE BIT BNE			000001	032705 001004	5099 053312 5100 053316
	; WRD.CNT,*	6(SP) #177 19\$	ENE		000177	000006	026627	5101 053320 5102 053326
71	PASS.CNT,*	R4 2(SP),#3 27\$ R4 28\$ R5,#1 31\$	CMP BNE CLR CMP BNE INC BR CMP BNE ROR	26\$:	000003	000002	005004 026627 001002 005204	5103 053330 5104 052332 5105 053340 5106 053342
		28\$ R5 ,#1	BR	27\$:		000001	000403 020527	5107 053344 5108 053346
71		31\$ R4 29\$ R5 29\$	BCC	28\$:			001030 006004 103010 005705	5110 053354 5110 053354 5111 053356 5112 053360
71	: *,DONE.FLG : SECTOR.NO,*	29\$ #1.4(SP) R2.GOOD.BLK 30\$	BNE MOV MOV BR ADD MOV		000004	000001 136166	001006 012766 010267	5113 053362 5114 053364 5115 053372
71 71 71 71	*.SECTOR.NO *,PASS.CNT	30\$ RAS.INC.R2 #-1,2(SP)	BR	29\$:	000002	137520 177777	000405 066702 012766	5115 053372 5116 053376 5117 053400 5118 053404

ILABO ML-11 LO SCELLANEOUS CO				.00 /					0329
15121				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
5123 053412	026627	000004	000001	30\$:	CMP BEQ	4(SP),#1 32\$; DONE.FLG,*		7184
5125 053422	016705	136150 136130			MOV	LST.ARR,R5			
127 053432	020205	150150		31\$:	CMP	R2, R5	; SECTOR.NO,*		
120 053436	000167	177040		32\$:	JMP TRAP	28			
131 053444 132 053446	103002				ADD CMP BEQ JMP TRAP ROR BHIS	LST.ARR.R5 ARR.INC.R5 R2.R5 32\$ 2\$ 67 R0 33\$			
5133 053450 5134 053454	000167 016705	177010 136116 136076		33\$:	MOV	1\$ LST.ARR,R5			7189
5135 053460 5136 053464	066705 020205	136076			ADD	LST.ARR,R5 ARR.INC,R5 R2,R5 34\$; SECTOR.NO,*		
138 053470	104455				CMP BNE TRAP . WORD . WORD . WORD	55	:		7192
140 053474	010672				.WORD	55 117 INTER DUMPER			
42 053500	012746	010136			MOV	#FNC.15(SP)	:		7193
44 053510 45 053514	012746	006442			MOV	MURD. 14, -(SP)			
146 053520 147 053524	012746	006034			MOV	MPHR.10,-(SP) MURD.14,-(SP) MTHR.FMT,-(SP) M4,-(SP) SP,R0	; SP,*		
148 053526 149 053530	104414 016700	140044			MOV TRAP MOV	ML.LUN,RO			7194
150 053534 151 053536	104451 104444				MOV TRAP TRAP ADD	51			
5152 053540 5153 053544 5154 053550	062706 062706 000207	000012 000014		34\$:	ADD ADD RTS	#12.SP #14.SP PC	:		7191 7031
155	000207								
5154 053550 5155 5156 5157 5162 5163 5167				; Maxi	mum stack	287 words depth per invocation: 17	7 words		
5163									
5167									

SEQ 0330

22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<

TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BL1.2 (80)

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 BGNTST: TEST NUMBER: TST 30 TEST NAME: REFRESH TIMING TEST TEST DESCRIPTION: TEST THE MEMORY ARRAY MODULES REFRESH TIMING AND CONTROL LOGIC REG BY: 1. WRITING ALL ONES INTO THE GOOD BLOCK. 2. DELAY FOR 2 MS READ THE GOOD BLOCK FOR ONES. KEEP COUNT OF BAD NIBBLES **ENCOUNTERED** 4. ALLOW A BAD NIBBLE THRESHOLD OF 20 BAD NIBBLES OUT OF THE 1280 NIBBLES TESTED. 5. IF THRESHOLD IS EXCEEDED THEN REPORT ERROR AND DROP THE UNIT. IMPLICIT INPUTS: A BIT VECTOR OF 16 BITS WHERE THE READ PROM DATA IS STORED AND ACCESSED FROM. local TST_PAT, ERR_CNT, ERR_FLG; !TEST PATTERN !ERROR COUNT !ERROR FLAG BGNSUB; CLR_MBUS; ERR_CNT = ZERO; TST_PAT = ONES; MLDT = .TST_PAT; MLD2 = .TST_PAT; MLE2 = .TST_PAT;

!LOAD DATA DIAG REGS WITH TST PAT

```
22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
15248 :ML4
15249 :
                                                                                                                TOPS-20 Bliss-16 V2(212)
                                                                                                                PA: (NEALE)BL3ML4, BL1,2 (80)
                         DAT_DM_XFER ();
MLCS1 = write;
                                                                                    SET UP A DATA DIAG MODE XFERR
                                                                                    !DO A WRITE FUNCTION
                          incr WRD_CNT from 0 to 127 do
                                                                                  !WRITE BLOCK WITH TST PAT
                              begin
DELAY (ONE_US);
DAT_CLK = ONE;
end;
                         CLR_MBUS;
DAT_DM_XFER ();
MLCS1 = read;
DELAY (TWO_TH_US);
                                                                                   SET UP A DATA DIAG MODE XFERR DO A READ FUNCTION DELAY FOR 2 MS
                         incr WRD_CNT from 0 to 127 do
                                                                                   !READ THE BLOCK
                              begin
                              PD TEMP = .MLPD;
DAT CLK = ONE;
                                                                                   !GET THE PROM DATA
                                                                                   !CLOCK DATA WORD INTO DIAG REG
                              DELAY (ONE_US);
                              RD_LNG_WRD;
                                                                                   !READ THE DIAG REG
                              incr NIB_PTR from 0 to 9 do
                                                                                 !LOOK AT 10 NIBBLES
                                   begin
                                   if .PD_TEMP [.NIB_PTR] IS_NOT_SET then TST_LNG_WRD (.NIB_PTR, .TST_PAT, ERR_FLG);
                                                                                    !FIND GOOD NIBBLES AND COMPARE THEM
                                   if .ERR_FLG IS_SET then ERR_CNT = .ERR_CNT + 1; !INCREMENT ERROR COUNT IF ERR_FLG IS SET
                                   end:
                              end:
                         ENDSUB;
                         if .ERR_CNT gtr 20
                                                                                 !ALLOW 20 NIBBLES TO FAIL
                         then
                                                                                   !ERROR IF GTR 20
                              ERROF (80, ASYNC, DUMPER);
PRINTB (FIV FMT, WRD 22, PHR 4, WRD 12, FNC 16, WRD 48);
                              DODU (.ML_LUN);
                              DOCLN;
                              end;
                         ENDTST:
```

CZMLABO ML-11 LOGIC MISCELLANEOUS CODING	TEST SECTION	MACRO P	11113	22-DEC-80	12:16 PAGE 346		SEQ 0333
15308 053566 0041 15309 053572 0246			\$130:	JSR CMP	R1,\$SAVE5 -(SP),-(SP)		7198
15309 053572 0246	46		15:	CMP	2		
15311 053576 1527	77 000040	137410		BISB	#40.aML.REG+40 ML.DUT.R3 #177770.R3 #7.aML.REG+40 R3.aML.REG+40 R5	:	7243 7245
15312 053604 0167 15313 053610 0427 15314 053614 1427 15315 053622 1503 15316 053626 0050 15317 053630 0127 15318 053634 0104 15320 053644 0104 15321 053654 0107 15322 053654 0127 15323 053662 0050 15324 053664 0127 15325 053670 0014 15326 053670 0014 15326 053670 0014 15327 053676 0014 15328 053704 0053 15330 053704 0053 15331 053710 0053 15332 053714 1527 15333 053714 1527 15333 053714 1527 15333 053714 1527 15333 053730 0037 15333 053744 0427 15338 053730 0037	77 000040 03 137772 03 177770 77 000007 77 137366			BIC	#177770,R3		
15314 053614 1427 15315 053622 1503	77 000007 77 137366	137372		BICB BISB CLR MOV MOV MOV	#7,aML.REG+40 R3.aML.REG+40		
15316 053626 0050	05			CLR	R5	; ERR.CNT ; *,TST.PAT ; TST.PAT,* ; TST.PAT,*	7247
15318 053634 0104	04 177777 777 137504 777 137510 777 137464 67 140306 777 000061			MOV	#-1.R4 R4, aML.REG+170 R4.aML.REG+200 R4.aML.REG+160 PC.DAT.DM.XFER #61, aML.REG	: TST.PAT.*	7247 7248 7249 7250 7251 7252 7253 7255
15319 053640 0104	77 137510			MOV	R4, and . REG+200	: TST.PAT.*	7250
15321 053650 0047	67 140306			JOK	PC DAT DM XFER	; 131.741,-	7252
15322 053654 0127	77 000061	137272		MOV	#61,aML.REG	WRD.CNT	7253 7255
15324 053664 0127	02 000001		2\$: 3\$:	MOV	#1.R2	; *,\$\$TMP2	7257
15324 053664 0127 15325 053670 0014 15326 053672 0167 15327 053676 0014 15328 053700 0050 15329 053704 0053 15330 053706 0013 15331 053710 0053 15332 053712 0007 15333 053714 1527 15334 053722 0052 15336 053730 0037 15338 053740 0167 15339 053744 0427 15339 053750 1427	03 126220		39:	BEQ MOV	L\$DLY,R3	; *,\$\$TMP1	
15327 053676 0014	04 66 000002		48:	BEQ	5\$ 2(SP)		
15329 053704 0053	03		45.	CLR DEC BNE DEC	R3	; \$\$TMP ; \$\$TMP1	
15330 053706 0013 15331 053710 0053	74		5\$:	BNE	4\$ 92	; SSTMP2	
15332 053712 0007	66	477750		BR	R3 4\$ R2 3\$ #20,aML.REG+120	, 651172	
15333 053714 1527 15334 053722 0052	77 000020 01	137352	6\$:	BISB	#20,29L.REG+120 R1	: WRD.CNT	7258 7255
15335 053724 0201	27 000177			CMP	R1_#177	; WRD.CNT,*	
15334 053722 0052 15335 053724 0201 15336 053730 0037 15337 053732 1527	77 000040 03 137636	137254		BISB	2\$ #40,aML.REG+40	:	7259
15338 053740 0167	05 15/656			MOV	ML.DUT_R5		
15340 053750 1427	77 000007	137236		BICB	#177770,R3 #7,aML.REG+40		
15341 053756 1503	67 140174			BISB	R3.aML.REG+40 PC.DAT.DM.XFFR		7262
15343 053766 0127	03 177770 77 000007 77 137232 67 140174 77 000071 02 003720	137160		MOV	W71 aML REG	;	7262 7263 7264
15345 054000 0014	11		75:	BEQ	R3.aML.REG+40 PC,DAT.DM.XFER W71.aML.REG W3720,R2 10\$ L\$DLY,R3	; *,\$\$TMP2	7264
15341 053756 1503 15342 053762 0047 15343 053766 0127 15344 053774 0127 15345 054000 0014 15346 054002 0167 15347 054006 0014 15348 054010 0050	03 126110			BISB JSR MOV MOV BEQ MOV BEQ CLR DEC	L\$DLY,R3	; *,\$\$TMP1	
15348 054010 0050	66 000002		8\$:	CLR		: SSTMP : SSTMP1	
15349 054014 0053	03 74			DEC	2(SP) R3 8\$ R2 7\$ R1	; \$\$TMP1	
15351 054020 0053	02		9\$:	BNE DEC BR CLR MOV	R2	; \$\$TMP2	
15353 054024 0050	01		10\$:	CLR	R1	; WRD.CNT	7266
15354 054026 0177	67 137352 77 000020 02 000001	137064 137232	10\$: 11\$:	MOV	aML.REG+230.PD.TEMP		7268
15341 053756 1503 15342 053762 0047 15343 053766 0127 15344 053774 0127 15345 054000 0014 15346 054002 0167 15347 054006 0014 15348 054010 0050 15349 054014 0053 15350 054016 0013 15351 054020 0053 15352 054022 0007 15353 054024 0050 15355 054034 1527 15356 054042 0127 15357 054046 0014	02 000001	IJIEJE		BISB MOV BEQ	aml.REG+230,PD.TEMP #20,aml.REG+120 #1,R2 15\$	* .SSTMP2	7268 7268 7269 7270
15557 054046 0014	11		12\$:	BEQ	155		

EQ 0334			12:16 PAGE 347			CTION	ODING SE	CELLANEOUS CO
TOPS PA:	22-Dec-1980 09:24:31 22-Dec-1980 09:21:22				:ML4			5359 5360 5361
		; *,\$\$TMP1	LSDLY,R3	MOV		126042	016703	5362 054050
		: SSTMP : SSTMP1	2(SP)	CLR	13\$:	000002	005066	5364 054056
		; SSTMP2	138	BNE	148:		001374	5366 054064 5367 054064
			L\$DLY,R3 14\$ 2(SP) R3 13\$ R2 12\$ aML.REG+170,D1.TEMP aML.REG+200,D2.TEMP aMREG+160,E2.TEMP	MOV BEQ CLR DEC BNE DEC BR MOV CLR MOV ASR ASR ASR ADD MOV BIC MOV CLR JSR ADD TST BNE MOV MOV ADD JSR		137246	000766 017767	5368 054070 5369 054072
			aML.REG+200,DZ.TEMP	MOV	135052 15 \$: 135046 135042	137246 137250 137222	017767	5370 054100 5371 054106
7273 7276		: NIB.PTR : NIB.PTR,*	amREG+160,E2.TEMP R2 R2,R3 R3 R3 R3 MPD.TEMP,R3 R3,-(SP) R2,-(SP) M177770,(SP) M1,-(SP) -(SP) PC,BL\$GT2 M10,SP R0 17\$ R2,-(SP)	MOV	16\$:		005002 010203	5372 054114 5373 054116
			RS RS	ASR			006203	5375 054122
			MPD.TEMP,R3	ASR		013120	062703	5377 054128
		; NIB.PTR,*	R3,-(SP) R2,-(SP)	MOV		4 22220	010346	376 034134
			#1,-(SP)	WOA		177770 000001	012746	5381 054142
			PC_BL\$GT2	JSR		126764 000010	004767	5374 054120 5375 054124 5376 054124 5377 054126 5378 054132 5379 054134 5381 054142 5382 054146 5383 054150 5384 054154 5385 054164 5386 054164 5388 054166
			RO 178	TST		000010	005700	5385 054160
		: NIB.PTR.*	R2,-(SP) R4,-(SP) R4,-(SP) M6,-(SP) SP,(SP) PC,TST.LNG.WRD M6,SP (SP),M1 18\$ R5 R2 R2,M11 16\$	MOV			010246	5387 054164
		; ERR.FLG.*	#6,-(SP)	MOV		000006	012746	5389 054170
		, EM., E.	PC.TST.LNG.WRD			141744	004767	5390 054176 5391 054176 5392 054202
7280		; ERR.FLG.*	(SP) #1	CMP	17\$:	000006	021627	5393 054206
7273		: ERR.CNT : NIB.PTR : NIB.PTR,*	R5 R2	INC	18\$:		005205	5395 054214 5396 054216
1213		: NIB.PTR,*	RŽ,#11 16\$	CMP		000011	020227	5397 054220
7266		; WRD.CNT ; WRD.CNT,*		ADD CMP BNE INC INC CMP BLE INC CMP		000177	005201	5399 054226 5400 054230
7284			11\$ 67	BLE TRAP ROR BHIS			003674 104467	5401 054234 5402 054236
			R0 19\$	ROR			103002	5403 054240 5404 054242
7288		; ERR.CNT,*	1\$ R5_#24 20\$	CMD	19\$:	177324 000024	000167 020527	5405 054244 5406 054250
7291		:	33	BLE			104455	5407 054254 5408 054256
			120 ASYNC DUMPER WWRD.48,-(SP) WFNC.16,-(SP)	BLE TRAP . WORD . WORD . WORD MOV MOV			010464	5410 054262
7292		:	MURD.48,-(SP)	MOV		007026 010150	012746	5412 054266

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 348	SEQ 0335
15415 15416	ML4	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
15418 054276. 012746 006426 15419 054302 012746 007454 15420 054306 012746 006530 15421 054312 012746 006062 15422 054316 012746 000006 15423 054322 010600	MOV #WRD.12,-(SP) MOV #PHR.4,-(SP) MOV #WRD.22,-(SP) MOV #FIV.FMT,-(SP) MOV #6,-(SP) MOV SP,R0 TRAP 14	; SP,*
15424 054324 104414 15425 054326 016700 137246 15426 054332 104451	MOV SP,RO TRAP 14 MOV ML.LUN,RO TRAP 51	; 7293
15427 054334 104444 15428 054336 062706 000016 15429 054342 022626 15430 054344 000207	MOV ML.LUN, RO TRAP 51 TRAP 44 ADD #16, SP 20\$: CMP (SP)+, (SP)+ RTS PC	; 7290 7198
15432 15433 15438 15439	; Routine Size: 184 words ; Maximum stack depth per invocation:	15 words
15447 054346 15448 054346 004767 177214 15449 054352 104466 15450 054354 006000 15451 054356 103773 15452 054360 000207	T30:: 1\$: JSR PC,\$T30 TRAP 66 ROR RO BLO 1\$ RTS PC	; 7295
15454 15455 15460 15461 15462; 7298 ! <blf pa<="" td=""><td>; Routine Size: 6 words ; Maximum stack depth per invocation:</td><td>0 words</td></blf>	; Routine Size: 6 words ; Maximum stack depth per invocation:	0 words

15464 :ML4 15465 : 15466 15467 : 15468 : 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BLI.2 (81) BGNTST: 5469 5470 5471 5473 5475 5476 5479 5480 TEST NUMBER: TST 31 TEST NAME: ADDRESS COUNTER TEST TEST DESCRIPTION:
TEST THE ABILITY OF THE ADDRESS
COUNTER TO SUCCESSFULLY COUNT
FROM BLOCK ZERO THROUGH THE
DEVICES LAST BLOCK BY: 1. WRITING THE LAST BLOCK WITH ONES PATTERN. 2. STARTING AT BLOCK ZERO WRITE ZEROES INTO ALL BLOCK UP TO THE 15484 15485 15486 15487 15488 15489 15490 15491 15492 15493 15494 15495 15496 15496 15501 15501 15501 15502 15503 15504 15505 15506 15507 15508 15508 15511 15512 LAST BLOCK ADRS MINUS ONE. READ THE LAST BLOCK FOR ONES AND ERROR IF ZEROES. 7320 7321 7322 7323 7324 7325 7326 7327 7328 7329 7330 3. STARTING AT BLOCK ZERO WRITE ZEROES INTO ALL BLOCK THROUGH THE LAST BLOCK. READ THE LAST BLOCK FOR ZEROES AND ERROR IF STILL ONES. IMPLICIT INPUTS: PD_TEMP: A BITVECTOR OF 16 BITS WHERE THE READ PROM DATA IS STORED AND ACCESSED FROM. local DODU_FLG, !DROP UNIT FLAG ERR_FLG. !ERROR FLG END_CNT, ENDING SECTOR NUMBER BG_PAT; BACKGROUND PATTERN BGNSUB; CLR MBUS; DODO FLG = ZERO; 7337 7338 7339 7340 7341 7342 7343 7346 7347 7348 7349 7350 BG_PAT = ONES; !BACKGROUND PAT OF ONES MLD1 = .BG PAT; MLD2 = .BG PAT; MLE2 = .BG PAT; !LOAD DATA DIAGS WITH BG PAT DAT DM = ONE: SET DATA DIAG MODE LAST_BLK_XFER (); MLCST = write; !DO A WRITE FUNCTION 15514 ; 15515 15516 15517 15518 incr WRD_CNT from 0 to 127 do !WRITE THE LAST BLOCK WITH THE BG PATTERN begin DELAY (ONE_US); DAT_CLK = ONE;

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CZMLABO ML-11 LOGIC TEST
MISCELLANEOUS CODING SECTION
                                                                                              22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
          :ML4
                                                                                                                             TOPS-20 Bliss-16 V2(212)
                                                                                                                             PA: <NEALE>BL3ML4.BL1.2 (81)
                                    end:
                              CLR_MBUS;
DAT_DM = ONE;
LAST_BLK_XFER ();
MLCST = read;
DELAY (ONE_US);
                                                                                              SET DATA DIAG MODE
                                                                                              !DO A READ FUNCTION
                               incr WD_CNT from 0 to 127 do
                                                                                              !READ THE LAST BLOCK FOR BG PATTERN
                                    begin
                                    PD TEMP = .MLPD;
DAT_CLK = ONE;
                                                                                              !GET PROM DATA
                                                                                              !CLOCK OUT THE DATA WORD
                                    DELAY (ONE US):
                                    RD_LNG_WRD;
                                                                                              !READ THE DATA WORD
                                    incr NIB_PTR from 0 to 9 do
                                                                                              !LOOK AT 10 NIBBLES
                                         begin
                                         if .PD_TEMP [.NIB_PTR] IS_NOT_SET then TST_LNG_WRD (.NIB_PTR, .BG_PAT, ERR_FLG);
                                                                                              !FIND GOOD NIBBLES AND COMPARE THEM
                                         if .ERR_FLG IS_SET
                                                                                              !SEE IF ERROR WAS FOUND
                                         then
                                              begin !ERRO
ERROF (81, INTER, DUMPER);
PRINTB (THR_FMT, PHR_4, WRD_13, FNC_22);
PRINTB (TWO_FMT, FNC_13, WRD_56);
                                                                                              !ERROR IF FLG IS SET
                                               EXIT_TST;
                                               end:
                                         end:
                                    end:
                              END_CNT = .LST_BLK - 1;
MLDT = ZEROES;
MLD2 = ZEROES;
MLE2 = ZEROES;
                                                                                              !END AT LAST BLOCK -1
                                                                                              LOAD DATA DIAG REG WITH COMP BG PAT
                               incr TWICE from 0 to 1 do
                                                                                              !REPEAT LOOP TWICE
                                    begin
CLR_MBUS;
DAT_DM = ONE;
FIRST_BLK_XFER ();
MLCS1 = write;
                                                                                              SET DATA DIAG MODE
SET UP A FIRST BLOCK XFERR
                                                                                              DO A WRITE FUNCTION
                                    incr BLK_CNT from 0 to .END_CNT do
                                                                                          !CLOCK THE ADDRESS COUNTER UP TO END_CNT
                                         incr ADRS_CNT from 0 to 127 do
   15573
                                              begin
DAT_CLK = ONE;
```

begin DODU (.ML_LUN);

DOCLN; end;

ENDTST;

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 352		SEQ 0339
15632 15633 15637	:ML4		2	2-Dec-1980 09:24:31 TOPS 2-Dec-1980 09:21:22 PA:<
15638 054362 004167 127530 15639 054366 162706 000010	\$T31: JSR SUB	R1 SSAVES	7	7297
15640 054372 104402 15641 054374 152777 000040 15642 054402 016705 137174	1\$: TRAP BISB MOV	,	;	7334 7336
15643 054406 042705 177770 15644 054412 142777 000007 15645 054420 150577 136570	136574 BICB BISB	#40.aml.REG+40 ML.DUT.R5 #177770.R5 #7.aml.REG+40 R5.aml.REG+40 2(\$P)		
15647 054430 012701 177777 15648 054434 010177 136704	MOV	#-1.R1 R1.aML.REG+170 R1.aML.REG+200	; DODU.FLG ; *,BG.PAT ; BG.PAT,* ; BG.PAT,*	7338 7339 7340
15649 054440 010177 136710 15650 054444 010177 136664 15651 054450 152777 000010 15652 054456 004767 141432 15653 054462 012777 000061	136616 MOV BISB JSR	R1.aML.REG+160 #10.aML.REG+120 PC.LAST.BLK.XFER	BG.PAT.*	734.2 734.3 734.3
15655 054470 005002	CLR MOV MOV MOV MOV MOV MOV MOV SISB JSR MOV CLR 2\$: MOV 3\$: BEQ MOV BEQ CLR DEC BNE DEC BR	M-1,R1 R1.amL.REG+170 R1.amL.REG+200 R1.amL.REG+160 M10.amL.REG+120 PC.LAST.BLK.XFER M61.amL.REG R2 M1,R3	WRD.CNT *,\$\$TMP2	7345 7347 7349
15656 054476 001411 15657 054500 016704 125412 15658 054504 001404	3\$: BEQ MOV BEQ	L\$DLY,R4	; *,\$\$TMP1	
15659 054506 005066 000006 15660 054512 005304 15661 054514 001374	4\$: CLR DEC BNE	6(SP) R4 4\$; \$\$TMP1	
15663 054520 000766 15664 054522 152777 000020	5\$: DEC BR 136544 6\$: BISB INC	R4 4\$: R3 3\$ #20,aML.REG+120	; \$\$TMP2	7350 7347
15666 054532 020227 000177 15667 054536 003755	CMP	R2 R2,#177 2\$; WRD.CNT;	7347
15668 054540 152777 000040 15669 054546 016705 137030 15670 054552 042705 177770	136446 BISB MOV BIC	#40,aML.REG+40 ML.DUT.R5 #177770,R5	:	7351
15667 054536 003755 15668 054540 152777 000040 15669 054546 016705 137030 15670 054552 042705 177770 15671 054556 142777 000007 15672 054564 150577 136424 15673 054570 152777 000010 15674 054576 004767 141312 15675 054602 012777 000071 15676 054610 012703 000001 15677 054614 001411 15678 054616 016704 125274 15680 054622 001404 15681 054630 005304 15682 054632 001374 15683 054634 005303 15684 054636 000766 15685 054640 005002	136446 BLE BISB MOV BIC BICB BISB BISB BISB BISB JSR MOV MOV 7\$: BEQ MOV BEQ S\$: CLR DEC BNE 9\$: DEC BR 10\$: CLR	2\$ #40.aML.REG+40 ML.DUT.R5 #177770.R5 #7.aML.REG+40 R5.aML.REG+40 #10.aML.REG+120 PC.LAST.BLK.XFER #71.aML.REG #1.R3 10\$ L\$DLY.R4 9\$ 6(SP) R4 8\$ R3 7\$ R2		7354
15675 054602 012777 000071 15676 054610 012703 000001	136344 MOV	#71 aML REG	*,SSTMP2	7354 7355 7356 7357
15677 054614 001411 15678 054616 016704 125274 15679 054622 001404	7\$: BEQ MOV BEQ	105 L\$DLY,R4 9\$; *,\$\$TMP1	
15680 054624 005066 000006 15681 054630 005304 15682 054632 001374	8\$: CLR DEC BNE	6(SP) R4 8\$	SSTMP1	
15683 054634 005303 15684 054636 000766	9\$: DEC BR	R3 7\$; SSTMP2	
15685 054640 005002	10\$: CLR	R2	; WD.CNT	7359

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0340	SEQ		12:16 PAGE 353	22-DEC-80			CTION	ODING SE	ABO ML-11 L
TOP PA:	22-Dec-1980 09:24:31 22-Dec-1980 09:21:22				:ML4				687 688
736 736 736		1	aML.REG+230,PD.TEMP #20,aML.REG+120	MOV	115:	136250 136416	136536 000020 000001	017767	590 054642 691 054650
736		; *,\$\$TMP2	15\$	MOV BISB MOV BEQ CLR DEC BNE DEC BR MOV	12\$:			012703 001411	692 054656 693 054662
		; *,\$\$TMP1	L\$DLY,R4 14\$	BEQ			125226	001404	95 054670
		; \$\$TMP1	6(SP) R4	DEC	13\$:		000006	005304	597 054676 697 054676
		; \$\$TMP2	R3	DEC	145:			005303	399 052702
			R4 13\$ R3 12\$ aML.REG+170,D1.TEMP aML.REG+200,D2.TEMP aML.REG+160,E2.TEMP	MOV	15\$:	134236 134232 134226	136432 136434 136406	017767 017767 017767	701 054706 702 054714 703 054722
736 736		; NIB.PTR ; NIB.PTR,*	R3 R3,R4 R4 R4	CLR MOV ASR ASR ASR ADD	16\$:			005003 010304 006204	704 054730 705 054732 706 054734
			D/.	ASR ASR ADD			013120	006204 062704	707 U54736 708 054740 709 054742
		; NIB.PTR,*	MPD.TEMP,R4 R4,-(SP) R3,-(SP) W177770,(SP) W1,-(SP) -(SP)	MOV MOV BIC MOV CLR			177770 000001	010346 042716 012746	711 054750 712 054752 713 054756
			PC.BL3G12	JSR			126150 000010	005046 004767 062706	714 054762 715 054764 716 054770
		; NIB.PTR,* ; BG.PAT,*	17\$ R3,-(SP) R1,-(SP)	ADD TST BNE MOV MOV				001011 010346 010146	18 054776 19 055000 20 055002
		; ERR.FLG,*	#12,-(SP) SP,(SP)	ADD			000012	012746 060616	721 055004 722 055010
			PC.TST.LNG.WRD	JSR ADD	470	000004	141130 000006 000004	060616 004767 062706	723 055012 724 055016
737		; ERR.FLG,*	18\$	BNE	17\$:	000001	000004	001037	
737			121 INTER	MOV MOV ADD JSR ADD CMP BNE TRAP . WORD . WORD . WORD MOV MOV				000121 010672	728 055034 729 055036
737		•	#10,SP R0 17\$ R3,-(SP) R1,-(SP) #12,-(SP) SP,(SP) PC,TST.LNG.WRD #6,SP 4(SP),#1 18\$ 55 121 INTER DUMPER #FNC.22,-(SP) #WRD.13,-(SP) #THR.FMT,-(SP) #4,-(SP) SP,R0 14 #WRD.56,(SP)	MOV MOV MOV			010230 006436 007454 006034 000004	012746 012746 012746 012746 012746 010600	722 055010 723 055012 724 055016 725 055022 726 055030 727 055032 728 055034 730 055040 731 055046 732 055046 733 055052 734 055056 735 055062 736 055072 739 055076 740 055102
		; SP,*	#4,-(SP) SP,R0	MOV			000004	012746 010600	735 055062 736 055066
737			14 MURD.56,(SP) MFNC.13,-(SP) MTWO.FMT,-(SP) M3,-(SP)	MOV TRAP MOV MOV MOV MOV			007122 010112 006024 000003	104414 012716 012746 012746 012746	720 055002 721 055004 722 055010 723 055012 724 055016 725 055032 726 055030 727 055032 728 055034 730 055040 731 055046 732 055066 733 055062 734 055066 737 055070 738 055072 739 055102 740 055106

SCELLANEOUS C	ODING SE	CIION						SEQ 0341
15744 15744 15745				:ML4			22-Dec-1980 09:24 22-Dec-1980 09:21	:31 TOP
15746 055112 15747 055114	010600 104414				MOV TRAP	SP.R0	; SP.*	
15749 055120 15750 055124	062706 000167	000020 000604			TRAP ADD JMP	63 #20.SP 37\$		737 737 736
	005203 020327 003675	000011		18\$:	INC	R3 R3,#11	: NIB.PTR : NIB.PTR,*	736
15754 055140 15755 055142	005202	000177			BLE INC CMP BLE MOV DEC	R3 R3,#11 16\$ R2 R2,#177 11\$; WD.CNT,*	735
15758 055150 15758 055154	003633 016716 005316	134416			MOV	(SP)	: *,END.CNT ; END.CNT	738
15759 055156 15760 055162 15761 055166	005077 005077 005077	136162 136166 136142			CLR CLR CLR CLR BISB	aml.REG+170 aml.REG+200 aml.REG+160		738 738 738 739 739
15762 055172	005005	000040 136374	136012	19\$:	CLR BISB	R5 #40.aML.REG+40	TWICE	739 739
15765 055206 15766 055212	042704	177770	135774		BIC BICB	ML.DUT.R4 #177770.R4 #7,aML.REG+40		
5767 055220 15768 055224 15769 055232	150477 152777 004767 012777	177770 000007 135770 000010 140602 000061	136042		BICB BISB BISB JSR	R4.aML.REG+40 #10.aML.REG+120 PC.FIRST.BLK.XFER	:	739
5770 055236 15771 055244	012777	000061	135710		MOV CLR BR	#61, aML.REG R3 22\$ R4 #20, aML.REG+120	BLK.CNT	739 739 739 739
5773 055250 15774 055252	005004	000020	136014	20\$:	BISB	R4 #20,aml.REG+120	: ADRS.CNT	740 740 740
5775 055260 5776 055262 5777 355266	005204 020427 003771 005203 020316	000177			INC	R4 R4 #177 21\$: ADRS.CNT,*	740
5778 ()55270 15779 ()55277	005203			22\$:	CMP BLE INC CMP BLE BISB	R3 R3, (SP)	; BLK.CNT ; BLK.CNT,END.CNT	739
15775 055260 15776 055262 15776 055266 15777 055266 15779 055272 15780 055274 15782 055304 15782 055304 15783 055310 15784 055314 15785 055326 15786 055326 15789 055334 15788 055334 15789 055334 15790 055352 15791 055354 15792 055360 15793 055360 15794 055372 15795 055370	003765 152777 016704 042704 142777 150477 152777 004767 012777 012703 001411 016704 005066 005304 001374 005303 000766	000040 136272 177770 000007 135666 000010 140554 000071 000001	135710		MUY	#20, aml.REG+120 R4 R4,#177 21\$ R3 R3,(SP) 20\$ #40, aml.REG+40 ML.DUT.R4 #177770,R4 #7, aml.REG+40 R4, aml.REG+40 #10, aml.REG+120 PC, LAST.BLK.XFER #71, aml.REG #1, R3 26\$ L\$DLY,R4 25\$ 6(SP) R4	•	740
5784 055314 5785 055322	142777	000007 135666	135672		BIC BICB BISB BISB JSR MOV MOV BEQ CLR DEC BNE DEC BR	#177770,R4 #7,aML.REG+40 R4.aML.REG+40		
5786 055326 15787 055334	152777	000010 140554	135740		BISB	#10.aML.REG+120 PC.LAST.BLK.XFER	:	740 740 740 740
15788 055340 15789 055346 15790 055352	012703 001411		135606	23\$:	MOV BEQ	#1.R3 26\$	* .SSTMP2	740
15790 055352 15791 055354 15792 055360 15793 055362	016704	124536			MOV	L\$DLY,R4 25\$; *,\$\$TMP1	
15794 055366 15795 055370 15796 055372	005304 001374	000006		24\$:	DEC	R4 24\$	SSTMP1	
15796 055372 15797 055374	005303			25\$:	DEC	R4 24\$ R3 23\$; \$\$TMP2	

5799				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
5801 5802 055376	005002			268.	CLD	92	. UD CHT	22-Dec-1980 09:21:22	
5803 055400 5804 055406	017767	136000 000020 000001	135512 135660	26\$: 27\$:	CLR MOV BISB	R2 aML.REG+230,PD.TEMP #20,aML.REG+120	: WD.CNT		7411 7413 7414 7415
5805 055414 5806 055420	012703	000001	10000	28\$:	MOV	#1 R3	*,\$\$TMP2		7415
5807 055422 5808 055426	016704	124470			MOV	L\$DLY,R4 30\$ 6(SP)	; *,\$\$TMP1		
5809 055430 5810 055434	005066	000006		29\$:	CLR	6(SP) R4	: SSTMP : SSTMP1		
5811 055436 5812 055440	001374 005303			30\$:	MOV BEQ MOV BEQ CLR DEC BNE DEC BR	R4 298 R3 288 aML.REG+170,D1.TEMP aML.REG+200,D2.TEMP aML.REG+160,E2.TEMP R3 R3,R4	; SSTMP2		
813 055442 814 055444	000766 017767	135674	133500	31\$:	MOV	28\$ aML.REG+170,D1.TEMP			
815 055452 816 055460	017767 017767	135674 135676 135650	133500 133474 133470		MOV	aml.REG+200,D2.TEMP aml.REG+160,E2.TEMP			
817 055466 818 055470	010304			32\$:	MOV	R3, R4	: NIB.PTR : NIB.PTR,*		7418 7421
820 055474	006204				ASR ASR	R4 R4 R4 MPD.TEMP.R4			
52 055500	062704	013120			ASR ADD MOV	APD. TEMP,R4			
824 055506 825 055510	010346	177770			MOV	R4,-(SP) R3,-(SP) #177770,(SP) #1,-(SP) -(SP)	; NIB.PTR,*		
26 055514 27 055520	012746	177770 000001			MOV	#1,-(SP) -(SP)			
28 055522 29 055526	004767 062706	125412 000010			CLR JSR ADD	PC_BL\$GT2 #10,SP			
830 055532 831 055534	005700 001011				BNE	RO			
832 055536 833 055540	010346 010146				MOV	R3,-(SP) R1,-(SP) #12,-(SP)	: NIB.PTR,* : BG.PAT,*		
835 055546	060616	000012			ADD	#12,-(SP) SP,(SP)	; ERR.FLG,*		
837 055554	062706	140372 000006 000004	000001	770.	ADD	#6.SP	500 St. 6		
839 055566	001025	000004	000001	33\$:	BNE	34 \$; ERR.FLG,*		7425
841 055572	000122				. WORD	122 ASVMC	•		7428
5843 055576 5844 055600	024052	006406			MOV ADD JSR ADD CMP BNE TRAP . WORD . WORD . WORD MOV MOV	#12,-(SP) SP,(SP) PC,TST.LNG.WRD #6,SP 4(SP),#1 34\$ 55 122 ASYNC DUMPER #WRD.10,-(SP) #WRD.51,-(SP) #WRD.51,-(SP) #THR.FMT,-(SP) 89,R0 14			7429
5845 055604 5846 055610	012746	006406 007054 007046 006034 000004			MOV	MRD.51,-(SP)			1467
5847 055614 5848 055620	012746 012746	006034 000004			MOV	#THR.FMT,-(SP) #4,-(SP)			
5849 055624 5850 055626	010600 104414				MOV	SP.R0	; SP.*		
5851 0556 3 0 5852 0556 36	012766 062706	000001 000012	000014		ADD	#1,14(SP) #12,SP	. *,DODU.FLG		7430 7427 7418
5834 055542 5835 055546 5836 055550 5837 055554 5838 055560 5839 055566 5840 055570 5841 055572 5842 055574 5843 055576 5844 055600 5845 055604 5846 055610 5847 055614 5848 055620 5849 055624 5850 055626 5851 055630 5852 055636 5853 055642	010600 104414 012766 062706 005203	000001 000012	000014	34\$:	MOV MOV TRAP MOV ADD INC	SP.R0 14 #1.14(SP) #12.SP R3			74

*

ILABO ML-11 L SCELLANEOUS C				-00 /							0343
5856 5857				:ML4						22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
5858 055644	020327	000011			CMP BL F	R3,#11			; NIB.PTR,*		
5860 055652 5861 055654	005202 020227	000177			BLE INC CMP BLE INC	R3,#11 32\$ R2 R2,#177 27\$ (SP)			; WD.CNT;		7411
5863 055662 15864 055664	005216 005101				LUM	(SP) R1			: END.CNT		7437 7438 7391
15865 055666 15866 055670	005205	000001			INC CMP BGT	R5 R5 #1			: END.CNT : BG.PAT : TWICE : TWICE,*		7391
5868 055676 15869 055702	000167	177272		35\$:	JMP TRAP	R1 R5 R5,#1 35\$ 19\$ 67 R0 36\$ 1\$					7439
5871 055706 5872 055710	103002	176456 000002	000001	740.	ROR BHIS JMP	36\$ 1\$					
15874 055722	001004		000001	36\$:	BNE	2(SP),#1 37\$; DODU.FLG,*		7443
15876 055730	104451	135650			MOV TRAP	ML.LUN,RO			•		7446
5878 055734 15879 055740	062706 000207	000010		37\$:	TRAP ADD RTS	#10.SP PC			:		7297
5880 5881 5882 5887 5888				; Rou ; Max	tine Size: imum stack	376 words depth per	invocation:	18 words			
5892 5896 055742 5897 055742	004767	176414		T31::	JSR	PC.\$T31					7448
5898 055746 5899 055750 5900 055752 5901 055754	104466 006000 103773 000207				TRAP ROR BLO RTS	66 R0 1\$ PC					
5900 055752 5901 055754 5902 5903 5904				Rou			invocation:	0 words			

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION

MACRO M1113 22-DEC-80 12:16 PAGE 357

SEQ 0344

15910 :ML4 15911 : 15912 15913 ;

7451 ! (BLF/PAGE>

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TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (81)

```
15915 :ML4
15916 :
15917 :
15918 :
15920 :
15921 :
15923 :
15924 :
15925 :
15926 :
15927 :
15928 :
15930 :
15931 :
15931 :
15931 :
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15959 :
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15959 :
15960 :
15961 :
15961 :
15962 :
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15963 :
15964 :
15965 :
15968 :
15969 :
                                                                                                                                                    22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 
22-Dec-1980 09:21:22 PA:<NEALE>BL3ML4.BLI.2 (8)
                                                                                                                                                                                                    PA: <NEALE>BL3ML4.BL1.2 (82)
                                                   BGNTST;
                                                     TEST NUMBER: TST 32
                                                       TEST NAME:
                                                                                   ARRAY MODULE SELECTION TEST
                                                       TEST DESCRIPTION:
                                                                   TEST FOR UNIQUE ARRAY MODULE
                                                                   SELECTION BY:
                                                                  1. WRITING THE RESPECTIVE ARRAY MODULE POSITION NUMBER INTO
                                                                          THE FIRST GOOD NIBBLE FOUND IN THE ARRAY. DO FOR ALL PRESENT ARRAYS.
                                                                  2. READ THE ARRAYS FOR THEIR
                                                                           RESPECTIVE POSTION NUMBERS.
                                                      IMPLICIT INPUTS:
                                                                  PD TEMP:
A BIT VECTOR OF 16 BITS WHERE
                                                                   THE READ PROM DATA IS STORED
                                       7479
7480
7481
7482
7483
7484
7485
7486
7487
7488
7489
7490
7491
                                                                   AND ACCESSED FROM.
                                                                  IO BUF :
A VECTOR OF 256 WORDS WHERE
DATA FOR MBUS READS AND WRITE
FUNCTION ARE FOUND.
                                                   local
                                                          WRDS_TSTED,
ARR_SEL,
FND_GD_NIB,
                                                                                                                                                    !WORDS TESTED
                                                                                                                                                    ARRAY SELECT
                                                           ARR_NUM;
                                                                                                                                                    !ARRAY NUMBER
                                       7494
7495
7496
7497
7498
7499
7500
7501
7502
7503
                                                  ARR_SEL = ZEROES;
ARR_SEL = .ARR_SEL - .ARR_INC;
                                                                                                                                                  !START ARR_SEL BACK ONE ARRAY
                                                 incr ARR_CNT from 0 to .OP_NUM_ARR do
begin
CLR_MBUS;
FND GD NIB = ZERO;
WRDS_TSTED = ZERO;
DAT_DM = ONE;
MLWC = not 255;
                                                                                                                                                 !TEST ALL PRESENT ARRAYS
                                                                                                                                                   SET DATA DIAG MODE
                                                                                                                                                   LOAD WORD COUNT
```

HISCELLAREOUS	CODING SECTI	UN .	3E
15971 :ML4 15972 :			22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (82)</neale>
15974 : 15975 : 15976 : 15977 :	7504 7505 7506 7507	MLBA = IO_BUF; ARR_SEL = .ARR_SEL + .ARR_INC; MLDA = .ARR_SEL; MLCS1 = write;	!LOAD IO_BUF !LOOK AT NEXT ARRAY !LOAD DSA WITH SELECTED ARRAY AT BLOCK ZERO !DO A WRITE FUNCTION
15978 : 15979 :	7508 7509 7510	do begin	!THIS LOOP WRITES ARRAY NUMBERS TO THE ARRAYS
15981 : 15982 : 15983 :	7511 7512 7513	DELAY (ONE_US); PD_TEMP = .MLPD; WRDS_TSTED = .WRDS_TSTED + 1;	GET THE PROM DATA COUNT WORDS TESTED
15985	7515	incr CNT from 0 to 8 do	!LOOK AT 9 NIBBLES
15986 :	7516 7517	if .PD_TEMP [.CNT] IS_NOT_SET	
15988 : 15989 :	7518 7519	then begin	THE A GOOD MIGDLE
15991 : 15992 :	7521 7522	case .CNT from 0 to 8 of set	SELECT AND LOAD GOOD NIBBLE WITH ARRAY CNT
15994 15995	7524 7525	[0]: (MLD1)<0, 4> = .ARR_CNT.	: !NIBBLE 0
15996 : 15997 : 15998 :	7526 7527 7528	[1] : (MLD1)<4, 4> = .ARR_CNT.	
15997 15998 15999 16000 16001 16002 16003	7529 7530 7531	[2] : (MLD1)<8, 4> = .ARR_CNT.	
16002 : 16003 :	7532 7533	[3] :	
10000 :	7535	(MLD1)<12, 4> = .ARR_CN	T; !NIBBLE 3
16006 : 16007 : 16008 :	7536 7537 7538 7539	[4]: (MLD2)<0, 4> = .ARR_CNT	
16009 ;	7539 7540 7541	[5] : (MLD2)<4, 4> = .ARR_CNT;	: !NIBBLE 5
16012 ; 16013 ;	7542 7543	[6] : (MLD2)<8, 4> = .ARR_CNT;	: !NIBBLE 6
16015 16016	7545 7546	[7] : (MLD2)<12, 4> = .ARR_CN	T; !NIBBLE 7
16017 ; 16018 ; 16019 ;	7548 7549	[8]: (MLE2)<8, 4> = .ARR_CNT; tes;	: !NIBBLE 8
16020 :	7550 7551	tes;	
16010 : 16011 : 16012 : 16013 : 16014 : 16015 : 16016 : 16017 : 16018 : 16020 : 16021 : 16023 : 16024 : 16025 :	7552 7553 7554 7555	<pre>FND_GD_NIB = ONE; exitloop; end;</pre>	SET FOUND GOOD NIBBLE FLG
10025 ;	7555		

```
16027 :ML4
16028 :
16029
16030 :
16031 :
16032 :
                                                                                                     22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                       TOPS-20 Bliss-16 V2(212)
                                                                                                                                       PA: <NEALE>BL3ML4.BLI.2 (82)
                                          DAT_CLK = ONE:
                                                                                                     !CLOCK GOOD NIBBLE INTO MEMORY & GET NXT PROM WRD
                                     until (.FND_GD_NIB IS_SET ) or (.WRDS_TSTED eql 14); !DO UNTIL FOUND GOOD NIBBLE OR 14 WRDS TSTED
16034
16035
16036
16037
16039
16040
16042
16043
16044
16045
16046
16049
16050
16051
                                     if .WRDS_TSTED eqt 14
                                                                                                    !IF 14 WORDS TSTED
                                          begin
ERRDF (111, INTER, DUMPER);
PRINTB (SEV_FMT, WRD_14, PHR_10, :NC_15, WRD_12, FNC_17, WRD_37, WRD_56);
                                           end:
                                     end:
                               ARR_SEL = ZEROES;
ARR_SEL = .ARR_SEL - .ARR_INC;
                       7571
7572
7573
                                                                                                  START ARR SEL BACK ONE AGAIN
                               incr ARR_CNT from 0 to .OP_NUM_ARR do !TEST ALL PRESENT ARRAYS
                                     begin
BGNSUB;
                                     CLR_MBUS;
                                     FND GD_NIB = ZERO;
DAT_DM = ONE;
MLWC = not 255;
                       7578
7579
7580
7581
7582
7583
7584
7586
7587
7588
7589
7591
7593
7594
7595
7596
7597
7596
7600
7601
7602
7603
                                                                                                     SET DATA DIAG MODE
                                                                                                     LOAD WORD COUNT
16053
16054
16055
16056
16057
16058
16059
16060
16061
16063
                                     MLBA = IO_BUF;
ARR_SEL = .ARR_SEL + .ARR_INC;
MLDA = .ARR_SEL;
                                                                                                     LOOK AT NEXT ARRAY
LOAD DSA WITH ARRAY SELECT
DO A READ FUNCTION
                                     MLCS1 = read:
                                     do
                                                                                                    !THIS LOOP READS ARRAYS FOR ARRAY NUMBERS
                                          begin
DELAY (ONE_US);
                                          PD TEMP = .MLPD:
                                                                                                   !GET THE PROM DATA
16064
                                          incr CNT from 0 to 8 do
                                                                                                    !LOOK AT 9 NIBBLES
16065
16066
16068
16069
16070
16071
16072
                                                 if .PD_TEMP [.CNT] IS_NOT_SET
                                                                                                    !FIND THE GOOD NIBBLES WHERE ARR NUM IS STORED
                                                then
                                                      begin
DAT_CLK = ONE;
                                                                                                    !CLOCK ARRAY WORD OUT
                                                      case .CNT from 0 to 8 of
                                                                                                 SELECT AND READ GOOD NIBBLE
16074
16075
16076
16077
16078
                                                            [0]:
                                                                 ARR_NUM = .(MLD1)<0, 4>;
                                                                                                                !NIBBLE 0
                                                           [1] :
ARR_NUM = .(MLD1)<4, 4>;
                                                                                                                !NIBBLE 1
16080
16081
                                                           [2] :
ARR_NUM = .(MLD1)<8, 4>;
                                                                                                                !NIBBLE 2
```

: ARR. SEL

: * ARR.SEL

7450

7494 7495

ARR. INC.RS

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION

133566

16138 16139				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOP:
16140 16141 055774	016766	133560	000002	:	MOV	OP.NUM.ARR,2(SP)		22-Dec-1980 09:21:22	749
16142 056002	016766 005003 000167 152777	000702	200002		MOV CLR JMP	R3 27\$: ARR.CNT		147
16144 056010	152777	000702 000040 135560	135176	1\$:	RISR	M40, aML.REG+40 ML.DUT.R2			749
16146 056022 16147 056026	042702	177770 000007 135154	135160		BIC	#177770,R2 #7.amL.REG+40			
16148 056034 16149 056040	150277	135154			MOV BIC BICB BISB CLR CLR BISB	#177770,R2 #7,aML.REG+40 R2,aML.REG+40 (SP)	: FND.GD.NIB		750
16150 056042 16151 056044	005004 152777	000010	135222		CLR BISB		; FND.GD.NIB ; WRDS.TSTED		750 750
16152 056052 16153 056060	012777	000010 177400 011600	135222 135104 135106		MOV	#-400, aml.REG+10 #10.BUF, aml.REG+20			750. 750
16154 056066 16155 056072	066705 010577	133470 135106			MOV	ARR.INC.RS R5.aML.REG+30	: *,ARR.SEL ; ARR.SEL,*		750 750 750 750 750 750 750 750 751
16156 056076 16157 056104	012777	011600 133470 135106 000061 000001	135050	2\$: 3\$:	MOV	#10.aML.REG+120 #-400.aML.REG+10 #10.BUF,aML.REG+20 ARR.INC.R5 R5.aML.REG+30 #61.aML.REG	*,\$\$TMP2		750 751
16158 056110 16159 056112 16160 056116 16161 056120 16162 056124 16163 056126 16164 056130 16165 056132 16166 056134 16167 056142	001411	124000		35:	MOV BEQ MOV BEQ	L\$DLY,R2	; *,\$\$TMP1		
16161 056120 16162 056124	001404	000004		45:	CLR	5\$ 4(SP)	; SSTMP1		
16163 056126	001374			5\$:	CLR DEC BNE DEC BR	R2 4\$ R1 3\$ aML.REG+230,PD.TEMP	; SSTMP2		
16163 056126 16164 056130 16165 056132 16166 056134 16167 056142	000766	135244	134756	6\$:	BR	3\$ aml.REG+230,PD.TEMP			751
16167 056142 16168 056144	005204	133211	134130		INC	R4 R2 R2,R1	: WRDS.TSTED		751 751 751 751
6169 056146 6170 056150	010201			7\$:	CLR MOV ASR	01	; CNT,*		751
6171 056152 6172 056154	006201 006201				ASR ASR ASR	R1 R1			
6173 056156 6174 056162	062701 010146	013120			MOV	MPD.TEMP,R1 R1,-(SP)			
16170 056150 16171 056152 16172 056154 16173 056156 16174 056162 16175 056164 16176 056166 16177 056172	010246	177770 000001			MOV	MPD.TEMP,R1 R1,-(SP) R2,-(SP) M177770,(SP) M1,-(SP) -(SP) PC,BL\$GT2 M10,SP R0 23\$ R2,R1 R1	; CNT,*		
16178 056176	005046				BIC MOV CLR JSR	#1,-(SP) -(SP)			
16178 056176 16179 056200 16180 056204 16181 056210	062706	124734 000010			ADD	#10,SP			
16182 056212	001155				ADD TST BNE	23\$; CNT,*		7521
16184 056216 16185 056220	006301	056224			ASL	R1 R\$(R1) PC	, CN1,-		136
16186 056224 16187 056226	066107 000022 000040 000072	0,000		8\$:	. WORD	8\$(R1),PC 9\$-8\$ 10\$-8\$			
16188 056230 16189 056232	000072				. WORD	10s-8s 12s-8s 13s-8s			
16182 056212 16183 056214 16184 056216 16185 056220 16186 056224 16187 056226 16188 056230 16189 056232 16190 056234 16191 056236 16192 056240	000112 000146 000164 000216				MOV ASL ADD .WORD .WORD .WORD .WORD .WORD	13\$-8\$ 15\$-8\$ 16\$-8\$ 18\$-8\$			

SCELLANEOUS CODING S	ECITON							0350
16194 16195 16196			:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
16197 056242 000236 16198 056244 000276 16199 056246 010301 16200 056250 042701 16201 056254 142777	177760 000017	135062	9\$:	.WORD .WORD MOV BIC BICB	19\$-8\$ 21\$-8\$ R3_R1 #177760_R1 #17_aML.REG+170	: ARR.CNT,*		7525
16202 056262 000412 16203 056264 010301 16204 056266 006301 16205 056270 006301 16206 056272 006301			10\$:	BR MOV ASL ASL ASL ASL	11\$ R3,R1 R1 R1 R1	; ARR.CNT,*		7528
6208 056276 042701 16209 056302 142777 16210 056310 150177	177417 000360 135030	135034	11\$:	BICB	#177417,R1 #360,aML.REG+170 R1,aML.REG+170			7521
6212 056316 010301 6213 056320 000301 6214 056322 042701	170377	175010	12\$:	BR MOV SWAB BIC	22\$ R3,R1 R1 #170377,R1 #7400,@ML.REG+170	: ARR.CNT,*		7521 7531
16216 056334 000413 16217 056336 010301 16218 056340 000301 16219 056342 006301 16220 056344 006301	007400	135010	13\$:	BIC BR MOV SWAB ASL ASL ASL	14\$ R3,R1 R1 R1 R1	; ARR.CNT,*		7534
6222 056350 006301 6223 056352 042701 6224 056356 042777 6225 056364 050177	007777 170000 134754	134760	145:	BIC BIC BIS	R1 #7777,R1 #170000,aML.REG+170 R1,aML.REG+170			
6226 056370 000463 6227 056372 010301 6228 056374 042701 6229 056400 142777 6230 056406 000412 6231 056410 010301 6232 056412 006301 6233 056414 006301	177760 000017	134746	15\$:	BR MOV BIC BICB	22\$ R3,R1 #177760,R1 #17,aML.REG+200	: ARR.CNT,*		7521 7537
231 056410 01030 232 056412 00630 233 056414 00630 234 056416 00630 235 056420 00630			16\$:	BR MOV ASL ASL ASL BIC	R3,R1 #177760,R1 #17,aML.REG+200 17\$ R3,R1 R1 R1 R1	; ARR.CNT,*		7540
16236 056422 042701 16237 056426 142777 16238 056434 150177 16239 056440 000437	177417 000360 134714	134720	17\$:	BICB	#360,aml.REG+200 R1,aml.REG+200			7521
16240 056442 010301 16241 056444 000301 16242 056446 042701 16243 056452 04277	170377 007400	134674	18\$:	BR MOV SWAB BIC BIC	22\$ R3,R1 R1 #170377,R1 #7400,@ML.REG+200	: ARR.CNT,*		7521 7543
16244 056460 000413 16245 056462 010301 16246 056464 000301 16247 056466 006301 16248 056470 006301	3	131014	198:	BIC BR MOV SWAB ASL ASL	20\$ R3,R1 R1 R1	: ARR.CNT,*		7546

CZMLABO ML-11 L MISCELLANEOUS C	ODING SEC	1104				12:16 PAGE 364			22-01000	SEQ 0351
16251				:ML4					22-Dec-1980 (22-Dec-1980 (09:24:31 TOPS 09:21:22 PA:<
16253 056472 16254 056474 16255 056476 16256 056502 16257 056510	006301 006301 042701 042777 050177	007777 170000 134640	134644	20\$:	ASL ASL BIC BIC BIS	R1 R1 #7777,R1 #170000,aML.REG+200 R1,aML.REG+200				
16238 036516 16259 056516 16260 056520 16261 056522 16262 056526	000411 010301 000301 042701	170377 007400	134600	21\$:	BR MOV SWAB BIC BIC	22\$ R3,R1 R1 #170377,R1 #7400,aml.REG+160	;	ARR.CNT,*		7521 7549
16263 056534 16264 056540	050177	134574 000001		22\$:	MOV	#1.(SP)		*,FND.GD.NIB		7552
16266 056546 16267 056550 16268 056554	005202 020227 003002	000010		23\$:	BIS MOV BR INC CMP BGT	24\$ R2 R2.#10 24\$ 7\$ #20.aml.REG+120 (SP).#1 25\$ R4.#16 25\$ 2\$ R4.#16 26\$ 55 157 INTER DUMPER	:	CNT ,*		7552 7553 7515
16270 056562 16271 056570	152777 021627 001405	177364 000020 000001	134504	24\$:	BISB	#20 aml .REG+120 (SP) .#1	;	FND.GD.NIB,*		7556 7558
16273 056576 16274 056602	020427 001402	000016			CMP	R4.#16 25\$;	WRDS.TSTED,*		
16276 056610 16277 056614	020427 001035	177274 000016		25\$:	CMD	R4 .#16 26\$;	WRDS.TSTED,*		7560
16278 056616 16279 056620 16280 056622	104455 000157 010672				BEQ CMP BEQ JMP CMP BNE TRAP . WORD . WORD . WORD	55 157 INTER	,			7563
16281 036626 16283 056632 16284 056636 16285 056642 16286 056646 16287 056652 16288 056656 16289 056662 16290 056666 16291 056672 16292 056674 16293 056676 16294 056700 16295 056704 16296 056710 16297 056712 16298 056716 16299 056720 16300 056724 16301 056726 16302 056732 16303 056740 16304 056742	024032 012746 012746 012746 012746 012746 012746 012746 012746 010600 104414	007122 006700 010162 006426 010136 007564 006442 006120 000010			MOV MOV MOV MOV MOV MOV MOV MOV TRAP TRAP ADD JMP INC CMP BGT JMP CLR SUB MOV CLR JMP	DUMPER #WRD.56,-(SP) #WRD.37,-(SP) #FNC.17,-(SP) #MRD.12,-(SP) #FNC.15,-(SP) #PHR.10,-(SP) #WRD.14,-(SP) #SEV.FMT,-(SP) #10,-(SP) SP,R0 14 63 #22,SP 56\$ R3 R3,2(SP) 28\$ 1\$ R5 ARR.INC,R5 OP.NUM.ARR,2(SP) R4 55\$	•			7564
16291 056672 16292 056674 16293 056676 16294 056700	104414	000022 000562			MOV TRAP TRAP ADD	SP,R0 14 63 #22,SP	:	SP,*		7560
16295 056704 16296 056710 16297 056712 16298 056716	000167 005203 020366 003002	000002		26 \$: 27 \$:	INC CMP BGT	56\$ R3 R3,2(SP) 28\$		ARR.CNT		7560 7562 7497
16299 056720 16300 056724 16301 056726 16302 056732	104463 062706 000167 005203 020366 003002 000167 005005 166705 016766 005004 000167	177064 132630 132622	000002	28\$:	JMP CLR SUB MOV	1\$ R5 ARR.INC,R5 OP.NUM.ARR.2(SP)	•	ARR.SEL *,ARR.SEL		7570 7571 7573
16303 056740 16304 056742	005004 000167	000516			CLR	R4 55 \$;	ARR.CNT		

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 365	SEQ 0352
16306 16307	;ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
16309 056746 104402 16310 056750 152777 000040 16311 056756 016702 134620 16312 056762 042702 177770 16313 056766 142777 000007 16314 056774 150277 134214	134236 29\$: TRAP BISB MOV BIC	2 #40,aML.REG+40 ML.DUT.R2 #177770,R2	7574 7575
16315 057000 005016 16316 057002 152777 000010 16317 057010 012777 177400	134264 CLR BISB	ML.DUT.R2 #177770,R2 #7,aML.REG+40 R2,aML.REG+40 (SP) ; FND.GD.NIB #10,aML.REG+120 #-400,aML.REG+10 ;	7577 7578 7579
16318 057016 012777 011600 16319 057024 066705 132532 16320 057030 010577 134150 16321 057034 012777 000071 16322 057042 012701 000001	134150 MOV ADD MOV 134112 MOV	#10.aml.REG+120 #-400.aml.REG+10 #17.BUF,aml.REG+20 ARR.INC.R5 R5.aml.REG+30 *ARR.SEL.* #71.aml.REG #1.R1 *,\$\$TMP2	7577 7578 7579 7580 7581 7582 7583 7587
16325 057046 001411 16324 057050 016702 123042 16325 057054 001404	515: BEQ MOV BEQ	34\$ L\$DLY,R2 ; *,\$\$TMP1 33\$ 4(SP) . \$\$TMP	
16328 057064 001374 16329 057066 005301 16330 057070 000766 16331 057072 017767 134306	33\$: DEC BNE BR 134020 34\$: MOV	R2 32\$ R1 31\$ aML.REG+230,PD.TEMP :	7588
16326 057056 005066 000004 16327 057062 005302 16328 057064 001374 16329 057066 005301 16330 057070 000766 16331 057072 017767 134306 16332 057100 005002 16333 057102 010201 16335 057104 006201 16336 057110 006201	35\$: MOV ASR ASR	R2.R1 ; CNT,*	7588 7590 7592
16337 057112 062701 013120 16338 057116 010146	MOV	R1 MPD.TEMP,R1 R1,-(SP) R2,-(SP) W1,-(SP) -(SP) PC_BL\$GT2 W10,SP R0 50\$ W20, aML.REG+120 R2,R1 R1 36\$(R1),PC	
16342 057132 005046 16343 057134 004767 124000 16344 057140 062706 000010 16345 057144 005700 16346 057146 001070 16347 057150 152777 000020 16348 057156 010201	JSR ADD TST BNE 134116 BISB	PC_BL\$GT2 #10_SP R0 50\$	
16348 057156 010201 16349 057160 006301 16350 057162 066107 057166 16351 057166 000022	MOV ASL ADD 36\$: .WORD	R2,R1 ; CNT,* R1 36\$(R1),PC 373-36\$	7595 7597
16339 057120 010246 16340 057122 042716 177770 16341 057126 012746 000001 16342 057132 005046 16343 057134 004767 124000 16344 057140 062706 000010 16345 057144 005700 16346 057146 001070 16348 057156 010201 16349 057160 006301 16351 057166 000022 16352 057170 000030 16353 057172 000036 16355 057174 000044 16355 057176 000052 16356 057200 000060 16357 057202 000076 16358 057204 000104 16359 057206 000122 16360 057210 117703 134130	. WORD . WORD . WORD . WORD	398-368 408-368 418-368 428-368	
16357 057202 000076 16358 057204 000104 16359 057206 000122 16360 057210 117703 134130	. WORD . WORD 37\$: MOVB	428-368 448-368 458-368 478-368 aml.REG+170,R3 ; *,ARR.NUM	7601

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-8	00 12:16 PAGE 366	s	SEQ 0353
16362 16363	;ML4		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
16365 057214 000440 16366 057216 117703 134122	38\$: BR MOVB	49\$ aML.REG+170,R3	; *,ARR.NUM	7604
16368 057224 017703 134114	39\$: MOV BR	aML.REG+170,R3	; *,ARR.NUM	7607
16370 057232 017703 134106	40\$: MOV	aML.REG+170,R3	; *.ARR.NUM	7610
16372 057240 117703 134110	41\$: BR MOVB	46\$ aML.REG+200,R3	; *,ARR.NUM	7613
16374 057244 000424 16374 057246 117703 134102 16375 057252 006203 16376 057254 006203 16377 057256 006203 16378 057260 006203	42\$: MOVB 43\$: ASR ASR ASR ASR BR	49\$ amlREG+200,R3 R3 R3 R3 R3 R3	: *,ARR.NUM : ARR.NUM : ARR.NUM : ARR.NUM : ARR.NUM	7616
16379 057262 000415 16380 057264 017703 134064	445: MOV	amL.REG+200,R3	; *,ARR.NUM	7619
16380 057264 017703 134064 16381 057270 000411 16382 057272 017703 134056 16383 057276 006203 16384 057300 006203 16385 057302 006203 16386 057304 006203 16388 057310 017703 134020 16389 057314 000303	45\$: MOV 46\$: ASR ASR ASR ASR BR	48\$ aML.REG+200,R3 R3 R3 R3 R3 R3 R3 48\$ aML.REG+160,R3 R3	: * ARR.NUM : ARR.NUM : ARR.NUM : ARR.NUM : ARR.NUM	7622
16387 057306 000402 16388 057310 017703 134020 16389 057314 000303	475: MOV 485: SWAB	aML.REG+160,R3 R3	: * ARR.NUM : ARR.NUM : * ARR.NUM	7625
16390 057316 042703 177760 16391 057322 012716 000001 16392 057326 000404 16393 057330 005202 16394 057332 020227 000010	50\$: INC	#177760,R3 #1,(SP) 51\$ R2 R2,#10 35\$ #20,aML.REG+120 (SP),#1 30\$ R4,R3 52\$ 55 123 ASYNC DUMPER #MRD.10(SP)	*,ARR.NUM : *,FND.GD.NIB : CNT : CNT,*	7628 7629 7590
16395 057336 003661 16396 057340 152777 000020	133726 51\$: BLE BISB	35\$ #20 am REG+120		7632
16396 057340 152777 000020 16397 057346 021627 000001 16398 057352 001233	7 100	(SP) ,#1	: FND.GD.NIB,*	7632 7634
16395 057336 003661 16396 057340 152777 000020 16397 057346 021627 000001 16398 057352 001233 16399 057354 020403 16400 057356 001434	CMP	R4 R3	; ARR.CNT,ARR.NUM	7636
16401 057360 104455 16402 057362 000123 16403 057364 010464	BNE CMP BEQ TRAP . WORD . WORD	55 123 ASYNC		7639
16395 057336 003661 16396 057340 152777 000020 16397 057346 021627 000001 16398 057352 001233 16400 057356 001434 16401 057360 104455 16402 057364 010464 16404 057366 024052 16405 057370 012746 006406 16406 057374 012746 006700 16407 057400 012746 010162 16408 057404 012746 006716 16409 057410 012746 006016 16411 057420 010600 16412 057424 010316 16414 057426 010446 16415 057434 012746 005110 16416 057434 012746 000003	. WORD MOV MOV MOV MOV MOV MOV MOV MOV TRAP	DUMPER WWRD.10,-(SP) WWRD.37,-(SP) WFNC.17,-(SP) WWRD.39,-(SP) WFOR.FMT,-(SP) N5,-(SP) SP,R0 14		7640
16411 057420 010600 16412 057422 104414 16413 057424 010316 16414 057426 010446 16415 057430 012746 005110 16416 057434 012746 000003	MOV TRAP MOV MOV MOV MOV	SP,RO 14 R3.(SP) R4(SP) #FMT.14(SP) #3,-(SP)	: SP,* : ARR.NUM,* : ARR.CNT,*	7641

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 367 SEQ 0354
16418 16419 16420	;ML4 22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
16421 057440 010600 16422 057442 104414 16423 057444 062706 00002 16424 057450 104467 16425 057452 006000	526. TDAD 47
16427 057456 000167 17726 16428 057462 005204 16429 057464 020466 00000	ROR RO BHIS 54\$ 54 53\$: JMP 29\$ 54\$: INC R4 32 55\$: CMP R4,2(SP) ; ARR.CNT,* BLE 53\$ 36 56\$: ADD #6,SP ; 7450 RTS PC ; 7450
16430 057470 003772 16431 057472 062706 00000 16432 057476 000207 16433	06 56\$: ADD #6.SP ; 7450
16434 16435 16440 16441 16445	; Routine Size: 425 words ; Maximum stack depth per invocation: 18 words
16449 057500 16450 057500 004767 17625 16451 057504 104466 16452 057506 006000	T32:: 1\$: JSR PC.\$T32 ; 7645 TRAP 66 ROR RO BLO 1\$ RTS PC
16453 057510 103773 16454 057512 000207 16455 16456 16457 16462 16463 16464; 7648 ! <blf< td=""><td>; Routine Size: 6 words ; Maximum stack depth per invocation: 0 words /PAGE></td></blf<>	; Routine Size: 6 words ; Maximum stack depth per invocation: 0 words /PAGE>

16466 :ML4 16467 : 16468 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BL1.2 (83) 6470 6471 6473 6473 BGNTST: TEST NUMBER: TST 33 SEQUENCER EXISTENCE TEST TEST NAME: TEST DESCRIPTION: TEST SEQUENCER TIMING AND CONTROL LOGIC FOR EXISTENCE BY: 1. FIRST WRITING THE GOOD BLOCK VIA DAT_DM MODE WITH A BACKGROUND PATTERN OF ONES. 2. VIA A MBUS WRITE FUNCTION WRITE ONES INTO THE GOOD BLOCK. 3. THEN VIA DAT_DM READ GOOD NIBBLES IN THE GOOD BLOCK AND XOR THEM AGAINST THE BACKGROUND PATTERN. RECORD THE NUMBER OF NIBBLES THAT WERE UNCHANGED OR PARTIALLY CHANGED BY THE MBUS WIRTE. 4. REPORT A FATAL ERROR AND DROP THIS UNIT IF THE NIBBLES TESTED EQUALS THE COUNT OF UNCHANGED NIBBLES. REPORT AN INTERMEDIATE DIAGNOSTIC MESSAGE IF AT LEAST SOME NIBBLES WERE CHANGED BY THE MBUS WRITE. IMPLICIT INPUTS: PD TEMP: A BIT VECTOR OF 16 BITS WHERE THE READ PROM DATA IS STORED AND ACCESSED FROM. A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITE FUNCTIONS 7685 7686 7687 7688 7689 7690 7691 7692 7693 7694 7695 7696 7697 7698 7699 7700 ARE FOUND. local BACK GROUND PATTERN SUM NIBBLE ARE BAD NIBBLES TESTED LALL NIBBLES ARE BAD RESULTS OF XOR BG PAT 16511 16512 16513 SUM BAD NIB_TSTED, RESULT; CLR_MBUS; BG_PAT = ONES; MLD1 = .BG_PAT; MLD2 = .BG_PAT; MLE2 = .BG_PAT; 16516 : 16517 : !BACKGROUND EQL ONES !LOAD DATA DIAG REG WITH BG PAT 16518 : 16519 : 16520 :

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CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 369
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22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                       TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BL1.2 (83)
                                                                                         SET UP A DATA DIAG XFERR AT THE GOOD BLOCK
                           DAT_DM_XFER ();
                            MLCS1 = write:
                                                                                         !DO A WRITE FUNCTION
                            incr WD CNT from 0 to 127 do
                                                                                         !WRITE BLOCK WITH BG PAT
                                begin
DELAY (ONE_US);
DAT_CLK = ONE;
end;
                           CLR_MBUS;
10_BUF = ZEROES;
                                                                                         !LOAD FIRST WORD OF IO_BUF
!MAKE XFERR SET ON ONE ADRS
!SET UP A GOOD BLOCK XFERR
!DO A WRITE FUNCTION
                           BAI = ONE;
GD BLK_XFER ();
MLCS1 = write;
TIME_OUT_LOOP;
                           BGNS(IB;
                           CLR_MBUS;
ALL_BAD = ZEROES;
SUM_BAD = ZEROES;
                           NIB_TSTED = ZEROES:
                           DAT_DM_XFER ();
MLCS1 = read;
                                                                                         !SET UP SAME DATA DIAG XFERR
                                                                                         !DO A READ FUNCTION
                           DELAY (ONE_US);
                            incr WD_CNT from 0 to 112 do
                                                                                         !READ 113 LONG WORDS
                                 begin
                                PD TEMP = .MLPD;
DAT_CLK = ONE;
DELAY (ONE_US);
                                                                                         !GET PROM DATA
                                                                                         !CLOCK OUT THE DATA WORD
                                RD_LNG_WRD;
                                                                                         !READ THE DATA WORD
                                 incr NIB_PTR from 0 to 8 do
                                                                                       !LOOK AT 9 NIBBLES
                                      if .PD_TEMP [.NIB_PTR] eqt ZERO
                                                                                        !FIND GOOD NIBBLE
                                      then
                                           NIB_TSTED = .NIB_TSTED + 1;
                                                                                         !INCREMENT COUNT OF NIBBLES TESTED
                                           XOR_LNG_WRD (.NIB_PTR, .BG_PAT, RESULT); !XOR NIBBLE WITH BG PAT
                                           if .RESULT<0, 4> eql ZERO
                                                                                       !SEE IF ALL BITS IN NIBBLE WERE BAD?
                                           then
16566
16567
                                                ALL_BAD = .ALL_BAD + 1
                                                                                       !INCREMENT COUNT IF ALL BAD
                                           else
16568
16569
16570
                                                if .RESULT<0, 4> neg %0'17' then SUM BAD = .SUM BAD + 1;
16571
                                                                                         SEE IF SOME BITS IN NIBBLE WERE BAD
16572
16573
                                                                                         !INCREMENT COUNT IF SOME BAD
                                           end:
16574
                                 end:
16575
16576 :
                           ENDSUB:
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16578 :ML4
16579 :
16580
                                                                                                                                                        22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BL1.2 (83)
16581
16582
16583
                                                                                                                                             !WHERE ALL NIBBLES XFERR'ED ALL BAD?
                                                if .NIB_TSTED eql .ALL_BAD
                                                then
16584
16585
16586
16587
16588
16589
16590
                                                       begin
ERRDF (84, SYNC, DUMPER);
PRINTB (SEV_FMT, WRD_22, PHR_4, WRD_9, WRD_12, WRD_23, FNC_5, WRD_19);
DODU (.ML_LUN);
                                  7760
7761
7762
7763
7764
7765
                                                        end
                                             else
16592
16593
16594
16595
16596
16597
                                                                                                                                                    !SEE IF SEE NIBBLE WERE BAD?
                                                        if .SUM_BAD gtr ZERO
                                                        then
                                                                begin
ERRDF (85, INTER, DUMPER);
PRINTB (SIX_FMT, PHR_4, WRD_9, WRD_12, WRD_23, FNC_5, WRD_19);
                                   7766
7767
                                                                                                                                                     SOME BAD IS OK SO GIVE INTERMEDIATE ERROR
                                   7768
7769
 16599 ;
                                               ENDTST:
16603

16607 057514

16608 057520

16609 057524

16610 057532

16611 057536

16612 057542

16613 057550

16614 057554

16615 057562

16616 057570

16617 057576

16618 057604

16620 057610

16620 057616

16621 057620

16623 057624

16623 057632

16624 057632

16626 057640

16627 057642

16629 057646

16629 057646

16629 057646

16629 057646

16630 057650

16631 057656
                                               124376
000010
000040
134044
177770
                             004167
162706
152777
016705
042705
142777
150577
012766
012777
016677
016677
004767
012777
005000
012701
001411
016702
                                                                                                                     R1,$SAVE5
                                                                                  $T33:
                                                                                                                                                                                                                                                                                  7647
                                                                                                    SUB
                                                                                                                     #40, amL . REG+40
                                                                 133462
                                                                                                    BISB
                                                                                                                                                                                                                                                                                  7694
                                                                                                                     ML.DUT.R5
#177770,R5
                                                                                                    MOV
                                                                                                                    #177770,R5
#7,aML.REG+40
R5,aML.REG+40
#-1,2(SP)
#-1,aML.REG+170
2(SP),aML.REG+200
2(SP),aML.REG+160
PC,DAT.DM.XFER
#61,aML.REG
R0
#1,R1
5$
16612
16613
16614
16615
16616
16617
16618
                                                000007
133440
177777
                                                                 133444
                                                                                                    BISB
                                                                 000002
133554
133556
                                                                                                                                                                                                                                                                                 7697
7698
7699
                                                                                                    MOV
                                                                                                                                                                                           : * .BG.PAT
                                                177777
                                                                                                    MOV
                                               000002
000002
134352
000061
                                                                                                   MOV
                                                                                                                                                                                          BG.PAT.*
                                                                 133530
                                                                                                    JSR
                                                                                                                                                                                                                                                                                  7701
7702
7704
7706
                                                                 133336
                                                                                                    MOV
                                                                                                    CLR
                                                                                                                                                                                           ; WD. CNT
                                                000001
                                                                                                   MOV
BEQ
                                                                                                                                                                                           : *, $$TMP2
                                                122264
                                                                                                    MOV
                                                                                                                     LSDLY,R2
                                                                                                                                                                                           : * . $$TMP1
                                                                                                    BEQ
                                                000006
                                                                                  3$:
                                                                                                    CLR
                                                                                                                     6(SP)
                                                                                                                                                                                          : SSTMP
                                                                                                   DEC
BNE
DEC
BR
                                                                                                                                                                                          : SSTMP1
                                                                                                                     R2
                                                                                                                                                                                           : SSTMP2
                              152777
                                                000020 133416 5$:
                                                                                                   BISB
                                                                                                                     #20, aml. REG+120
                                                                                                                                                                                                                                                                                 7707
                                                                                                                                                                                           : WD.CNT
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						12:16 PAGE 371			0358
16633 16634				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
10033	020027 00	00177			CMP	RO.#177	; WD.CNT,*		
16636 057660 16637 057664 16638 057666 16639 057674 16640 057700 16641 057704 16642 057712 16643 057716 16644 057722 16645 057730 16646 057734 16647 057742 16648 057734 16649 057750 16650 057750 16651 057760 16653 057776 16653 057776 16654 057776 16655 060002 16656 060004 16657 060006 16658 060010 16659 060014 16660 060022 16661 060026 16663 060036 16663 060036 16664 060036	152777 00 016705 13 042705 17	00040 33702 77770 00007 33276 31656 00010 36126 00061 33256	133320		BLE BISB MOV BIC BICB BISB CLR BISB JSR	#40,aML.REG+40 ML.DUT.R5 #177770,R5 #7,aML.REG+40 R5.aML.REG+40 IO.BUF #10,aML.REG+40 PC.GD.BLK.XFER #61,aML.REG	:		7708
16641 057704 16642 057712	142777 00 150577 13	00007 3276	133302		BICB	#7.aML.REG+40 R5.aML.REG+40			
16643 057716 16644 057722 16645 057730	005067 13 152777 00	00010	133264		BISB	10.BUF #10.BML.REG+40	:		7711 7712 7713 7714
16646 057734 16647 057742	005067 13 152777 00 004767 13 012777 00 105777 13 100375 104402 152777 00 016703 13 042703 17 142777 00 150377 13	0061 3256	133212	6\$:	TSTB	#61,aML.REG aML.REG+50	:		7714
16648 057746 16649 057750 16650 057752	104402	00040	133234	7\$:	BPL TRAP BISB	3			7715 7716
16650 057752 16651 057760 16652 057764	016703 13 042703 17	3616 7770			MOV	ML.DUT.R3 #177770.R3			7710
16652 057764 16653 057770 16654 057776		00040 33616 77770 00007 33212	133216		BIC BICB BISB	#7,aML.REG+40 R3,aML.REG+40			
16655 060002 16656 060004	005004 005016				CLR CLR CLR	#40, aml.REG+40 ML.DUT, R3 #177770, R3 #7, aml.REG+40 R3, aml.REG+40 R4 (SP) R5	: ALL.BAD : SUM.BAD		7718 7719
16654 057776 16655 060002 16656 060004 16657 060006 16658 060010 16659 060014 16660 060022 16661 060026 16662 060030 16663 060034 16664 060036	004767 13 012777 00	34146 00071 00001	133132		JSR MOV	PC.DAT.DM.XFER	NIB.TSTED		7718 7719 7720 7721 7722 7723
16660 060022 16661 060026	012701 00	00001	133132	8\$:	MOV BEQ MOV	#1 A1 11\$	*,\$\$TMP2		7723
16662 060030 16663 060034	001404	22062			BEQ	L\$DLY,R2 10\$; *,\$\$TMP1		
16664 060036 16665 060042	005066 00 005302	00006		9\$:	CLR	6(SP)	: SSTMP : SSTMP1		
16667 060046	001374 005301 000766			10\$:	BNE DEC BR	73 R1	; \$\$TMP2		
16669 060052 16670 060054	000766 005003 017767 13	33324	133036	11\$: 12\$:	CLR	9\$ R1 8\$ R3 aML.REG+230,PD.TEMP #20,aML.REG+120 #1,R1 16\$ L\$DLY,R2 15\$ 6(SP)	: WD.CNT		7725
16671 060062 16672 060070	152777 00 012701 00	33324 00020 00001	133036 133204		BISB MOV BEQ	#20.aML.REG+120 #1.R1	*,\$\$TMP2		7725 7727 7728 7729
16674 060076	001411	2014		13\$:	MOV	16\$ L\$DLY,R2	; *,\$\$TMP1		
16676 060104	005066 00	00006		14\$:	CLR	6(SP)	: \$STMP : \$STMP1		
16678 060112 16679 060114	001374 005301			15\$:	BNE	1/4	; SSTMP2		
16680 060116 16681 060120	000766 017767 13 017767 13 017767 13 005001 010102	3220	131024	16\$:	BR MOV	138 aML.REG+170,D1.TEMP			
16682 060126	017767 13	3220 3222 3174	131024 131020 131014		MOV	aML.REG+200,D2.TEMP			
16666 060044 16667 060046 16668 060050 16669 060052 16670 060054 16671 060062 16672 060070 16673 060074 16674 060076 16675 060102 16676 060104 16677 060110 16678 060112 16679 060114 16680 060116 16681 060120 16682 060126 16683 060134 16684 060142 16685 060146 16687 060150	010102 006202 006202			17\$:	MOV BEQ CLR DEC BNE DEC BR MOV MOV CLR MOV ASR ASR	R1 138 aML.REG+170,D1.TEMP aML.REG+200,D2.TEMP aML.REG+160,E2.TEMP R1 R1,R2 R2 R2	; NIB.PTR ; NIB.PTR,*		7732 7734

16689			;ML4			22-010	080 00.24.31 7000
16689 16690 16691			:"			22-Dec-19	980 09:24:31 TOPS 980 09:21:22 PA:<
16692 060152 16693 060154 16694 060160 16695 060162	006202 062702 010246	013120		ASR ADD MOV	R2 MPD.TEMP,R2 R2,-(SP)		
16693 060154 16694 060160 16695 060164 16696 060164 16698 060174 16699 060176	042716 012746 005046	177770 000001		ASR ADD MOV MOV BIC MOV CLR JSR ADD TST	R2,-(SP) R1,-(SP) #177770,(SP) #1,-(SP) -(SP) PC,BL\$GT2 #10,SP	; NIB.PTR,*	
16699 060176 16700 060202 16701 060206 16702 060210	004767 062706 005700	122736 000010		JSR ADD TST	PC_BL\$GT2 #10,SP R0 20\$ R5		
16703 060212	005205			INC	R5	; NIB.TSTED	7737 7738
16704 060214 16705 060216	010146	000004 000012		MOV	R1,-(SP) 4(SP),-(SP) #12,-(SP)	; NIB.TSTED ; NIB.PTR.* ; BG.PAT.*	7738
16706 060222 16707 060226	012746 060616			BNE INC MOV MOV MOV ADD	#12,-(SP) SP,(SP)	; RESULT,*	
16708 060230 16709 060234	004767	136406 000012 177760		JSR MOV BIC	SP, (SP) PC, XOR.LNG.WRD 12(SP), R2 #177760, R2	; RESULT,*	7740
16710 060240 16711 060244	042702	177760		BIC	#177760,R2	, RESOLT,	1140
16712 060246 16713 060250	005204			BNE	18\$ R4	: ALL.BAD	7742
16714 060252	020227	000017	18\$:	CMP	19\$ R2,#17 19\$		7742 7740 7745
16715 060256 16716 060260	001402	000006 000006		BR CMP BEQ INC	6(SP)	; SUM.BAD	
16717 060264 16718 060270	005201		19\$: 20\$:	INC	#6.SP R1	: NIB.PTR	7736 7732
16719 060272 16720 060276	020127 003722	000010		CMP	R1.#10	; NIB.PTR,*	
16721 060300 16722 060302 16723 060306	005203 020327 003662	000160		BLE INC CMP BLE	17\$ R3 R3 #160 12\$: WD.CNT : WD.CNT,*	7725
16724 060310 16725 060312	104467			TRAP ROR	67 R0		7750
16724 060310 16725 060312 16726 060314 16727 060316 16728 060320 16729 060322 16730 060324	103615 020504 001037			BLE TRAP ROR BLO CMP BNE	7\$ R5,R4 21\$; NIB.TSTED,ALL.BAD	7754
10/31 000320	104455 000124 010526			BNE TRAP . WORD . WORD . WORD MOV	RO 7\$ R5,R4 21\$ 55 124 SYNC DUMPER	•	7757
16732 060330 16733 060332 16734 060336	024052 012746 012746	006506 007774		MUV	#FNC.5,-(SP)		7758
16732 060330 16733 060332 16734 060336 16735 060342 16736 060346 16737 060352 16738 060356 16740 060366 16741 060372 16742 060376 16743 060400	012746 012746 012746 012746	006544 006426 006374 007454		MOV MOV MOV	MURD.23,-(SP) MURD.12,-(SP) MURD.9,-(SP) MPHR.4,-(SP)		
16739 060362 16740 060366 16741 060372	012746 012746 012746 010600 104414	006530 006120 000010		MOV MOV MOV TRAP	MPHR.4(SP) MURD.22(SP) MSEV.FMT(SP) M10(SP) SP.R0 14		
16742 060376 16743 060400	010600 104414			MOV	SP.RO	; SP,*	

ZMLABO ML-11 LI IISCELLANEOUS C	ODING SE	CTION			12:16 PAGE 373				SEQ	0360
16745 16746 16747			;ML4					22-Dec-1980 22-Dec-1980	09:24:31 09:21:22	TOP:
16748 060402 16749 060406 16750 060410	016700 104451 104444	133172		MOV TRAP TRAP	ML.LUN,RO 51 44		:			775
16751 060412 16752 060416 16753 060420	062706 000432 005716	000022	21\$:	MOV TRAP TRAP ADD BR TST	#22,SP 22\$ (SP) 22\$ 55 125 INTER DUMPER		: SUM.BAD			7756 7756 7766
16755 060424 16756 060426 . 16757 060430	104455 000125 010672			TRAP .WORD .WORD	55 125 INTER		:			776
16758 060432 16759 060434 16760 060440 16761 060444 16762 060450 16763 060454 16764 060460 16765 060464 16766 060470	024052 012746 012746 012746 012746 012746 012746 012746	006506 007774 006544 006426 006374 007454 006100 000007		BLE TRAP . WORD . WORD MOV MOV MOV MOV MOV MOV MOV MOV MOV MOV	### 19,-(SP) #### 19,-(SP) #### 12,-(SP) #### 12,-(SP) #### 12,-(SP) #### 12,-(SP) #### 14,-(SP) ##SIX.FMT,-(SP) ##7,-(SP)					7768
16766 060470 16767 060474 16768 060476	010600			MOV	SP.RÒ		; SP,*			
16768 060476 16769 060500 16770 060504 16771 060510 16772	062706 062706 000207	000020 000010	22\$:	ADD ADD RTS	#20.SP #10.SP PC		:			7766 7647
16773 16774 16779			; Rout ; Maxi	ine Size: mum stack	255 words depth per invocation:	19 words				
16780 16784 16788 060512 16789 060512 16790 060516 16791 060520 16792 060522 16793 060524 16794 16795	004767 104466 006000 103773 000207	176776	T33:: 1\$:	JSR TRAP ROR BLO RTS	PC,\$T33 66 R0 1\$ PC					7769
16795			Rout		6 words depth per invocation:	0 words				

16805 16806 16807;

7772 ! CBLF/PAGE>

"

TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BLI.2 (84)

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 16809 :ML4 16809 ; P 16810 ; 16811 ; 16812 ; 16813 ; 16814 ; 16816 ; 16816 ; 16817 ; 16818 ; 16819 ; 16820 ; 7773 7774 7775 BGNTST; 7776 7777 ! TEST NUMBER: TST 34 7778
7779
7780
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7796
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7818 TEST NAME: SYNC DATA BUS CONTINUITY TEST (WRITE PATH) 16821 16822 16823 16824 16825 16826 16827 16830 16831 16833 16833 16835 16836 16837 TEST DESCRIPTION: TEST THE CONTINUITY OF THE SYNCHRONOUS MODULE WRITE PATH DATA BUS BY: 1. VIA DA) DM MODE WRITE A BACKGROUND PATTERN OF ONES INTO THE GOOD BLOCK. 2. VIA MBUS WRITE FUNCTION WRITE A ZEROES PATTERN INTO THE GOOD BLOCK. 3. VIA DAT_DM MODE READ GOOD NIBBLES IN THE GOOD BLOCK FOR ZEROES. 16838 16839 4. REPEAT WITH COMPLIMENT 16840 16841 16842 16843 16844 16845 16846 16847 16848 16849 16850 DATA AND BACKGROUND PATTERNS. IMPLICIT INPUTS: PD_TEMP A BIT VECTOR OF 16 BITS WHERE THE READ PROM DATA IS STORED AND ACCESSED FROM. IO_BUF 16850 16851 16852 16853 16854 16855 16856 16857 16858 A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITE FUNCTION ARE FOUND. local 16860 16861 16862 16863 DODU_FLG. !DROP UNIT FLAG BG PAT, RESULT, BACKGROUND PATTERN RESULTS FROM XOR TST_PAT;

```
16865 ;ML4
16866 ;
16867
16868 ;
16869 ;
16870 ;
16871 ;
16872 ;
16873 ;
16874 ;
                                                                                                                                 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22
                                                                                                                                                                            TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BL1.2 (84)
                                        DODU_FLG = ZERO;
BG_PAT = ZEROES;
TST_PAT = ONES;
                                                                                                                                 !BG PAT EQL 0'S
                                        incr TWICE from 0 to 1 do
                                                                                                                                 !REPEAT LOOP TWICE
                                               begin
BGNSUB;
16875
16876
                                               CLR_MBUS;
MLDT = .BG_PAT;
MLD2 = .BG_PAT;
MLE2 = .BG_PAT;
DAT_DM_XFER ();
                             7833
7834
7835
7836
7836
7837
7838
7849
7843
7844
7845
7846
7847
7851
7852
7853
7854
7856
7857
7858
7858
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7861
7862
7863
7864
7865
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7867
7867
7868
7868
7869
7870
7871
7872
                                                                                                                                 !LOAD DATA DIAG REG WITH BG PAT
 16878
16879
16880
16881
16882
16883
16884
16885
16886
                                                                                                                                 SET UP A DATA DIAG XFERR AT THE GOOD BLK
                                               MLCS1 = write:
                                                incr WD_CNT from 0 to 127 do
                                                                                                                                 !LOAD BLOCK WITH BG PAT
                                                      begin
DELAY (ONE_US);
DAT_CLK = ONE;
end;
16888
16889
16890
16891
16892
16893
16894
16895
16896
16897
                                               CLR_MBUS;
                                               BAI = ONE;

IO BUF = .TST_PAT;

GD_BLK_XFER ();

MLCS1 = write;
                                                                                                                                 SET ON FIRST IO_BUF ADRS
                                                                                                                                 FIRST 10 BUF WORD EQL'S TST_PAT
                                                                                                                                 !DO A WRITE FUNCTION
                                               TIME OUT LOOP;
CLR_MBUS;
                                               DAT_DM_XFER ();
MLCS1 = read;
                                                                                                                                 ISET UP A DATA DIAG XFERR AT SAME BLOCK
                                                                                                                                DO A READ FUNCTION
16898
16899
                                               DELAY (ONE_US);
16900
16901
16902
16903
16904
16905
16906
16907
16908
16909
16910
16911
16912
16913
                                               incr WD_CNT from 0 to 112 do
                                                                                                                                !READ 113 LONG WORDS
                                                      begin
PD_TEMP = .MLPD;
DAT_CLK = ONE;
DELAY (ONE_US);
                                                                                                                                 !GET THE PROM DATA
                                                                                                                                 !CLOCK OUT THE DATA WORD
                                                      RD_LNG_WRD;
                                                                                                                                !READ THE DATA WORD
                                                      incr NIB PTR from 0 to 8 do
                                                                                                                                !LOOK AT 9 NIBBLES
                                                              If .PD_TEMP [.NIB_PTR] IS_NOT_SET !FIND GOOD NIBBLES
                                                              then
                                                                     begin
XOR_LNG_WRD (.NIB_PTR, .TST_PAT, RESULT);
                                                                                                                                                             !XOR NIBBLE DATA WITH TST_PAT
16914 :
16915 :
16916 :
16917 :
                                                                     if .RESULT<0, 4> neg ZERO
                                                                                                                                !SEE IF EQUAL
                                                                     then
                                                                           begin !ERRUR IF NEW ERROF (86, SYNC, DUMPER); PRINTB (FIV FMT, WRD 24, WRD 25, WRD 10, WRD 12, FNC_5); PRINTB (FMT_5, .TST_PAT, .RESULT, .NIB_PTR);
16918 :
```

```
MACRO M1113 22-DEC-80 12:16 PAGE 377
CZMLABO ML-11 LOGIC TEST
MISCELLANEOUS CODING SECTION
                                                                                                                                                                                                                              SEQ 0364
                                                                                                                                  22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                             TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BL1.2 (84)
                                                                               DODU_FLG = ONE;
                                                                        end
                                                         end:
                                                  ENDSUB:
                                                                                                                                  !DROP THIS UNIT IF DODU IS_SET !AND EXITS TEST
                                                  if .DODU_FLG IS_SET
                                                        begin
DODU (.ML_LUN);
                                                         DOCLN:
   16939
16940
16941
16942
16943
16944
16945
16953
16954
16955
                                                         end:
                                                  BG_PAT = not .BG_PAT;
TST_PAT = not .TST_PAT;
end;
                                                                                                                                  COMPLIMENT BG_PAT COMPLIMENT TST_PAT AND REPEAT
                                           ENDTST:
                                                                                                    R1,$SAVE5
-(SP),-(SP)
-(SP)
                                           123364
                                                                       $T34:
                                                                                                                                                                                                                                       7771
                                                                                      CLR
                                                                                                                                                                  DODU.FLG
    16956
16957
16958
16959
16960
16961
16962
                                                                                                                                                                  BG.PAT
                                                                                                    #-1,R2
-(SP)
                                           177777
                                                                                      MOV
                                                                                                                                                                  *,TST.PAT
                                                                                      CLR
                                                                                                                                                                  TWICE
                                                                        15:
                                                                                                    #40, aml. REG+40
ML. DUT. R5
#177770, R5
#7, aml. REG+40
                                                         132436
                                                                                      BISB
                                                                                      MOV
                                                                                      BIC
                                                         132420
                                                                                      BICB
BISB
MOV
MOV
JSR
MOV
CLR
MOV
BEQ
MOV
                                                                                                                                                              : BG.PAT,*
: BG.PAT,*
: BG.PAT,*
   16967
16968
16969
16970
16971
16972
16973
16974
                                                                                                    R1, aML.REG+160
PC.DAT.DM.XFER
#61, aML.REG
R3
                                                         132326
                                                                                                                                                              : WD.CNT
                                           000001
                                                                                                                                                               : * . $$TMP2
                                           121254
                                                                                                     L$DLY,R5
                                                                                                                                                               : *, $$TMP1
```

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECT	ION MA	ACRO M1113	22-DEC-80	12:16 PAGE 378		SEC	0365
16976 16977		ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
16979 060644 005066 0 16980 060650 005305 16981 060652 001374	000006	48:	CLR DEC BNE	6(SP) R5 4\$	SSTMP1		
16982 060654 005304 16983 060656 000766		5\$:	BNE DEC BR BISB	R4 3\$; SSTMP2		
16984 060660 152777 0/ 16985 060666 005203	000020 13	2406 6\$:	BISB	#20,aML.REG+120	. WD. CNT		7843 7840
16986 060670 020327 0	000177		CMP	R3,#177	WD.CNT,*		, 0.10
16988 060676 152777 0 16989 060704 016705 1 16990 060710 042705 1	32672 77770	2310	BISB MOV BIC	#40,aML.REG+40 ML.DUT.R5 #177770.R5			7844
16991 060714 142777 0	00007 13 32266	2272	BICB	#7, aML. REG+40			
16993 060726 152777 0	00010 13	2260	BISB	R5, aML.REG+40 #10, aML.REG+40	: TST.PAT.*		7847
16995 060740 004767 1	35116	2202	MOV JSR MOV TSTB	PC.GD.BLK.XFER #61.aML.REG aML.REG+50	; 131.541,-		7849 7850
16997 060752 105777 1	00061 13 32246	78:	TSTB	aml.REG+50	•		7850
16999 060760 152777 0	00040 13	2226	BISB	#40.aML.REG+40	,		7851
17000 060766 016705 1	32610 77770		BIC	#177770,R5			
17002 060776 142777 0 17003 061004 150577 1	00007 13 32204	2210	BISB	#7,aML.REG+40 R5,aML.REG+40			
17004 061010 004767 13 17005 061014 012777 0	33146 100071 13	2132	JSR	PC.DAT.DM.XFER	:		7853 7854 7855
17006 061022 012704 0	00001	8\$:	JSR MOV MOV BEQ MOV	#1 P4	: *,\$\$TMP2		7855
17008 061030 016705 12	21062		MOV	LSDLY,R5	; *,\$\$TMP1		
17009 061034 001404 17010 061036 005066 00 17011 061042 005305 17012 061044 001374	00006	98:	BEQ CLR DEC BNE DEC BR CLR MOV BEQ CLR DEC BNE DEC BNE DEC BNE DEC BNE DEC	11\$ L\$DLY,R5 10\$ 6(SP) R5 9\$ R4 8\$ R3 aML.REG+230,PD.TEMP	; \$\$TMP ; \$\$TMP1		
17010 061036 005066 00 17011 061042 005305 17012 061044 001374 17013 061046 005304 17014 061050 000766		108:	BNE	9\$; SSTMP2		
17014 061050 000766			BR	85			7057
17016 061054 017767 1	32324 13	2036 12 \$:	MOV	R3 aml.REG+230,PD.TEMP #20,aml.REG+120 #1,R4	; WD.CNT		7857 7859 7860 7861
17018 061070 012704 0	32324 13 00020 13 00001		MOV	MEDATE ANEUVIEU	*,\$\$TMP2		7861
17019 061074 001411 17020 061076 016705 12 17021 061102 001404	21014	13\$:	MOV	#1,R4 16\$ L\$DLY,R5 15\$ 6(SP) R5 14\$; *,\$\$TMP1		
17020 061076 016705 12 17021 061102 001404 17022 061104 005066 00	00006	145:	CLR	6(SP)	: SSTMP1		
17024 061110 005305			BNE	R5 14\$			
17025 061114 005304 17026 061116 000766 17027 061120 017767 13		15\$:	DEC	R4 13\$; SSTMP2		
17022 061104 005066 00 17023 061110 005305 17024 061112 001374 17025 061114 005304 17026 061116 000766 17027 061120 017767 13 17028 061126 017767 13	32220 13 32222 13 32174 13	0024 16\$: 0020 0014	MOV	R4 13\$ aml.REG+170.D1.TEMP aml.REG+200.D2.TEMP aml.REG+160.E2.TEMP R4			
17027 061120 017767 1 17028 061126 017767 1 17029 061134 017767 1 17030 061142 005004	32174 13	0014	MOV	amL .REG+160,E2.TEMP	· NIO DTD		794/
11000 001142 000004			CEN	N4	; NIB.PTR		7864

MLABO ML-11 L SCELLANEOUS C	- JULIAU 3E								0366
17032				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
17035 061144 17036 061146 17037 061150	010405 006205 006205			17\$:	MOV ASR ASR	R4,R5 R5 R5	; NIB.PTR,*		7866
17038 061152 17039 061154 17040 061160	006205 062705 010546	013120			ASR ADD MOV	R5 MPD.TEMP,R5 R5,-(SP)			
17035 061144 17036 061146 17037 061150 17038 061152 17039 061154 17040 061160 17041 061162 17042 061164 17043 061170	010446 042716 012746	177770 000001			MOV	#PD.TEMP,R5 R5,-(SP) R4,-(SP) #177770,(SP) #1,-(SP) -(SP)	; NIB.PTR,*		
17044 061174 17045 061176 17046 061202 17047 061206	005046 004767 062706	121736 000010			MOV CLR JSR ADD	#10.SP			
17048 061210 17049 061212 17050 061214	001060 010446 010246				ADD TST BNE MOV MOV	R0 19\$ R4,-(SP) R2,-(SP) #12,-(SP)	; NIB.PTR,* ; TST.PAT,*		7869
17051 061216 17052 061222	012746 060 <u>616</u>	000012			MOV ADD JSR	W12,-(SP) SP,(SP) PC,XOR.LNG.WRD	; RESULT,*		
17054 061230	004/6/	135412 000017	000012		BIT	PC.XOR.LNG.WRD #17,12(SP)	; *,RESULT		7871
17056 061240 17057 061242 17058 061244	104455 000126 010526				BIT BEQ TRAP .WORD .WORD	#17,12(SP) 18\$ 55 126 SYNC DUMPER	;		7874
17059 061246 17060 061250 17061 061254 17062 061260 17063 061264 17064 061270 17065 061274	024052 012746 012746 012746 012746 012746 012746	007774 006426 006406 006560 006552 006062 000006			MOV MOV MOV MOV	DUMPER #FNC.5,-(SP) #MRD.12,-(SP) #MRD.10,-(SP) #MRD.25,-(SP) #MRD.24,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14			7875
17066 061300 17067 061304	012746 010600 104414	000006			MOV	#6,-(SP) SP,R0	; SP,*		
17069 061310 17070 061312	010416	000030			MOV	R4, (SP) 30(SP), -(SP)	; NIB.PTR,* ; RESULT,* ; TST.PAT,*		7876
7071 061316 17072 061320 17073 061324	010246 012746 012746	004430 000004			MOV MOV	R4.(SP) 30(SP),-(SP) R2,-(SP) MFMT.5,-(SP) M4,-(SP) SP,R0	; TST.PAT,*		
17074 061330 17075 061332	010600				MOV	SP.RO	; SP,*		
17076 061334 17077 061342 17078 061344	012766 062706 063706	000001 000026 000006	000036	188.	ADD ADD	#1,36(SP) #26,SP #6,SP R4 R4,#10 17\$; *,DODU.FLG		7877 7873 7868 7864
17079 061352 17080 061354	005204	000010		18\$: 19\$:	INC	R4 R4_#10	NIB.PTR		7864
17063 061264 17064 061270 17065 061274 17066 061300 17067 061304 17068 061306 17069 061310 17070 061312 17071 061316 17072 061320 17073 061324 17074 061332 17076 061332 17076 061332 17078 061332 17078 061332 17078 061332 17080 061352 17081 061360 17082 061362 17083 061364 17084 061370 17085 061372	005203 020327	000160			MOV TRAP MOV MOV MOV MOV TRAP MOV ADD ADD INC CMP BLE INC CMP BLE TRAP ROR	R3 R3 #160 12\$ 67 R0	; WD.CNT ; WD.CNT,*		7857
17085 061372	020327 003631 104467 006000				TRAP	67	:		7882

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 380	SEQ 03	67
17088 17089 17090	:ML4		22-Dec-1980 09:24:31 T 22-Dec-1980 09:21:22 P	OPS A:<
17091 061376 103002 17092 061400 000167 177142 17093 061404 026627 000002 17094 061412 001004	000001 20\$: JMP CMP BNE	21\$ 1\$ 2(SP),#1 22\$; DODU.FLG,* 7	886
17095 061414 016700 132160 17096 061420 104451 17097 061422 104444	MOV TRAP TRAP 22\$: COM	ML.LUN,RO	;	890
17098 061424 005101 17099 061426 005102 17100 061430 005216	INC	44 R1 R2 (SP)	; BG.PAT 7 ; TST.PAT 7 ; TWICE 7 ; TWICE,*	894 895 830
17101 061432 021627 000001 17102 061436 003760 17103 061440 062706 000010 17104 061444 000207	CMP BLE ADD RTS	(SP),#1 20\$ #10,SP PC	; TWICE,*	771
17105 17106 17107 17112 17113		: 232 words k depth per invocation: 24 words		
17117 17121 061446 17122 061446 004767 177054 17123 061452 104466 17124 061454 006000 17125 061456 103773 17126 061460 000207	T34:: 1\$: JSR TRAP ROR BLO RTS	PC,\$T34 66 R0 1\$ PC	;	7896
17127 17128 17129 17134 17135	; Routine Size ; Maximum stac	: 6 words k depth per invocation: 0 words		
17136 ; 7899 ! <blf f<="" td=""><td>AUL /</td><td></td><td></td><td></td></blf>	AUL /			

```
22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 
22-Dec-1980 09:21:22 PA:<NEALE>BL3ML4.BLI.2 (85)
                           BGNTST:
                             TEST NUMBER: TST 35
                             TEST NAME:
                                              SYNC DATA BUS CONIINUITY /READ PATH
                             TEST DESCRIPTION:
                                    TEST THE CONITNUITY OF THE SYNCHRONOUS MODULE READ DATA BUS BY:
                                     1. VIA MBUS WRITE FUNCTION WRITE ONES INTO THE GOOD BLOCK.
                                    2. VIA MBUS READ FUNCTION READ THE GOOD BLOCK FOR ONES.
                                     3. REPEAT WITH COMPLIMENT DATA PATTERN.
                             IMPLICIT INPUTS:
                                     A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITE
                                    FUNCTION ARE FOUND.
                          local
                               DODU_FLG,
TST_PAT,
BG_PAT;
17166 :
17167 :
                                                                                      !DROP UNIT FLAG
17168
                                                                                      !BACKGROUND PATTERN
17169
17170
                          DODU_FLG = ZERO;
17171
                           TST_PAT = ONES:
                          BG PAT = ZEROES:
                           incr TWICE from 0 to 1 do
                                                                                     !REPEAT LOOP TWICE
                               begin
CLR_MBUS;
BAI = ONE;
IO_BUF = .TST_PAT;
GD_BLK_XFER ();
MLCS1 = write;
17177
                                                                                      SET ON FIRST IO BUF ADRS
17178
17179
17180
                                                                                      !FIRST IO_BUF ADRS GET TST PAT
!SET UP A GOOD BLOCK XFERR
!DO A WRITE FUNCTION (WRITES THE TST_PAT)
                                TIME_OUT_LOOP;
                               BGNSUB:
                               CLR MBUS:
                               incr ID_CNT from 0 to 255 do
                                                                                      !LOAD IO_BUF WITH BG PAT
                                  10_BUF [.10_CNT] = .BG_PAT;
                               GD_BLK_XFER ();
MLCS1 = read;
                                                                                      SET UP A GOOD BLOCK XFERR
                                                                                      !DO A READ FUNCTION (READ THE TST PAT)
17190
                               TIME_OUT_LOOP;
17191
17192 :
                               incr IO_CNT from 0 to 255 do
                                                                                     !READ THE IO_BUF FOR TEST PATTERN
```

```
MACRO M1113 22-DEC-80 12:16 PAGE 382
CZMLABO ML-11 LOGIC TEST
MISCELLANEOUS CODING SECTION
                                                                                                                                                                                                                     SEQ 0369
    17194 :ML4
17195 :
17196 :
17197 :
17198 :
17199 :
17200 :
17201 :
                                                                                                                             22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                      TOPS-20 Bliss-16 V2(212)
                                                                                                                                                                      PA: <NEALE>BL3ML4.BL1.2 (85)
                                                       if .IO_BUF [.IO_CNT] neg .TST_PAT
                                                                                                                         !SEE IF IO_BUF WORD EQLS TST PAT
                                                             !ERROR AND SET DODU_FLG IF NEQ
ERROF (87, SYNC, DUMPER);
PRINTB (SEV_FMT, WRD 24, WRD 25, WRD 10, WRD 12, WRD 23, FNC 6, WRD 19);
PRINTB (FMT_2, .TST_PAT, .IO_BUF [.IO_CNT], (.TST_PAT xor .IO_BUF [.IO_CNT]));
DODU_FLG = ONE;
                                                              end:
                                                ENDSUB:
                                                if .DODU_FLG IS_SET
                                                                                                                  !DROP THIS UNIT IF DODU FLG IS SET
                                                       begin
DODU (.ML_LUN);
                                                       DOCLN;
                                                       end:
                                                TST_PAT = not .TST_PAT;
BG_PAT = not .BG_PAT;
                                                                                                                          !COMPLIMENT TST_PAT
                                                                                                                         ! COMPLIMENT BG_PAT AND REPEAT
                                                end:
                                         ENDTST:
                           004167
005046
012701
005004
005005
152777
                                         122430
                                                                    $T35:
                                                                                                R1,$SAVE5
-(SP)
                                                                                                                                                           DODU.FLG
                                         177777
                                                                                                                                                        : *,TST.PAT
                                                                                                 #-1,R1
                                                                                   CLR
                                                                                                                                                           BG.PAT
                                                                                   CLR
                                                                                                                                                        : TWICE
                                         000040
132070
177770
000007
131464
                                                                                                #40,aML.REG+40
ML.DUT.R3
#177770,R3
#7,aML.REG+40
                                                       131506 1$:
                                                                                   BISB
                           016703
042703
142777
150377
152777
010167
004767
105777
100375
104402
152777
016703
042703
                                                                                   MOV
                                                                                   BIC
                                                       131470
                                                                                   BICB
                                                                                   BISB
BISB
                                                                                                 R3, aML.REG+40
#10, aML.REG+40
                                         000010
130036
134314
000061
131444
                                                       131456
                                                                                                 R1,10.BUF
                                                                                   MOV
                                                                                                                                                           TST.PAT,*
                                                                                                PC.GD.BLK.XFER
#61.aML.REG
aML.REG+50
2$
                                                                                   JSR
                                                       131400
                                                                                   MOV
                                                                     25:
                                                                                   TSTB
                                                                                   BPL
TRAP
                                                                                                #40.aML.REG+40
ML.DUT.R3
#177770,R3
                                                       131422
                                                                                   BISB
```

MOV

LABO ML-11 LOGI CELLANEOUS CODI 7249			14		22-04	SEQ 0370
7250 7251		:			22-06	ec-1980 09:24:31 TOPS ec-1980 09:21:22 PA:<
7252 061602 14 7253 061610 11 7254 061614 00	60377 000007 50377 131400	131404	BICB	#7,aML.REG+40		
7254 061614 00 7255 061616 01	05002 10203	45	BICB BISB CLR E: MOV	#7.aML.REG+40 R3.aML.REG+40 R2 R2.R3 R3	: IO.CNT ,*	7944 7945
7256 061620 00 7257 061622 01	06303 10463 011600		MOV	R4,10.80F(R3)		
7259 061630 06	20227 000377		INC CMP BLE JSR	R2 R2,#377	: BG.PAT,* : IO.CNT : IO.CNT,*	7944
261 061656 00	34220 12777 000071 05777 131350	131304	JSR MOV	PC .GD.BLK.XFER	:	7947 7948
7263 061650 10 7264 061654 10	05777 131350 00375	55	: TSTB	aML.REG+50		1740
7265 061656 00 7266 061660 01)5002 10203	65	BPL CLR MOV	PC.GD.BLK.XFER #71, aml.REG aml.REG+50 5\$ R2 R2,R3	: IO.CNT .*	7951 7953
7266 061660 01 7267 061662 00 7268 061664 06 7269 061670 02	06303 52703 011600		ASL	R3 #10.BUF,R3 (R3),R1		
770 061672 00	(1301)1454 V455		CMP BEQ	(R3) ,R1 7\$; *,TST.PAT	705
272 061676 00 273 061700 01	0127 10526		. WORD	7\$ 55 127 SYNC		7956
7272 061676 00 7273 061700 01 7274 061702 02 7275 061704 01 7276 061710 01 7278 061714 01 7278 061720 01 7279 061734 01 7281 061734 01	24052 12746 006506 12746 010004		BEQ TRAP .WORD .WORD .WORD MOV MOV	DUMPER WARD.19,-(SP) WFNC.6,-(SP)		7957
76 061710 01 77 061714 01	2746 010004 2746 006544		MUY	MURD 25 - (SP)		
78 061720 01 79 061724 01	2746 006426 12746 006406		MOV	MARD.12,-(SP) MARD.10,-(SP)		
81 061734 01 282 061740 01	2746 006552		MOV MOV MOV	MURD.25,-(SP) MURD.24,-(SP) MSEV.FMT,-(SP) M10,-(SP) SP,R0 14		
283 061744 01 284 061750 01	2746 006120 12746 000010 10600		MOV	#10,-(SP) SP.RO	; SP,*	
285 061752 10 286 061754 01	1316		MOV TRAP MOV	14 (R3) (SP)		7958
287 061736 01 288 061760 04	0146 6616 000002 0166 000002		BIC	R1,-(SP) 2(SP),(SP)	; TST.PAT,*	
290 061770 05	7 2 / 4		BIS	(SP)+, (SP)	; TST.PAT,*	
7282 061740 017283 061744 017284 061750 017285 061752 107287 061756 017288 061760 047289 061770 051772 017292 061774 017293 061776 017294 062002 017296 062010 107297 062012 017298 062024 07298 062024 07300 062026 027301 062032 007303 062036 00730 062036 00730 062036 00730 007300 062036 007300 062036 00730 007300 062036 00730 007300 062036 00730 00730 007300 062036 00730 0	0146 12746 004266 12746 000004 10600 14414 12766 000001 132706 000032 15202 10227 000377 13712 14467 16000		MOV MOV BIC BIS MOV MOV MOV MOV TRAP MOV ADD INC CMP BLE TRAP ROR	(R3),(SP) R1,-(SP) 2(SP),(SP) R1,2(SP) (SP)+,(SP) (R3),-(SP) R1,-(SP) WFMT.2,-(SP) WFMT.2,-(SP) SP,R0 14	; TST.PAT,*	
7294 062002 01 7295 062006 01	2746 004266 2746 000004 0600		MOV	#4,-(SP) SP,R0	; SP,*	
7296 062010 10 7297 062012 01	2766 000001 2706 000032	000032	MOV	14 #1,32(SP)	; *,DODU.FLG	7959
299 062024 00 299 062024 00	2706 000032)5202 20227 000377	75	: INC	#1,32(SP) #32,SP R2 R2,#377 6\$ 67 R0	IO.CNT .*	7959 7955 7951
7301 062032 00	20227 000377 33712 4467		BLE	6\$ 67	; 10.CN1,*	7046
7303 062036 00	6000		ROR	RO		7960

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SEC				SEQ 0371
17305 17306	iML4		2	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
17308 062040 103650 17309 062042 021627	000001	BLO 3\$ CMP (SP),#1 BNE 8\$; DODU.FLG,*	7964
17310 062046 001004	131524	BNE 8\$ MOV ML.LUN.RO	;	7967
17312 062054 104451 17313 062056 104444		MOV ML.LUN,RO TRAP 51 TRAP 44		
17314 062060 005101 17315 062062 005104	8\$:	COM R1 COM R4 INC R5	: TST.PAT : BG.PAT	7971 7972 7933
17316 062064 005205 17317 062066 020527	000001	CMP R5.#1	; TWICE,*	7933
17319 062072 003602 17319 062074 005726 17320 062076 000207		BLE 1\$ TST (SP)+ RTS PC	:	7898
17321 17322	; Routin			
17323 17328	; Maximu	me Size: 135 words mm stack depth per invocation:	20 words	
17333				
17337 062100 17338 062100 004767	177356 135::	JSR PC.\$T35 TRAP 66	;	7973
17338 062100 004767 17339 062104 104466 17340 062106 006000 17341 062110 103773 17342 062112 000207		ROR RO		
17342 062112 000207 17343		BLO 1\$ RTS PC		
17344	Routin	me Size: 6 words mm stack depth per invocation:	O woods	
17345 17350 17351	, ridX I/IIC	an stack depth per invocation:	o words	
17351 17352 ; 7976	! <blf page=""></blf>			

TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (86)

17354 :ML4 17355 17356 17357 17358 17359 17360 17361 17362 17363 17364 17365 17366 17367 17368 17369 17370 17370 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 BGNTST: ! TEST NUMBER: TST 36 TEST NAME: RAM BUS ADRS COUNTER TEST /WRITE PATH TEST DESCRIPTION: TEST ABILITY OF THE RAM BUS ADDRESS COUNTERS TO LOAD/UNLOAD THE SKIP RAM DURING WRITE FUNCTIONS BY: 1. LOADING A REPEATING COUNT OF 0 TO 63 INTO THE NIBBLES OF THE FIRST 64 WORDS OF THE IO_BUF. 17372 17373 17374 17375 17376 2. VIA MBUS WRITE FUNCTION WRITE THE CONTENTS OF THE IO_BUF INTO THE GOOD BLOCK. 17377 17378 17379 17380 17381 17382 17383 17384 17385 17386 17387 17388 17389 17390 17391 17391 17393 17394 17395 17396 17397 17396 17397 17398 17398 17399 17400 17401 17402 17403 17404 17405 17406 8000 8001 8002 8003 3. VIA DAT_DM READ GOOD NIBBLES IN THE GOOD BLOCK FOR THE UNBROKEN COUNT OF 0 TO 63. 8004 8005 ONCE A BAD NIBBLE IS ENCOUNTERED MASK THAT NIBBLE FROM FURTHER 8006 READS. 8007 8008 8009 4. REPEAT READING NIBBLES UNTIL 113 WORDS ARE READ OR ALL 10 NIBBLES 8010 8011 8012 8013 8014 8015 8016 8017 8018 8019 8020 8021 8022 8023 8024 8025 8026 8027 ARE MASKED. IMPLICIT INPUTS: PD_TEMP A BIT VECTOR OF 16 BITS WHERE THE READ PROM DATA IS STORED AND ACCESSED FROM. IO BUF A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITE FUNCTION ARE FOUND. 17407 17408 local DODU_FLG,

!DROP UNIT FLAG

```
17410 :ML4
17411 :
17412
17413 :
                                                                                                   22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 
22-Dec-1980 09:21:22 PA:<NEALE>BL3ML4.BLI.2 (86)
                                    WRD_CNT,
NIB_IGNORE : bitvector [16],
                                                                                                   WORD COUNT
                                    ERR FLG,
BAD NIB CNT,
PASS CNT,
NIB PAT;
                                                                                                   ERROR FLAG
                                                                                                   PASS COUNT
                                                                                                   !NIBBLE PATTERN
                              DODU_FLG = ZERO;
                              WRD_CNT = ZERO:
                      8039 incr CNT from 0 to 63 do 8040
                                                                                                !LOAD 64 WORDS WITH REPEATING COUNTS OF 0-63
                                     incr PAT_CNT from -1 to 11 by 4 do !LOAD NIBBLES IN WORD WITH REPEATING COUNTS OF 0-63
                                          begin
(IO_BUF [.WRD_CNT])<0, 4> = .PAT_CNT + 1; !LOAD FIRST NIBE
(IO_BUF [.WRD_CNT])<4, 4> = .PAT_CNT + 2; !LOAD SECOND NIE
(IO_BUF [.WRD_CNT])<8, 4> = .PAT_CNT + 3; !LOAD THIRD NIBE
(IO_BUF [.WRD_CNT])<12, 4> = .PAT_CNT + 4; !LOAD FORTH NIBE
WRD_CNT = .WRD_CNT + 1; !INCREMENT TO NEXT WORD
                                                                                                               !LOAD FIRST NIBBLE IN WORD
                                                                                                               !LOAD SECOND NIBBLE IN WORD
                                                                                                               LOAD THIRD NIBBLE IN WORD
17432
17433
17434
17435
17436
17437
17438
17439
                              BGNSUB:
                              CLR_MBUS;
GD_BLK_XFER ();
MLCS1 = write;
                                                                                                   SET UP A GOOD BLOCK XFERR
                                                                                                   !DO A WRITE FUNCTION
                              TIME OUT LOOP;
                              CLR_MBUS;
17440 :
                              NIB_IGNORE = ZEROES;
17441
17442
17443
17444
17445
17446
17447
17448
17449
17450
17451
17452
17453
17454
17454
17456
17457
                              PASS CNT = ZEROES;
NIB PAT = ZEROES;
BAD NIB CNT = ZEROES;
                              DAT_DM_XFER ();
MLCS1 = read;
                                                                                                   SET UP A DATA DIAG MODE AT THE GOOD BLOCK
                                                                                                   !DO A READ FUNCTION
                              DELAY (ONE_US);
                                                                                                   !LOOP UNTIL THE BLOCK IS READ OR 9 BAD NIBBLES FOUND
                                    begin
                                    PD TEMP = .MLPD;
DAT CLK = ONE;
DELAY (ONE_US);
                                                                                                   !GET THE PROM DATA
                                                                                                   !CLOCK OUT THE DATA WORD
                                    RD_LNG_WRD;
                                                                                                  !READ THE DATA DIAG REGISTERS
                                    incr NIB PTR from 0 to 8 do
                                                                                                 !LOOK AT 9 NIBBLES
                                          begin
                                          17460 :
                                               begin
                                               if .NIB_IGNORE [.NIB_PTR] IS_NOT_SET !SEE IF THIS NIBBLE FOUND BAD BEFORE
17463
                                                   begin
```

```
MACRO M1113 22-DEC-80 12:16 PAGE 387
CZMLABO ML-11 LOGIC TEST
MISCELLANEOUS CODING SECTION
                                                                                                                                                                               SEQ 0374
                                                                                                      22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                       TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (86)
                                                        TST_LNG_WRD (.NIB_PTR, .NIB_PAT, ERR_FLG); !TEST THE NIBBLE IF NEVER FOUND BAD
                                                         if .ERR_FLG IS_SET
                                                                                                      !SEE IF TEST FOUND AN ERROR
                                                         then
                                                              ERROR AND SET DODU_FLG IS SET

PRINTB (SIX_FMT, FNC_18, WRD_50, WRD_10, WRD_12, FNC_5, WRD_19);

DODU_FLG = ONE;
                                                                                                      !ERROR AND SET DODU_FLG IS SET
                                                              end
                                                         end
                                                   end
                                             else
                                                                                                      !THIS NIBBLE IS BAD
                                                  begin
NIB_IGNORE [.NIB_PTR] = ONE;
BAD_NIB_CNT = .BAD_NIB_CNT + 1;
                                                                                                      !SET THIS NIBBLE NIB_IGNORE FLAG
                                                                                                      !INCREMENT BAD NIB COUNT
                                             NIB_PAT = .NIB_PAT + 1;
                                                                                                     !INCREMENT NIB PAT
                         8101
   7490
   17491
                                       PASS_CNT = .PASS_CNT + 1;
                                                                                                     !INCREMENT PASS COUNT
   17492
   17493
                                 until (.PASS_CNT eql 113) or (.BAD_NIB_CNT eql 9); !REPEAT UNTIL COMPLETE
  17494
17495
17496
17497
                                 ENDSUB:
  17497

17498

17499

17500

17501

17502

17503

17504

17508

17512 062114

17513 062120

17514 062124

17515 062126

17516 062130

17517 062132

17518 062136

17519 062140
                                  if .DODU_FLG IS_SET
                                                                                                     !DROP THIS UNIT IF DODU_FLG SET
                                       begin
DODU (.ML_LUN);
                                       DOCLN;
                                       end;
                                 ENDTST:
                                 121776 000006
                                                        $T36:
                                                                               R1,$SAVE5
                                                                                                                                                                                     7975
                                                                    SUB
                                                                               #6,SP
                                                                               -(SP)
                                                                                                                                                                                     8036
8037
8039
                                                                                                                              DODU.FLG
                                                                   CLR
CLR
MOV
MOV
                                                                               R3
R2
                                                                                                                               WRD.CNT
                                                                                                                              CNT
                                                                               #-1,R0
R3,R1
                                  177777
                                                                                                                            : *,PAT.CNT
                                                                                                                             : WRD.CNT, *
```

SEQ 03								ABO ML-11 LI ELLANEOUS C
22-Dec-1980 09:24:31 T 22-Dec-1980 09:21:22 P				:ML4				521 522 853
	; PAT.CNT,*	#10.BUF,R1 R0,R5	ADD MOV INC			011600	062701 010005 005205	524 062142 525 062146 536 063146
		#177760.R5 #17,(R1) R5,(R1)	BIC BICB BISB			177760 000017	042705 142711 150511	527 062152 528 062156 529 062162
8	; PAT.CNT,*	#10.BUF,R1 R0,R5 R5 #177760,R5 #17,(R1) R5,(R1) R0,R5 #2,R5 R5 R5 R5 R5 R5 #177417,R5 #360,(R1) R0,R5 #3,R5 R5 #170377,R5 #170377,R5 #17040,(R1) R5,(R1) R0,R5 #4,R5 R5 R	BIC BISB MOV ADD ASL ASL ASL BIC BICB BISB MOV ADD SWAB BIC BIS MOV ADD SWAB ASL ASL ASL ASL ASL ASL ASL ASL ASL ASL			000002	010005 062705 006305 006305	530 062164 531 062166 532 062172 533 062174
		R5 R5 #177417,R5 #360,(R1)	ASL BIC BICB			177417 000360	006305 006305 042705 142711	534 062176 535 062200 536 062202 537 062206
8	; PAT.CNT,*	R5,(R1) R0,R5 #3,R5	MOV ADD			000003	010005 062705	539 062214 539 062214 540 062216
		#170377,R5 #7400,(R1) R5,(R1)	BIC BIC BIS			170377 007400	042705 042711 050511	\$42 082224 \$43 082230 \$44 082234
8	; PAT.CNT,*	RO.RS #4.RS RS	MOV ADD SWAB			000004	010005 062705 000305 006305	545 062236 546 062240 547 062244 548 062244
		R5 R5 R5	ASL ASL ASL				006305 006305 006305	549 062250 550 062252 551 062254
		#7777,R5 #170000,(R1) R5,(R1)	BIC			170000	042705 042711 050511	552 062256 553 062262 554 062266
88	; WRD.CNT ; *,PAT.CNT ; PAT.CNT,*	#4,R0 R0,#13	ADD			000004 000013	062700 020027 003715	556 062272 557 062276 558 062302
8	; CNT ,*	R2 R2,#77 1\$	BLE INC CMP BLE			000077	005202 020227 003707	559 062304 560 062306 561 062312
88	:	2	BLE TRAP BISB MOV	3\$:	130670	000040 131252	050511 005203 062700 020027 003715 005202 020227 003707 104402 152777 016702 042702 142777 100375 152777 016702 042702 142777	554 062266 555 062270 556 062272 557 062276 558 062304 558 062304 560 062304 561 062312 562 062314 563 062314 563 062334 565 062334 566 062334 567 062366 571 062366 572 062366 573 062374 574 062400 575 062404
		#40,aML.REG+40 ML.DUT.R2 #177770,R2 #7,aML.REG+40 R2,aML.REG+40 PC.GD.BLK.XFER #61,aML.REG aML.REG	BIC BICB BISB		130652	000040 131252 177770 000007 130646 133510 000061 130640	142777	
86	;		JSR MOV TSTB BPL	4\$:	130574		012777 105777 100375	569 062352 570 062360 571 062364
80	•	#40, aml.REG+40 ML.DUT, R2 #177770, R2 #7, aml.REG+40	BISB MOV BIC BICB		130620	000040 131202 177770 000007	152777 016702 042702	572 062366 573 062374 574 062400

17577	OGIC TES ODING SE			:ML4				2-Dec-1980 0	SEG 0 · 24 · 31	
17578 17579				;			2	22-Dec-1980 09 22-Dec-1980 09	9:21:22	TOPS PA:<
17580 062412 17581 062416 17582 062422 17583 062424 17584 062426	150277 005066 005004 005003 005005	130576 000004			BISB CLR CLR CLR CLR JSR	R2, aml.REG+40 4(SP) R4 R3 R5	: NIB.IGNORE : PASS.CNT : NIB.PAT : BAD.NIB.CNT			8056 8057 8058 8059 8060 8061 8062
17585 062430 17586 062434 17587 062442 17588 062446 17589 062450 17590 062454 17591 062456 17592 062462 17593 062464 17594 062466 17596 062472 17596 062472 17597 062500 17598 062516 17601 062520 17602 062526 17603 062526 17604 062536 17605 062532	004767 012777 012701	131526 000071 000001	130512		MOV	PC.DAT.DM.XFER #71,aML.REG #1,R1	*,\$\$TMP2			8060 8061 8062
17588 062446 17589 062450 17590 063454	001411 016702 001404	117442		5\$:	BEQ MOV BEQ	L\$PLY,R2	; *,\$\$TMP1			
7591 062456 17592 062462	005066 005302	000006		6\$:	CLR DEC BNE	6(SP) R2	; SSTMP1			
7594 062466 17595 062470	005301 000766			7\$:	DEC	R2 6\$ R1 5\$; SSTMP2			
17596 062472 17597 062500 17598 062506	017767 152777 012701 001411 016702 001404	130706 000020 000001	130420 130566	8\$:	MOV BISB MOV	aml.REG+230,PD.TEMP #20,aml.REG+120 #1,R1	* * * * * * * * * * * * * * * * * * *			8066 8067 8068
17598 062506 17599 062512 17600 062514	001411	117376		9\$:	BEQ MOV BEQ	L\$DLY,R2	; *,\$\$TMP1			0000
7602 062522 7603 062526	005066 005302	000006		10\$:	CLR DEC BNE DEC	11\$ 6(SP) R2	; SSTMP ; SSTMP1			
7604 062530 7605 062532 7606 062534	001374 005301 000766			11\$:	DEC BR	R2 10\$ R1 9\$; SSTMP2			
7608 062544 7609 062552	017767 017767 017767	130602 130604 130556	126406 126402 126376	12\$:	MOV MOV	aML.REG+170,D1.TEMP aML.REG+200,D2.TEMP aML.REG+160,E2.TEMP				
17610 062560 17611 062562 17612 062564 17613 062566	005001 010100 006200 006200			13\$:	MOV CLR MOV ASR ASR ASR MOV ADD MOV ADD MOV BIC MOV CLR JSR	9\$ aML.REG+170.D1.TEMP aML.REG+200.D2.TEMP aML.REG+160.E2.TEMP R1 R1.R0 R0 R1 R1.R2 SP.R2 R0,R2 R0,-(SP) MPD.TEMP,(SP) R1(SP) M177770,(SP) M1,-(SP) -(SP) PC.BL\$GT2 M10.SP R0	: NIB.PTR : NIB.PTR,*			8071 8074
7614 062570 17615 062572 17616 062576	012702 060602	000004			MOV ADD	#4.R2 SP.R2	: NIB.IGNORE,*			8078
17618 062602 17619 062604	010046	013120			MOV	RU,-(SP) MPD.TEMP.(SP)	:			8074
17619 062604 17620 062610 17621 062612 17622 062616 17623 062622	010146 042716 012746	177770 000001			BIC	R1,-(SP) #177770,(SP)	; NIB.PTR,*			
7623 062622 7624 062624 7625 062630 7626 062634	005046 004767 062706 005700	120310 000010			CLR JSR ADD TST	-(\$P) PC_BL\$GT2 #10,SP R0 15\$				
7627 062636 17628 062640 17629 062642 17630 062644 17631 062650	001066 010246 010146 042716 012746	177770 000001			ADD TST BNE MOV MOV BIC MOV	15\$ R2,-(SP) R1,-(SP) #177770,(SP) #1,-(SP)	: NIB.PTR,*			8078

17633				:ML4				22-0-0-1080 00-24-31	TOPS
17634				:				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
17636 062654 17637 062656 17638 062662 17639 062666	005046 004767 062706 005700	120256 000010			CLR JSR ADD TST	-(SP) PC_BL\$GT2 #10_SP R0			
17640 062670 17641 062672 17642 062674 17643 062676	001065 010146 010346 012746	000010			MOV MOV MOV	16\$ R1,-(SP) R3,-(SP) #10,-(SP)	; NIB.PTR.* ; NIB.PAT.*		8081
17644 062702	060616				ADD JSR	SP. (SP) PC.TST.LNG.WRD	; ERR.FLG,*		
17646 062710	026627 (133236 000010	000001		CMP	10(SP) ,#1 14\$; ERR.FLG,*		8083
17648 062720 17649 062722 17650 062724	104455 000130 010570 024052				TRAP .WORD .WORD .WORD MOV	130 ARR.DAT			8086
17652 062730 17653 062734 17654 062740 17655 062744 17656 062750 17657 062754 17658 062760 17659 062764 17660 062770 17661 062772 17662 062774 17663 063002	012746 (012746 (012746 (012746 (006506 007774 006426 006406 007046 010172 006100			MOV MOV MOV MOV MOV	MMRD.19,-(SP) MFNC.5,-(SP) MMRD.12,-(SP) MMRD.10,-(SP) MMRD.50,-(SP) MFNC.18,-(SP) MSIX.FMT,-(SP) SP.RO			8087
17660 062770	010600	00007			MOV	SP,RO	; SP,*		
17662 0627774 17663 063002 17664 063006 17665 063012	012766 062706 062706 000414	000001 000020 000006	000026	14\$:	MOV ADD ADD	#1,26(SP) #20,SP #6,SP	*,DODU.FLG		8088 8085 8080 8074 8096
17666 063014 17667 063016 17668 063020 17669 063024 17670 063030 17671 063032 17672 063036 17673 063040 17674 063044 17675 063046	010246 010146 042716 012746	177770 000001		15\$:	MOV MOV BIC MOV	R2,-(SP) R1,-(SP) #177770,(SP) #1,-(SP) (SP),-(SP) PC,BL\$PU2 R5	NIB.PTR.*		8096
7670 063030	004767 1	120340			JSR	PC,BL\$PU2			
7673 063040	062706	000010			ADD	#10.SP	: BAD.NIB.CNT		8097 8095
7675 063046 17676 063050	005203 005201 020127 003663	000010		165:	INC INC CMP	R5 R1 R1 #10	; NIB.PAT ; NIB.PTR ; NIB.PTR,*		8097 8095 8100 8071
17678 063056 17679 063060	005204	000161			INC	R4	: PASS.CNT		8103 8105
17680 063064	001405				BEQ	17\$ P5 #11			0103
17682 063072	001402				BEQ	17\$, DAD. NID. CNI, -		
17684 063100 17685 063102 17686 063104	104467 006000 103002	111312		17\$:	TRAP	67 R0			
17666 063014 17667 063016 17668 063020 17669 063024 17670 063030 17671 063032 17672 063036 17673 063040 17674 063044 17675 063046 17676 063056 17679 063056 17679 063056 17681 063066 17681 063066 17682 063072 17683 063074 17684 063100 17685 063104 17686 063104	005203 005201 020127 003642 005204 020427 001405 020527 001402 000167 104467 006000 103002			16\$:	MOV JSR INC ADD INC INC CMP BLE INC CMP BEQ CMP BEQ JMP TRAP ROR BHIS JMP	#10.SP R3 R1 R1.#10 13\$ R4 R4.#161 17\$ R5.#11 17\$ 8\$ 67 R0 18\$: NIB.PAT : NIB.PTR : NIB.PTR,* : PASS.CNT : PASS.CNT,* : BAD.NIB.CNT,*		

CZMLABO ML-11 LO	GIC TES	T	MACRO M1113 22	P-DEC-80	12:16 PAGE	B 14 391					
MISCELLANEOUS CO	DING SE	CTION								SEQ 0	378
17689 17690			ML4						22-Dec-1980 22-Dec-1980	09:24:31 09:21:22	TOPS PA:<
17692 063112	021627	000001	18\$:	CMP	(SP),#1 19\$; DODU.FLG.*			8109
17693 063116 17694 063120 17695 063124	016700 104451	130454		MOV TRAP TRAP	ML.LUN,RO			:			8112
17697 063130 17698 063134 17699 17700	062706 000207	000010	19\$:	ADD RTS	#10.SP PC			:			7975
17700 17701 17706 17707			; Routi ; Maxim	ine Size: num stack	265 words death per	invocation:	21 words				
17718 063144	004767 104466 006000 103773 000207	176752	T36:: 1\$:	JSR TRAP ROR BLO RTS	PC,\$T36 66 RO 1\$ PC			:			8114
17720 063150 17721 17722 17723 17728 17729 17730 ;	8117	! <blf pa<="" td=""><td>; Maxim</td><td>ne Size: num stack</td><td>6 words depth per</td><td>invocation:</td><td>0 words</td><td></td><td></td><td></td><td></td></blf>	; Maxim	ne Size: num stack	6 words depth per	invocation:	0 words				

17732 :ML4 17733 : 17734 : 17735 : 17736 : 17737 : 17738 : 17740 : 17741 : 17742 : 17743 : 17745 : 17745 : 17746 : 17747 : 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BL1.2 (87) 8118 8119 8120 8121 8123 8124 8125 8126 8127 8130 8131 8133 8133 8135 8137 BGNTST: ! TEST NUMBER: TST 37 TEST NAME: RAM BUS ADRS COUNTER TEST /READ PATH TEST DESCRIPTION: TEST ABILITY OF RAM/BUS ADRS COUNTERS TO LOAD/UNLOAD THE SKIP RAM DURING READ FUNCTIONS BY: 1. LOADING A REPEATING COUNT OF 0 TO 63 INTO THE NIBBLES OF THE FIRST 64 WORDS OF THE IO_BUF. 2. VIA MBUS WRITE FUNCTION WRITE 17755 THE CONTENTS OF THE 10 BUF 17756 INTO THE GOOD BLOCK. 17757 17758 17759 17760 3. CLEAR OUT THE IO BUF 4. VIA MBUS READ FUNCTION READ 17761 17762 17763 THE GOOD BLOCK FOR THE REPEATING COUNT OF 0 TO 63. 8146 8147 8148 8149 8150 8151 8152 8153 8154 8156 8157 8158 8159 8160 17764 17765 ! IMPLICIT INPUTS: 17766 : 17767 : IO_BUF 17768 17769 A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITE FUNCTION ARE FOUND. 17770 17771 : 17772 17773 17774 local 17775 DODU_FLG. !DROP UNIT FLAG WRD CNT, PAT INC, SIZ EXP, POS EXP, TEMP; 17776 : !WORD COUNT 17777 : PATTERN INCREMENT 8161 8162 8163 8164 8165 8166 8167 SIZE EXPRESSION
POSITIONAL EXPRESSION
TEMPORARY STORAGE LOCATION 17778 17779 17780 : 17781 : 17782 : 17783 : 17784 : 17785 : 17786 ; CLR_MBUS; DODU_FLG = ZERO; WRD_CNT = ZERO; 8168 8169 incr COUNT from 0 to 63 do !LOAD 64 WORDS WITH REPEATING COUNTS OF 0-63

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TOPS-20 Bliss-16 V2(212)
17788 :ML4
17789 :
17790
17791 :
                                                                                                        22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                           PA: <NEALE>BL3ML4.BL1.2 (87)
                                    incr PAT_CNT from -1 to 11 by 4 do
                                                                                                     !LOAD NIBBLES IN WORD WITH REPEATING COUNT OF 0-63
                                            begin
(10_BUF
                                           (IO BUF [.WRD_CNT])<0, 4> = .PAT_CNT + 1; !LOAD FIRST NIBE
(IO BUF [.WRD_CNT])<4, 4> = .PAT_CNT + 2; !LOAD SECOND NIE
(IO BUF [.WRD_CNT])<8, 4> = .PAT_CNT + 3; !LOAD THIRD NIBE
(IO BUF [.WRD_CNT])<12, 4> = .PAT_CNT + 4; !LOAD FORTH NIBE
WRD_CNT = .WRD_CNT + 1; !INCREMENT TO NEXT WORD
                                                                                                                    !LOAD FIRST NIBBLE IN WORD
                                                                                                                    LOAD SECOND NIBBLE IN WORD
                                                                                                                    LOAD THIRD NIBBLE IN WORD
                                            end:
                       8180 GD_BLK_XFER ();
8181 MLCS1 = write;
                                                                                                        SET UP A GOOD BLOCK XFERR
                                                                                                        !DO A WRITE FUNCTION
                      8181 MLCS1 = write;
8182 TIME OUT_LOOP;
8183 BGNSUB;
8184
8185 incr IO_CNT fro
8186 IO_BUF [.IO
8187
8188 CLR_MBUS;
8189 GD_BLK_XFER ();
8190 MLCS1 = read;
8191 TIME OUT_LOOP;
8192 CLR_MBUS;
8193 SIZ_EXP = 4;
                               incr IO CNT from 0 to 255 do
                                                                                                     !CLEAR OUT IO_BUF
                                10_BUF [.10_CNT] = ZEROES;
                               CLR_MBUS;
GD_BLK_XFER ();
MLCS1 = read;
                                                                                                      SET UP A GOOD BLOCK XFERR
                                                                                                        !DO A READ FUNCTION
                       8193
8194
8195
8196
8197
                               SIZ_EXP = 4;
WRD_CNT = 0;
                                                                                                       !FIELD SIZE FOR NIBBLES ALWAYS 4 BITS
                               incr COUNT from 0 to 63 do
                                                                                                       !READ 64 WORDS IN IO BUF
                                      incr PAT_CNT from -1 to 11 by 4 do
                                                                                                        !READ REPEATING COUNTS OF 0-63
                                           begin
POS_EXP = ZERO;
PAT_INC = ONE;
                                                                                                        !FIELD SELECTOR SELECTS THE FOUR NIBBLES
                                            TEMP = .10_BUF [.WRD_CNT];
                                                                                                      !GET A WORD OUT OF IO_BUF
                                           incr CNT from 0 to 3 do
                                                                                                       !READ THE FOUR NIBBLES IN WORD
                                                  begin
                                                 if .TEMP<.POS_EXP, .SIZ_EXP> neg (.PAT_CNT + .PAT_INC)
                                                                                                       !COMPARE NIBBLE WITH RESPECTIVE 0 -63 CNT
                                                  then
                                                       begin !ERROR AND SET DODU_FLG IF NEQ ERROF (89, ARR_DAT, DUMPER); PRINTB (SIX_FMT, FNC_18, WRD_50, WRD_10, WRD_12, FNC_6, WRD_19); DODU_FLG = ONE;
                                                 POS_EXP = .POS_EXP + 4;
PAT_INC = .PAT_INC + 1;
                                                                                                        !POINT TO THE NEXT NIBBLE IN WORD
                                                                                                     !INCREMENT THE 0-63 COUNT
                                                  end:
17841 :
17842 :
                                           WRD_CNT = .WRC_CNT + 1; !GET THE NEXT IO_BUF WORD
                                            end:
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17846 17846 17846 17846 17846 17846 17846 17846 17846 17846 17846 17846 17846 17846 17846 17846 18225 18225 18225 18225 18226 1822	MISCELLANEOUS CODING SEC	.110N			SEQ 0381
17874 063222 005002 17875 063224 012703 177777 1\$: MOV	17844 :ML4 17845 :			22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (8</neale>	7)
17874 063222 005002 17875 063224 012703 177777 1\$: MOV	17847 : 8222 17848 : 8223 17849 : 8224				
17874 063222 005002 17875 063224 012703 177777 1\$: MOV	17850 : 8225 17851 : 8226 17852 : 8227 17853 : 8228 17854 : 8229 17855 : 8230 17856 : 8231 17857 : 8232	begin DODU (.ML_LUN); DOCLN; end;		!DROP THIS UNIT IF DODU_FLG SET	
17874 063222 005002 17875 063224 012703 177777 1\$: MOV	17861 17865 063152 004167	120740 \$T37:	JSR R1,\$SAVE5		8116
17874 063222 005002 17875 063224 012703 177777 1\$: MOV	17866 063156 162706 17867 063162 152777	000012 000040 130024	SUB #12.SP BISB #40.aML.REG+40		
17874 063222 005002 17875 063224 012703 177777 1\$: MOV	17868 063170 016705 17869 063174 042705 17870 063200 142777 17871 063206 150577	130406 177770 000007 130006	MOV ML.DUT,R5 BIC #177770,R5 BICB #7,aML.REG+40 BISB R5,aML.REG+40		
17874 063222 005002 17875 063224 012703 177777 1\$: MOV	17872 063212 005066 17873 063216 005066	000010 000002	CLR 10(SP) CLR 2(SP)	; DODU.FLG : WRD.CNT	8166 8167
17876 063230 016604 000002 28: MOV 2(SP),R4 ; WRD.CNT,* 17877 063234 006304 17878 063236 062704 011600 ADD WID.BUF,R4 17879 063242 010305 MOV R3,R5 ; PAT.CNT,* 17880 063244 005205 INC R5 17881 063246 042705 177760 BIC W177760,R5 17882 063252 142714 000017 BICB W17, (R4) 17883 063256 150514 BISB R5, (R4) 17884 063260 010305 MOV R3,R5 ; PAT.CNT,* 17885 063262 062705 000002 ADD W2,R5	17874 063222 005002	177777 15.	CLR R2	COUNT	8169
17878 063236 062704 011600 ADD #10.BUF,R4 17879 063242 010305 MOV R3,R5 ; PAT.CNT,* 17880 063244 005205 INC R5 17881 063246 042705 177760 BIC #177760,R5 17882 063252 142714 000017 BICB #17,(R4) 17883 063256 150514 BISB R5,(R4) 17884 063260 010305 MOV R3,R5 ; PAT.CNT,* 17885 063262 062705 000002 ADD #2,R5	17876 063230 016604	000002 28:	10V 2(SP),R4	; WRD.CNT,*	8173
17881 063256	17878 063236 062704	011600	ADD #10.BUF,R4 MOV R3,R5	; PAT.CNT,*	
17885 063266 150514 17886 063262 062705 000002 MOV R3,R5 ; PAT.CNT,* 17887 063262 06305 ASL R5 17888 063270 006305 ASL R5 17889 063274 006305 ASL R5 17889 063274 006305 BIC W177417,R5 17891 063302 142714 000360 BICB W360,(R4) 17892 063306 150514 BISB R5,(R4) 17893 063310 010305 MOV R3,R5 ; PAT.CNT,* 17894 063312 062705 000003 ADD W3,R5 ; PAT.CNT,* 17895 063316 000305 SWAB R5 17896 063320 042705 170377 BIC W170377,R5 17897 063324 042714 007400 BIC W170377,R5 17897 063324 042714 007400 BIC W170377,R5	17882 063252 142714	177760 000017	BIC #177760,R5 BICB #17,(R4)		
17888 063272 006305 17889 063274 006305 17890 063276 042705 177417 17891 063302 142714 000360 17892 063306 150514 17893 063310 010305 17894 063312 062705 000003 17895 063316 000305 17896 063320 042705 170377 17897 063324 042714 007400 8ICB M360, (R4) 8ISB R5, (R4) MOV R3, R5 17896 063320 042705 170377 8IC M170377, R5 17897 063324 042714 007400 BIC M7400, (R4)	17883 063256 150514 17884 063260 010305 17885 063262 062705 17886 063266 006305	000002	BISB R5, (R4) MOV R3,R5 ADD #2,R5 ASL R5	; PAT.CNT,*	8174
17889 063274 006305 17890 063276 042705 177417 BIC W177417,R5 17891 063302 142714 000360 BICB W360,(R4) 17892 063306 150514 BISB R5,(R4) 17893 063310 010305 MOV R3,R5 ; PAT.CNT,* 8175 17894 063312 062705 000003 ADD W3,R5 17895 063316 000305 SWAB R5 17896 063320 042705 170377 BIC W170377,R5 17897 063324 042714 007400 BIC W7400,(R4)	17888 063270 006305 17888 063272 006305		ASL R5 ASi. R5		
17893 063310 010305 17894 063312 062705 000003 ADD #3,R5 17895 063316 000305 SWAB R5 17896 063320 042705 170377 BIC #170377,R5 17897 063324 042714 007400 BIC #7400,(R4)	17889 063274 006305 17890 063276 042705 17891 063302 142714 17892 063306 150514	177417 000360	ASL R5 BIC #177417,R5 BICB #360,(R4)		
17896 063320 042705 170377 BIC #170377,R5 17897 063324 042714 007400 BIC #7400,(R4)	17893 063310 010305 17894 063312 062705 17895 063316 000305	000003	10V R3, R5 10D #3, R5	; PAT.CNT,*	8175
	17896 063320 042705 17897 063324 042714	170377 007400	BIC #170377,R5 BIC #7400,(R4)		

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 395	SEQ (0382
17899 17900	ML4		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
17902 063330 050514 17903 063332 010305 17904 063334 062705 000004 17905 063340 000305 17906 063342 006305 17908 063346 006305	BIS MOV ADD SWAB ASL ASL ASL BIC BIC BIS INC ADD CMP BLE INC CMP BLE JSR	R5, (R4) R3, R5 W4, R5 R5 R5 R5 R5 R5	PAT.CNT,*	8176
17909 063350 006305 17910 063352 042705 007777 17911 063356 042714 170000	ASL BIC BIC	R5 #7777,R5 #170000,(R4)		
17912 063362 050514 17913 063364 005266 000002 17914 063370 062703 000004 17915 063374 020327 000013	INC ADD CMP	#7777,R5 #170000,(R4) R5,(R4) 2(SP) #4,R3 R3,#13 2\$ R2 R2	WRD.CNT *,PAT.CNT PAT.CNT,*	8177 8171
17917 063400 003713 17917 063402 005202 17918 063404 020227 000077 17919 063410 003705	BLE INC CMP	2\$ R2 R2,#77	COUNT COUNT ,*	8169
17920 063412 004767 132444 17921 063416 012777 000061 17922 063424 105777 127574	127530 MOV 3\$: TSTB	PC.GD.BLK.XFER ; #61.aML.REG ; aML.REG+50 ;		8180 8181
17924 063432 104402 17925 063434 005003	4\$: TRAP CLR 5\$: MOV	35	IO.CNT IO.CNT,*	8182 8185 8186
17928 063442 005064 011600 17929 063446 005203 17930 063450 020327 000377	ASL CLR INC CMP	R4 10.8UF (R4) R3 R3.#377	IO.CNT IO.CNT,*	8185
17931 063454 003770 17932 063456 152777 000040 17933 063464 016704 130112 17934 063470 042704 1777770	127530 BLE BISB MOV BIC	%40, aml.REG+40 ; ML.DUT, R4 #177770, R4		8186
17932 063456 152777 000040 17933 063464 016704 130112 17934 063470 042704 177770 17935 063474 142777 000007 17936 063502 150477 127506 17937 063506 004767 132350 17938 063512 012777 000071 17939 063520 105777 127500	127512 BICB BISB JSR MOV 6\$: TSTB	5\$ #40.aml.REG+40 ML.DUT.R4 #177770,R4 #7.aml.REG+40 R4.aml.REG+40 PC.GD.BLK.XFER #71.aml.REG aml.REG+50 6\$ #40.aml.REG+40 ML.DUT.R4 #177770,R4 #7.aml.REG+40 R4.aml.REG+40 R4.aml.REG+40 R4.aml.REG+40 R4.aml.REG+40 #4.6(SP) 2(SP) R1		8189 8190
17940 063524 100375 17941 063526 152777 000040 17942 063534 016704 130042 17943 063540 042704 177770	127460 BISB MOV BIC	6\$ #40,aML.REG+40 ; ML.DUT.R4 #177770.R4		8191
17941 063526 152777 000040 17942 063534 016704 130042 17943 063540 042704 177770 17944 063544 142777 000007 17945 063552 150477 127436 17946 063556 012766 000004 17947 063564 005066 000002	127442 BICB BISB MOV CLR	#7.aml.REG+40 R4.aml.REG+40 #4.6(SP) 2(SP)	*.SIZ.EXP	8193
17932 063456 152777 000040 17933 063464 016704 130112 17934 063470 042704 177770 17935 063474 142777 000007 17936 063502 150477 127506 17937 063506 004767 132350 17938 063512 012777 000071 17939 063520 105777 127500 17940 063524 100375 17941 063526 152777 000040 17942 063534 016704 130042 17943 063540 042704 177770 17944 063544 142777 000007 17945 063556 012766 000007 17946 063556 012766 000004 17947 063564 005066 000002 17948 063572 012703 177777 17950 063576 005001 17952 063604 016604 000002 17953 063610 006304	127530 BISB MOV BIC BICB BISB JSR MOV BISB JSR MOV BIC BISB MOV BIC BISB MOV CLR CLR CLR CLR MOV MOV ASL	R1 #-1,R3 R5 #1,(SP) 2(SP),R4 R4	*,SIZ.EXP WRD.CNT COUNT *,PAT.CNT POS.EXP *,PAT.INC WRD.CNT,*	8193 8194 8196 8198 8200 8201 8202

22-Dec-1980 09:24:31 TC				:ML4				955
22-Dec-1980 09:24:31 TO 22-Dec-1980 09:21:22 PA				:				56 57
92	: *,TEMP : CNT	10.BUF (R4),4(SP) R2	MOV		000004	011600	016466	58 063612
82 82	; TEMP, * ; POS.EXP, * ; SIZ.EXP, *	4(SP),-(SP)	MOV	9\$:		000004	016646	758 063612 759 063620 760 063622 761 063626
	; SIZ.EXP,*	4(SP),-(SP) R5,-(SP) 12(SP),-(SP) -(SP)	MOV			000012	016646	
		PC_BL\$GT1	JSR			117154 000010	004767	
	; PAT.CNT,* ; PAT.INC,*	R3,R4	MOV			000010	010304	66 063646
	; PAI.INC,*	PC.BL\$GT1 #10.SP R3.R4 (SP).R4 R0.R4 10\$ 55 131 ARR.DAT DUMPER	CMP				020004	68 063652
82	:	55	BEQ TRAP .WORD .WORD .WORD				104455	70 063656
		ARR.DAT	. WORD				010570	72 063662
82		##W 17 \ 3F /	MOV			006506 010004	012746	068 063652 169 063654 170 063656 171 063660 172 063662 173 063664 174 063666 175 063672 176 063702 178 063706
		#FNC.6,-(SP) #URD.12,-(SP)	MOV			006426	012746	76 063676
		MIRD. 50, -(SP)	MOV			007046	012746	77 063702 778 063706 779 063712
		MURD.10,-(SP) MURD.50,-(SP) MFNC.18,-(SP) MSIX.FMT,-(SP) M7,-(SP)	MOV			006100 000007	012746	80 063716
	; SP,*	SP.RO 14	MOV MOV TRAP			000007	010600	81 063722 82 063726
82 82	: *,DODU.FLG	#1 30(SP)	MOV		000030	000001 000020 000004	012766	82 063726 83 063730 84 063732 85 063740 86 063744
82 82 82	* POS.EXP	#20.SP #4.R5 (SP) R2 R2.#3 9\$	ADD ADD INC	10\$:		000004	062705	66 065/44
82 82	; PAT.INC ; CNT ; CNT,*	R2	INC			000007	005216 005202 020227	87 063750 88 063752 89 06 <u>37</u> 54
		R2,#3	BLE			000003	003720	90 063760
82 81	; WRD.CNT ; *,PAT.CNT ; PAT.CNT,*	2(SP) #4,R3	BLE INC ADD CMP			000002 000004 000013	062703	92 063766
		2(SP) #4,R3 R3,#13 8\$ R1	BLE			000013	020327	87 063750 88 063752 89 063754 90 063760 91 063766 92 063766 93 063772 94 063776 95 064000 96 064010 99 064012 00 064014 01 064016 02 064024 03 064026 04 064036 05 064036 07 064042 08 09
81	; COUNT ,*	R1 R1,#77	BLE INC CMP			000077	020127	96 064002
82	:	R1 .#77 7\$ 67	BLE TRAP ROR BLO CMP				104467	98 064010
		R0 4\$	ROR BLO				103606	00 064014
82	; DODU.FLG,*	10(SP),#1 11\$	BNE		000001	000010	026627 001004 016700	02 064024
82	:	ML.LUN,RO	MOV TRAP TRAP			127546	104451	05 064026 04 064032
81		#12.SP PC	ADD RTS	115:		000012	104451 104444 062706 000207	05 064034 06 064036

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SEC	MACRO M1113 2	2-DEC-80 12:16 PAGE 397		SEQ 0384
18011 18012	;ML4			22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
18013 18014 18019 18020 18024	; Maxin	num stack depth per invocatio	n: 19 words	
18030 064050 104466 18031 064052 006000 18032 064054 103773	177102 T37::	JSR PC,\$T37 TRAP 66 ROR RO BLO 1\$ RTS PC		8230
18033 064056 000207 18034 18035 18036 18041 18042 18043 ; 8233	: Rout: : Maxin	ine Size: 6 words num stack depth per invocatio	n: 0 words	

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (88)

BGNTST:

TEST NUMBER: TST 38

TEST NAME: SYNC DATA BUS BIT UNIQUENESS TEST/WRITE PATH

TEST DESCRIPTION:

TEST SYNCHRONOUS DATA BUS FOR DATA BIT UNIQUENESS BY:

- 1. LOADING THE FIRST 16 WORDS IN THE IO BUF WITH A SHIFTING ZERO IN A FIELD OF ONES PATTERN.
- 2. VIA MBUS WRITE FUNCTION WRITE SHIFTING PATTERN THROUGH THE DATA BUS AND INTO THE GOOD BLOCK.
- 3. VIA DAT DM MODE READ THE GOOD BLOCK AND SAVE ALL GOOD NIBBLE DATA, IN THEIR PROPER SEQUENCE, INTO A STACK STRUCTURE.
- 4. INTERRIGATE STACK STRUCTURE FOR SHIFTED DATA PATTERN.

IMPLICIT INPUTS:

PD_TEMP
A BIT VECTOR OF 16 BITS WHERE
THE READ PROM DATA IS STORED
AND /CCESSED FROM.

IO BUF A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITE FUNCTION ARE FOUND.

STACK
A VECTOR OF 198 BYTE LOCATIONS
WHERE GOOD NIBBLE DATA IS STORED
WHEN STRIPPING AWAY BAD NIBBLE
LOCATIONS OF A BLOCK.

```
18101 ;ML4
18102 ;
18103
18104 ;
18105 ;
                                                                                                                           22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 
22-Dec-1980 09:21:22 PA:<NEALE>BL3ML4.BLI.2 (88)
18106
18107
18108
18109
                                      local
                                             SAV NIB,
DODU FLG,
NIB BIT,
TST PAT,
ALL ONES 1,
ALL ONES 2,
STK PTR,
                                                                                                                            STORES THE SHIFTED BIT PATTERN
                                                                                                                            DROP UNIT FLAG
                                                                                                                           TEST PATTERN
HOW MANY '17' NIBBLE PATTERN POSITION TO READ
HOW MANY '17' NIBBLE PATTERN POSITION TO READ
18110 ;
18111
18112
18113
18114
18115
                                                                                                                            STACK POINTER
                                              COUNT:
                                                                                                                            ! COUNTER
18116 ;
18117 ;
                                      BGNSUB:
                                      CLR MBUS;
DODU FLG = ZERO;
TST_PAT = ONE;
18118 :
18119 :
18119;
18120;
18121;
18123;
18123;
18124;
18125;
18126;
18127;
18128;
18129;
18130;
18131;
18132;
                                                                                                                           !ONE IN A FIELD OF ZEROES
                                       incr CNT from 0 to 15 do
                                                                                                                           !WRITE 16 WORDS WITH SHIFTING O IN FIELD OF 1'S.
                                              begin
                                            IO_BUF [.CNT] = not .TST_PAT;
TST_PAT = .TST_PAT^ONE;
end;
                                     GD_BLK_XFER ();
MLCS1 = write;
TIME_OUT_LOOP;
STRIPPER (21, 8);
STK_PTR = -1;
NIB_BIT = ONE;
ALL_ONES_1 = ZERO;
ALL_ONES_2 = 3;
                                                                                                                           SET UP A GOOD BLOCK XFERR
                                                                                                                           !WRITE SHIFTING PATTERN THROUGH SYNC BUS
                                                                                                                           !CALL ROUTINE TO STRIP OUT BAD NIBBLE DATA FROM ARRAY WORDS
                                                                                                                           !RESET THE STACK POINTER
18132
18133
18134
18135
18136
18137
18138
18139
                                                                                                                          SHIFTING NIBBLE PAT OF 1 IN FIELD OF O'S READ NO '17' NIBBLE PATTERN ON FIRST PASS READ THREE '17' NIBBLE PATTERN ON FIRST PASS
                                      incr BY_FOUR_WRDS from 0 to 3 do
                                                                                                                          !READ 4 GROUPS OF 4 WORDS
                                             begin
                                             incr BY_ONE_WRD from 0 to 3 do
                                                                                                                          !READ 4 GROUPS OF 1 WORD
18140 :
18141 :
                                                    begin
COUNT = ZERO:
                                                                                                                          !CLEAR COUNT
18142
18143
18144
18145
                                                    until .COUNT eql .ALL_ONES_1 do
                                                                                                                          !READ X NUMBER OF '17' NIBBLE PAT
                                                           begin
COUNT = .COUNT + 1;
                                                                                                                           !INCREMENT COUNT
!INCREMENT STACK POINTER
18146 :
18147 :
18148 :
18149 :
                                                           STK_PTR = .STK_PTR + 1;
                                                           if (.stack [.STK_PTR]) neg %o'000017' !COMPARE STACK WITH '17'
                                                           then
18150 :
18151 :
18152 :
18153 :
                                                                 !ERROR AND SET DODU_FLG IF NEQ
ERROF (90, SYNC, DUMPER);
PRINTB (SIX_FMT, WRD_23, WRD_39, PHR_4, WRD_12, FNC_5, WRD_19);
PRINTB (FMT_5, ONES, .stack [.STK_PTR], .STR_PTR);
DODU_FLG = ONE;
                                                                  end:
```

```
MACRO M1113 22-DEC-80 12:16 PAGE 400
CZMLABO ML-11 LOGIC TEST
MISCELLANEOUS CODING SECTION
   18157 :ML4
18158 :
18159
                                                                                                                                                        TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (88)
                                                                                                                   22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
   18160 ;
18161 ;
18162 ;
18163 ;
18164 ;
18165 ;
18166 ;
18167 ;
                                                         end:
                                                  STK_PTR = .STK_PTR + 1; !INCREMENT THE STACK POINTER SAV_NIB = ( not .NIB_BIT) and (%o'000017'); !GENERATE THE SHIFTED
                                                                                                                               !GENERATE THE SHIFTED BIT
                                                   if (.stack [.STK_PTR]) neg (.SAV_NIB) !COMPARE STACK TO SHIFTED BIT
                                                        !ERROR AND SET DODU_FLG IF NERROF (91, SYNC, DUMPER);
PRINTB (SIX_FMT, WRD_23, WRD_39, PHR_4, WRD_12, FNC_5, WRD_19);
PRINTB (FMT_15, .STK_PTR);
PRINTB (FMT_5, .SAV_NIB, .stack [.STK_PTR]);
DODU_FLG = ONE;
                                                                                                                   !ERROR AND SET DODU_FLG IF NEQ
   18170 :
18171 :
   18172
18173
18174
18175
                                                  COUNT = ZEROES:
                                                                                                                 !CLEAR COUNT
   18177
   18178
18179
                                                  until .COUNT eql .ALL_ONES_2 do
                                                                                                       !READ X NUMBER OF '17' NIBBLE PAT
                                                         begin
   18180 :
                                                         COUNT = .COUNT + 1;
                                                                                                                   !INCREMENT COUNT
   18181 ;
                                                         STK_PTR = .STK_PTR + 1;
                                                                                                                   !INCREMENT STACK POINTER
   18182 :
   18183
18184
18185
18186
18187
18188
18189
                                                         if (.stack [.STK_PTR]) neg %0'000017' !COMPARE STACK POINTER WITH '17'
                                                              !ERROR AND SET DODU_FLG IF SET ERROF (92, SYNC, DUMPER);
PRINTB (SIX_FMT, WRD_23, WRD_39, PHR_4, WRD_12, FNC_5, WRD_19);
PRINTB (FMT_5, ONES, .stack [.STK_PTR], .STR_PTR);
DODU_FLG = ONE;
   18190
18191
                                                               end:
   18192
18193
18194
18195
18196
18197
                                                         end;
                                                  NIB_BIT = .NIB_BIT^ONE;
                                                                                                                SHIFT THE SHIFTED NIBBLE BIT
                                                  end:
                                                                                                                  RESET THE SHIFTED NIBBLE BIT READ ONE MORE '17' PATTERN READ ONE LESS '17' PATTERN
                                            NIB_BIT = ONE;
                                            ALL_ONES_1 = .ALL_ONES_1 + 1;
ALL_ONES_2 = .ALL_ONES_2 - 1;
                                     ENDSUB:
                                      if .DODU_FLG IS_SET
                                                                                                                !DROP THIS UNIT IF DODU_FLG SET
                                      then
                                            begin
DODU (.ML_LUN);
                                            DOCLN;
                                            end:
                                      ENDTST;
```

SEQ 0387

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113	22-DEC-80 12:16	PAGE 401
WIRCELTANEOUS CODING RECLION			

SEQ 0388

MISCELLANEOUS CODING SECTION	SEQ 0388
18213 :ML4 18214 : 18215	22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 22-Dec-1980 09:21:22 PA: <neale>BL3ML4.BLI.2 (88)</neale>
18223 064060 004167 120032 \$T38: JSR R1.\$SAVE5 18224 064064 162706 000012 SUB #12.SP	; 8232
18225 064070 104402 1\$: TRAP 2	8296 8298
18227 064100 016705 127476 MOV ML.DUT,R5	, 6276
18226 064072 152777 000040 127114 BISB #40.aml.REG- 18227 064100 016705 127476 MOV ML.DUT.R5 18228 064104 042705 177770 BIC #177770.RS 18229 064110 142777 000007 127076 BICB #7.aml.REG-4 18230 064116 150577 127072 BISB R5.aml.REG-4 18231 064122 005001 CLR R1 18232 064124 012766 000001 000006 MOV #1.6(SP) 18233 064132 005004 18234 064134 010405 28: MOV R4.R5 18235 064136 006305 18236 064140 016665 000006 011600 MOV 6(SP).IO.BUI 18237 064146 005165 011600 18238 064152 006366 000006 18239 064156 005204 18240 064160 020427 000017 18241 064166 003763 18242 064166 004767 131670 18243 064172 012777 000061 126754 18244 064200 105777 127020 38: TSTB aml.REG-50	60
18231 064122 005001 18232 064124 012766 000001 000006 CLR R1 18232 064124 012766 000001 000006 MOV #1.6(SP)	; DODU.FLG 8300 ; *,TST.PAT 8301
18232 064124 012766 000001 000006 MOV #1,6(SP) 18233 064132 005004 CLR R4	: CNT 8303
18233 064132 005004 CLR R4 18234 064134 010405 28: MOV R4.R5 18235 064136 006305 ASL R5	
18236 064140 016665 000006 011600 MOV 6(SP), IO, BUI 18237 064146 005165 011600 COM IO, BUF (R5) 18238 064152 006366 000006 ASL 6(SP)	(R5) ; TST.PAT,*
18235 064136 006305 18236 064140 016665 000006 011600 MOV 6(SP), IO.BUI 18237 064146 005165 011600 COM IO.BUF(R5) 18238 064152 006366 000006 ASL 6(SP) 18239 064156 005204 INC R4	; TST.PAT 8306 ; CNT 8303 ; CNT,*
18240 064160 020427 000017 CMP R4,#17 18241 064164 003763 BLE 2\$	
18241 064164 003763 18242 064166 004767 131670 18243 064172 012777 000061 126754 18244 064200 105777 127020 38: TSTB aml.REG+50	ER : 8309 : 8310
18245 064204 100375 ppi 7e	
18245 064204 100375 18246 064206 012746 000025 18247 064212 012746 000010 18248 064216 004767 127772 18249 064222 012702 177777 18250 064226 012766 000011 000004 18251 064234 005066 000010 18252 064240 012766 000003 000006 18252 064240 012766 000003 000006 18255 064240 012766 000003 000006 18257 064240 012766 000003 000006	; 8312
18248 064216 004767 127772 JSR PC, STRIPPER 18249 064222 012702 177777 MOV #-1,R2	
18248 064216 004767 127772 JSR PC, STRIPPER 18249 064222 012702 177777 MOV #-1.R2 18250 064226 012766 000001 000004 MOV #1.4(SP) 18251 064234 005066 000010 CLR 10(SP)	; *,STK.PTR 8313 ; *,NIB.BIT 8314
18251 064234 005066 000010 CLR 10(SP) 18252 064240 012766 000003 000006 MOV #3,6(SP)	; ALL.ONES.1 8315 ; *,ALL.ONES.2 8316
18253 064246 005004 CLR R4 18254 064250 005005 4\$: CLR R5 18255 064252 005003	; *,STK.PTR 8313 ; *,NIB.BIT 8314 ; ALL.ONES.1 8315 ; *,ALL.ONES.2 8316 ; BY.FOUR.WRDS 8318 ; BY.ONE.WRD 8321
18254 064250 005005 4\$: CLR R5 18255 064252 005003 5\$: CLR R3 18256 064254 020366 000010 6\$: CMP R3,10(SP)	; COUNT ; COUNT, ALL. ONES. 1 8323
18252 064240 012766 000003 000006 MOV #3,6(SP) 18253 064246 005004 18254 064250 005005 4\$: CLR R5 18255 064252 005003 5\$: CLR R3 18256 064254 020366 000010 6\$: CMP R3,10(SP) 18257 064260 001455 BEQ 7\$ 18258 064262 005203 INC R3 18259 064264 005202 INC R2 18260 064266 126227 012612 000017 CMPB STACK(R2),#1	: COUNT 8327
18259 064264 005202 18260 064266 126227 012612 000017 INC R2 CMPB STACK(R2),#1	; COUNT ; STK.PTR ; *(STK.PTR),* 8328
18261 064274 001767 BEQ 6\$ 18262 064276 104455 TRAP 55	
18263 064300 000132 .WORD 132	: 8333
18256 064264 020366 000010 68: CMP R3,10(SP) 18257 064260 001455 18258 064262 005203 18269 064264 005202 18260 064266 126227 012612 000017 18261 064274 001767 18262 064276 104455 18263 064300 000132 18264 064302 010526 18266 064304 024052 18266 064306 012746 006506 **TRAP** **TRAP** **TRAP** **STACK(R2),#1 **TRAP** **TRAP** **STACK(R2),#1 **TRAP** **TRAP** **STACK(R2),#1 **TRAP** **TRAP** **STACK(R2),#1 **TRAP** **TRAP* **TRAP** **TRAP** **TRAP** **TRAP** **TRAP* **TRAP* **TRAP** **TRAP** **TRAP* **TR	
18266 064306 012746 006506 MOV MURD.19,-(SF	8334

ABO ML-11 LO CELLANEOUS CO 3268				:ML4			22-01090 00-3	SEQ 0389
269 270				:			22-Dec-1980 09:2 22-Dec-1980 09:2	24:31 TOPS 21:22 PA:<
271 064312 1272 064316 1273 064322 1274 064326 1275 064332 1276 064336 1277 064342	012746 012746 012746 012746 012746 012746 012746	007774 006426 007454 006716 006544 006100 000007			MOV MOV MOV MOV MOV MOV	WFNC.5,-(SP) WWRD.12,-(SP) WPHR.4,-(SP) WWRD.39,-(SP) WWRD.23,-(SP) WSIX.FMT,-(SP) W7,-(SP) SP,R0 14	; SP,*	
279 064350 280 064352	104414				MOV TRAP MOV	14 R2.(SP)	; STK.PTR,*	8335
11 064354 12 064356 13 064362	005046 116216 012746	012612 177777 004430 000004			CLR MOVB MOV	R2,(SP) -(SP) STACK(R2),(SP) #-1,-(SP)	: *(STK.PTR),*	8333
84 064366 85 064372 86 064376	012746 012746 010600	000004			MOV MOV TRAP	STACK(R2),(SP) #-1,-(SP) #FMT.5,-(SP) #4,-(SP) SP,R0	; SP,*	
287 064400 288 064402 289 064406	104414 012701 062706	000001 000030			ADD	14 #1 R1 #30 SP 6\$ R2	. *,DODU.FLG	8336 8332 8325 8341 8342
91 064414	000/20	000017		7\$:	BR INC	6\$ R2	: STK.PTR	8325 8341
3 064424	046666	000017 000004	000014 000014		BIC	#17,14(SP) 4(SP),14(SP)	: *,SAV.NIB ; NIB.BIT,SAV.NIB	
064416 064432 064434 064440	012766 046666 005000 156200 020066 001454	012612 000014			CLR BISB CMP	RO STACK(R2),RO RO,14(SP)	*(STK.PTR),* *,SAV.NIB	8344
064446	104455 000133 010526				BEQ TRAP .WORD .WORD .WORD	RO, 14 (SP) 8\$ 55 133 SYNC		8347
064454 064456 064462 064466 064472 064476	024052 012746 012746 012746 012746	006506 007774 006426 007454 006716 006544 006100 000007			MORD MOV	DUMPER MURD. 19, -(SP) MFNC. 5, -(SP) MURD. 12, -(SP) MURD. 39, -(SP) MURD. 39, -(SP) MURD. 23, -(SP) MSIX.FMT, -(SP) M7, -(SP) SP, R0 14 R2. (SP)		8348
064516 (064520 1	10600				MOV	SP.R0	; SP,*	
064522 064524	010216 012746	005150 000002			MOV	ŘŽ,(SP) WFMT.15,-(SP)	; STK.PTR,*	8349
4 064530 (5 064534 (6 064536 1	012746 010600 104414	000002			MOV MOV TRAP	R2.(SP) WFMT.15(SP) W2(SP) SP.R0	; SP,*	
01 064454 02 064456 03 064466 04 064466 05 064476 06 064506 07 064506 09 064516 11 064520 12 064524 14 064530 15 064534 16 064536 17 064540 18 064542 19 064562	012746 012746 012746 010600 104414 010216 012746 010600 104414 005016 116216 016646	012612 000040 004430 000003			CLR MOVB MOV MOV	(SP) STACK(R2),(SP) 40(SP),-(SP) #FMT.5,-(SP) #3,-(SP) SP,R0	*(STK.PTR),* ; SAV.NIB,*	8350
322 064562	12746	000003			MOV	SP,RO	; SP,*	

.

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	0 12:16 PAGE 403		SEQ 0390
18324 18325	ML4		22-Dec-1980 09:24 22-Dec-1980 09:21	4:31 TOPS
18327 064564 104414 18328 064566 012701 000001 18329 064572 062706 000032 18330 064576 005003 18331 064600 020366 000006	8\$: CLR	14 #1.R1 #32,SP R3 R3.6(SP) 10\$; *,DODU.FLG ; COUNT ; COUNT,ALL.ONES.2	8351 8346 8354 8356
18335 064610 005203 18335 064612 126227 012612 18336 064620 001767	INC INC OOO017 CMPR	R3 R2 STACK(R2) _#17	; COUNT ; STK.PTR ; *(STK.PTR),*	8358 8359 8361
18337 064622 104455 18338 064624 000134 18339 064626 010526 18340 064630 024052 18341 064632 012746 006506	BEQ TRAP .WORD .WORD .WORD	9\$ 55 134 SYNC DUMPER		8364
18340 064630 024052 18341 064632 012746 006506 18342 064636 012746 007774 18343 064642 012746 006426 18344 064646 012746 007454 18345 064652 012746 006716 18346 064656 012746 006544 18347 064662 012746 006100 18348 064666 012746 000007	MOV MOV MOV MOV MOV MOV MOV	MARD. 19,-(SP) MFNC. 5,-(SP) MARD. 12,-(SP) MPHR. 4,-(SP) MARD. 39,-(SP) MARD. 23,-(SP) MSIX.FMT,-(SP) M7,-(SP)		8365
18349 064672 010600 18350 064674 104414 18351 064676 010216 18352 064700 005046	MOV TRAP MOV	SP,R0 14 R2.(SP) -(SP)	; SP,* ; STK.PTR,*	8366
18353 064702 116216 012612 18354 064706 012746 177777 18355 064712 012746 004430 18356 064716 012746 000004	CLR MOVB MOV MOV MOV TRAP	STACK(R2),(SP) #-1,-(SP) #FMT.5,-(SP) #4,-(SP) SP,R0 14	; *(STK.PTR),* ; SP,*	
18357 064722 010600 18358 064724 104414 18359 064726 012701 000001 18360 064732 062706 000030 18361 064736 000720 18362 064740 006366 000004 18363 064744 005205 18364 064746 020527 000003 18365 064752 003002 18366 064754 000167 177272 18367 064760 012766 000001 18368 064766 005266 000010 18369 064772 005366 000001 18370 064776 005204 18371 065000 020427 000003 18372 065004 003002 18373 065006 000167 177236 18374 065012 022626 18375 065014 104467 18376 065016 006000 18377 065020 103002 18378 065022 000167 177042	ADD BR	#1,R1 #30,SP 9\$ 4(SP)	*,DODU.FLG	8367 8363 8356 8372 8321
18364 064746 020527 000003 18365 064752 003002 18366 064754 000167 177272	RGT	R5 R5 M3 11\$ 5\$; NIB.BIT ; BY.ONE.WRD ; BY.ONE.WRD,*	
18366 064754 000167 177272 18367 064760 012766 000001 18368 064766 005266 000010 18369 064772 005366 000006 18370 064776 005204	000004 11\$: MOV INC DEC INC	5\$ #1,4(SP) 10(SP) 6(SP)	; *,NIB.BIT ; ALL.ONES.1 ; ALL.ONES.2 ; BY.FOUR.WRDS ; BY.FOUR.WRDS,*	8375 8376 8377 8318
18371 065000 020427 000003 18372 065004 003002 18373 065006 000167 177236	BGT	R4 R4,#3 12\$ 4\$; BY.FOUR.WRDS.*	
18374 065012 022626 18375 065014 104467 18376 065016 006000 18377 065020 103002 18378 065022 000167 177042	ROR	(SP)+,(SP)+ 67 R0 13\$		8296 8378

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 404	SEQ 0391
18380 18381	:ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
18382 18383 065026 005301 18384 065030 001004	138: DEC BNE	R1 14\$; DODU.FLG 8382
18385 065032 016700 12654 18386 065036 104451	42 MOV TRAP TRAP	ML.LUN,RO	; 8385
18387 065040 104444 18388 065042 062706 00001 18389 065046 000207	12 148: TRAP ADD RTS	#12,SP PC	; 8232
18391 18392 18397 18398	; Routine Size: ; Maximum stack	252 words depth per invocation: 26 words	
18406 065050 18407 065050 004767 17700 18408 065054 104466 18409 065056 006000 18410 065060 103773 18411 065062 000207	138:: JSR TRAP ROR BLO RTS	PC.\$T38 66 R0 1\$ PC	; 8387
18411 065062 000207 18412 18413 18414 18419 18420 18421 ; 8390 ! <blf< td=""><td>; Routine Size: ; Maximum stack F/PAGE></td><td>6 words depth per invocation: 0 words</td><td></td></blf<>	; Routine Size: ; Maximum stack F/PAGE>	6 words depth per invocation: 0 words	

```
TOPS-20 Bliss-16 V2(212)
PA:<NEALE>BL3ML4.BLI.2 (89)
BGNTST:
 ! TEST NUMBER: TST 39
  TEST NAME:
                  SYNC DATA BUS BIT UNIQUENESS TEST/READ PATH
 ! TEST DESCRIPTION:
         TEST SYNCHRONOUS DATA BUS READ
         PATH FOR DATA BIT UNIQUENESS BY:
         1. LOADING THE FIRST 16 WORDS IN THE ID_BUF WITH A SHIFTING ZERO IN A FIELD OF ONES PATTERN.
         2. VIA MBUS WRITE FUNCTION WRITE SHIFTING PATTERN INTO THE GOOD BLOCK.
         3. CLEAR THE IO_BUF.
         4. VIA MBUS READ FUNCTION READ THE SHIFTING PATTERN THROUGH THE
              READ PATH.
         5. INTERIGATE THE IO_BUF FOR THE SHIFTING PATTERN.
  IMPLICIT INPUTS:
         A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITE FUNCTION ARD FOUND.
local
    DODU_FLG.
                                                       !DROP UNIT FLAG
     TST_PAT;
                                                       !TEST PATTERN
CLR_MBUS;
DODU FLG = ZERO;
TST_PAT = ONE;
                                                      ONE IN A FIELD OF ZEROES
incr CNT from 0 to 15 do
                                                       !WRITE 16 WORDS WITH SHIFTED 0 IN A FIELD OF 1'S
    begin

IO_BUF [.CNT] = not .TST_PAT;

TST_PAT = .TST_PAT^ONE;
    end:
GD_BLK_XFER ();
MLCS1 = write;
TIME_OUT_LOOP;
                                                       SET UP A GOOD BLOCK XFERR
                                                       WRITE SHIFTING PATTERN
BGNSUB:
incr CNT from 0 to 15 do
                                                      !CLEAR OUT THE IO_BUF
    IO_BUF [.CNT] = ZEROES;
CLR_MBUS;
```

```
CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 406 MISCELLANEOUS CODING SECTION
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```
18479 :ML4
18480 :
18481
                                                                                                                                                                                                                                                                                               22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (89)
                                                             8443 GD_BLK_XFER ();
8444 MLCS1 = read;
8445 TIME_OUT_LOOP;
8446 TST_PAT = ONE;
8447
8448 incr CNT from 0
8449 begin
8450
8451 if .IO_BUF [
then
8452 begin
8453
8454 ERRDF (9
PRINTB (9
P
                                                                                                                                                                                                                                                                                               SET UP A GOOD BLOCK XFERR READ SHIFTING PATTERN THROUGH SYNC BUS
                                                                                                                                                                                                                                                                                               !SHIFTING PATTERN
                                                                                         incr CNT from 0 to 15 do
                                                                                                                                                                                                                                                                                               !READ IO BUF FOR SHIFTING O IN FIELD OF 1'S
                                                                                                          if .10_BUF [.CNT] neq ( not .TST_PAT) !COMPARE IO_BUF TO SHIFTED PAT
                                                                                                                      begin
ERRDF (93, SYNC, DUMPER);
PRINTB (FIV_FMT, WRD_23, FNC_6, WRD_19, WRD_39, PHR_4);
PRINTB (FMT_2, ( not .TST_PAT), .10_BUF [.CNT]);
PODU_FLG = ONE;
                                                                                                TST_PAT = .TST_PAT^ONE;
                                                                                                        end:
                                                                                       ENDSUB:
                                                                                     if .DODU_FLG IS_SET
                                                                                                                                                                                                                                                                                      !DROP THIS UNIT IF DODU_FLG IS_SET
                                                                                                       begin
DODU (.ML_LUN);
                                                                                                        DOCLN;
                                                                                                         end;
18511 ;
18515 ;
18519 065064 ;
18520 065070 ;
18521 065076 ;
18522 065102 ;
18523 065106 ;
18524 065114 ;
18525 065120 ;
18526 065122 ;
18526 065122 ;
18527 065126 ;
18528 065130 ;
18529 065132 ;
18530 065134 ;
18531 065140 ;
18532 065144
                                                                                        ENDTST:
                                                                                       117026
000040
126500
177770
                                                                                                                                                          $T39:
                                                                                                                                                                                                                             R1,$SAVE5
                                                                                                                                                                                            JSR
                                                                                                                       126116
                                                                                                                                                                                                                             #40, aML . REG+40
                                                                                                                                                                                            BISB
                                                                                                                                                                                                                            ML.DUT.R5
#177770,R5
                                                                                                                                                                                            MOV
                                                                                                                                                                                            BIC
                                                                                         000007
126074
                                                                                                                         126100
                                                                                                                                                                                            BICB
                                                                                                                                                                                                                             #7. aml . REG+40
                                                                                                                                                                                           BISB
                                                                                                                                                                                                                            R5, aML.REG+40
R5
                                                                                                                                                                                           CLR
                                                                                                                                                                                                                                                                                                                                                                 : DODU.FLG
                                                                                                                                                                                                                            #1.R4
R0
                                                                                         000001
                                                                                                                                                                                            MOV
                                                                                                                                                                                                                                                                                                                                                                 : *,TST.PAT
                                                                                                                                                                                            CLR
                                                                                                                                                                                                                                                                                                                                                                 : CNT
                                                                                                                                                          15:
                                                                                                                                                                                           MOV
                                                                                                                                                                                                                             RO.R1
                                                                                                                                                                                                                                                                                                                                                                 : CNT.*
                                                                                                                                                                                            ASL
                                                       010461
005161
006304
                                                                                        011600
                                                                                                                                                                                            MOV
                                                                                                                                                                                                                             R4.10.BUF (R1)
                                                                                                                                                                                                                                                                                                                                                                : TST.PAT,*
                                                                                        011600
                                                                                                                                                                                                                             10.BUF (R1)
                                                                                                                                                                                                                                                                                                                                                                : TST.PAT
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    8431
```

MLABO ML-11 LI SCELLANEOUS C	- TING 3L								0394
18535				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	PA:
18537 065146 18538 065150 18539 065154	005200 020027 003765	000017			INC CMP BLE JSR	RO RO .#17	; CNT ; CNT,*		842
18537 065146 18538 065150 18539 065154 18540 065156 18541 065162 18542 065170 18543 065174 18544 065176	004767 012777 105777 100375	130700 000061 126030	125764	2\$:	JSR MOV TSTB BPL	PC.GD.BLK.XFER #61.aML.REG aML.REG+50 2\$;		8434 8435
18242 062200	104402 005000 010001			3\$: 4\$:	TRAP CLR MOV	R0 R0,R1	CNT CNT .*		8436 8436 8446
18547 065204 18548 065206 18549 065212 18550 065214	006301 005061 005200 020027	011600 000017			ASL	R1 IO.BUF(R1) R0 R0,#17	: CNT : CNT,*		8439
18546 065202 18547 065204 18548 065206 18549 065212 18550 065214 18551 065220 18552 065222 18553 065230 18554 065234 18555 065240 18556 065246 18557 065252 18558 065256 18569 065270 18561 065272 18561 065272 18562 065276 18563 065300 18564 065302	003770 152777 016703		125764		INC CMP BLE BISB MOV BIC BICB BISB	/ 0	;		8440
18555 065240 18556 065246 18557 065252	142777 150377 004767 012777 105777 100375 012704	000040 126346 177770 000007 125742 130604 000071 125734	125746		JOK	#40,aML.REG+40 ML.DUT.R3 #177770,R3 #7,aML.REG+40 R3,aML.REG+40 PC.GD.BLK.XFER #71,aML.REG aML.REG+50 5\$ #1,R4	;		8443
18558 065256 18559 065264 18560 065270 18561 065272	012/// 1057/7 100375 012704	000071 125734 000001	125670	5\$:	MOV TSTB BPL MOV	#71,aML.REG aML.REG+50 5\$ #1.R4	;		
	005002 010201 006301			6\$:	CLR MOV ASL MOV	R2,R1	; *,TST.PAT ; CNT ; CNT,*		8446 8448 8451
18565 065304 18566 065310 18567 065312 18568 065314 18569 065316	060103 010401 005101 021301 001447	011600			MOV COM	#10.BUF,R3 R1,R3 R4,R1 R1 (R3),R1	; TST.PAT,*		
18570 065320 18571 065322 18572 065324 18573 065326	001447 104455 000135 010526				BEQ TRAP .WORD	7\$ 55 135 SYNC			8454
18567 065312 18568 065314 18569 065316 18570 065320 18571 065322 18572 065324 18573 065326 18574 065330 18575 065332 18576 065332 18576 065336 18577 065342 18589 065356 18589 065356 18581 065362 18582 065366 18583 065370 18584 065372 18585 065374 18586 065376 18587 065402 18588 065406	024052 012746 012746 012746 012746 012746 012746 012746 010600 104414 011316 046616 040166 052616	007454 006716 006506			CMP BEQ TRAP .WORD .WORD .WORD MOV	(R3),R1 7\$ 55 135 SYNC DUMPER MPHR.4,-(SP) MURD.39,-(SP) MURD.19,-(SP) MFNC.6,-(SP) MFNC.6,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0 14 (R3),(SP)	;		8455
18578 0653546 18579 065352 18580 065356 18581 065362	012746 012746 012746 012746	007454 006716 006506 010004 006544 006062 000006			MOV MOV MOV	#FNC.6,-(SP) #WRD.23,-(SP) #FIV.FMT,-(SP) #6,-(SP)			
18582 065366 18583 065370 18584 065372	010600 104414 011316				MOV TRAP MOV	SP,R0 14 (R3),(SP)	; SP,*		8456
18586 065376 18587 065402	046616 040166	000002			BIC	(R3),(SP) R1,-(SP) 2(SP),(SP) R1,2(SP) (SP)+,(SP)			

	CZMLABO ML-11 L MISCELLANEOUS C	OGIC TES	T	MACRO M1113	22-DEC-80	12:16 PAGE	F 15 408				SEQ 0395
	18590 18591			ML4						22-Dec-1980 22-Dec-1980	09:24:31 TOPS 09:21:22 PA:<
	18592 18593 065410 18594 065412 18595 065414 18596 065420 18597 065424	011346 010146 012746 012746 010600	004266 000004		MOV MOV MOV MOV TRAP	(R3),-(SP) R1,-(SP) #FMT.2,-(SP) 84,-(SP) SP,R0) SP)		; SP,*		
	18598 065426 18599 065430 18600 065434 18601 065440	104414 012705 062706 006304	000001 000026	7\$:	MOV	14 #1,R5 #26,SP R4 R2 R2,#17 6\$ 67 R0 3\$ R5 8\$ ML.LUN,R0			*,DODU.FLG		8457 8453 8460 8448
	18602 065444 18604 065450	020227 020227 003713	000017		ASL INC CMP BLE TRAP	R2,#17 6\$			CNT,*		
1	18605 065452 18606 065454 18607 065456 18608 065460 18609 065462	104467 006000 103647			ROR BLO DEC BNE	67 R0 3\$:		8461
	18608 065460 18609 065462	005305	12/110		DEC	R5 8\$; DODU.FLG		8465
	18610 065464 18611 065470 18612 065472 18613 065474 18614 18615	016700 104451 104444	126110	90	MOV TRAP TRAP	ML.LUN,RO 51 44 PC					8468
	18613 065474 18614	000207		8\$:	RTS						8389
	18615 18616 18621 18622			; Rout ; Maxi	tine Size: imum stack	133 words depth per	invocation:	17 words			
	18630 065476 18631 065476 18632 065502 18633 065504 18634 065506 18635 065510 18636 18636 18637 18638 18643 ;	004767 104466 006000 103773 000207	177362	T39:: 1\$:	JSR TRAP ROR BLO RTS	PC,\$T39 66 R0 1\$ PC					8470
	18637 18638 18643 ;	8473	! <blf pa<="" td=""><td>; Rout ; Maxi</td><td>ine Size: imum stack</td><td>6 words depth per</td><td>invocation:</td><td>0 words</td><td></td><td></td><td></td></blf>	; Rout ; Maxi	ine Size: imum stack	6 words depth per	invocation:	0 words			

18645 : ML4 18646 : 18647 18648 : 18649 : 18650 : 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (90) BGNTST: TEST NUMBER: TST 40 TEST NAME: ARRAY ADDRESS MUX TEST TEST DESCRIPTION: TEST FOR UNIQUE MOS RAM ROW 18659 18660 18661 18662 18663 18664 18665 18666 18667 AND COLUMN ADDRESSING BY: 1. FIRST FINDING A ERROR FREE 16K OR 64K CHUNK OF MEMORY. THIS REPRESENTS ONE ROW OF EITHER 16K OR 64K MOS RAMS. 2. WRITE A BACKGROUND OF ALL ONES INTO THE GOOD CHUNK 18669 3. WRITE ZEROES INTO THE FIRST BLOCK OF THE GOOD CHUNK. 18670 18671 18672 18673 8498 4. READ REMAINING BLOCKS IN 18674 18675 18676 GOOD CHUNK FOR ONES. 18677 ; IMPLICIT INPUTS: 18678 18679 18680 18681 18682 18683 18684 18685 18686 IO_BUF A VECTOR OF 256 WORDS WHERE DATA FOR MBUS 8508 8509 8510 8511 8512 8513 READ AND WRITE TRANSFERS CAN BE FOUND. local DSA_ADRS, FND_GD_CHK; !DSA ADRS COUNTER FOUND GOOD 16K/64K CHUNK FLAG 18690 18691 LOAD FIRST ID BUF WORD WITH ONES IO_BUF = ONES; 18692 18693 DSA_ADRS = -.RAS_INC; FND_GD_CHK = ZERO; !CLEAR FLAG 18694 18695 18696 do !DO UNTIL FOUND GOOD CHUNK OR LBT begin CLR_MBUS; BAI = ONE; 18697 18698 SET ON FIRST IO BUF WORD ECC_DIS = ONE; 18699 !DISABLE ECC

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22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                   TOPS-20 Bliss-16 V2(212)
18702
18703
18704
18705
18706
                                                                                                                                                  PA: <NEALE>BL3ML4.BL1.2 (90)
                                        DSA_ADRS = .DSA_ADRS + .RAS_INC;
MLWC = .W_C_SIZE;
MLBA = IO_BOF;
                                                                                                              !INCREMENT DSA ADRS COUNTER
                                                                                                             !16K OR 64K WORDS
!LOAD UBUS ADRS
!LOAD DSA ADRS
!DO A WRITE FUNCTION
                                        MLDA = .DSA_ADRS;
ML_FUNC = write;
TIME_OUT_LOOP;
18708
18709
18710
                                        if .SC IS_NOT_SET
                                                                                                             !DID XFERR CAUSE AN SC
                                         then
                                              begin
MLWC = .W_C_SIZE;
MLBA = IO_BOF;
                                                                                                             !XFERR WAS OK
                                                                                                              !LOAD WORD COUNT
                                                                                                             !LOAD UBUS ADRS
                                              MLDA = .DSA_ADRS;
                                                                                                             !LOAD DSA ADRS
                                              ML_FUNC = WRT_CHK;
18717 :
                                                                                                             !DO A WRITE CHECK FUNCTION
18718 :
                                              TIME_OUT_LOOP;
18719 :
18720
18721
18722
18723
18723
18724
18725
18726
18727
18728
                                               if .SC IS_NOT_SET
                                                                                                            !IS THIS CHUNK GOOD
                                                    FND_GD_CHK = ONE;
                                                                                                            !YES SET FLG
                                               end
                                 until (.FND_GD_CHK IS_SET ) or (.DSA_ADRS eql .LST_ARR + .ARR_INC);
18729 :
18730 :
                                                                                                            !REPEAT UNTIL FOUND GOOD CHUNCK OR AT LBT
18731 :
                                  if .DSA_ADRS eql .LST_ARR + .ARR_INC
18732
18733
                                  then
                                        begin

ERRDF (111, INTER, DUMPER);

PRINTB (FIV_FMT, FNC_13, FNC_17, WRD_50, WRD_60, WRD_56);

PRINTB (THR_FMT, WRD_14, PHR_10, FNC_15);
18734
18735
18737
                                        DODU (.ML_LUN);
18738
                                        DOCLN:
18739 :
                                        end
18740 :
                                  else
                                                                                                          !A GOOD CHUNCK WAS FOUND CONTINUE TEST
18741 :
18742 :
18743 :
                                        begin
CLR_MBUS;
                                        BAI = ONE;
                                                                                                             DISABLE ECC FIRST BLOCK IN CHUNCK GETS ZEROES
                                         ECC DIS = ONE
                                         IO BUF = ZEROES;
                                        MLDA = .DSA_ADRS;

MLDA = .DSA_ADRS;

MLWC = not 255;

MLBA = IO_BUF;

ML_FUNC = write;

TIME OUT LOOP;

CLR_MBUS;

BAI = ONE;

IO_BUF = ONES;

FCT_DIS = ONE;
18746 :
18747 :
18748 :
18749 :
18750 :
                                                                                                             LOAD DSA
LOAD WORD COUNT
LOAD UBUS ADRS
                                                                                                             !DO A WRITE FUNCTION
18751 :
18752 :
18753 :
18754 :
18755 ;
                                                                                                            !READ REMAINING BLOCKS FOR ONES
                                         ECT_DIS = ONE;
                                        MLDA = .DSA_ADRS + 1;
                                                                                                             !LOAD DSA WITH NEXT DSA INCREMENT
```

2\$ #100000,aML.REG

W.C.SIZE, aML.REG+10 WIO.BUF, aML.REG+20 R2, aML.REG+30 W77, aML.REG

DSA.ADRS, *

8533

000061 125360

100000

25:

125300

125300 125302

125250

TSTB BPL

MOV MOV

MOV

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-	-80 12:16 PAGE 412		SEQ 0399
18812 18813	ML4		22-Dec-1980 22-Dec-1980	09:24:31 TOPS 09:21:22 PA:<
18812 18813 18814 18815 065704 152777 00000 18816 065712 105777 12530 18817 065716 100375 18818 065720 032777 10000 18820 065730 012700 00000 18821 065734 020027 00000 18822 065740 001406 18823 065742 016701 12360 18824 065746 066701 12360 18825 065752 020201 18826 065754 001267 18827 065756 016701 12360 18828 065762 066701 12350 18829 065766 020201 18830 065770 001047 18831 065772 104455 18832 065774 000157 18833 065776 010672 18833 066002 012746 00710 18836 066006 012746 00710 18837 066012 012746 00710 18838 066016 012746 01010 18839 066022 012746 01010 18839 066022 012746 01010 18839 066022 012746 01010 18839 066022 012746 00000 18841 066032 012746 00000 18842 066036 010600 18843 066040 104414 18844 066042 012716 01013	51 125242 BISB 06 38: TSTB	#51.aML.REG		
18815 065704 152777 00005 18816 065712 105777 12536 18817 065716 100375 18818 065720 032777 10006 18819 065726 001002 18820 065730 012700 00006	00 125226 BPL	3\$ #100000,aML.REG	:	8542
18820 065730 012700 00000 18821 065734 020027 0000	01 MOV 01 4\$: CMP	4\$ #1.R0 R0.#1	; *,FND.GD.CHK ; FND.GD.CHK,*	8544 8549
18821 065734 020027 00000 18822 065740 001406 18823 065742 016701 12363 18824 065746 066701 12363	30 MOV 10 ADD	S\$ LST.ARR,R1		
18824 065746 066701 12361 18825 065752 020201 18826 065754 001267 18827 065756 016701 12361	CMP	ARR. INC, R1 R2, R1 1\$; DSA.ADRS,*	
18827 065756 016701 12361 18828 065762 066701 12351 18829 065766 020201	14 5\$: MOV ADD CMP	LST.ARR,R1 ARR.INC,R1 R2.R1	; DSA.ADRS,*	8553
18830 065770 001047 18831 065772 104455 18832 065774 000157 18833 065776 010672	RNF	ARR. INC, R1 R2, R1 6\$ 55 D 157	;	8556
18833 065776 010672 18834 066000 024052	TRAP .WOR .WOR	D INTER		
18815 065704 152777 00000 18816 065712 105777 12530 18817 065716 100375 18818 065720 032777 10000 18820 065730 012700 00000 18821 065734 020027 00000 18822 065740 001406 18823 065742 016701 12360 18825 065752 020201 18826 065754 001267 18827 065756 016701 12360 18828 065762 066701 12360 18829 065766 020201 18830 065770 001047 18831 065772 104455 18832 065774 000157 18833 065776 010672 18833 065776 010672 18834 066000 024052 18835 066002 012746 00710 18837 066012 012746 00710 18838 066016 012746 01010 18839 066022 012746 01010 18839 066022 012746 01010 18841 066032 012746 01010 18842 066036 010600 18843 066040 104414 18844 066042 012716 01013	MOV MOV	#WRD.56,-(SP) #WRD.60,-(SP) #WRD.50,-(SP) #FNC.17,-(SP) #FNC.13,-(SP) #FIV.FMT,-(SP) #6,-(SP)	:	8557
18838 066016 012746 01016 18839 066022 012746 01011	12 MOV	#FNC.17,-(SP) #FNC.13,-(SP)		
18840 066026 012746 00600 18841 066032 012746 00000 18842 066036 010600	62 MOV MOV MOV	WFIV.FMT,-(SP) W6,-(SP) SP. P0	; SP,*	
18842 066036 010600 18843 066040 104414 18844 066042 012716 01013	TRAP	SP,RO 14 #FNC.15,(SP)	; sr."	8558
18845 066046 012746 00756 18846 066052 012746 00644 18847 066056 012746 00603 18848 066062 012746 00000	MOV MOV MOV	#PHR.10,-(SP) #MRD.14,-(SP) #THR.FMT(SP)		
18848 066062 012746 00000 18849 066066 010600	MOV MOV TRAP	WFNC.15,(SP) WPHR.10,-(SP) WHRD.14,-(SP) WTHR.FMT,-(SP) W4,-(SP) SP,R0 14	; SP,*	
18851 066072 016700 12550 18852 066076 104451	02 MOV TRAP TRAP	ML.LUN,RO	:	8559
18853 066100 104444 18854 066102 062706 00002	26 ADD	#26,SP PC		8555
18856 066110 152777 00004 18857 066116 016701 12544	60 125076 6\$: RTS BISB MOV	M/A 000 050./A		8555 8553 8563
18858 066122 042701 17777 18859 066126 142777 00000 18860 066134 150177 1250	70 07 125060 BICB 54 BICB	#177770,R1 #7,@ML.REG+40 P1 @ML.REG+40		
18845 066046 012746 00756 18846 066052 012746 00644 18847 066056 012746 00603 18848 066062 012746 00000 18850 066070 104414 18851 066072 016700 12550 18852 066076 104451 18853 066100 104444 18855 066106 000207 18856 066110 152777 00000 18858 066122 042701 17777 18858 066122 042701 17777 18858 066122 042701 17777 18860 066134 150177 12503 18861 066140 152777 00000 18863 066154 005067 12342 18863 066154 005067 12342 18865 066164 012777 17740 18865 066164 012777 17740	70 BIC 97 125060 BICB 84 BISB 10 125046 BISB 92 125120 BISB CLR MOV	ML.DUT.R1 W177770,R1 W7,aML.REG+40 R1,aML.REG+40 W10,aML.REG+40 W2,aML.REG+120 IO.BUF R2,aML.REG+30 W-400,aML.REG+10 WIO.BUF,aML.REG+20		8565 8566
18864 066160 010277 12502 18865 066164 012777 17740	20 CLR 20 MOV 20 124772 MOV 20 124774 MOV	R2, aML.REG+30 #-400, aML.REG+10	DSA.ADRS.*	8565 8566 8567 8568 8569 8570
18866 066172 012777 01160	00 124774 MOV	#10.BUF, aML.REG+20		8570

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DDING SECTION	SEQ 0400
;ML4	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
142777 000077 124746 BICB #77.aML.REG ; 152777 000061 124740 BISB #61.aML.REG ; 105777 125004 7\$: TSTB aML.REG+50 BPL 7\$ 152777 000040 124764 BISB #40.aML.REG+40 ;	8571
152777 000040 124764 BISB #40.aML.REG+40 ; 016701 125346 MOV ML.DUT.R1 042701 177770 BIC #177770,R1 142777 000007 124746 BICB #7.aML.REG+40	8572
152777 000010 124734 BISB #10,aml.REG+40 ; 012767 177777 123312 MOV #-1.IO.BUF ; 152777 000002 125000 BISB #2,aml.REG+120 ; 010201 MOV R2.R1	8574 8575 8576 DSA.ADRS,*
010177 124700 MOV R1.aML.REG+30 012777 011600 124662 MOV #IO.BUF.aML.REG+20 ;	8578 8579
062702 000400 ADD #400,R2 010277 124636 MOV R2,aML.REG+10 142777 000077 124620 BICB #77,aML.REG 152777 000051 124612 BISB #51,aML.REG 105777 124656 8\$: TSTB aML.REG+50 100375 BPL 8\$	8580
032777 040000 124636 BIT #40000,aML.REG+40 ; 001441 BEQ 9\$	8583
104455 000160 .WORD 160 010464 .WORD ASYNC	8586
012746 006442 MOV MURD.14,-(SP) ; 012746 007156 MOV MURD.60,-(SP) 012746 007046 MOV MURD.50,-(SP)	8587
005316 PIUV @PL.REG+30, (SP) ;	SP,* 8588
012746 004644 MOV WFMT.9,-(SP) 012746 000002 MOV W2,-(SP) 010600 MOV SP,R0 ; 104414 TRAP 14	SP.*
016700 125126 MOV ML.LUN,RO ; 104451 TRAP 51 104444 TRAP 44	8589
012746 000002 MOV #2,-(SP) 010600 MOV SP,R0 ; 104414 TRAP 14 016700 125126 MOV ML.LUN,R0 ; 104451 TRAP 51 104444 TRAP 44 062706 000020 ADD #20,SP ; 000207 9\$: RTS PC ;	8585 8472

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18956 ;ML4 18957 ; 18958 18959 ; 18960 ; 18961 ; 18962 ; 18963 ; 18964 ; 18965 ; 18966 ; 18967 ; 18968 ; 18969 ; 18970 ; 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BL1.2 (91) BGNTST: ! TEST NUMBER: TST 41 TEST NAME: NIBBLE OFFSET TEST TEST DESCRIPTION: TEST NIBBLE OFFSET COUNTERS TO OFFSET GOOD NIBBLE DATA A MAX OF 14 WORDS ON DETECTION OF ALL BAD NIBBLES BY: 18971 18972 18973 8609 8610 8611 8612 8613 8614 8615 8616 8617 8618 8619 8620 1. LOADING FIRST 2 1/4 WORDS OF THE IO_BUF WITH ZEROES AND THE REMAINING OF BUFFER WITH ONES. 18974 18975 18976 18977 2. VIA DAT_DM MODE WRITE THE GOOD BLOCK WITH BACKGROUND ON ONES. 3. VIA PROM R/W MODE FORCE ALL ARRAY NIBBLES BAD. 18978 18979 .4. VIA A MBUS WRITE FUNCTION LOAD IO_BUF INTO THE GOOD BLOCK. 18980 18981 18982 18983 18984 18985 18986 18987 18988 18989 18990 18991 18992 18993 5. VIA DAT DM MODE READ FIRST 15 ARRAY WORDS FOR ZEROES AND THE REMAINING WORDS FOR ONES. IMPLICIT INPUTS: PD TEMP A BIT VECTOR OF 16 BITS WHERE THE READ PROM DATA IS STORED AND ACCESSED FROM. A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITE FUNCTIONS ARE FOUND. 18994 18995 18996 18997 18998 18999 19000 19001 19002 19003 19004 19005 19006 19007 19008 19009 19010 local DODU FLG, TST_PAT, START, FINISH, DROP UNIT FLAG STARTING WORD ENDING WORD ERR_FLG: !ERROR FLAG BGNSUB; CLR_MBUS: DODU_FLG = ZERO; incr WD_CNT from 0 to 255 do IO_BUF [.WD_CNT] = ONES; !LOAD IO BUF WITH ONES IO_BUF [0] = ZEROES; IO_BUF [1] = ZEROES; IO_BUF [2] = %o'177760'; !LOAD FIRST 2 1/4 WORDS WITH ZEROES

19058
19071 : 8701 8701 end; 19072 : 8702 end; 19073 : 8703 8704 end; 19074 : 8704 end; 19075 : 8705 S705 S705 ST PAT = not .TST_PAT; !NOW READ FOR ONES 19076 : 8706 ST PAT = not .TST_PAT; !NOW READ FOR ONES 19077 : 8707 START = 15; !START = 15 19078 : 8708 FINISH = 126; end END AT 126 19079 : 8709 end ENDSUB; 19080 : 8710 ENDSUB; 19081 : 8711 ENDSUB; 19082 : 8712 if .DODU_FLG IS_SET !DROP THIS UNIT IF DODU_FLG SET 19083 : 8713 hen heg in 19084 : 8716 DODU (.ML_LUN); 19087 : 8717 DOCUM; end; 19088 : 8718 end; 19089 : 8719 ENDSTST; 19090 066500 004167 115412 ST41: JSR R1_\$SAVE5 : 19090 066510 104402 O00012 SUB #12,SP : 19100 066510 104402 O00012 SUB #12,SP : 19101 066512 152777 00040 124474 BISB #0,0,2mL.REG+40 : 19102 066520 016704 125056 MOV ML_DUT_R4 19103 066530 142777 000007 124456 BISB #7,1,REG+40 19104 066530 142777 000007 124456 BISB #7,1,REG+40 19105 066530 142777 000007 124456 BISB #7,1,REG+40 19105 066530 142777 000007 124456 BISB #7,2mL,REG+40
19074
19074
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19098 066500 004167 115412 \$T41: JSR R1,\$SAVE5 ; SUB #12,SP ; SUB #12,
19103 066530 142777 000007 124456 BICB #7,aml.REG+40 19105 066536 150477 124452 BISB R4.aml.REG+40
19103 066530 142777 000007 124456 BICB #7,aml.REG+40 19105 066536 150477 124452 BISB R4.aml.REG+40
19103 066530 142777 000007 124456 BICB #7,aml.REG+40 19105 066536 150477 124452 BISB R4.aml.REG+40
19103 066530 142777 000007 124456 BICB #7,aml.REG+40 19105 066536 150477 124452 BISB R4.aml.REG+40
19105 066536 150477 124452 BISB R4.aML.REG+40 19106 066542 005066 000004 CLR 4(\$P) ; DODU.FLG 8641 19107 066546 005002 CLR R2 ; WD.CNT 8643
19106 066542 005066 000004 CLR 4(\$P) ; DODU.FLG 8641 19107 066546 005002 CLR R2 ; WD.CNT 8643
19107 066546 005002 CLR R2 ; WD. CNT 8643
19108 066550 010203 2\$: MOV R2,R3 ; WD.CNT,* 8644
19108 066550 010203 2\$: MOV R2,R3 ; WD.CNT,* 8644 19109 066552 006303 ASL R3 19110 066554 012763 177777 011600 MOV M-1,I0.BUF(R3) 19111 066562 005202 INC R2
19111 066562 005202 19112 066564 020227 000377 CMP R2,#377 ; WD.CNT,*
19112 066564 020227 000377 CMP R2,#377 ; WD.CNT,*
19113 066570 003767 19114 066572 005067 123002 BLE 2\$ 1914 066572 005067 123002 CLR IO.BUF ; 8646
19115 066576 005067 123000 CLR I0.BUF+2 19116 066602 012767 177760 122774 MOV #-20,10.BUF+4
19115 066576 005067 123000 CLR IO.BUF+2 19116 066602 012767 177760 122774 MOV #-20.10.BUF+4 19117 066610 012777 177777 124526 MOV #-1.aml.REG+170 19118 066616 012777 177777 124530 MOV #-1.aml.REG+200 19119 066624 012777 177777 124502 MOV #-1.aml.REG+160
19118 066616 012777 177777 124530 MOV #-1.aml.REG+200 : 8650
19119 066624 012777 177777 124502 MOV #-1,aML.REG+160 : 8651
19110 066562 005202 19112 066564 020227 000377

9123 9124				:ML4			22-	-Dec-1980 09:24:31 TOP -Dec-1980 09:21:22 PA:
9125				:			22-	-Dec-1980 09:21:22 PA:
9126 066644 9127 066646	005001 012702	000001		3\$:	MOV	R1 #1,R2	: WD.CNT : *,\$\$TMP2	865 865
9128 066652 9129 066654	001411	113236		3\$: 4\$:	BEQ	7\$ L\$DLY,R3	; *,\$\$TMP1	
9130 066660	001404				MOV BEQ CLR	6\$		
9132 066666	005303	000010		5\$:	DEC	10(SP) R3	SSTMP!	
9129 066654 9130 066660 9131 066662 9132 066666 9133 066670 9134 066672 9135 06674 9138 066704 9138 066704 9139 066712 9140 066714 9141 066722 9142 066726 9143 066732 9145 066744 9145 066744 9146 066750 9147 066754 9148 066760 9149 066766 9150 066772 9151 067004 9153 067006 9153 067006 9154 067014 9155 067020	001374			6\$:	DEC BNE DEC	R3 5\$ R2 4\$: ŝŝīro?	
9135 066674	000766	000020	124370	78:	BR BISB	4\$ #20,aml.REG+120		945
2137 066704	005201		124310		INC	R1	WD.CNT	865 865
9139 066712	005201 020127 003755 152777	000177			BLE	R1.#177 3\$; WD.CNT,*	
9140 066714	152777 016704	000040 124654	124272		BLE BISB MOV	#40,aML.REG+40	:	865
9142 066726	042704	000040 124654 177770 000007 124250 177777 000023 132240 000100 127070 000061 124220	124254		BIC	#40.aml.REG+40 ML.DUT.R4 #177770.R4 #7.aml.REG+40 R4.aml.REG+40 #-1(SP) #23(SP)		
9144 066740	150477	124250	124234		BIC BICB BISB	R4. aML. REG+40		
9146 066750	012746	000023			MOV	#-1,-(SP) #23,-(SP)	•	866
9147 066754 9148 066760	004767 152777 004767 012777 105777 100375 152777 016704	132240	124306		JSR BISB	#23(SP) PC.WRT.PD #100.aML.REG+120 PC.GD.BLK.XFER #61.aML.REG aML.REG+50		944
9149 066766	004767	127070			JSR	PC.GD.BLK.XFER		866 866 866
151 067000	105777	124220	124154	8\$:	MOV	amL.REG+50		866
153 067006	152777		124200		BPL			866
154 067014	016704	000040 124562 177770			MOV	#40.aML.REG+40 ML.DUT.R4 #177770.R4		
156 067024	142777	000007	124162		BICB	#7, aml. REG+40 R4, aml. REG+40 6(SP) #16,4(SP) R5		
158 067036	005066	000007 124156 000006 000016			BICB BISB CLR	6(SP)	; START	866
7160 067042	005005		000004		MOV CLR JSR	#16,4(SP) R5	; START ; * FINISH ; TST.PAT	867 867
161 067052	004767	125104 000071 000001	124070		JSR MOV	PC.DAT.DM.XFER		857
163 067064	012702	000001	124010	00.	MOV	#1.A2	: *,\$\$TMP2	866 867 867 867 867
165 067072	016703	113020		9\$:	MOV	PC.DAT.DM.XFER #71.aML.REG #1.R2 12\$ L\$DLY,R3	; *,\$\$TMP1	
7167 067076	001404	000014		10\$:	BEQ CLR	11\$ 14(SP)		
9168 067104	005303				DEC	R3	; SSTMP1	
2170 067110	005302			115:	DEC	R2	; SSTMP2	
772 067114	005004			12\$: 13\$:	MOV BEQ MOV BEQ CLR DEC BNE DEC BR CLR MOV DEC	95 R4	; TWICE	867
9173 067116	016601 005301	000006		13\$:	MOV	6(SP),R1	; TWICE ; START, WRD.CNT ; WRD.CNT	867 867
9155 067020 9156 067024 9157 067032 9158 067036 9159 067042 9160 067050 9161 067052 9163 067064 9163 067064 9164 067070 9165 067072 9166 067076 9167 067100 9169 067106 9170 067110 9171 067112 9172 067114 9173 067116 9176 067126 9176 067126	142777 150477 005066 012766 005005 004767 012777 012772 001411 016703 001404 005066 005303 001374 005302 000766 005004 016601 005301 000534 017767	124252	123764	145:	BR	11\$ 14(SP) R3 10\$ R2 9\$ R4 6(SP),R1 R1 22\$ aml.REG+230,PD.TEMP #20,aml.REG+120	, 40.00	
9177 067134	152777	124252 000020	123764 124132	143:	MOV BISB	#20, aml . REG+120		868 868

CZMLABO ML-11 L MISCELLANEOUS C	ODING SE	CTION	MACRO P	11113	22-DEC-80	12:16 PAGE 419				0406
19179 19180 19181				:ML4				22-Dec-1980 22-Dec-1980	09:24:31 09:21:22	TOPS PA:
	012702	000001		15\$:	MOV BEQ	#1_R2 18\$; *,\$\$TMP2			8683
19184 067150 19185 067154	016703	112742			MOV	L\$DLY,R3 17\$; *,\$\$TMP1			
19186 067156 19187 067162	005066 005303	000014		16\$:	CLR		SSTMP1			
19188 067164 19189 067166	001374			17\$:	DEC	16\$ R2	; \$\$TMP2			
19190 067170 19191 067172 19192 067200	017767 017767	124146 124150 124122	121752 121746 121742	18\$:	MOV BEQ CLR DEC BNE DEC BR MOV MOV	amREG+170,D1.TEMP				
19193 067206	017767	124122	121742		MOV	aML.REG+160,E2.TEMP	. NID DID			0404
19182 067142 19183 067146 19184 067150 19185 067154 19186 067156 19187 067162 19188 067164 19189 067166 19190 067170 19191 067172 19192 067200 19193 067206 19194 067214 19195 067216 19196 067220 19197 067222 19198 067226	010203			19\$:	CLR MOV ASR ASR	14(SP) R3 16\$ R2 15\$ aML.REG+170,D1.TEMP aML.REG+200,D2.TEMP aML.REG+160,E2.TEMP R2 R2,R3 R3	: NIB.PTR : NIB.PTR,*			8686 8688
19197 067222 19198 067224 19199 067226	006203 006203				ASR	R3 R3				
19199 067226 19200 067232 19201 067234	062703 010346	013120			ADD	MPD.TEMP,R3 R3,-(SP)				
19202 067236	042716 012746	177770 000001			MOV MOV BIC MOV	R3,-(SP) R2,-(SP) #177770,(SP) #1,-(SP) -(SP)	; NIB.PTR,*			
19202 067236 19203 067242 19204 067246 19205 067250 19206 067254 19207 067260	005046 004767				CLR	-(SP) PC_BL\$GT2				
19206 067254 19207 067260	005046 004767 062706 005700	113664 000010			ADD TST BNE	PC_BL\$GT2 #10,SP R0 21\$				
19209 067264	001051 010246				MOV	21\$ R2,-(SP)	: NIB.PTR.* : TST.PAT.*			8691
19210 067266 19211 067270	010346	000020			MOV	R5,-(SP) #20,-(SP)				
19212 067274 19213 067276 19214 067302	004767	126644 000020	000001		JSR	PC.TST.LNG.WRD	; ERR.FLG,*			0/07
19215 067310	001034	000020	000001		BNE	20\$; ERR.FLG,*			8693
19217 067314 19218 067316	060616 004767 026627 001034 104455 000136 010570				ADD JSR CMP BNE TRAP . WORD . WORD MOV MOV MOV MOV MOV MOV TRAP	136 ARR. DAT				86%
19219 067320 19220 067322	024052 012746	006406			. WORD MOV	DUMPER WARD.10,-(SP)				8697
19221 067326	012746	006406 007004 006732 006034 000004			MOV	MURD.46,-(SP) MURD.41,-(SP)				
19224 067342	012746	000004			MOV	#THR.FMT,-(SP) #4,-(SP)				
19226 067350	104414				TRAP	14 92 (SP)	; SP,* ; NIB.PTR,*			9409
19212 067274 19213 067276 19214 067302 19215 067310 19216 067312 19217 067314 19218 067316 19219 067320 19220 067322 19221 067332 19223 067336 19224 067342 19225 067332 19226 067350 19226 067350 19227 067352 19228 067354 19229 067360 19230 067364 19231 067366 19232 067370 19233 067376	024052 012746 012746 012746 012746 012746 010600 104414 010216 012746 010600 104414 012766 062706	004532 000002			MOV MOV MOV TRAP	21\$ R2,-(SP) R5,-(SP) R5,-(SP) W20,-(SP) SP,(SP) PC,TST,LNG.WRD 20(SP),W1 20\$ 55 136 ARR.DAT DUMPER WWRD.10,-(SP) WWRD.41,-(SP) WWRD.41,-(SP) WTHR.FMT,-(SP) WTHR.FMT,-(SP) WFMT.6,-(SP) WFMT.6,-(SP) WP,-(SP) SP,R0 14 R2,(SP) WFMT.6,-(SP) MP,-(SP) SP,R0 14 W1,34(SP)	, 110.111,-			8698
19230 067364 19231 067366	010600 104414				MOV TRAP	SP.R0 14	; SP,*			
19232 067370 19233 067376	012766 062706	000001 000016	000034		MOV	#1.34(SP) #16,SP	*,DODU.FLG			8699 8695

ZMLABO ML-11 LO IISCELLANEOUS CO	ODING SE	CTION	PACKU P	11113 2	2-DEC-80	12:16 PAGE 420		S	EQ 0407
19235 19236 19237				:ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOP:
19238 067402 19239 067406 19240 067410 19241 067414	062706 005202 020227	000006 000010		20\$: 21\$:	ADD INC CMP	#6.SP R2 R2.#10 19\$: NIB.PTR : NIB.PTR.*	869 868
19241 067414 19242 067416 19243 067420 19244 067424	003700 005201 020166	000004		22\$:	BLE INC CMP	19\$ R1 R1,4(SP) 14\$; WRD.CNT ; WRD.CNT,FINISH	867
19245 067426 19246 067430 19247 067436	005105 012766 012766	000017 000176	000006 000004		BLE INC CMP BLE COM MOV MOV INC	R5 #17,6(SP) #176,4(SP)		: TST.PAT : *.START : *.FINISH : TWICE	870 870 870 867
19248 067444 19249 067446 19250 067452 19251 067454	020427 003621 022626	000001			CMP	R4 R4,#1 13\$ (SP)+,(SP)+		: TWICE,*	
19250 067452 19251 067454 19252 067456 19253 067460 19254 067462 19255 067464 19256 067470	104467 006000 103002				BLE CMP TRAP ROR BHIS	67 R0 23\$ 1\$			863 870
1922/ 00/4/0	103002 000167 026627 001004 016700 104451	177020 000004	000001	23\$:	CWD	4(SP),#1 24\$; DODU.FLG,*	871
19258 067500 19259 067504 19260 067506	104444	124074			MOV TRAP TRAP	ML.LUN,R0 51 44		•	871
19261 067510 19262 067514 19263 19264	062706 000207	000012		24\$:	ADD RTS	#12.SP PC		:	859
19265 19270 19271				; Rout ; Maxi	ine Size: mum stack	263 words depth per invocation:	23 words		
19275 19279 067516	004767 104466 006000 103773 000207	176756		T41:: 1\$:	JSR TRAP ROR BLO RTS	PC.\$T41 66 R0 1\$ PC			8718
19285 19286 19287				; Rout		6 words depth per invocation:	0 words		

19296 19297 19298 ; 8721 !<BLF/PAGE>

```
19300 :ML4
19301 :
19302
19303 :
                                                                                      22-Dec-1980 09:24:31 22-Dec-1980 09:21:22
                                                                                                                   TOPS-20 Bliss-16 V2(212)
PA:<NEALE>BL3ML4.BL1.2 (92)
                          BGNTST:
                             TEST NUMBER: TST 42
                             TEST NAME: CS1 FUNCTION ABORT TEST
                             TEST DESCRIPTION:
                                    TEST CS1 FUNCTION ABORTS ON DETECTION OF CLASS 'A' & 'B' ERRORS BY:
                                    1. VIA MBUS WRITE FUNCTION LOAD THE GOOD BLOCK WITH BACKGROUND PATTERN OF ONES.
                                    2. CLEAR THE IO_BUF
                                    3. DO A MBUS READ FUNCTION. WHILE THE READ IS IN PROGRESS WRITE TO
                                         MLDA (CLASS 'A' ERROR) READ THE ID_BUF FOR ONES.
                                    4. CLEAR THE IO_BUF
                                    5. VIA PROM R/W MODE FORCE UV ERROR TO THE UV ADRS ERROR PROM
                                         (CLASS 'B' ERROR). DO A MBUS READ.
                                    6. READ IO_BUF FOR ZEROES
                             IMPLICIT INPUTS:
                                    IO BUF
                                    A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND
                                    WRITE FUNCTION ARE FOUND.
                          CLR_MBUS;
BAI = ONE;
IO_BUF = ONES;
GD_BLK_XFER ();
MLCS1 = write;
                                                                                     SET ON FIRST IO BUF ADRS
LOAD FIRST IO BUF ADRS
SET UP A GOOD BLOCK XFERR
                                                                                      !WRITE BACKGROUND PATTERN
                          TIME OUT LOOP:
                                                                                     !FORCE CLASS 'A' AND CLASS 'B' ERRORS
                           incr TWICE from 0 to 1 do
                               begin
BGNSUB;
                               CLR MBUS;
                               incr CNT from 0 to 255 do IO_BUF [.CNT] = ZEROES;
                                                                          !CLEAR OUT IO_BUF
                               if .TWICE eal 1
                                                                                    !IF 2ND PASS THEN FORCE 'B' ERROR
                               then
                                    begin
PROM_RW = ONE;
MLPD = %o'777';
19354
```

MISCELLANEOUS CODING S	ECTION					SEQ 0411
19412 :ML4 19413 :				22-Dec-1980 09:24:3 22-Dec-1980 09:21:2	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2</neale>	(92)
19412 :ML4 19413 : 19414 : 19415 : 8826 19416 : 8827 19417 : 8828 19418 : 8829						
19426 067532 004167 19427 067536 152777 19428 067544 016703 19429 067550 042703 19430 067554 142777	114322 000040 123450 124032 177770 000007 123432	\$142: JSR BISB MOV BIC BICB	R1,\$SAVE3 #40,aML.REG+40 ML.DUT,R3 #177770,R3 #7,aML.REG+40	;		8720 8723
19431 067562 150377 19432 067566 152777 19433 067574 012767 19434 067602 004767 19435 067606 012777	114322 000040 123450 124032 177770 000007 123432 123426 000010 123420 177777 121776 126254 000061 123340	BIC BICB BISB BISB MOV JSR MOV	R1.\$SAVE3 #40,aML.REG+40 ML.DUT.R3 #177770,R3 #7.aML.REG+40 R3.aML.REG+40 #10.aML.REG+40 #-1,JO.BUF PC.GD.BLK.XFER #61,aML.REG aML.REG+50 1\$ R3 2			8755 8756 8757 8758
19437 067620 100375 19438 067622 005003 19439 067624 104402 19440 067626 152777 19441 067634 016702	000040 123360 123742 177770 000007 123342 123336	1\$: TSTB BPL CLR 2\$: TRAP BISB MOV BIC	1\$ R3 2 #40,aML.REG+40 ML.DUT.R2	; 1	MICE	8761 8762 8763
19443 067644 142777 19444 067652 150277 19445 067656 005000 19446 067660 010001 19447 067662 006301	000007 123342	BICB BISB CLR 3\$: MOV ASL	#40,aML.REG+40 ML.DUT.R2 #177770,R2 #7,aML.REG+40 R2,aML.REG+40 R0 R0,R1 R1	: CI	NT NT,*	8766 8767
19449 067670 005200 19450 067672 020027 19451 067676 003770	011600	CLR INC CMP BLE	IO.BUF (R1) R0 R0,#377 3\$ R3,#1	: CI	NT ,*	8766
19452 067700 020327	000001	CMP BLE CMP BNE	R3,#1	; T(WICE,*	8769
19426 067532 004167 19427 067536 152777 19428 067544 016703 19429 067550 042703 19430 067554 142777 19431 067562 150377 19432 067566 152777 19433 067574 012767 19434 067602 004767 19435 067606 012777 19436 067614 105777 19437 067620 100375 19438 067622 005003 19439 067624 104402 19440 067626 152777 19441 067634 016702 19442 067640 042702 19443 067662 150277 19444 067652 150277 19445 067660 010001 19447 067662 006301 19448 067664 005061 19448 067660 010001 19447 067662 006301 19448 067660 010001 19447 067662 006301 19450 067676 005002 19450 067770 020327 19451 067676 005202 19453 067734 001006 19454 067736 012777 19458 067736 012777 19458 067736 012777 19458 067736 005002 19460 067740 001004 19461 067742 005002 19463 067760 005002	000100 123360 000777 123462 126134 000071 123220	BISB MOV 4\$: JSR MOV CLR TST BNE INC	#777, aml.REG+12 PC,GD.BLK.XFER #71, aml.REG R2 R3 5\$ R2	8 : TO	MICE	8772 8773 8776 8777 8779
19462 067744 012777 19463 067752 105777 19464 067756 100375 19465 067760 006002	177777 123232 123246	5\$: MOV TSTB BPL ROR	#-1,aML.REG+30 aML.REG+50 5\$ R2	;		8783

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 425	SEQ 0412
19467 19468 19469	:ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
19470 067762 103037 19471 067764 005002 19472 067766 010201 19473 067770 006301	6\$: MOV	R1	CNT 8787 CNT,* 8790
19474 067772 026127 011600 19475 070000 001423 19476 070002 104455 19477 070004 000137	177777 ASL CMP BEQ TRAP .WORD .WORD .WORD MOV	10.BUF (R1) ,#-1 7\$ 55 137	8793
19470 067762 103037 19471 067764 005002 19472 067766 010201 19473 067770 006301 19474 067772 026127 011600 19475 070000 001423 19476 070002 104455 19477 070004 000137 19478 070006 010526 19480 070012 012746 006406 19481 070016 012746 006506 19482 070022 012746 010112 19483 070026 012746 007540 19484 070032 012746 006046 19485 070036 012746 006046 19487 070044 104414 19488 070042 010600 19487 070044 104414 19488 070046 000435 19489 070050 005202 19490 070050 005202 19491 070056 003743 19492 070060 000437 19493 070064 010201 19495 070066 006301 19496 070070 005761 011600 19497 070074 001425	MOV	SYNC DUMPER MURD.10,-(SP) MURD.19,-(SP) MFNC.13,-(SP) MPHR.8,-(SP) MFOR.FMT,-(SP) MFOR.FMT,-(SP)	8794
19480 070012 012746 006406 19481 070016 012746 006506 19482 070022 012746 010112 19483 070026 012746 007540 19484 070032 012746 006046 19485 070036 012746 000005 19486 070042 010600 19487 070044 104414 19488 070046 000435 19489 070050 005202 19490 070052 020227 19491 070056 003743 19492 070060 000437 19493 070062 005002	MOV MOV MOV TRAP	3P, NU ; 3P, 1	
19489 070050 005202 19490 070052 020227 000100 19491 070056 003743	7\$: BR INC CMP BLE BR	10\$ R2 R2,#100 6\$.CNT,*
19492 070060 000437 19493 070062 005002 19494 070064 010201 19495 070066 006301 19496 070070 005761 011600	8\$: CLR 9\$: MOV ASL	R1	.CNT 8804 .CNT,* 8807
19498 070076 104455 19499 070100 000140	BEQ TRAP .WORD .WORD .WORD	10.BUF (R1) 11\$ 55 140 SYNC DUMPER	8810
19500 070102 010526 19501 070104 024052 19502 070106 012746 006406 19503 070112 012746 006506 19504 070116 012746 010112 19505 070122 012746 007552 19506 070126 012746 006046 19507 070132 012746 006046 19508 070136 010600 19509 070140 104414 19510 070142 062706 000014 19511 070146 000404 19512 070150 005202 19513 070152 020227 000100 19514 070156 003742 19515 070160 032777 19516 070160 032777 19518 070172 000150 19519 070174 010526	MOV MOV MOV	##RD.10,-(SP) ##RD.19,-(SP) #FNC.13,-(SP) #PHR.9,-(SP) #FOR.FMT,-(SP) #5,-(SP) SP,R0 14	8811
19506 070126 012746 006046 19507 070132 012746 000005 19508 070136 010600 19509 070140 104414	TRAP	#FOR.FMT,-(SP) #5,-(SP) SP,R0 ; SP,1	
19510 070142 062706 000014 19511 070146 000404	10\$: ADD BR	#14.SP ;	8812
19512 070150 005202 19513 070152 020227 000100 19514 070156 003742	115: INC	#14.SP 12\$ R2 R2,#100 9\$ #100000,aML.REG ;	CNT 8804
19515 070160 032777 100000	122766 12\$: BLE	#100000,aML.REG ;	8819
19510 070140 062706 000014 19511 070146 000404 19512 070150 005202 19513 070152 020227 000100 19514 070156 003742 19515 070160 032777 100000 19516 070166 001026 19517 070170 104455 19518 070172 000150 19519 070174 010526 19520 070176 024052 19521 070200 012746 010112	BNE TRAP .WORD .WORD .WORD MOV	13\$ 55 150 SYNC DUMPER WENC.13,-(SP) ;	8822
19520 070176 024052 19521 070200 012746 010112	MOV	#FNC.13,-(SP) ;	8823

CZMLABO ML-11 LI MISCELLANEOUS C	OGIC TES	T	MACRO M1113 2	2-DEC-80	12:16 PAGE 426				SEQ 04	613
19523 19524 19525			:ML4					22-Dec-1980 22-Dec-1980	0 09:24:31 T 0 09:21:22 F	TOPS PA:<
19526 070204 19527 070210 19528 070214 19529 070220 19530 070224 19531 070230 19532 070234 19533 070236 19534 070240 19535 070244 19536 070246	012746 012746 012746 012746 012746 012746 010600 104414 062706 104467 006000	006506 006416 007366 007152 006062 000006	13\$:	MOV MOV MOV MOV MOV TRAP ADD TRAP ROR BHIS	#WRD.19,-(SP) #WRD.11,-(SP) #PHR.1,-(SP) #WRD.59,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14 #16,SP 67 R0		; SP,*		8	8821 8824
19537 070250 19538 070252 19539 070256	103002 000167 005203 020327	177346	14\$: 15\$:	BHIS JMP INC	R0 15\$ 2\$ R3 R3,#1 14\$. TWICE		,	8761
19540 070240	020327 003772 000207	000001	,,,,,	CMP BLE RTS	R3.#1 14\$ PC		: TWICE,*			8720
19541 070264 19542 070266 19543 19544 19545 19550 19551 19555 19559 070270			; Rout ; Maxii	ine Size: mum stack	175 words depth per invocation:	11 words				
19555 19559 070270 19560 070270 19561 070274 19562 070276 19563 070300 19564 070302 19565 19566 19567	004767 104466 006000 103773 000207	177236	15:	JSR TRAP ROR BLO RTS	PC,\$T42 66 R0 1\$ PC		:		8	8827
19573	9970	1.015/04		ine Size: mum stack	6 words depth per invocation:	0 words				
19574 ;	0030	! BLF/PA	OE >							1200

19576 :ML4 19577 : 19578 : 19579 : 19580 : 19581 : 19582 : 19583 : 19584 : 19585 : 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BLI.2 (93) BGNTST: TEST NUMBER: TST 43 19586 19586 19587 19588 19589 19590 19591 19592 19593 TEST NAME: LAST BLOCK TRANSFER TEST TEST DESCRIPTION: TEST THE LAST BLOCK INDICATOR BIT FOR SETTING/NOT SETTING AND THE DSA REGISTER FOR INCREMENTING BY: 1. DOING MBUS TRANSFERRS AT EACH BLOCK FROM BLOCK ZERO TO LAST BLOCK -1 AND TEST LBT CLEAR AND DSA REGISTER TO BE INCREMENTED. 19594 19595 19596 19597 19598 19599 19600 19601 19602 19603 2. DO A MBUS TRANSFERR AT THE LAST BLOCK. TEST LBT TO BE SET AND TEST FOR CLEAR. 8856 8857 TEST DSA REG TO BE INCREMENTED. 19606 19607 8858 8859 8860 8861 8862 8863 8864 8865 8866 8867 8868 8869 8870 8871 8872 8873 8874 8875 8876 IMPLICIT INPUTS: IO BUF 19608 A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITE 19609 19610 19611 19612 19613 19614 19615 19616 19616 19617 19618 19619 19620 19621 19621 19623 19623 19624 19625 19626 19627 19628 19629 19630 FUNCTION ARE FOUND. local DODU_FLG; !DROP UNIT FLAG DODU_FLG = ZERO; incr DSA_CNT from 0 to .LST_BLK - 1 do !DO XFERRS UP TO THE LAST BLOCK begin BGNSUB; CLR_MBUS; ECC_DIS = ONE !DISABLE ECC MLWC = not 255; LOAD WORD COUNT 8877 8878 8879 8880 8881 8882 MLBA = IO BUF; !LOAD UBUS ADRS MLDA = .DSA_CNT; LOAD DSA WITH DSA_CNT MLCS1 = write: !DO A WRITE FUNCTION TIME_OUT_LOOP; if .LBT IS_SET !SEE IF THE LAST BLOCK XFERR BIT SET

```
19632 :ML4
19633
19634
19636
19636
19637
19638
19640
19641
19642
19643
19644
19645
19645
19651
19650
19651
19653
19653
19656
19656
19657
19658
19659
19659
19660
                                                                                                                                                 22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                                                 TOPS-20 Bliss-16 V2(212)
                                                                                                                                                                                                  PA: <NEALE>BL3ML4.BL1.2 (93)
                                                       then
                                                              begin

ERRDF (97, ASYNC, DUMPER);

PRINTB (THR_FMT, WRD_27, PHR_5, WRD_29);

PRINTB (FMT_7, .DSA_CNT);

DODU_FLG = ONE;
                                                                                                                                                !ERROR AND SET DODU_FLG IF SET
                                                       if .MLDA neg .DSA_CNT + 1
                                                                                                                                         SEE IF THE DSA REG INCREMENTED
                                                             begin

ERRDF (98, ASYNC, DUMPER);

PRINTB (THR_FMT, REG_6, WRD_31, WRD_14);

PRINTB (FMT_7, .DSA_CNT);

DODU_FLG = ONE;
                                                                                                                                            !ERROR AND SET DODU_FLG IF NOT
                                                       ENDSUB:
                                                       end:
                                8903 BGNSUB;
8904 CLR_MBUS;
8905 ECC_DIS = ONE;
8906 LAST_BLK_XFER ();
8907 MLCST = write;
8908 TIME_OUT_LOOP;
8909
8910 if .MLDA neq .LST_BLK + 1
8911 then
8912 begin
ERRDF (101, ASYNC, DUMPER);
PRINTB (THR_FMT, REG_6, WRD_31, WRD_14)
8915 PRINTB (FMT_7, .LST_BLK);
8916 PRINTB (FMT_7, .LST_BLK);
8917 end;
8918
8919 if .LBT IS_SET
8920 then
8921 begin
8922 MLDA = ONES;
8923
                                              BGNSUB:
                                                                                                                                                 !DISABLE ECC
                                                                                                                                                 SET UP A LAST BLOCK XFERR
                                                                                                                                                 !DO A WRITE FUNCTION
  19661
19662
19663
                                                                                                                                                !SEE IF DSA REGISTER INCREMENTED
 19664
19665
19666
19667
19668
19669
19670
19671
19672
19673
19674
19675
19676
19676
19677
19678
19680
19681
19681
                                                      begin

ERRDF (101, ASYNC, DUMPER);

PRINTB (THR_FMT, REG_6, WRD_31, WRD_14);

PRINTB (FMT_7, .LST_BLK);

DODU_FLG = ONE;
                                                                                                                                                !ERROR AND SET DODU_FLG IF NOT
                                                                                                                                            !SEE IF LBT BIT SET
                                                                                                                       !IF SET THEN TRY TO CLEAR IT
                                                      if .LBT IS_SET
                                                                                                                                                !SEE IF BIT CLEARED
                                                       then
                                                              begin
                                                                                                                                                !ERROR IF NOT
                                                              ERROF (99, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_27, PHR_2, WRD_11, WRD_17, REG_6);
                                                              DODU_FLG = ONE;
                                                               end:
                                  8931
 19684
19685
                                                      end
                                              else
                                                                                                                                                !LBT NOT SET
                                                      begin
                                                                                                                                                !ERROR AND SET DODU_FLG
```

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 429

MISCELLANEOUS CODING S	ECTION								SEQ 0416
19688 :ML4 19689 :						22-Dec-1980 22-Dec-1980	09:24:31 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (93</neale>)
19688 :ML4 19689 :19690 19691 : 8935 19692 : 8936 19693 : 8937 19694 : 8938 19695 : 8939 19696 : 8940 19697 : 8941 19698 : 8942 19699 : 8943 19700 : 8944 19701 : 8945 19702 : 8946 19703 : 8946 19705 : 8948	ERR PRI DOD end		ASYNC, REMT, W	DUMPER) IRD_27, PI	HR_1, WRD_11, WRD_	27);			
19697 : 8941	24 DOD		CET			ISSOS TUTE I	BUT 15 BOOM		
19699 : 8943	then	OU_FLG IS	-2E1			!DROP THIS U	INTI IL DODO	TLE SEL	
19694 : 8938 19695 : 8939 19696 : 8940 19697 : 8941 19698 : 8942 19699 : 8943 19700 : 8944 19701 : 8945 19702 : 8946 19703 : 8947 19704 : 8948 19705 : 8949	DOD DOD end	in OU (.ML_L LN; I;	.UN);						
19705 : 8949 19709	ENDTST;								
19713 070304 004167	113566		\$143:	JSR CLR	R1,\$SAVE4		; DODU.	51.6	8829 8869 8871
19713 070304 004167 19714 070310 005001 19715 070312 016704 19716 070316 005002 19717 070320 000541 19718 070322 010203 19719 070324 005203 19720 070326 104402 19721 070330 152777 19722 070336 016700 19723 070342 042700 19724 070346 142777 19725 070366 012777 19728 070374 012777 19729 070402 010277	121254			MOV	LST.BLK,R4		:		8871
19717 070320 000541				CLR BR	LST.BLK,R4 R2 6\$ R2,R3		; DSA.C		
19/18 0/0322 010203			1\$:	MOV	RZ,R3 R3		; DSA.C	NT,*	8891
19720 070326 104402 19721 070330 152777	000040	122656	2\$:	BISB	2 #40.aml.REG+40				8872 8873
19720 070324 003203 19721 070326 104402 19721 070330 152777 19722 070336 016700 19723 070342 042700 19724 070346 142777 19725 070354 150077 19726 070360 152777 19727 070366 012777 19728 070374 012777	000040 123240 177770 000007 122634 000002 177400 011600 122576			INC TRAP BISB MOV BIC BICB BISB BISB MOV MOV	#40,aML.REG+40 ML.DUT.RO #177770,RO #7,aML.REG+40 R0,aML.REG+40 #2,aML.REG+120 #-400,aML.REG+120 #-400,aML.REG+16 #10.BUF,aML.REG+16 R2,aML.REG+30				
19724 070346 142777	000007	122640		BICB	#7.24L.REG+40				
19726 070360 152777	000002	122706		BISB	#2, aML. REG+120		;		8875
19727 070366 012777 19728 070374 012777 19729 070402 010277	011600	122706 122570 122572		MOV	#10.BUF, aml. REG	20			8875 8876 8877 8878
19739 070402 010277 19730 070406 012777	000061	122540		MOV	R2, aML.REG+30 M61, aML.REG		: DSA.C	NT,*	8878 8879
19731 070414 105777 19732 070420 100375	000061		3\$:	TSTB BPL	#61,aML.REG aML.REG+50 3\$				
19733 070422 032777	002000	122574		BEQ	#2000, aml. REG+50)	;		8882
19730 070406 0102777 19731 070414 105777 19732 070420 100375 19733 070422 032777 19734 070430 001433 19735 070432 104455 19736 070434 000141 19737 070436 010464 19738 070442 012746				TRAP	4 \$ 55 141		:		8885
19737 070436 010464				. WORD . WORD . WORD	ASYNC				
19739 070442 012746	006610 007472			MOV	DUMPER MARD. 29,-(SP)		,		8886
19740 070446 012746 19741 070452 012746	001412			MOV	MPHR.5,-(SP) MWRD.27,-(SP)				

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 430		
			S	EQ 0417
19743 19744 19745	ML4		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
19746 070456 012746 006034 19747 070462 012746 000004	MOV	#THR.FMT,-(SP) #4,-(SP) SP,R0 14		
19748 070466 010600 19749 070470 104414	MOV TRAP	14	; SP,*	
19748 070466 010600 19749 070470 104414 19750 070472 010216 19751 070474 012746 004562 19752 070500 012746 000002 19753 070504 010600 19754 070506 104414 19755 070510 012701 000001 19756 070514 062706 000016 19757 070520 027703 122460 19758 070524 001433 19759 070526 104455 19760 070530 000142 19761 070532 010464 19762 070534 024052 19763 070536 012746 006432 19765 070546 012746 006632	MOV MOV MOV	R2, (SP) WFMT.7, -(SP) W2, -(SP) SP, R0	; DSA.CNT,*	8887
19753 070504 010600 19754 070506 104414	MOV	SP,R0	; SP,*	
19754 070506 104414 19755 070510 012701 000001 19756 070514 062706 000016 19757 070520 027703 122460	MOV	#1,R1 #16,SP aml.REG+30,R3	: *.DODU.FLG	8888 8884 8891
19756 070514 062706 000016 19757 070520 027703 122460	4\$: CMP	amL.REG+30,R3		8891
19759 070526 104455	BEQ TRAP . WORD . WORD . WORD	#ML.REG+30,R3 5\$ 142 ASYNC DUMPER MWRD.14,-(SP) MWRD.31,-(SP) MREG.6,-(SP) MTHR.FMT,-(SP) M4,-(SP) SP,R0 14 R2 (SP)		8894
19761 070532 010464	WORD	ASYNC		
19763 070536 012746 006442	MOV	MWRD. 14, -(SP)		8895
19763 070536 012746 006442 19764 070542 012746 006632 19765 070546 012746 010316 19766 070552 012746 006034 19767 070556 012746 000004	MOV	#REG.6,-(SP)		
19767 070556 012746 006034	MOV	#THR.FMT,-(SP) #4,-(SP)		
19768 070562 010600 19769 070564 104414	MOV TRAP	SP.R0 14	; SP,*	
19766 070552 012746 006034 19767 070556 012746 000004 19768 070562 010600 19769 070564 104414 19770 070566 010216 19771 070570 012746 004562	MOV	R2,(SP) #FMT.7,-(SP) #2,-(SP) SP,R0 14 #1,R1	; DSA.CNT,*	8896
19773 070600 010600	MOV	#2,-(SP) SP.RO	; SP,*	
19774 070602 104414 19775 070604 012701 000001	TRAP	14 #1 .R1	; *,DODU.FLG	8807
19775 070604 012701 000001 19776 070610 062706 000016 19777 070614 104467 19778 070616 006000 19779 070620 103642	MOV ADD 5\$: TRAP	#16,SP 67 R0 2\$ R2 R2,R4	:	8897 8893 8898
19778 070616 006000	PUP	RO		0070
19780 070622 005202	INC	R2	: DSA.CNT : DSA.CNT,*	8871
19782 070626 002635	6\$: CMP BLT	1\$; DSA.CNI,*	
19784 070632 152777 000040	6\$: CMP BLT 7\$: TRAP 122354 BISB	#40,aML.REG+40		8901 8903
19786 070644 042704 177770	122336 BICB BISB 122404 BISB	#177770,R4		
19787 070650 142777 000007 19788 070656 150477 122332	122336 BICB BISB	#7,aML.REG+40 R4,aML.REG+40		
19789 070662 152777 000002 19790 070670 004767 125220	122404 BISB JSR	#2,aML.REG+120 PC_LAST_BLK_XFER		8905 8906 8907
19784 070632 152777 000040 19785 070640 016704 122736 19786 070644 042704 177770 19787 070650 142777 000007 19788 070656 150477 122332 19789 070662 152777 000002 19790 070670 004767 125220 19791 070674 012777 000061 19792 070702 105777 122316	122252 MOV TSTB	#61,aML.REG aML.REG+50		8907
19793 070706 100375 19794 070710 016702 120656	BPL	8\$ LST_BLK_B2		8910
19776 070610 062706 000016 19777 070614 104467 19778 070616 006000 19779 070620 103642 19780 070622 005202 19781 070624 020204 19782 070626 002635 19783 070630 104402 19784 070632 152777 000040 19785 070640 016704 122736 19786 070644 042704 177770 19787 070650 142777 000007 19788 070650 142777 000007 19789 070662 152777 000002 19790 070670 004767 122332 19791 070674 012777 000061 19792 070702 105777 122316 19793 070706 100375 19794 070710 016702 122316 19795 070714 005202 19796 070716 027702 12262	INC	R2 am peg+30 p2		6710
19797 070722 001434	BEQ	#40,aml.REG+40 ML.DUT.R4 #177770,R4 #7,aml.REG+40 R4,aml.REG+40 #2,aml.REG+120 PC,LAST.BLK.XFER #61,aml.REG aml.REG+50 8\$ LST.BLK,R2 R2 aml.REG+30,R2 9\$		

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 431		SEO 0/18
19799	;ML4			SEQ 0418 22-Dec-1980 09:24:31 TOPS
19800 19801				22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
19802 070724 104455 19803 070726 000145 19804 070730 010464 19805 070732 024052	TRAP .WORD .WORD .WORD	55 145 ASYNC		8913
19806 070734 012746 006442 19807 070740 012746 006632 19808 070744 012746 010316 19809 070750 012746 006034	MOV MOV MOV MOV MOV	ASYNC DUMPER WWRD.14,-(SP) WWRD.31,-(SP) WREG.6,-(SP) WTHR.FMT,-(SP) W4,-(SP) SP,R0		8914
19811 070760 010600 19812 070762 104414	MOV TRAP	199	; SP,*	
19814 070770 012746 004562 19815 070774 012746 000002	MOV MOV MOV	LST.BLK, (SP) #FMT.7,-(SP) #2,-(SP) SP,R0 14		8915
19816 071000 010600 19817 071002 104414	MOV	SP,R0 14	; SP,*	
19818 071004 012701 000001 19819 071010 062706 000016 19820 071014 032777 002000 19821 071022 001440 19822 071024 012777 177777	MOV ADD 122202 98: RIT	#1,R1 #16,SP #2000_amREG+50	: *,DODU.FLG	8916 8912 8919
19824 071040 001457	122152 BEQ 122164 BIT BEQ	10\$ #-1.aML.REG+30 #2000,aML.REG+50 11\$:	8922 8924
19825 071042 104455 19826 071044 000143 19827 071046 010464 19828 071050 024052	BEQ TRAP .WORD .WORD .WORD	143 ASYNC DUMPER	•	8927
19829 071052 012746 010316 19830 071056 012746 006470 19831 071062 012746 006416	MOV MOV	#REG.6,-(SP) #MRD.17,-(SP) #MRD.11,-(SP) #PHR.2,-(SP) #MRD.27,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14		8928
19834 071076 012746 006062 19835 071102 012746 000006	MOV	#FIV.FMT,-(SP) #6,-(SP)		
19836 071106 010600 19837 071110 104414	MOV TRAP	SP.R0	; SP,*	
19832 071066 012746 007404 19833 071072 012746 006602 19834 071076 012746 006062 19835 071102 012746 000006 19836 071106 010600 19837 071110 104414 19838 071112 012701 000001 19839 071116 062706 000016 19840 071122 000426 19841 071124 104455 19843 071130 010464	MOV ADD BR 10\$: TRAP .WORD	#16.SP	*,DODU.FLG	8929 8926 8919 8935
19832 071066 012746 006602 19833 071072 012746 006602 19834 071076 012746 006062 19835 071102 012746 000006 19836 071106 010600 19837 071110 104414 19838 071112 012701 000001 19839 071116 062706 000016 19840 071124 104455 19842 071126 000144 19843 071130 010464 19844 071132 024052 19845 071134 012746 006602 19846 071140 012746 006416 19847 071144 012746 006602 19848 071150 012746 006602 19849 071154 012746 006602 19850 071160 012746 006046 19851 071164 010600 19852 071166 104414 19853 071170 012701 000001	MOV MOV MOV MOV TRAP MOV ADD BR 10\$: TRAP WORD WORD WORD MOV MOV MOV MOV MOV MOV MOV MOV	118 55 144 ASYNC DUMPER WWRD.27,-(SP) WWRD.11,-(SP) WPHR.1,-(SP) WHRD.27,-(SP) WFOR.FMT,-(SP) W5,-(SP) SP,R0 14 W1,R1		8936
19850 071160 012746 000005 19851 071164 010600	MOV	#5,-(SP) SP.RO	; SP,*	
19852 071166 104414 19853 071170 012701 000001	TRAP	14 #1,R1	: *,DODU.FLG	8937

-	CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 432		SEQ 0419	
	19855 19856 19857	;ML4	#1/ CD		22-Dec-1980 09:24:31 TOP 22-Dec-1980 09:21:22 PA:	
	19858 071174 062706 000014 19859 071200 104467 19860 071202 006000 19861 071204 103611	11\$: TRAP ROR BLO DEC BNE MOV TRAP	#14,SP 67 R0 7\$ R1 12\$ ML.LUN,R0		893 893	
	19862 071206 005301 19863 071210 001004	DEC BNE	R1 12\$; DODU.FLG	894	2
	19864 071212 016700 122362 19865 071216 104451 19866 071220 104444 19867 071222 000207	IKAP	ML.LUN,RO 51 44 PC	•	894	
	19867 071222 000207 19868	12\$: RTS	PC	:	882	9
	19869 19870 19875 19876 19880	; Routine Size: ; Maximum stack	232 words depth per invocation: 12	words		
	19884 071224 19885 071224 004767 177054 19886 071230 104466 19887 071232 006000 19888 071234 103773 19889 071236 000207 19890	T43:: 1\$: JSR TRAP ROR BLO RTS	PC,\$T43 66 R0 1\$ PC		894	7
	19891 19892 19897 19898 19899 ; 8950 ! <blf <="" td=""><td></td><td>6 words depth per invocation: 0 w</td><td>vords</td><td></td><td>-</td></blf>		6 words depth per invocation: 0 w	vords		-

```
19901 :ML4
19902 :
19903 :
                                                                                                            22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                TOPS-20 Bliss-16 V2(212)
                                                                                                                                                PA: <NEALE>BL3ML4.BL1.2 (94)
                                 BGNTST:
                                  ! TEST NUMBER: TST 44
                                  ! TEST NAME:
                                                         INVALID ADRS TEST
                                 ! TEST DESCRIPTION:
                                             TEST THE DETECTION OF ILLEGAL DSA ADDRESSES BY:
                                             1. DOING A MBUS WRITE FUNCTION
AT ALL POSSIBLE ILLEGAL DSA
                                                    ADDRESSES AND TEST THE
                                                    IAE BIT SET.
                                ! IMPLICIT INPUTS:
                                             IO BUF
A VECTOR OF 256 WORDS WHERE
DATA FOR MBUS READS AND WRITE
FUNCTION ARE FOUND.
                       8977
8978
8979
8980
8981
8982
8983
8984
8985
8986
8987
8988
8989
8990
8991
19933
19934
19935
19936
19937
19938
19939
19940
19941
19942
19943
19945
19945
                                local
                                       IAE_CNT;
                                                                                                           !INVLID ADRS COUNT
                                 IAE_CNT = .LST_BLK;
                                                                                                           !START AT LAST BLOCK + 1
                                                                                                           !TEST FOR ALL INVALID ADDRESSES
                                       begin
IAE_CNT = .IAE_CNT + 1;
BGNSUB;
                                                                                                           !INCREMENT IAE_CNT
                                       CLR_MBUS;
MLDA = .IAE_CNT;
MLWC = not 255;
                                                                                                           !LOAD DSA
                                                                                                           LOAD WORD COUNT
                                       MLBA = IO_BUF;
                                       MLCS1 = write;
                                                                                                           !DO A WRITE FUNCTION
                                       if .IAE IS_NOT_SET
                                                                                                           !SET IF IAE SET
                                             begin !ERROR IF NOT SE
ERROF (102, ASYNC, DUMPER);
PRINTB (FIV_FMT, WRD_30, PHR_1, WRD_11, WRD_30, WRD_10);
                                                                                                           !ERROR IF NOT SET
                                             end;
                                       ENDSUB:
```

19957 ; M.4 19958 ; 19958 ; 19958 ; 19959 ; 19											354 0451
19960 19003	19958 :							22-Dec-1980 09:24 22-Dec-1980 09:21	4:31 1:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (94</neale>	,
1997 071240 010146 120324 15141 1414 1514 1414 1514 1414 1	19960 : 9 19961 : 9 19962 : 9	9003	end until .	IAE_CNT	eql %o'	177777':		!REPEAT UNTIL ALL	L TESTE	D	
19971 071240 010146 19972 071242 016701 19973 071242 016701 19973 071242 016701 19973 071243 005201 19973 071250 104402 19974 071250 104402 19975 071250 104402 19975 071250 104402 19976 071250 104402 19977 071266 016700 1221316 19977 071266 042700 177770 19978 071270 12777 000001 19978 071270 12777 000001 19978 071270 12777 000001 19978 071270 12777 000001 19978 071270 12777 000001 19978 071270 12777 000001 19978 071270 12777 000001 19978 071270 12777 000001 19978 071270 12777 000001 19978 071270 12777 000001 19978 071270 12777 000001 19978 071270 12777 000001 19978 071270 12777 000001 19978 071270 12777 000001 121676 1998 071340 00001 121676 1998 071340 010465 1998 071340 010464 1998 071340 010464 1998 071340 010464 1998 071340 010464 1998 071340 010464 19999 071350 010274 000000 107460 00600 19999 071350 010274 000000 107460 00600 19999 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071350 010274 00000 071450 000000 10000 071450 000000 10000 071450 000000 10000 071450 0000000 10000 071450 0000000 10000 071450 0000000 10000 071450 00000000000000000000000000000000000	19963 9	9006	ENDTST;								
19998 071406 104414 19999 071410 062706 000016 19999 071414 104467 20000 071414 104467 20001 071416 006000 20002 071420 103713 20003 071422 020127 177777 20004 071426 001307 20004 071426 001307 20005 071430 012601 20006 071432 000207 20008 ; Routine Size: 62 words TRAP 14 ADD #16,SP ; Routine Size: 62 words	19971 071240 010 19972 071242 016 19973 071246 005	0146 6701 5201	120324		15:	MOV	R1,-(SP) LST.BLK,R1 R1		*,IAE	. CNT NT	8949 8983 8987
19998 071406 104414 19999 071410 062706 000016 19999 071414 104467 20000 071414 104467 20001 071416 006000 20002 071420 103713 20003 071422 020127 177777 20004 071426 001307 20004 071426 001307 20005 071430 012601 20006 071432 000207 20008 ; Routine Size: 62 words TRAP 14 ADD #16,SP ; Routine Size: 62 words	19975 071250 104	2777	000040	121734	25:	BISB	#40, amL. REG+40		:		8988
19998 071406 104414 19999 071410 062706 000016 19999 071414 104467 20000 071414 104467 20001 071416 006000 20002 071420 103713 20003 071422 020127 177777 20004 071426 001307 20004 071426 001307 20005 071430 012601 20006 071432 000207 20008 ; Routine Size: 62 words TRAP 14 ADD #16,SP ; Routine Size: 62 words	19977 071264 042	2700	177770	121714		BIC	#177770,R0				
19998 071406 104414 19999 071410 062706 000016 19999 071414 104467 20000 071414 104467 20001 071416 006000 20002 071420 103713 20003 071422 020127 177777 20004 071426 001307 20004 071426 001307 20005 071430 012601 20006 071432 000207 20008 ; Routine Size: 62 words TRAP 14 ADD #16,SP ; Routine Size: 62 words	19979 071276 150	0077	121712	121716		BISB	RO, aML. REG+40			MT A	9000
19998 071406 104414 19999 071410 062706 000016 19999 071414 104467 20000 071414 104467 20001 071416 006000 20002 071420 103713 20003 071422 020127 177777 20004 071426 001307 20004 071426 001307 20005 071430 012601 20006 071432 000207 20008 ; Routine Size: 62 words TRAP 14 ADD #16,SP ; Routine Size: 62 words	19981 071306 012	2777	177400	121650		MOV	#-400, aML.REG+10)	IAE.U	N1,*	8991 8003
19998 071406 104414 19999 071410 062706 000016 19999 071414 104467 20000 071414 104467 20001 071416 006000 20002 071420 103713 20003 071422 020127 177777 20004 071426 001307 20004 071426 001307 20005 071430 012601 20006 071432 000207 20008 ; Routine Size: 62 words TRAP 14 ADD #16,SP ; Routine Size: 62 words	19983 071322 012	777	000061	121624		MOV	#61.aML.REG	1			8993
19998 071406 104414 19999 071410 062706 000016 19999 071414 104467 20000 071414 104467 20001 071416 006000 20002 071420 103713 20003 071422 020127 177777 20004 071426 001307 20004 071426 001307 20005 071430 012601 20006 071432 000207 20008 ; Routine Size: 62 words TRAP 14 ADD #16,SP ; Routine Size: 62 words	19985 071336 001	1026	002000	1210/0		BNE	3\$				1000
19998 071406 104414 19999 071410 062706 000016 19999 071414 104467 20000 071414 104467 20001 071416 006000 20002 071420 103713 20003 071422 020127 177777 20004 071426 001307 20004 071426 001307 20005 071430 012601 20006 071432 000207 20008 ; Routine Size: 62 words TRAP 14 ADD #16,SP ; Routine Size: 62 words	19987 071342 000 19988 071344 010	0146				. WORD	146 ASYNC				0770
19998 071406 104414 19999 071410 062706 000016 19999 071414 104467 20000 071414 104467 20001 071416 006000 20002 071420 103713 20003 071422 020127 177777 20004 071426 001307 20004 071426 001307 20005 071430 012601 20006 071432 000207 20008 ; Routine Size: 62 words TRAP 14 ADD #16,SP ; Routine Size: 62 words	19989 071346 024 19990 071350 012	052 746	006406			. WORD	DUMPER MURD 10 - (SP)				8999
19998 071406 104414 19999 071410 062706 000016 19999 071414 104467 20000 071414 104467 20001 071416 006000 20002 071420 103713 20003 071422 020127 177777 20004 071426 001307 20004 071426 001307 20005 071430 012601 20006 071432 000207 20008 ; Routine Size: 62 words TRAP 14 ADD #16,SP ; Routine Size: 62 words	19991 071354 012 19992 071360 012	2746 2746	006624 006416			MOV	MURD.30,-(SP) MURD.11,-(SP)				
19998 071406 104414 19999 071410 062706 000016 19999 071414 104467 20000 071414 104467 20001 071416 006000 20002 071420 103713 20003 071422 020127 177777 20004 071426 001307 20004 071426 001307 20005 071430 012601 20006 071432 000207 20008 ; Routine Size: 62 words TRAP 14 ADD #16,SP ; Routine Size: 62 words	19993 071364 017 19994 071370 012	746	007366 006624			MOV	#PHR.1,-(SP) #WRD.30,-(SP)				
19998 071406 104414 19999 071410 062706 000016 19999 071414 104467 20000 071414 104467 20001 071416 006000 20002 071420 103713 20003 071422 020127 177777 20004 071426 001307 20004 071426 001307 20005 071430 012601 20006 071432 000207 20008 ; Routine Size: 62 words TRAP 14 ADD #16,SP ; Routine Size: 62 words	19995 071374 012 19996 071400 012	746 746	006062			MOV	#FIV.FMT,-(SP) #6,-(SP)				
20003 071422 020127 177777 CMP R1,N-1 ; IAE.CNT,* 9004 20004 071426 001307 BNE 1\$ 20005 071430 012601 MOV (SP)+,R1 ; 9004 20006 071432 000207 RTS PC ; Routine Size: 62 words	19998 071406 104	414				TRAP	14		; SP,*		
20003 071422 020127 177777 CMP R1,N-1 ; IAE.CNT,* 9004 20004 071426 001307 BNE 1\$ 20005 071430 012601 MOV (SP)+,R1 ; 9004 20006 071432 000207 RTS PC ; Routine Size: 62 words	20000 071414 104	467	000016		3\$:	TRAP	67				8997 9000
20005 071430 012601 MOV (SP)+,R1 ; 20006 071432 000207 RTS PC ; Routine Size: 62 words ; Routine Size: 62 words	20002 071420 103	713	177777			BLO	2\$ 21 #-1		. TAF C		2004
20007 20008 ; Routine Size: 62 words	20005 071426 001	307	177777			BNE	1\$		IAE.U	NI,*	
20008 ; Routine Size: 62 words ; Maximum stack depth per invocation: 8 words	20006 071432 000	207				RTS	PC				8949
	20008				Rout	ine Size:	62 words	ation: 8 words			

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 435

20018
20019
20023
20027 071434
20028 071434 004767 177600
20029 071440 104466
20030 071442 006000
20031 071444 103773
20032 071446 000207
RTS PC

; Routine Size: 6 words
; Maximum stack depth per invocation: 0 words
20040
20041
20042 ; 9007 !<BLF/PAGE>

9004

20044	ML4							22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (95)</neale>	
20046 20047 20048		9008 9009	BGNTST;							
20048 20050 20051 20052 20053 20054 20055 20056 20057 20060 20061 20062 20063 20064 20065 20066 20067 20068 20069 20070 20071 20072 20073 20076 20076 20076 20076 20077 20078		9011 9012 9013	TEST N	IUMBER:	TST 45					
20053		9014	TEST N	IAME:	ADRS ON	ERFLOW E	SIT TEST			
20055 20056 20057		9016 9017 9018	TEST D	ESCRIPTI TEST THE	ON: DETECT	ION OF	ADDRESS OVERFLOWS	BY:		
20058	:	9019		1. STAF	TING AT	THE LAS	ST BLOCK DO A TWO	BLOCK TRANSFER.		
20060		9020		2. READ	THE AC	E BIT SE	т.			
20062	:	9022	IMPLIC	IT INPUT	s:					
20063		9024		IO BUF		LI SOSOU	HERE DATA FOR MR	IS READS AND LIRITE		
20065		9021 9022 9023 9024 9025 9026 9027 9028 9029 9031 9032 9033 9034 9035 9036 9037 9038 9039 9040 9041 9042 9043	_	FUNCTION	S ARE F	OUND.	WIERE DATA FOR TE	US READS AND WRITE		
20067	:	9028								
20069		9029	CLR_MBUS	ot 511;				!DO TWO BLOCK XFERR		
20070		9031	MLWC = n MLBA = I MLDA = . MLCS1 =	O BUF;				!DO TWO BLOCK XFERR !LOAD UBUS ADRS !START AT LAST BLOCK !DO A WRITE FUNCTION		
20072		9033	MLCS1 = TIME_OUT	write;				!DO A WRITE FUNCTION		
20074		9035						1000 10 100 000		
20076	:	9036	if .AOE		EI			!SEE IF AOE SET		
20077	:	9038	begi ERRD	n F (103,	SYNC. D	UMPER):		!ERROR IF NOT SET		
20079		9040	PRIN end;	ITB (FOR_	FMT, WR	D_26. PH	IR_1, WRD_11, FNC	_19);		
20081		9042								
20086			ENDTST;							
20090	071450 071456	016700	000040 122120 177770	121536	\$145:	MOV	#40, aml. REG+40 ML. DUT, RO	:		9009
20080 20081 20082 20086 20090 20091 20092 20093 20094 20095 20096 20097	071450 071456 071462 071466 071474 071500 071506 071514	152777 016700 042700 142777 150077 012777	177770	121520		BIC	ML.DUT.RO #177770,RO #7,aML.REG+40 RO,aML.REG+40 #-1000,aML.REG+ #10.BUF,aML.REG LST.BLK,aML.REG			
20094	071474	150077	000007 121514 177000			BISB	RO, aML . REG+40	10		0070
20096	071506	012/1/	011600	121456 121460		MOV	#10.BUF, aML.REG	+20		9030 9031 9032
20097	0/1514	016777	120052	121462		MOV	LST.BLK, aML.REG	+50 ;		9032

CZMLABO ML-11 LOGIC 1 MISCELLANEOUS CODING	EST SECTION	MACRO M	1113 22	-DEC-80	12:16 PAGE 437				SEQ	0424
20099 20100			ML4					22-Dec-1980 22-Dec-1980	09:24:31 09:21:22	TOPS PA:<
20102 071522 01277 20103 071530 10577	7 000061 7 121470	121424	15:	MOV	#61,aML.REG aML.REG+50		;			9033
20104 071534 10037 20105 071536 03277 20106 071544 00102		121470		BPL BIT BNE	1\$ #1000,aML.REG+60		:			9036
20107 071546 10445 20108 071550 00014	5			TRAP .WORD .WORD .WORD	2 \$ 55 147		:			9039
20110 071554 02405 20111 071556 01274 20112 071562 01274 20113 071566 01274 20114 071572 01274 20115 071576 01274	6 006046			MOV MOV MOV MOV	SYNC DUMPER MFNC.19,-(SP) MWRD.11,-(SP) MPHR.1,-(SP) MWRD.26,-(SP) MFOR.FMT,-(SP) M5,-(SP) SP,R0		:			9040
20116 0/1602 012/4	0			MOV MOV TRAP	19		; SP,*			
20118 071610 10441 20119 071612 06270 20120 071616 00020 20121	6 000014		2\$:	ADD RTS	#14.SP PC		:			9038 9006
20122 20123 20128 20129			; Routi ; Maxim	ne Size: um stack	52 words depth per invocation:	6 words				
20133 20137 071620 20138 071620 00476 20139 071624 10446 20140 071626 00600 20141 071630 10377 20142 071632 00020	7 177624 6 0 3 7		T45:: 1\$:	JSR TRAP ROR BLO RTS	PC,\$T45 66 R0 1\$ PC		;			9041
20140 071626 00600 20141 071630 10377 20142 071632 00020 20143 20144 20145 20150 20151 20152 ; 904			; Routi ; Maxim	ne Size: um stack	6 words depth per invocation:	0 words				
20132; 904	4 ! <blf p<="" td=""><td>AGE></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></blf>	AGE>								

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20154 :ML4
20155 :
20156 :
20157 :
                                                                                22-Dec-1980 09:24:31 22-Dec-1980 09:21:22
                                                                                                         TOPS-20 Bliss-16 V2(212)
PA:<NEALE>BL3ML4.BLI.2 (96)
                         BGNTST:
20161
                         ! TEST NUMBER: TST 46
                           TEST NAME:
                                           SYNC BUS PARITY TEST
                           TEST DESCRIPTION:
                                  TEST ABILITY OF SYNC BUS TO DETECT AND GENERATE GOOD PARITY BY:
                                  1. VIA MBUS WRITE FUNCTION WRITE ALTERNATING ONES AND ZEROES TO THE
                                       DEVICE AND READ THE DPAR BIT CLEARED.
                                  2. REPEAT WITH SHIFTED DATA
                                  3. VIA MBUS READ FUNCTION READ THE ALTERNATING PATTERN AND MOPE CLEARED.
                        ! IMPLICIT INPUTS:
                                  IO BUF
                                  A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITES
20178
                                  FUNCTIONS ARE FOUND.
20180
20181 ;
                        if .PAR_DIS IS_SET
                                                                              SEE IF PARITY IS DISABLED
                         then
                             begin
PRINTB (THR_FMT, FNC_3, WRD_7, WRD_37);
                                                                               !PRINT MESSAGE AND EXIT TST IF YES
                             EXIT_TST;
20187 :
                             end:
                        CLR_MBUS;
BAI = ONE;
IO_BUF = %o'125252';
                                                                                !SET ON FIRST IO BUF ADRS
                                                                                !ALTERNATE 1, 0 PATTERN
                         incr TWICE from 0 to 1 do
                                                                                !REPEAT LOOP TWICE
20194
20195
                             begin
BGNSUB;
                             GD_BLK_XFER ();
MLCS1 = write;
                                                                                !SET UP A GOOD BLOCK XFERR
                                                                                !DO A WRITE FUNCTION
                             TIME_OUT_LOOP;
                             if .DPAR IS_SET
                                                                                SEE IF DPAR GOT SET
                             then
                                 begin :ERRUR : ERRUR : PRINTB (FOR_FMT, WRD_23, WRD_6, WRD_7, WRD_9);
                                                                                !ERROR IF SET
                             IO_BUF = .IO_BUF^ONE;
                                                                                !SHIFT THE IO BUF & REPEAT
                                                                              CLEAR OUT ERROR REG & REPEAT
                             MLER = ZEROES:
```

```
CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 439
```

```
22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                    TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (96)
                                     ENDSUB:
                                      end:
                              incr TWICE from 0 to 1 do
                                                                                                                      !REPEATE LOOP TWICE
                                     begin
BGNSUB;
                                      CLR_MBUS;
                                                                                                                       SET ON FIRST IO BUF ADRS
SET UP A GOOD BEOCK XFERR
DO A READ XFERR
                                      BAI = ONE
                                     GD_BLK_XFER ();
MLCS1 = read;
TIME_OUT_LOOP;
                                     if .MDPE IS_SET
                                                                                                                      !SEE IF READ GENERATED BAD PARITY
                                      then
                                            begin !ERROR
ERROF (106, SYNC, DUMPER);
PRINTB (FOR_FMT, WRD_23, WRD_6, WRD_7, WRD_8);
                                                                                                                        !ERROR IF MDPE SET
                                     ENDSUB;

IO BUF = .IO BUF^ONE;

GD_BLK_XFER ();

MLCS1 = write;
                                                                                                                       !MAKE DATA PATTERN HAVE ONE LESS ONE
                                                                                                                    SET UP A GOOD BLK XFER CHANGE THE BACKGROUND IN MEMORY
                                     TIME_OUT_LOOP;
                                      end:
                             ENDTST:
               010146
026727
001021
012746
012746
012746
012746
012746
010600
104414
104463
062706
000167
152777
016701
042701
142777
                                                                                          R1,-(SP)
                                                                                                                                                                                                                                9043
                                                            $T46:
                              117724 000001
                                                                                          PAR.DIS.#1
                                                                           BNE
MOV
MOV
                             006700
006344
007750
006034
000004
                                                                                         MURD.37,-(SP)
MURD.7,-(SP)
MFNC.3,-(SP)
MTHR.FMT,-(SP)
                                                                                                                                                                                                                                9073
                                                                           MOV
                                                                           MOV
071662
071666
071672
071674
071676
071700
071704
071710
071716
071722
071726
                                                                                          #4,-(SP)
SP,R0
14
63
                                                                           MOV
                                                                                                                                                     ; SP. *
                                                                           MOV
                                                                           TRAP
                                                                           TRAP
                              000012
000402
000040
121660
177770
                                                                                          #12.SP
                                                                           ADD
                                                                           JMP
                                                                                         #40.aML.REG+40
ML.DUT.R1
#177770.R1
                                             121276 15:
                                                                           BISB
                                                                           MOV
                                                                           BIC
                                             121260
                                                                          BICB
                                                                                          #7, aML. REG+40
```

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-8	00 12:16 PAGE 440		SEQ 0427
20266	iML4		22-Dec-1980 09:24 22-Dec-1980 09:21	:31 TOPS
20268 071734 150177 121254 20269 071740 152777 000010 20270 071746 012767 125252 20271 071754 005001	121246 BISB 117624 MOV	R1, aML.REG+40 #10, aML.REG+40 #-52526, IO.BUF R1	TWICE	9078 9079 9081
20272 071756 104402 20273 071760 004767 124076 20274 071764 012777 000061 20275 071772 105777 121226 20276 071776 100375 20277 072000 132777 000040 20278 072006 001424	2\$: TRAP JSR MOV 3\$: TSTB	PC.GD.BLK.XFER #61.aML.REG aML.REG+50		9081 9082 9084 9085
20276 071776 100375 20277 072000 132777 000040	121226 BITB	#40.aML.REG+60		9088
20279 072010 104455 20280 072012 000151 20281 072014 010526	BEQ TRAP .WORD .WORD .WORD	4\$ 55 151 SYNC DUMPER		9091
20277 072000 132777 000040 20278 072006 001424 20279 072010 104455 20280 072012 000151 20281 072014 010526 20282 072016 024052 20283 072020 012746 006374 20284 072024 012746 006344 20285 072030 012746 006336 20286 072034 012746 006046 20288 072040 012746 006046 20288 072040 012746 006046 20288 072050 010600 20290 072052 104414 20291 072054 062706 000014 20292 072060 006367 117514	MOV MOV MOV MOV	MWRD.9,-(SP) MWRD.6,-(SP) MWRD.23,-(SP) MFOR.FMT,-(SP)		9092
20288 072044 012746 000005 20289 072050 010600 20290 072052 104414	MOV	#5,-(SP) SP,R0 14	; SP,*	
20293 072064 005077 121144 20294 072070 104467	4\$: ASL CLR TRAP	#14,SP 10.BUF aml.REG+60		9090 9095 9096
20295 072072 006000 20296 072074 103730 20297 072076 005201 20298 072100 020127 000001 20299 072104 003724	ROR BLO INC CMP BLE	R0 2\$ R1 R1,#1 2\$: TWICE,*	9081
20300 072106 005001 20301 072110 104402	5\$: TRAP	3	: TWICE	9100 9101 9102
20302 072112 152777 000040 20303 072120 016700 121456 20304 072124 042700 177770 20305 072130 142777 000007 20306 072136 150077 121052 20307 072142 152777 000010 20308 072150 004767 123706 20309 072154 012777 000071 20310 072162 105777 121036	BIC	#40, aml.REG+40 ML.DUT.RO #177770,RO #7, aml.REG+40 R0, aml.REG+40 #10, aml.REG+40 PC, GD.BLK.XFER #71, aml.REG		9102
20306 072136 150077 121052 20307 072142 152777 000010	121044 BISB	RO, aML.REG+40 #10, aML.REG+40		9104
20308 072150 004767 123706 20309 072154 012777 000071 20310 072162 105777 121036	120772 JSR MOV TSTB	PC,GD.BLK.XFER #71,aML.REG aML.REG+50		9104 9105 9106
20312 072170 032777 000400	121016 BPL BIT BEO			9109
20296 072074 103730 20297 072076 005201 20299 072104 003724 20300 072106 005001 20301 072110 104402 20302 072112 152777 000040 20303 072120 016700 121456 20304 072124 042700 177770 20305 072130 142777 000007 20306 072136 150077 121052 20307 072142 152777 000010 20308 072150 004767 123706 20309 072154 012777 000071 20310 072162 105777 121036 20311 072166 100375 20312 072170 032777 000400 20313 072176 001424 20314 072200 104455 20315 072204 010526 20316 072204 010526 20317 072206 024052 20318 072210 012746 006364	BEQ TRAP . WORD . WORD	7\$ 55 152 SYNC DUMPER WWRD.8,-(SP) WWRD.7,-(SP)		9112
20318 072210 012746 006360 20319 072214 012746 006344	MOV MOV	MWRD.8,-(SP) MWRD.7,-(SP)		9113

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12	:16 PAGE 441		SEQ	0428
20321 20322 20333	;ML4			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
20324 072220 012746 006336 20325 072224 012746 006544 20326 072230 012746 006046 20327 072234 012746 000005	MOV # MOV # MOV # MOV #	WRD.6,-(SP) WRD.23,-(SP) FOR.FMT,-(SP) F5,-(SP)	. 50 4		
20329 072242 104414 20330 072244 062706 000014 20331 072250 104467 20332 072252 006000	7\$: TRAP 6 ROR R BLO 5 ASL I JSR P	MRD.23,-(SP) FOR.FMT,-(SP) F,-(SP) P,R0 4 14,SP	; SP.*		9111 9114
20334 072256 006367 117316 20335 072262 004767 123574 20336 072266 012777 000061 20337 072274 105777 120724 20338 072300 100375	120660 MOV #	0.BUF C.GD.BLK.XFER 61.aML.REG ML.REG+50 S	!		9117 9118 9119
20339 072302 005201 20340 072304 020127 000001	CMP R	1,//1	: TWICE .*		9100
20342 072312 012601 20343 072314 000207	98: BLE 5. MOV (\$ SP)+,R1	:		9043
20345 20346 20351 20352	; Routine Size: ; Maximum stack d	153 words lepth per invocation: 7 words			
20360 072316 20361 072316 004767 177312 20362 072322 104466 20363 072324 006000 20364 072326 103773 20365 072330 000207	T46:: 1\$: JSR P TRAP 6 ROR R BLO 1: RTS P	C.\$T46 6 0 \$	•		9121
20366 20367 20368 20373 20374 ; 9124 ! <blf f<="" td=""><td>; Routine Size: ; Maximum stack d</td><td>6 words epth per invocation: 0 words</td><td></td><td></td><td></td></blf>	; Routine Size: ; Maximum stack d	6 words epth per invocation: 0 words			

20376 :ML4 20377 : 20378 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 BL:ss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (97) BGNTST: TEST NUMBER: TST 47 TEST NAME: WRITE READ ML11 TEST DESCRIPTION: PROVIDES A MBUS READ/WRITE DATA TRANSFER TROUBLE SHOOTING LOOP BY: 1. LOAD APPROPRIATE RH REGISTERS. DO A WRITE FUNCTION. 2. LOAD APPROPRIATE RH REGISTERS DO A WRITE CHECK FUNCTION. 3. COMPLIMENT DATA AND REPEAT. IMPLICIT INPUTS: IO BUF A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READ AND WRITE FUNCTIONS IS FOUND. 9156 9157 9158 9159 9160 9161 local TST_PAT; !TEST PATTERN TST_PAT = %0'052525'; incr TWICE from 0 to 1 do !WRITE READ 1'S AND 0'S ON MBUS begin
CLR_MBUS;
BAI = ONE;
IO_BUF = .TST_PAT;
GD_BLK_XFER ();
MLCS1 = write;
TIME_OUT_LOOP;
CLR_MBUS;
BAI = ONE;
GD_BLK_XFER (). SET ON FIRST IO BUF ADRS
LOAD FIRST IO BUF ADRS
SET UP A GOOD BLOCK XFERR
DO A WRITE FUNCTION SET ON FIRST ID BUF ADRS GD_BLK_XFER (); MLCS1 = WRT_CHK; !DO A WRITE CHECK FUNCTION TIME_OUT_LOOP;

п	12CELLAMEON2 C	ODING 25	CITUN							SEW 0430
	20432 :ML4 20433 :							22-Dec-1980 09:24:37 22-Dec-1980 09:21:22	1 TOPS-20 Bliss-16 V2(212) 2 PA: <neale>BL3ML4.BLI.2 (9)</neale>	7)
	20435 :	9177	if.	.WCE IS_	SET			!SEE IF WRITE CHECK	ERROR SET	
	20437	9179	the	begin				!ERROR IF SET		
	20438 20439 20440	9177 9178 9179 9180 9181 9182 9183		PRINTB end;	(SIX_FM)	FNC_4	, WRD_10, WRD_12,	!ERROR IF SET FNC_5, FNC_6, FNC_3);	
	20442	9184 9185	TST	PAT = n	ot .TST_	PAT;		!COMPLIMENT TST_PAT	AND REPEAT	
	20445	9186 9187	ENDTST;							
	20453 072332 20454 072336	004167 012701 005002 152777	111506 052525		\$147:	JSR MOV	R1,\$SAVE2 M52525,R1	1 2	,TST.PAT WICE	9123 9161 9163 9164
	20455 072344 20456 072344 20457 072352	152777 016700 042700	000040	120642	1\$:	BISB MOV	R2 #40,aML.REG+40 ML.DUT.RO	; "	WICE	9164
	20459 072362	142777	000007	120624		BICB	ML.DUT.RO #177770,RO #7,aML.REG+40 RO,aML.REG+40			
	20461 072374	152777	000010	120612		BICB BISB BISB MOV JSR	#10,aML.REG+40 R1,10.BUF PC,GD.BLK.XFER	÷ 1.	ST.PAT,*	9166 9167
	20463 072406 20464 072412 20465 072420	142777 150077 152777 010167 004767 012777 105777	000040 121224 177770 000007 120620 000010 117172 123450 000061 120600	120534	2\$:	TSTB	PC.GD.BLK.XFER #61.aML.REG aML.REG+50 2\$			9166 9167 9168 9169
	20466 072424 20467 072426 20468 072434	100375 152777 016700 042700	000040	120560		BPL BISB MOV BIC BICB BISB BISB JSR MOV TSTB	#40, aml. REG+40 ML. DUT, RO #177770, RO			9170
	20470 072444	142777 150077 152777	000007	120542		BICB	#7.aML.REG+40 RO.aML.REG+40			
	20472 072456	152777	000010	120530		BISB	M10. aml. REG+40 PC. GD. BLK. XFER	:		9172
	20473 072464 20474 072470 20475 072476 20476 072502 20477 072504 20478 072512 20479 072514 20480 072516	004767 012777 105777 100375 032777 001430 104455 000155	000007 120536 000010 123372 000051 120522	120456	3\$:		#51 , aml . REG aml . REG+50 3\$;		9172 9173 9174
	20477 072504	032777	040000	120502		BPL	#40000, aml. REG+	40 ;		9177
	20479 072514 20480 072516	104455				BEQ TRAP . WORD	4\$ 55 155	:		9180
	20481 072520 20482 072522 20483 072524 20484 072530 20485 072534	011026 024052 012746 012746 012746	007750 010004 007774			. WORD . WORD . WORD MOV MOV MOV	TRBLE.LOOP DUMPER WFNC.3,-(SP) WFNC.6,-(SP) WFNC.5,-(SP)			9181

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 444	SEQ 0431
20487 20488 20489	;ML4	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
20490 072540 012746 006426 20491 072544 012746 006406 20492 072550 012746 007756 20493 072554 012746 006100 20494 072560 012746 000007 20495 072564 010600	MOV #WRD.12,-(SP) MOV #WRD.10,-(SP) MOV #FNC.4,-(SP) MOV #SIX.FMT,-(SP) MOV #7,-(SP) MOV SP,R0 TRAP 14 ADD #20,SP 4\$: COM R1	; SP,*
20497 072570 062706 000020 20498 072574 005101 20499 072576 005202 20500 072600 020227 000001	ADD #20,SP 4\$: COM R1 INC R2 CMP R2,#1 BLE 1\$ RTS PC	757.PAT 9184 TWICE 9163
20501 072604 003657 20502 072606 000207 20503		; 9123
20504 20505 20510 20511 20515	; Routine Size: 87 words ; Maximum stack depth per invocation:	11 words
20519 072610 20520 072610 004767 177516 20521 072614 104466 20522 072616 006000 20523 072620 103773 20524 072622 000207	T47:: 1\$: JSR PC.\$T47 TRAP 66 ROR RO BLO 1\$ RTS PC	; 9185
20526 20527 20532	; Routine Size: 6 words ; Maximum stack depth per invocation:	0 words
20534 ; 9188 ! <blf f<="" td=""><td>PAGE></td><td></td></blf>	PAGE>	

TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (98) 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 BGNTST: ! TEST NUMBER: TST 48 TEST NAME: CRC DATA BUS TEST TEST DESCRIPTION:
TEST THE CRC DATA BUS BETWEEN THE CRC
GENERATORS AND THE CRC/MBUS DATA
MUX FOR CONTINUITY AND BIT UNIQUENESS 1. WHILE IN ECC DIAGNOSTIC MODE DO GOOD BLOCK TRANSFERS WITH VARIOUS CRC_A, CRC_B AND PAR_CRC_WRD WHICH WILL EXERCISE BUS CONTINUITY AND UNIQUENESS. 2. THEN IN DATA DIAGNOSTEC MODE READ THE CRC NIBBLES FROM ONE CRC GROUP FOR CORRECT CRC_A, CRC_B AND PAR_CRC_WRD DATA PATTERNS. IMPLICIT INPUTS:
PD TEMP
A BIT VECTOR OF 16 BITS WHERE THE READ PROM DATA IS STORED OAND ACCESSED FROM. local CRC_NIB POINTER TO THE CRC NIBBLE CRC_TSTED, ERR_FLG, NIB_PAT; STORES HOW MANY CRC NIBBLES WE HAVE TESTED !ERROR FLAG !TEST DATA FOR THE CRC NIBBLE CRC_NIB = 9; **!SET CRC NIBBLE POINTER** incr LOOP from 0 to 4 do !REPEAT THE TEST WITH FIVE NIBBLE PATTERNS begin BGNSUB; START OF THE SCOPE LOOP CLR_MBUS; ECC_DM = ONE; SET ECC DIAG MOD case .LOOP from 0 to 4 of SELECT ONE OF THE FIVE NIBBLE PATTERNS set !PATTERN 0 NIB_PAT = %b'110';

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22-Dec-1980 09:24:31 22-Dec-1980 09:21:22
                                                                                                                             TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (98)
        [1] :
                                                                                 !PATTERN 1
               begin
MLE1 = ONES;
               MLE2 = ONES;
               NIB_PAT = %6'001';
               end:
        [2] :
                                                                                 !PATTERN 2
              begin

CRC_A = ONES;

CRC_B = ONES;

NIB_PAT = %b'000';
               end:
                                                                                 !PATTERN 3
        [3] :
              begin
PAR_CRC_WRD = ONES;
CRC_B = ONES;
NIB_PAT = %b'011';
        [4] :
                                                                                 !PATTERN 4
              begin
PAR_CRC_WRD = ONES;
CRC_A = ONES;
NIB_PAT = %b'101';
               end:
        tes:
GD_BLK_XFER ();
MLCS1 = write;
TIME_OUT_LOOP;
DAT_DM = ONE;
GD_BLK_XFER ();
MLCS1 = read;
CRC_TSTED = ZERO;
                                                                                 SET UP A GOOD BLOCK TRANSFER
DO A WRITE TRANSFER
WAIT UNTIL DRIVE READY
SET DATA DIAG MODE
SET UP A GOOD BLOCK TRANSFER
DO A READ TRANSFER
DELAY (ONE_US);
                                                                                 !TEST ONE CRC GROUP '6 CRC NIBBLES'
        begin
        PD TEMP = .MLPD;
DAT_CLK = ONE;
                                                                                 READ THE PROM DATA FOR THIS ARRAY WORD CLOCK OUT AN ARRAY WORD INTO THE DATA DIAG REG'S DELAY ONE MICRO SECOND
        DELAY (ONE_US);
        RD_LNG_WRD;
                                                                                  !READ THE DATA DIAG REG'S INTO MEMORY
        if .PD_TEMP [.CRC_NIB] IS_NOT_SET
                                                                                 !TEST THIS CRC NIBBLE IF GOOD
        then
              begin

CRC_TSTED = .CRC_TSTED + 1; !INCREMENT THE GOOD NIBBLE COUNTER

TST_LNG_WRD (.CRC_NIB, .NIB_PAT, ERR_FLG); !TEST THE CRC NIBBLE
```

HIZCELLANEOUS CO	ODING SE	CTION								SEQ 0434
20648 :ML4 20649 :							22-Dec-1980 09:	24:31 21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (98</neale>	,
20651 :	9293		11	.ERR_FL	G IS_SET		SEE IF THE TEST	T FOUND	AN ERROR	
20652 :	9294		the	begin			!REPORT ERROR II	F FLAG I	S SFT	
20654 :	9296			FRRDF	(131, SY	NC , DUMPER);				
20656	9298			PRINTB	(FMT_5,	T, WRD 64, WRD 24 .NIB_PAT, .NIB_S	AVE [NIB_9], 8);	<i>);</i>		
20657 :	9299			end;						
20659 ;	9301		end	;						3875 51
20661 :	9303		end							
20663	9304	unt	il .CRC_	ISIED e	ql 6;		!REPEAT UNTIL OF	NE CRC G	ROUP HAS BEEN TESTED	
20664 :	9306 9307	END	SUB;				!END OF SCOPE LI	00P		
20666	9308 9309									
20671		ENDTST;								
20675 072624 20676 072630	004167 162706	111266 000006 000011		\$T48:	JSR SUB	R1,\$SAVE5		:		9187
20677 072634	012705	000011			MOV MOV	#6,SP #11,R5 R5,-(SP) (SP) (SP)		; *,CRC ; CRC.N	NIB	9228 9287
20679 072642	006216				ASR	(SP)		, CAC.A	10,-	7201
20681 072646	006216				ASR ASR	(SP)				
20682 072650 20683 072654	062716	013120			ADD	MPD.TEMP,(SP)		; LOOP		9230
20684 072656	104402	0000/0	120724	1\$:	CLR TRAP BISB	2		: 2001		9230 9231 9232
20686 072666	016704	000040 120710 177770 000007 120304	120326		MOV	#40, aML.REG+40 ML.DUT, R4 #177770, R4		,		9232
20687 072672	042704	000007	120310		BICB	#177770,R4 #7,aML.REG+40				
20689 072704	150477	120304	120356		BISB	DI SHE DECLIO				027/
20690 072710 20691 072716	010104	000001	120330		MOV	R1 .R4		; LOOP.	•	9234 9236
20693 072722	152777 010104 006304 066407 000012 000020	072726			ADD	#1, aml. REG+40 #1, aml. REG+40 R1, R4 R4 2\$(R4), PC 3\$-2\$ 4\$-2\$ 5\$-2\$ 6\$-2\$ 7\$-2\$				
20694 072726	000012			2\$:	. WORD	35-25				
20696 072732	000042				. WORD	5\$-2\$				
20698 072736	000104				WORD	7\$-2\$				
20690 072710 20691 072716 20692 072720 20693 072722 20694 072726 20695 072730 20696 072732 20697 072734 20698 072736 20699 072740 20700 072744 20701 072746	000442	000006		3\$:	BISB MOV ASL ADD .WORD .WORD .WORD .WORD MOV BR MOV	#6,R2		: *,NIB	.PAT	9240 9236 9244
20701 072746	000442	177777	120350	45:	MOV	8\$ #-1,aML.REG+150		:		9244

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	0 12:16 PAGE 448	SEQ 0435
20703 20704	:ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
20706 072754 012777 177777 20707 072762 012702 000001	120352 MOV MOV BR	#-1,aML.REG+160 #1,R2 :*,NIB.PAT	9245 9246
20709 072770 152777 000077 20710 072776 152777 000077 20711 073004 005002	120326 5\$: BISB 120330 BISB	#77.aML.REG+150 #77.aML.REG+160	9251 9252 9253
20712 073006 000421 20713 073010 052777 037400 20714 073016 152777 000077	120306 6\$: BIS 120310 BISB	8\$ #37400 aml REG+150	9245 9246 9236 9251 9252 9253 9258 9258 9259 9260 9236 9265 9265 9267 9271
20715 073024 012702 000003 20716 073030 000410 20717 073032 052777 037400	AR AR	#77, aml.REG+160 #3.R2 #37400, aml.REG+150 *,NIB.PAT	9260 9236 9265
20717 073032 052777 037400 20718 073040 152777 000077 20719 073046 012702 000005 20720 073052 004767 123004 20721 073056 012777 000061 20722 073064 105777 120134	120256 BISB MOV 8\$: JSR	#37400.aML.REG+150 #77.aML.REG+150 #5.R2 PC.GD.BLK.XFER #61.aML.REG aML.REG+50	9266 9267 9271
20721 073056 012777 000061 20722 073064 105777 120134 20723 073070 100375	RPI		
20715 073024 012702 000003 20716 073030 000410 20717 073032 052777 037400 20718 073040 152777 000077 20719 073046 012702 000005 20720 073052 004767 123004 20721 073056 012777 000061 20722 073064 105777 120134 20723 073070 100375 20724 073072 152777 000010 20725 073100 004767 122756 20726 073112 005066 000002 20728 073116 012703 000001 20729 073122 001411 20730 073124 016704 106766	120174 BISB JSR 120042 MOV CLR	#10.aML.REG+120 PC.GD.BLK.XFER #71.aML.REG 2(SP) : CRC.TSTED	9274 9275 9276 9277 9278
20728 073116 012703 000001 20729 073122 001411 20730 073124 016704 106766	105: BEQ	#1,R3 ; *,\$\$TMP2 13\$ L\$DLY,R4 ; *,\$\$TMP1	9278
20733 073136 005304	11\$: CLR	12\$ 6(SP) ; \$\$TMP R4 ; \$\$TMP1	
20734 073140 001374 20735 073142 005303	12\$: DEC BNE BR	11\$ R3 10\$	
20736 073144 000766 20737 073146 017767 120232 20738 073154 152777 000020 20739 073162 012703 000001 20740 073166 001411 20741 073170 016704 106722 20742 073174 001404 20743 073176 005066 000006 20744 073202 005304	117744 13\$: MOV 120112 BISB MOV	aML.REG+230,PD.TEMP #20,aML.REG+120 #1,R3 17\$;*,\$\$TMP2	9282 9283 9284
20740 073166 001411 20741 073170 016704 106722 20742 073174 001404	14\$: BEQ MOV BEQ	16\$	
20745 073204 001374	14\$: BEQ MOV BEQ 15\$: CLR DEC BNE 16\$: DEC BR 115732 17\$: MOV	15\$; \$\$TMP1	
20741 073170 016704 106722 20742 073174 001404 20743 073176 005066 000006 20744 073202 005304 20745 073204 001374 20746 073206 005303 20747 073210 000766 20748 073212 017767 120126 20749 073220 017767 120130 20750 073226 017767 120102	16\$: DEC BR 115732 17\$: MOV 115726 MOV 115722 MOV	R3 ; \$\$TMP2 14\$ aML.REG+170,D1.TEMP	
20750 073226 017767 120102 20751 073234 011646 20752 073236 010546	115722 MOV MOV MOV	aML.REG+160,E2.TEMP (SP),-(SP) R5,-(SP) ; CRC.NIB,*	9287
20736 073144 000766 20737 073146 017767 120232 20738 073154 152777 000020 20739 073162 012703 000001 20740 073166 001411 20741 073170 016704 106722 20742 073174 001404 20743 073176 005066 000006 20744 073202 005304 20745 073204 001374 20746 073206 005303 20747 073210 000766 20748 073212 017767 120130 20750 073236 010767 120102 20751 073234 011646 20752 073236 010546 20753 073240 042716 177770 20754 073244 012746 000001 20756 073252 004767 107662 20757 073256 062706 000010	CLR	#177770,(SP) #1,-(SP) -(SP)	
20756 073252 004767 107662 20757 073256 062706 000010	JSR ADD	PC_BL\$GT2 #10,SP	

59			:ML4			22	-Dec-1980 09:24:31 TOP
60 61			;			22	-Dec-1980 09:24:31 TOP -Dec-1980 09:21:22 PA:
62 073262 005700 63 073264 001066 64 073266 005266 65 073272 010546				TST	R0 19\$		
64 073266 005266	000002			INC	2(SP)	: CRC.TSTED	929 929
66 073274 010246	000013			MOV	R2,-(SP)	; CRC.TSTED ; CRC.NIB.* ; NIB.PAT.*	929
68 073302 060616	000012			MOV	SP, (SP)	; ERR.FLG,*	
69 073304 004767 70 073310 026627	122636 000012	000001		ADD JSR CMP	PC,TST.LNG.WRD 12(SP),#1	; ERR.FLG,*	929
71 073316 001047 72 073320 104455				BNE	188		929
73 073322 000203 74 073324 010526				BNE TRAP .WORD .WORD	SYNC		
75 073326 024052	006606			WORD	DUMPER		929
77 073334 012746	007310			MOV	#WRD.73,-(SP)		727
79 073344 012746	006406 007310 006552 007224 006046 000005			MOV	MURD.64,-(SP)		
75 073326 024052 76 073330 012746 77 073334 012746 78 073340 012746 79 073344 012746 80 073350 012746 81 073354 012746 82 073360 010600 83 073362 104414 84 073364 012716 85 073370 016704 86 073374 006204 87 073376 006204 88 073400 006204 89 073402 006204	000005			MOV	2(SP) R5,-(SP) R2,-(SP) W12,-(SP) M12,-(SP) SP,(SP) PC,TST.LNG.WRD 12(SP),W1 18% 55 203 SYNC DUMPER MWRD.10,-(SP) MWRD.73,-(SP) MWRD.24,-(SP) MWRD.64,-(SP) MFOR.FMT,-(SP) MFOR.FMT,-(SP) SP,R0 14 W10,(SP)		
83 073362 104414				MOV	3P.RU 14	; SP,*	
84 073364 012716 85 073370 016704	000010 115562			MOV	NIB.SAVE+4,R4	:	929
6 073374 006204 7 073376 006204				ASR ASR	R4 R4		
18 073400 006204 19 073402 006204				ASR ASR	R4 R4 R4 R4		
90 073404 000304	177770			SWAB	#177770 P4		
92 073412 010446				MOV	R4,-(SP)	; NIB.PAT,*	
94 073416 012746 95 073422 012746	004430 000004			MOV	R4,-(SP) R2,-(SP) WFMT.5,-(SP) W4,-(SP) SP,R0 14	, NID.PAI,-	
6 073426 010600	000004			MOV MOV TRAP	SP,RO	; SP,*	
98 073432 062706	000024		100	ADD	#24.SP	;	929
00 073442 026627	000024 000006 000002	000006	18\$: 19\$:	CMP	2(SP),#6	: CRC.TSTED.*	929 928 930
02 073452 104467				ADD ADD CMP BNE TRAP ROR BHIS JMP INC CMP BLE ADD RTS	#24, SP #6, SP 2(SP), #6 13\$ 67 R0 21\$ 1\$		
03 073454 006000 04 073456 103002				ROR BHIS	R0 21\$		
05 073460 000167 06 073464 005201	177172		20 \$:	INC	1\$ R1	: 100P	923
02 073452 104467 03 073454 006000 04 073456 103002 05 073460 000167 06 073464 005201 07 073466 020127 08 073472 003772 09 073474 062706 10 073500 000207	000004			CMP	R1,#4 20\$ #10,SP	; LOOP,*	725
09 073474 062706	000010			ADD	#10,SP PC	;	918

CZMLABO ML-11 LOGIC TES MISCELLANEOUS CODING SE	MACRO M1113	22-DEC-80 12:16 PAGE 450 ²		SEQ	0437
20815 20816 20817 20822	;ML4			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
20823 20827 20831 073502 20832 073502 004767 20833 073506 104466 20834 073510 006000 20835 073512 103773 20836 073514 000207	177116 T48::	JSR PC.\$T48 TRAP 66 ROR RO BLO 1\$ RTS PC			9307
20838 20839 20844 20845 20846; 9310	: Rou : Max ! ⟨BLF/PAGE>	tine Size: 6 words imum stack depth per invocation	n: 0 words		

20848 :ML4 20849 : 20850 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (99) BGNTST TEST NUMBER: TST 49 TEST NAME: UNIQUE NIBBLE CRC GENERATION TEST TEST DESCRIPTION: TEST CRC CODES GENERATED FOR ONE CRC GROUP (52 UNIQUE UNIBUS NIBBLES) BY: 1. DOING MASS BUS WRITE TRANSFERSS WITH THE FIRST IO BUF CRC GROUP HAVING 51 NIBBLES OF %B'0000' PATTERN AND THE REMAINING NIBBLE WITH PATTERNS OF %B'0000' TO %B'1111'. 2. THEN IN DATA DIAGNOSTIC MODE CALCULATE THE CRC CODE FOR THE FIRST CRC GROUP AND COMPARE IT AGAINST THE HARDWARE GENERATED CRC CODE. 3. REPEAT PATTERN OF %B'0000' TO %B'1111' FOR EACH OF THE 52 NIBBLES. IMPLICIT INPUTS: IO_BUF A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITES TRANSFERS IS FOUND. local DODU_FLG . !DROP UNIT FLAG !TEMPORARY STORAGE LOCATION !SELECTS 4 BITS 'ONE NIBBLE' FROM THE ID_BUF !CLEAR THE DROP UNIT FLAG TEMP NIB SÉL; DODO_FLG = ZEROES; incr CNT from 0 to 13 do IO_BUF [.CNT] = ZEROES; !CLEAR THE FIRST 14 IO_BUF WORDS incr WRD_CNT from 0 to 12 do begin NIB_SEL = -4; !DO THIS TEST ON 13 IO_BUF WORDS !RESET THE NIBBLE SELECTOR incr NIB_TST from 0 to 3 do !DO THIS TEST ON 4 NIBBLES PER WORD begin if (.WRD_CNT eql 12) and (.NIB_TST eql 2) then exitloop; !THE LAST 2 NIBBLES ARE NOT TESTED

!DROP THIS UNIT IF THE DODU FLAG GOT SET

if .DODU_FLG IS_SET

DOCLN; end;

begin DODU (.ML_LUN);

then

ENDTST;

MLABO ML-11 L SCELLANEOUS C		ITON							SEQ 0440
20964 073516	004167	110374		\$149:	JSR	R1,\$SAVE5 -(SP),-(SP) 2(SP)	;		9309
20966 073524	005066	200000			CLR	2(SP)	:	DODU.FLG	9349
20967 073530 20968 073530	010001			15:	CLR	RO RO,R1		DODU.FLG CNT CNT,*	9349 9351 9352
20969 073534	006301				ASL	R1		CIVI ,-	7332
20970 073542	005200	011600			CLR	IO.BUF (R1) R0		CNT	9351
0972 073544	020027	000015			CMP	RO_#15	:	CNT,*	,33,
0974 073552	005002				BLE	1\$ R2		WRD CNT	9354
0975 073554	012716	177774		2\$:	MOV	#-4.(SP)	:	WRD.CNT *,NIB.SEL WRD.CNT,*	9354 9356 9370
0977 073562	006301				MOV	R2 .Ř1 R1	:	WRD.CNT,*	9370
0978 073564	005004	00001/		70.	CLR	R4	:	NIB.TST WRD.CNT,*	9358 9361
980 073572	001003	000014		3\$:	BNE	R2.#14		WRD.CNI,*	9361
0981 073574	020427	000002			ENE CMP BEQ	R4.#2 8\$:	NIB.TST,*	
983 073602	062716	000004		45:	ADD	#4,(SP)	:	*,NIB.SEL TEMP	9363
984 073606 985 073610	005005				CLR	#4,(SP) R5 R3 2		TÉMP NIB.PAT	9364
986 073612	104402			5\$:	CLR	2	:		9363 9364 9366 9367 9369
7987 073614 1988 073616	010546	000002			MOV	R5,-(SP) 2(SP),-(SP)		TEMP, * NIB. SEL, *	9369
989 073622	012746	000002 000004			MOV	#4,-(SP) R3,-(SP) PC_BL\$PU1			
991 073630	004767	107446			MOV JSR	R5,-(SP) PC_AL\$PU1	:	NIB.PAT,*	
992 073634	010005				MOV	RO.R5 R5.IO.BUF(R1)	:	*,TEMP TEMP,*	9367 9370
994 073642	152777	011600 000040	117344		MOV	MAA SIM DECAAN	:	TEMP,*	9370
995 073650	016700	117726 177770 000007 117322 122164 000061 117314			MOV BIC BICB BISB	ML.DUT.RO			
0997 073660	142777	000007	117326		BICB	#7, aML.REG+40			
0998 073666	150077	117322			BISB	RO, aML. REG+40			0777
1000 073676	012777	000061	117250		MOV	#61,aML.REG	:		9372 9373
1001 073704	105777			6\$:	TSTB	ML.DUT.RO #177770.RO #7.aML.REG+40 RO.aML.REG+40 PC.GD.BLK.XFER #61.aML.REG aML.REG+50 6\$			
1003 073712	005067	117230 117226 117224 117206 117204 117202 120644 121240			CLR	A.GEN B.GEN P.GEN A.CAL B.CAL P.CAL	;		9375
1005 073722	005067	117224			CLR	B. GEN P. GEN	:		9375 9376 9377 9378 9379 9380 9381 9383
1006 073726	005067	117206			CLR	A. CAL	:		9378
0997 073660 0998 073666 0998 073672 1000 073676 1001 073704 1002 073710 1003 073712 1004 073716 1005 073722 1006 073726 1007 073732 1008 073736 1009 073746 1011 073752 1012 073754	005067	117202			CLR	P. CAL			93/9
1009 073742	004767	120644			CLR JSR JSR	PC, CAL. CRC			9381
073056 0797 073660 0798 073666 0799 073672 1000 073704 1002 073710 1003 073712 1004 073716 1005 073722 1006 073726 1007 073732 1008 073736 1009 073746 1011 073756 1012 073756	016700 042700 142777 150077 004767 012777 105777 105777 100375 005067 005067 005067 005067 005067 005067 004767 004767 004767 004767	121240			ROR	PC, CAL. CRC PC, ERR. CHK. CRC RO 7\$ 55			9383
1012 073754	103055				BCC	75			9386

						22 2	SEQ 0441
		:ML4				22-Dec-1 980 22-Dec-1 980	09:24:31 TOPS 09:21:22 PA:
			. WORD	204			
004340			WORD	DUMPER			079
007224			MOV	MRD.64,-(SP)	•		9387
006034			MOV	#THR.FMT,-(SP)			
000004			MOV	SP.RO	; SP,	•	
117130			MOV	P.GEN, (SP)	:		9388
117116			MOV	B.GEN, -(SP)			
000004			MOV	#4,-(SP)			
117072			TRAP	14 D CAL (SD)			0796
117062			MOV	A.CAL,-(SP)	•		9389
005462			MOV	#FMT.20,-(SP)			
000004			MOV	SP,RO	; SP, 1		
000001	000044		MOV	#1,44(SP)	: *,00	ODU.FLG	9390
000010		7\$:	ADD	#10.SP			9390 9385 9367 9391
			ROR	RO 5			9391
000017			INC	R3 R3 #17	; NIB.	PAT	9366
			BLE		, 110.		0704
			INC	R4 P4 #3	NIB.	TST .	93% 9358
000003		88.	BLE	3\$			9354
000014			CMP	R2.#14	: WRD.	CNT,*	7334
177372	000001	98.	JMP	2\$ 2(SP) #1	. 0001	1516+	9401
	000001		BNE	10\$ MI LIN PO	. 0000	J. FLO, -	9404
111.402			TRAP	51			7404
		10\$:	RTS	(SP)+,(SP)+ PC	;		9309
	006360 007224 006330 006034 000004 117130 117116 005412 000004 117062 117060 005462 000004 000001 000032 000010 000017 011600 000003	117130 117120 117116 005412 000004 117072 117062 117060 005462 000004 000032 000010 000017 011600 000003 000014 177372 000002 000001	#L4 :ML4 :		. MORD		### 222-Dec-1980 22-Dec-1980 222-Dec-1980 222-Dec-198

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SEQ 0442

9406

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21104 :ML4
21105 :
21106 :
21107 :
21108 :
21109 :
21110 :
21111 :
21111 :
                                                                                                        22-Dec-1980 09:24:31 22-Dec-1980 09:21:22
                                                                                                                                           TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (100)
                                BGNTST:
                                 ! TEST NUMBER: TST 50
                                   TEST NAME:
                                                                    UNIQUE WORD CRC GENERATION TEST
                                 ! TEST DESCRIPTION:
                                            TEST CRC CODES GENERATED FOR ONE CRC GROUP (13 UNIBUS WORDS) BY:
                                            1. DOING MASS BUS WRITE TRANSFERS USING VARIOUS WORD DATA PATTERENS.
                                            2. THEN IN DATA DIAGNOSTIC MODE
                                                  CALCULATE THE CRC CODE FOR THE FIRST CRC GROUP AND COMPARE IT TO THE HARDWARE GENERATED CRC CODE.
                                 ! IMPLICIT INPUTS:
                                            A VECTOR OF 256 WORDS WHERE DATA
                                            FOR MBUS READS AND WRITES TRANSFERS
                                            IS FOUND.
                                 local
                                      DODU_FLG,
TST_PAT;
                                                                                                        !DROP UNIT FLAG
                                                                                                        !TEST PATTERN SAVE LOCATION
                                DODU_FLG = ZEROES;
                                                                                                        !CLEAR THE DROP UNIT FLAG
                                incr PAT_SEL from 0 to 3 do begin BGNSUB;
                                                                                                        !RUN THE TEST ON 4 PATTERNS
                                                                                                        !START OF SCOPE LOOP
21144
21145
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21158
                                       CLR_MBUS;
                                      case .PAT_SEL from 0 to 3 of
                                                                                                       SELECT ONE OF THE 4 PATTERNS
                                            set
                                                  TST_PAT = ONES;
                                            [1] : TST_PAT = %0'052525';
                                                  TST_PAT = %0'125252';
                                            [3] :
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CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 457
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21160 :ML4
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                                                                                                                                                                                                                                                                                                                                                                          22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (100)
                                                                                                                                                                                 TST_PAT = %0'070707';
                                                                                                                                          incr CNT from 0 to 255 do IO_BUF [.CNT] = .TST_PAT;
                                                                                                                                                                                                                                                                                                                                                                       !LOAD THE FIRST 13 IO_BUF WORDS WITH TST_PAT
                                                                                                                                        GD_BLK_XFER ();
MLCS1 = write;
TIME OUT LOOP;
A_GER = ZEROES;
B_GEN = ZEROES;
P_GEN = ZEROES;
A_CAL = ZEROES;
B_CAL = ZEROES;
P_CAL = ZEROES;
CAL_CRC ();
                                                                                                                                                                                                                                                                                                                                                                        SET UP A GOOD BLOCK TRANSFER
DO A WRITE TRANSFER
WAIT FOR THE TRANSFER TO COMPLETE
CLEAR THE GEN & CAL SAVE LOCATIONS
                                                                                                                                                                                                                                                                                                                                                                       !CALCULATE WHAT THE CRC CODE SHOULD BE
                                                                                                                                          if ERR_CHK_CRC ()
                                                                                                                                                                                                                                                                                                                                                                        !NOW SEE IF THE GENERATED CRC CODE IS THE SAME
                                                                                                                                                          begin

ERRDF (133, SYNC, DUMPER);

PRINTB (THR_FMT, WRD 5, WRD 64, WRD 8);

PRINTB (FMT 19, .B_GEN, .A_GEN, .P_GEN);

PRINTB (FMT 20, .B_CAL, .A_CAL, .P_CAL);

DODU_FLG = ONE;
                                                                                                                                                                                                                                                                                                                                                                        !REPORT THE ERROR IF NOT THE SAME
                                                                                                                                                             end:
                                                                                                                                         ENDSUB:
                                                                                                                                                                                                                                                                                                                                                                       !END OF SCOPE LOOP
                                                                                                                                          end;
                                                                                                                     if .DODU_FLG IS_SET
                                                                                                                                                                                                                                                                                                                                                                    !DROP THIS UNIT IF THE DODU FLAG GOT SET
                                                                                                                      then
                                                                                                                                        begin
DODU (.ML_LUN);
                                                                                                                                         DOCLN:
                                                                                                                                         end;
                                                                                                                     ENDTST:
                                                                            004167
005005
005004
104402
152777
016703
                                                                                                                     107670
                                                                                                                                                                                                     $T50:
                                                                                                                                                                                                                                                                                       R1,$SAVE5
                                                                                                                                                                                                                                               CLR
                                                                                                                                                                                                                                                                                                                                                                                                                                                                  DODU.FLG
                                                                                                                                                                                                                                               CLR
                                                                                                                                                                                                                                                                                                                                                                                                                                                                  PAT.SEL
                                                                                                                                                                                                                                                TRAP
                                                                                                                     000040 116752
117334
                                                                                                                                                                                                                                                                                       #40,aML.REG+40
ML.DUT,R3
                                                                                                                                                                                                                                              BISB
```

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 458 MISCELLANEOUS CODING SECTION ;ML4 21215 21217 21217 21217 21217 21217 21217 21217 21217	SEQ 0445 24:31 TOPS
21219 07/2/4 0/2707 177770 010 4477770 07	21:22 PA:<
21218 074246 042703 177770 21219 074252 142777 000007 116734 21220 074260 150377 116730 21221 074264 010403 21222 074266 006303 BIC #177770,R3 BICB #7,aml.ReG+40 BISB R3,aml.ReG+40 R4,R3 FAT.SEL,*	9450
21221 074264 010403	2.5.
21228 074304 012701 177777 3\$: MOV #-1,R1 ; *,TST.PAT 21229 074310 000410 BR 7\$ 21230 074312 012701 052525 4\$: MOV #52525,R1 ; *,TST.PAT 21231 074316 000405 BR 7\$	9450 9450 9457 9450
21232 074320 012701 125252 5\$: MOV #-52526,R1 ; *,TST.PAT 21233 074324 000402 BR 7\$ 21234 074326 012701 070707 6\$: MOV #70707,R1 ; *,TST.PAT 21235 074332 005002 7\$: CLR R2 ; CNT	9460 9450 9463 9466 9467
21237 074336 006303 ASL R3 21238 074340 010163 011600 MOV R1_IO_BUF(R3) TST_PAT_+	9467
21241 074352 003770 21242 074354 004767 121502 21243 074360 012777 000061 116566 21244 074366 105777 116632 9\$: TSTB aml.REG+50	9469 9470
21245 074374 005067 116546 CLR A.GEN 21247 074400 005067 116544 CLR B.GEN 21248 074404 005067 116542 CLR P.GEN 21249 074410 005067 116524 CLR A.CAL	9472 9473 9474 9475 9476 9477 9478 9480
21255 074436 103054 BCC 10\$ 21256 074440 104455 TRAP 55 ; 21257 074442 000205 .WORD 205 21258 074444 010526 .WORD SYNC	9483
21250 074414 005067 116522	9484
21265 074474 010600 MOV SP,R0 ; SP,* 21266 074476 104414 TRAP 14 21267 074500 016716 116446 MOV P.GEN,(SP) ; 21268 074504 016746 116436 MOV A.GEN,-(SP) 21269 074510 016746 116434 MOV B.GEN,-(SP)	9485

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 4593		SEQ 0446
21271 21272	:ML4		22-0 22-0	Dec-1980 09:24:31 TOPS Dec-1980 09:21:22 PA:<
21274 074514 012746 005412 21275 074520 012746 000004 21276 074524 010600	MOV MOV MOV TRAP	#FMT.19,-(SP) #4,-(SP) SP.R0 14	; SP,*	
21278 074526 104414 21278 074530 016716 116410 21279 074534 016746 116400 21280 074540 016746 116376 21281 074544 012746 005462 21282 074550 012746 00004	MOV MOV MOV MOV MOV	P.CAL, (SP) A.CAL, -(SP) B.CAL, -(SP) B.CAL, -(SP) WFMT.20, -(SP) W4, -(SP) SP,R0 14 W1,R5 W32,SP 67 R0 1\$ R4 R4,W3 1\$	•	9486
21283 074554 010600 21284 074556 104414 21285 074560 012705 000001 21286 074564 062706 000032	MOV TRAP MOV ADD 10\$: TRAP	SP.RO 14 #1.R5 #32.SP	; SP,* ; *,DODU.FLG	9487 9482 9488
21287 074570 104467 21288 074572 006000 21289 074574 103616	10\$: TRAP ROR BLO	67 R0 1\$;	
21291 074600 020427 000003 21292 074604 003612 21293 074606 005305	ROR BLO INC CMP BLE DEC BNE	R4,#3 1\$ R5 11\$; PAT.SEL,* ; DODU.FLG	9445
21294 074610 001004 21295 074612 016700 116762 21296 074616 104451	MOV TRAP	ML.LUN.RO	;	9496
21297 074620 104444 21298 074622 000207	11\$: RTS	51 44 PC	:	9408
21300 21301 21306 21307	; Routine Size: ; Maximum stack	129 words depth per invocation: 19	words	
21311 21315 074624 21316 074624 004767 177372 21317 074630 104466 21318 074632 006000 21319 074634 103773 21320 074636 000207 21321 21322	T50:: 1\$: JSR TRAP ROR BLO RTS	PC.\$T50 66 RO 1\$ PC		9498
21322 21323	; Routine Size: ; Maximum stack	6 words depth per invocation: 0 w	words	

21332 21333 21334 ;

9501 ! CBLF/PAGE>

```
22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                      TOPS-20 Bliss-16 V2(212)
                                                                                      PA: <NEALE>BL3ML4.BLI.2 (101)
BGNTST:
 ! TEST NUMBER: TST 51
 ! TEST NAME:
                          CORRECTABLE ERROR SYNDRONE DECODE TEST
 ! TEST DESCRIPTION:
          TEST SYNDRONE DECODE AND ERROR CORRECTION TO DECODE AND CORRECT SINGLE BIT AND MULTIPLE BIT
          CHANNEL ERRORS BY:
          1. VIA ECC DIAG MODE FORCE SYNDRONE
BITS TO DECODE SINGLE BIT AND
               MULTIPLE BIT CHANNEL ERRORS.
          2. THEN DO A MASS BUS READ TRANSFER
               AND EXAM THE ID BUF FOR CORRECT BIT COMPLIMENTING.
IMPLICIT INPUTS:
          IO_BUF
          A VECTOR OF 256 WORDS WHERE DATA
          FOR MBUS READS AND WRITES TRANSFERS
          IS FOUND.
 local
  BITS_XFERED;
                                                          !POINTS TO THE CHANNEL BEING TESTED
BAI = ONE;
IO_BUF = ZEROES;
                                                          !BEFORE WE START LETS CLEAR OUT THE ML11'S
                                                          GOOD BLOCK BY WRITTING ZEROES TO IT
GD_BLK_XFER ();
MLCS1 = write;
TIME OUT LOOP;
BITS_XFERED = -1;
                                                         !RESET THE CHANNEL POINTER
 incr PLOG from 0 to 5 do
                                                         !TEST ONE CRC GROUP '6 ARRAY WORDS'
     begin
     incr CHANNEL from 0 to 35 do
                                                         !TEST 36 BITS IN EACH ARRAY WORD
          begin
BGNSUB;
                                                          !START OF SCOPE LOOP
                                                         CLEAR THE DRIVE !INCREMENT THE CHANNEL POINTER
          CLR_MBUS;
          BITS_XFERED = .BITS_XFERED + 1;
          incr CNT from 0 to 255 do
                                                         !CLEAR THE FIRST CRC GROUP IN THE IO_BUF
            IO_BUF [.CNT] = ZEROES;
```

```
TOPS-20 Bliss-16 V2(212)
                                                               22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                              PA: NEALE >BL3ML4.BL1.2 (101)
                                                              !FORCE THIS BIT IN THIS WORD IN ERROR
!SET UP A GOOD BLOCK TRANSFER
!CLEAR ECC DISABLE
!DO A READ TRANSFER
!WAIT FOR THE TRANSFER TO COMPLETE
          FORCE REM (.PLOG, .CHANNEL);
GD_BLR_XFER ();
ECC_DIS = ZERO;
MLCS1 = read;
           TIME_OUT_LOOP;
           if not FIND_COMP_BIT (.BITS_XFERED)
                                                              !SEARCH THE IO_BUF FOR THE COMP BIT
               begin !REP
ERROF (134, SYNC, DUMPER);
PRINTB (THR_FMT, WRD_74, WRD_75, PHR_1);
PRINTB (FMT_21, .PLOG, .CHANNEL);
                                                               !REPORT AN ERROR IF THIS BIT IS NOT COMP
                end:
           incr WRD_CNT from 0 to 12 do !SEE IF ANY OTHER BITS GOT COMP'ED
                if .IO_BUF [.WRD_CNT] neg ZEROES !TEST THE IO_BUF FOR ZEROES
                    begin !REPORT AN ERROR ERROR (135, SYNC, DUMPER);
PRINTB (FOR_FMT, WRD_61, WRD_74, WRD_75, PHR_5);
PRINTB (FMT_21, .PLOG, .CHANNEL);
                                                               !REPORT AN ERROR IF ANY OTHERS ARE SET
          ENDSUB:
                                                               !END OF SCOPE LOOP
          end:
     end:
BITS_XFERED = -1;
                                                             !RESET THE CHANNEL POINTER
incr CHANNEL from 0 to 35 do
                                                              !TEST 36 CHANNELS FOR MULTIPLE BIT ERRORS
     BGNSUB:
                                                               START OF SCOPE LOOP CLEAR THE DRIVE
     CLR_MBUS;
BITS_XFERED = .BITS_XFERED + 1;
                                                              !INCREMENT THE CHANNEL POINTER
     incr CNT from 0 to 20 do
                                                              !CLEAR THE FIRST CRC GROUP IN THE IO_BUF
          IO_BUF [.CNT] = ZEROES:
     FORCE REM (58, .CHANNEL);
GD_BLR_XFER ();
                                                               !FORCE ALL 6 BITS IN THIS CHANNEL IN ERROR
                                                               SET UP A GOOD BLOCK TRANSFER
                                                              CLEAR ECC DISABLE DO A READ TRANSFER WAIT FOR THE TRANSFER TO CMOPLETE
     ECT DIS = ZERO:
     MLC51 = read;
     TIME_OUT_LOOP:
     incr BIT_OFFSET from 0 to 180 by 36 do
                                                              !SEE IF EVERY 36TH BIT GOT COMP'ED
           if not FIND_COMP_BIT (.BITS_XFERED + .BIT_OFFSET)
                                                                                   !SEARCH THE IO_BUF FOR THE COMP'ED BITS
          then
               begin
                                                              !REPORT AN ERROR IF ANY OF THE BITS ARE NOT COMP'ED
```

WIRETTAMEONE CODING 2	ECITON			SEQ 0450
21448 :ML4 21449 :			22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212 22-Dec-1980 09:21:22 PA: <neale>BL3ML4.BLI.2</neale>	(101)
21451 : 9606 21452 : 9607 21453 : 9608 21454 : 9609	ERRDF (134 PRINTB (TH PRINTB (FM end;	, SYNC, DUMPER); R_FMT, WRD_74, WRD_75, PH T_21, 58, .CHANNEL);	R_1);	
21455 : 9610 21456 : 9611 21457 : 9612	incr WRD_CNT from	0 to 12 do	!ALSO SEE IF ANY OTHER BIT GOT COMP'ED	
21458 : 9613	if .IO_BUF [.W	RD_CNTJ neg ZEROES	!TEST THE IO_BUF FOR ALL ZEROES	
21459 : 9614 21460 : 9615	then			
21461 : 9616 21462 : 9617	ERROF (135	, SYNC, DUMPER);	75 LIRD 12 LIRD 67) ·	
21460 : 9615 21461 : 9616 21462 : 9617 21463 : 9618 21464 : 9619	PRINTB (FM end;	, SYNC, DUMPER); V_FMT, WRD_61, WRD_74, WR T_21, 58, .CHANNEL);		
21465 : 9620				
21467 : 9622	ENDSUB; end;		!END OF SCOPE LOOP	
21461 : 9616 21462 : 9617 21463 : 9618 21464 : 9619 21465 : 9620 21466 : 9621 21467 : 9622 21468 : 9623 21469 : 9624	ENDTST;			
71973	107252 000010 116342 \$T51:	JSR R1.\$SAVE5 BISB #10.amL.REG+40		9500 9536
21480 074656 004767 21481 074662 012777 21482 074670 105777	107252 000010 116342 114722 121200 000061 116264 116330 1\$:	JSR R1.\$SAVE5 BISB #10.aml.REG+40 CLR IO.BUF JSR PC.GD.BLK.XFER MOV #61.aml.REG TSTB aml.REG+50 BPL 1\$		9500 9536 9537 9538 9539
21483 074674 100375 21484 074676 012705 21485 074702 005004	177777	CLR R4	; *,BITS.XFERED ; PLOG	9541 9543 9546 9547 9548
21486 074704 005003	2\$: 3\$:	TRAP 2	CHANNEL	9546 9547
21488 074710 152777 21489 074716 016702	000040 116276 116660	BISB #40,aML.REG+40		9548
21490 074722 042702	177770 000007 116260 116254	BIC #177770.R2		
21492 074734 150277 21493 074740 005205	116254	BICB W7.aML.REG+40 BISB R2.aML.REG+40 INC R5	; BITS.XFERED	9550
21492 074734 150277 21493 074740 005205 21494 074742 005001 21495 074744 010102	45:	CLR R1 MOV R1,R2 ASL R2	CNT .*	9550 9552 9553
21496 074746 006302 21497 074750 005062 21498 074754 005201 21499 074756 020127 21500 074762 003770 21501 074764 010446	011600	ASL R2	, CNI,	7333
21496 074746 006302 21497 074750 005062 21498 074754 005201 21499 074756 020127		CLR IO.BUF (R2) INC R1 CMP R1,#377 BLE 4\$ MOV R4,-(SP)	; CNT ; CNT,*	9552
21500 074762 003770	000377	CMP R1.#377 BLE 4\$ MOV R4,-(SP)		
21501 074764 010446		MOV R4,-(SP)	; PLOG,*	9555

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 464		SEQ 0451
21503	:ML4		22-Dec-1980 09 22-Dec-1980 09	
21505 21506 074766 010346 21507 074770 004767 120606	MOV	R3,-(SP)	; CHANNEL,*	7:21:22 PA:S
21506 074766 010346 21507 074770 004767 120606 21508 074774 004767 121062 21509 075000 142777 000002 21510 075006 012777 000071 21511 075014 105777 116204 21512 075020 100375	116266 BICB 116140 MOV 5\$: TSTB	R3,-(SP) PC,FORCE.REM PC,GD.BLK.XFER #2,aML.REG+120 #71,aML.REG aML.REG+50 5\$ R5,-(SP) PC,FIND.COMP.BIT (SP)+ R0 6\$ 55 206 SYNC DUMPER #PHR.1,-(SP) #WRD.75,-(SP) #WRD.75,-(SP) #WRD.74,-(SP) #THR.FMT,-(SP) #4,-(SP) SP,R0 14 R3,(SP)		9556 9557 9558
21513 075022 010546 21514 075024 004767 120400 21515 075030 005726 21516 075032 006000	BPL MOV JSR TST ROR BLO TRAP . WORD . WORD . WORD MOV MOV	R5,-(SP) PC,FIND.COMP.BIT (SP)+ R0	; BITS.XFERED,*	9561
21518 075036 104455 21519 075040 000206 21520 075042 010526 21521 075044 024052	TRAP .WORD .WORD	55 206 SYNC DUMPER		9564
21522 075046 012746 007366 21523 075052 012746 007330 21524 075056 012746 007316 21525 075062 012746 006034 21526 075066 012746 000004	MOV MOV MOV MOV	#PHR.1,-(SP) #WRD.75,-(SP) #WRD.74,-(SP) #THR.FMT,-(SP)		9565
21527 075072 010600 21528 075074 104414	MOV TRAP	SP RÒ	; SP,*	
21529 075076 010316 21530 075100 010446 21531 075102 012746 005534 21532 075106 012746 000003	MOV	R3, (SP) R4,-(SP) WFMT.21,-(SP) W3,-(SP) SP,R0 14	; CHANNEL,* ; PLOG,*	9566
21533 075112 010600 21534 075114 104414	MOV TRAP	SP.R0 14	; SP,*	
21535 075116 062706 000020 21536 075122 005001 21537 075124 010102 21538 075126 006302	6\$: CLR 7\$: MOV	R1	WRD.CNT,*	9563 9569 9571
21536 075122 005001 21537 075124 010102 21538 075126 006302 21539 075130 005762 011600 21540 075134 001434 21541 075136 104455 21542 075140 000207 21543 075142 010526	BEQ TRAP .WORD	10.BUF (R2) 8\$ 55 207		9574
21536 075122 005001 21537 075124 010102 21538 075126 006302 21539 075130 005762 011600 21540 075134 001434 21541 075136 104455 21542 075140 000207 21543 075142 010526 21544 075144 024052 21545 075146 012746 007330 21547 075156 012746 007330 21548 075162 012746 007316 21549 075166 012746 007174 21549 075166 012746 006046 21550 075172 012746 006046 21553 075202 010316 21554 075204 010446 21555 075206 012746 005534 21556 075212 012746 000003	6\$: CLR 7\$: MOV ASL TST BEG TRAPWORDWORDWORD MOV	R1,R2 R2 IO.BUF(R2) 8\$ 55 207 SYNC DUMPER MPHR.5,-(SP) MURD.75,-(SP) MURD.74,-(SP) MURD.61,-(SP) MFOR.FMT,-(SP) M5,-(SP) SP,R0 14 R3,(SP) R4,-(SP) MFMT.21,-(SP) M3,-(SP) SP,R0		9575
21551 075176 010600 21552 075200 104414	MOV	SP.RO	; SP,*	
21553 075202 010316 21554 075204 010446 21555 075206 012746 005534 21556 075212 012746 000003	MOV MOV MOV	R3,(SP) R4,-(SP) #FMT.21,-(SP)	; CHANNEL,* ; PLOG,*	9576
21557 075216 010600	MOV	SP.RO	; SP,*	

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 465	SEQ 045	52
21559 21560 21561	:ML4			OPS A:<
21562 075220 104414 21563 075222 062706 000022 21564 075226 005201 21565 075230 020127 000014	8\$: INC	14 #22.SP R1 R1.#14	WRD.CNT 95	573 569
21567 075236 022626 21568 075240 104467 21569 075242 006000 21570 075244 103620	CMP BLE CMP TRAP ROR BLO INC CMP BLE INC CMP BLE MOY	7\$ (SP)+,(SP)+ 67 R0 3\$ R3 R3,#43 3\$ R4 R4,#5 2\$ #-1,R5 R3 2		547
21571 075246 005203 21572 075250 020327 000043 21573 075254 003614	INC CMP BLE	R3 R3,#43	CHANNEL,*	546
21574 075256 005204 21575 075260 020427 000005 21576 075264 003607	INC CMP BLE	R4 R4.#5	PLOG,*	543
21577 075266 012705 177777 21578 075272 005003 21579 075274 104402	MOV CLR 9\$: TRAP	#-1,R5 R3	*,BITS.XFERED 95 CHANNEL 95	584 586 587 588
21580 075276 152777 000040 21581 075304 016704 116272 21582 075310 042704 177770 21583 075314 142777 000007 21584 075322 150477 115666	9\$: TRAP 115710 BISB MOV BIC BICB BISB BISB	#40.aML.REG+40 ML.DUT.R4 #177770.R4 #7.aml.REG+40 R4.aML.REG+40 R5 R1	95	588
21585 075326 005205 21586 075330 005001 21587 075332 010102 21588 075334 006302	10\$: MOV ASL CLR INC CMP BLE MOV MOV JSR JSR JSR 115676 BICB MOV	R2 ;	BITS.XFERED 95 CNT 95 CNT,* 95	590 592 593
21589 075336 005062 011600 21590 075342 005201 21591 075344 020127 000024	INC CMP	IO.BUF (R2) R1 R1,#24	CNT 95	592
21592 075350 003770 21593 075352 012746 000072 21594 075356 010346	MOV	105 #72,-(SP) R3,-(SP)	CHANNEL,*	595
21595 075360 004767 120216 21596 075364 004767 120472 21597 075370 142777 000002 21598 075376 012777 000071 21599 075404 105777 115614	115676 BICB 115550 MOV 11\$: TSTB BPL	PC.FORCE.REM PC.GD.BLK.XFER #2.aML.REG+120 #71.aML.REG aML.REG+50	95 95 95	596 597 598
21590 075342 005201 21591 075344 020127 000024 21592 075350 003770 21593 075352 012746 000072 21594 075356 010346 21595 075360 004767 120472 21596 075364 004767 120472 21597 075370 142777 000002 21598 075376 012777 000071 21599 075404 105777 115614 21600 075410 100375 21601 075412 005002 21602 075414 010246 21603 075416 060516 21604 075420 004767 120004 21605 075424 005726 21606 075430 103433 21608 075436 010526 21610 075436 010526 21611 075440 024052 21612 075442 012746 007366 21613 075446 012746 007330	12\$: MOV ADD JSR TST	PC.FIND.COMP.BIT	BIT.OFFSET 96 BIT.OFFSET,* 96 BITS.XFERED,*	501
21606 075426 006000 21607 075430 103433 21608 075432 104455 21609 075434 000206 21610 075436 010526	ROR BLO TRAP .WORD .WORD	R0 13\$ 55 206 SYNC DUMPER WPHR.1,-(SP) WWRD.75,-(SP)	96	506
21612 075442 012746 007366 21613 075446 012746 007330	MOV	#PHR.1,-(SP) #WRD.75,-(SP)	96	507

SEQ 0453 22-Dec-1980 09:24:31 TOP			:ML4			21615
22-Dec-1980 09:24:31 TOP: 22-Dec-1980 09:21:22 PA:						21616 21617
		MURD.74,-(SP) WTHR.FMT,-(SP) W4,-(SP) SP,R0 14 R3,(SP) W72,-(SP) WFMT.21,-(SP) W3,-(SP) SP.R0 14 W20 SP	MOV	007316	012746	21618 075452 21619 075456
	; SP,*	#4,-(SP) SP.RO	MOV	000004	012746	21620 075462 21621 075466
960	; CHANNEL,*	14 83 (SP)	MOV TRAP MOV		104414	21622 075470 21623 075472
700	, (1946),	#72,-(SP)	MOV	000072 005534 000003	012746	21624 075474
	; SP,*	#3,-(SP)	MOV MOV MOV MOV TRAP	000003	012746	21626 075504
040	, 57,-	14 #20 SP	TRAP	000020	104414	21628 075512
960 960	: *,BIT.OFFSET : BIT.OFFSET,*	#20.SP #44.R2 R2.#264 12\$	138: ADD CMP	000020 000044 000264	062702	21630 075520
041		12\$ R1	BLE	000204	003731	21632 075530
961 961	; WRD.CNT ; WRD.CNT,*	R1,R2	BLE CLR 14\$: MOV ASL TST		010102	21634 075534
		10.BUF (R2) 15\$ 55 207 SYNC DUMPER	TST	011600	005762	21636 075540
9610	:	55	TRAP		104455	21638 075546
		SYNC	BEQ TRAP .WORD .WORD .WORD		010526	21640 075552
961	:	MRD.67,-(SP)	MUV	007240	012746	21642 075556
		MRD. 75, -(SP)	MOV	007330	012746	21644 075566
		DUMPER MURD.67,-(SP) MURD.12,-(SP) MURD.75,-(SP) MURD.61,-(SP) MURD.61,-(SP) MFIV.FMT,-(SP) M6,-(SP) SP,R0 14 R3,(SP) M72,-(SP) MFMT.21,-(SP) M5,-(SP) SP,R0 14 M24,SP	MOV	007174	012746	21646 075576 21647 075602 21648 075606
		#6,-(SP)	MOV	006062 000006	012746	21648 075606 21649 075612
	; SP,*	3P. NO 14	TRAP		104414	21650 075614
9618	; CHANNEL,*	#72,-(SP)	MOV	000072	012746	21652 075620
		#3,-(SP)	MOV	000072 005534 000003	012746	21654 075630
	; SP,*	5P.RU 14	TRAP	000024	012746 012746 010600 104414 010316 012746 012746 012746 010600 104414 062706 005201 020127 003730 022626	21656 075636
9615 9611	WRD.CNT	R1	15\$: INC	000024	005201	21658 075644
	; WRD.CNT,*	R1 .#14 14\$	BLE	000014	003730	21660 075652
9587 9619		(SP)+, (SP)+ 67	TRAP		104467	21662 075656
		14\$ (SP)+,(SP)+ 67 R0 9\$ R3 R3,#43 9\$ PC	MOV MOV TRAP MOV MOV MOV TRAP ADD 15\$: INC CMP BLE CMP TRAP ROR BLO INC CMP BLE CMP		103604	21647 075602 21648 075606 21649 075612 21650 075614 21651 075616 21652 075620 21653 075624 21654 075630 21655 075634 21656 075636 21659 075646 21659 075646 21660 075652 21661 075656 21663 075660 21664 075660 21666 075666 21667 075672 21668 075674 21669
9586	; CHANNEL,*	R3,#43	CMP	000043	103604 005203 020327 003600 000207	21666 075666
9500	:	PC PC	BLE		000207	21668 075674

CZMLABO ML-11 LOGIC TE MISCELLANEOUS CODING S	ST MACRO M	1113 22-DEC-80 12:16	PAGE 467		SE	EQ 0454
21671 21672		:ML4			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
21673 21674 21675 21680 21681		: Routine Size: 271 : Maximum stack depth	words h per invocation:	18 words		
21685 21689 075676 21690 075676 004767 21691 075702 104466 21692 075704 006000 21693 075706 103773 21694 075710 000207	176736	T51:: 1\$: JSR PC.\$1 TRAP 66 ROR RO BLO 1\$ RTS PC	r51	•		9622
21695 21696 21697 21702 21703 21704 ; 9625	! <blf page=""></blf>	; Routine Size: 6 wo ; Maximum stack depth	ords h per invocation:	0 words		

21708	
21713 9630 TEST NUMBER: TST 52 21714 9631 21715 9632 TEST NAME: UNCORRECTABLE ERROR SYNDRONE DECODE TEST AT CHANNELS > 35 21716 9633 21717 9634 21718 9635 21719 9636 TEST SYNDRONE DECODE TO DETECT BUT NOT CORRECT UNCORRECTABLE CHANNEL 21720 9638 21721 9638 1. VIA ECC. DIAG. MODE. FORCE. SYNDRONE	
21716: 9633 TEST DESCRIPTION: 21718: 9635 TEST SYNDRONE DECODE TO DETECT BUT	
21718 : 9635 ! TEST SYNDRONE DECODE TO DETECT BUT 21719 : 9636 ! NOT CORRECT UNCORRECTABLE CHANNEL 21720 : 9637 ! ERRORS BY: 21721 : 9638 ! 21722 : 9639 ! 1. VIA ECC DIAG MODE FORCE SYNDRONE	
21722: 9639 1. VIA ECC DIAG MODE FORCE SYNDRONE 21723: 9640 BITS TO INDICATE CHANNEL ERRORS 21724: 9641 AT WORDS OF A CRC GROUP BUT AT CHANNELS	
21725 : 9042 : GREATER THAN 33 .	
21727 : 9644 : 2. DO A MASS BUS READ TRANSFER	
21728 9645 3. THEN EXAMIN THE ID BUF FOR NO 21730 9647 BITS COMPLIMENTED AND ECH AND 21731 9648 BITS SET	
21733 9650 IMPLICIT INPUTS: 21734 9651 IO_BUF 21735 9652 A VECTOR OF 256 WORDS WHERE DATA 21736 9653 FOR MBUS READS AND WRITES TRANSFERS 21737 9654 IS FOUND. 21738 9655 21739 9656	
21740 : 9657 : 21741 : 9658	
21742: 9659 Local 21743: 9660 PLOG: !INDEX INTO REMAINDER TABLE 'REM_TBL'	
21744 : 9661 21745 : 9662 BAI = ONE; 21746 : 9663 IO_BUF = ZEROES; 21747 : 9664 GD_BUK_XFER (); 21748 : 9665 MLCS1 = write; 21749 : 9666 TIME_OUT_LOOP; 21750 : 9667 21751 : 9668 incr_PLOG_SEL from 0 to 6 do !TEST SYNDRONE DECODE AT 7 DIFFERENT PLOG VALUES	
21750 : 9667 21751 : 9668 incr PLOG_SEL from 0 to 6 do !TEST SYNDRONE DECODE AT 7 DIFFERENT PLOG VALUES	
21752 : 9669 begin 21753 : 9670	
21754: 9671 case .PLOG_SEL from 0 to 6 of !SELECT A PLOG VALUE 21755: 9672 set 21756: 9673	
21757; 9674 [0]: !POINTS TO WORD 0 IN THE CRC GROUP 21758; 9675 PLOG = 0; 21759; 9676 21760; 9677 [1]: !POINTS TO WORD 1 IN THE CRC GROUP	
21759 : 9676 21760 : 9677 [1] : !POINTS TO WORD 1 IN THE CRC GROUP	

PRINTB (FMT_21, .PLOG, .CHANNEL);

!REPORT AN ERRUR IF ANT LU
!REPORT ANT LU
!REPORT AN ERRUR IF ANT LU
!REPORT ANT LU
!REPORT AN ERRUR IF ANT LU
!REPORT end:

ENDSUB: !END OF SCOPE LOOP end:

	002110 00									3E4 0431
21818 :ML4 21819 : 21820							22-Dec-1980 09:24 22-Dec-1980 09:21	1:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (1</neale>	102)
21821 : 21822 : 21823 : 21824 :	9730 9731 9732 9733	end ENDTST;								
21828 21832 075712 21833 075716 21834 075724 21835 075730 21836 075734 21837 075742 21838 075746 21839 075750	004167 152777 005067 004767 012777 105777 100375 005005	106200 000010 113650 120126 000061 115256	115270 115212	\$T52: 1\$:	JSR BISB CLR JSR MOV TSTB BPL CLR	R1.\$SAVE5 #10.aML.REG+40 IO.BUF PC.GD.BLK.XFER #61.aML.REG aML.REG+50 1\$ R5		PL 06 - S	FI	9624 9662 9663 9664 9665
21840 075752 21841 075754 21842 075756 21843 075762 21844 075764 21845 075766 21846 075770 21847 075772	005005 010504 006304 066407 000016 000022 000030 000036 000044 000052 000060 005003	075762		2\$: 3\$:	CLR MOV ASL ADD .WORD .WORD .WORD .WORD .WORD .WORD .WORD .WORD	R5 R5,R4 R4 3\$(R4),PC 4\$-3\$ 5\$-3\$ 6\$-3\$ 7\$-3\$ 8\$-3\$ 9\$-3\$		PLOG.S	ĒĹ,*	9668 9671
21849 075776 21850 076000 21851 076002 21852 076004 21853 076010 21854 076012 21855 076016 21856 076020 21857 076024 21858 076026	000421	000001		4\$: 5\$:	MUA	11s #1,83		PLOG *,PLOG		9675 9671 9678
21854 076012 21855 076016	000416 012703 000413 012703	000002		6\$:	BR MOV BR	11\$ #2,R3 11\$		*,PL06		9671 9681 9671 9684 9671 9687 9671 9690
21856 076020 21857 076024 21858 076026	012703	000003		7\$:	MOV BR	#3.R3 11\$		*,PLOG		9684 9671
21858 076026 21859 076032 21860 076034	000410 012703 000405	000004		8\$:	MOV	#4,R3		*,PL06		9687 9671
21860 076034 21861 076040	012703 000402	000005		9\$:	MOV	#5.R3 11\$		*,PLOG		9671
21862 076042 21863 076046	012703	000072 000044		10\$: 11\$: 12\$:	MOV MOV TRAP BISB	#5.R3 11\$ #72.R3 #44.R4		*,PLOG	NEL	9693 9696 9697 9698
21858 076026 21859 076032 21860 076034 21861 076040 21862 076042 21863 076046 21864 076052 21865 076054 21866 076062 21867 076066 21868 076072 21869 076100 21870 076104 21871 076106	000405 012703 000402 012703 012704 104402 152777 016702 042702 142777 150277 005001 010102	000040 115514 177770 000007 115110	115132 115114	123:	BISB MOV BIC BICB BISB	#40, aml.REG+40 ML.DUT, R2 #177770, R2 #7, aml.REG+40 R2, aml.REG+40 R1, R2				9697 9698
21870 076104 21871 076106	005001	115110		13\$:	CLR MOV	R1 R1, R2		COUNT,		9701 9702

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 471	SEQ 0458
21873 21874 21875	ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
21876 076110 006302 21877 076112 005062 011600 21878 076116 005201	ASL CLR INC	R2 IO.BUF (R2) R1	COUNT 9701
21879 076120 020127 000377 21880 076124 003770 21881 076126 010346 21882 076130 010446	BLE MOV MOV	135	PLOG, * 9704 CHANNEL, *
21883 076132 004767 117444 21884 076136 004767 117720 21885 076142 142777 000002 21886 076150 012777 000071 21887 076156 105777 115042	114776 MOV 14\$: TSTB	PC.FORCE.REM PC.GD.BLK.XFER #2.aML.REG+120 #71.aML.REG aML.REG+50 14\$ #100,aML.REG+60 ;	9705 9706 9707
21887 076156 105777 115042 21888 076162 100375 21889 076164 132777 000100 21890 076172 001403 21891 076174 005777 115164	BEQ	#100,aML.REG+60 ;	9710
21892 076200 100434 21893 076202 104455 21894 076204 000210 21895 076206 010526	15\$: TRAP .WORD .WORD .WORD	15\$ 27L.REG+210 16\$ 55 210 SYNC DUMPER	9713
21896 076210 024052 21897 076212 012746 006374 21898 076216 012746 007340 21899 076222 012746 006406 21900 076226 012746 007240 21901 076232 012746 006046 21902 076236 012746 000005	MORD MOV MOV MOV MOV MOV MOV	DUMPER MURD.9,-(SP) MURD.76,-(SP) MURD.10,-(SP) MURD.67,-(SP) MFOR.FMT,-(SP) M5,-(SP) SP,R0 14	9714
21903 076242 010600 21904 076244 104414 21905 076246 010416	MOV TRAP MOV	SP.RO :	SP,* CHANNEL,* 9715
21906 076250 010346 21907 076252 012746 005534 21908 076256 012746 000003	MOV	R4, (SP) R3,-(SP) #FMT.21,-(SP) #3,-(SP) SP,R0 :	CHANNEL,* PLOG,* 9715
21908 076256 012746 000003 21909 076262 010600 21910 076264 104414 21911 076266 062706 000022 21912 076272 005001 21913 076274 010102	MOV TRAP	SP.RO :	SP,*
21912 076272 005001 21913 076274 010102 21914 076276 006302 21915 076300 005762 011600	165: CLR 178: MOV ASL TST	R1 R1,R2 R2 I0.Buf (R2)	ONT 9712 9718 ONT,* 9720
21914 076276 006302 21915 076300 005762 011600 21916 076304 001436 21917 076306 104455 21918 076310 000211 21919 076312 010526	BEQ TRAP . WORD . WORD	18\$ 55 211 SYNC	9723
21906 076250 010346 21907 076252 012746 005534 21908 076256 012746 000003 21909 076262 010600 21910 076264 104414 21911 076266 062706 000022 21912 076272 005001 21913 076274 010102 21914 076276 006362 21915 076300 005762 011600 21918 076310 000211 21919 076312 010526 21921 076316 012746 007240 21922 076322 012746 006426 21923 076326 012746 007316 21924 076332 012746 007316 21925 076332 012746 007316 21926 076342 012746 0070174 21926 076342 012746 0070174	BEQ TRAP . WORD . WORD . WORD MOV MOV MOV MOV MOV MOV MOV	14 #22.SP R1 R1.R2 R2 IO.BUF (R2) 18\$ 55 211 SYNC DUMPER #WRD.67,-(SP) #WRD.12,-(SP) #WRD.75,-(SP) #WRD.74,-(SP) #WRD.61,-(SP) #FIV.FMT,-(SP) #6,-(SP)	9724

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 1	12:16 PAGE 472		SEQ 045	59
21929 21930 21931	:ML4			22-Dec-1980 09:24:31 TO 22-Dec-1980 09:21:22 PA	OPS A:<
21932 076352 010600 21933 076354 104414 21934 076356 010416 21935 076360 010346 21936 076362 012746 005534 21937 076366 012746 000003	MOV TRAP MOV MOV MOV	SP,R0 14 R4,(SP) R3,-(SP) WFMT.21,-(SP) W3,-(SP) SP,R0 14	; SP,* ; CHANNEL,* ; PLOG,*	97	725
21938 076372 010600 21939 076374 104414 21940 076376 062706 000024 21941 076402 005201 21942 076404 020127 000014 21943 076410 003731	MOV TRAP ADD 18\$: INC CMP BLE	#24,5P R1 R1,#14 17\$; SP,* ; CNT ; CNT,*		722
21944 076412 022626 21945 076414 104467 21946 076416 006000 21947 076420 103614 21948 076422 005204 21949 076424 020427 000076	18\$: INC CMP BLE CMP TRAP ROR BLO INC CMP BLE INC CMP	(SP)+,(SP)+ 67 R0 12 \$ R4 R4,#76	; CHANNEL ,*		597 726 596
21950 076430 003610 21951 076432 005205 21952 076434 020527 000006 21953 076440 003002 21954 076442 000167 177304 21955 076446 000207	BLE INC CMP BGT JMP 19\$: RTS	RO 12\$ R4 R4.#76 12\$ R5 R5.#6 19\$ 2\$; PLOG.SEL,*		568
21956 21957 21958 21963 21964	: Routine Size:		18 words		
21972 076450 21973 076450 004767 177236 21974 076454 104466 21975 076456 006000 21976 076460 103773 21977 076462 000207	T52:: 1\$: JSR TRAP ROR BLO RTS	PC.\$T52 66 R0 1\$ PC		97	731
21978 21979 21980	; Routine Size: ; Maximum stack	6 words depth per invocation:	0 words		

SEQ 0460

21989 21990 21991

9734 ! <BLF/PAGE>

21993 :M	L4		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (103)</neale>
21995	9735			
21997 :	9736	BGNTST;		
21998 :	9737			
21999 :	9738	TECT MUMBER. TET 57		
22000 ;	9739	TEST NUMBER: TST 53		
22002 :	9741	! TEST NAME: MULTIPLE CHANNEL ERROR	SYNDRONE DECODE TEST	
22003 ;	9742			
22004 ;	9743	! TEST DESCRIPTION:		
22005 ;	9744	TEST SYNDRONE DECODE TO DETECT BUT NOT CORRECT UNCORRECTABLE MULTIPLE CHANNEL ERRORS BY:		
22000	9745	FRRORS RY:		
22008 :	9747	Ennons or.		
22009 ;	9748	! 1. VIA ECC DIAGNOSTIC MODE FORCE SYND	PRONE	
22010 :	9749	1. VIA ECC DIAGNOSTIC MODE FORCE SYNE BITS TO INDICATE MULTIPLE CHANNEL BY ASSIGNING CRC_A EQUAL TO CRC_B	ERRORS	
22011 :	9/50	BY ASSIGNING CRC_A EQUAL TO CRC_B		
22013	9752	2. DO A MASS BUS READ TRANSFER		
22014 ;	9753	!		
22015 ;	9754	3. THEN EXAMIN THE 10 BUF FOR NO BITS	S	
22016 ;	9755	COMPLIMENTED AND ECH AND UNC BITS	SET	
22016 : 22017 : 22018 : 22020 : 22021 : 22023 : 22024 : 22025 :	9750	IMPLICIT INPUTS:		
22019 :	9758	! IO_BUF		
22020 ;	9759	! A VECTOR OF 256 WORDS WHERE DATA		
22021 ;	9760	FOR MBUS READS AND WRITES TRANSFERS		
22022 :	9/61	IS FOUND.		
22024 :	9763			
22025 ;	9764	<u></u>		
22026 ;	9765			
22027 ;	9766	local	LIMBER INTO DEMAINDED	TADLE IDEM TOLL
22020 :	9768	PLOG;	!INDEX INTO REMAINDER	IABLE KEM_IBL
22029 22030 22031 22032	9769	BAI = ONE;	FIRST LETS CLEAR THE	ML11'S GOOD
22031 ;	9770	IO_BUF = ZEROES;	!FIRST LETS CLEAR THE !BLOCK BY WRITING ZERO	ES TO IT
22032 :	9771	IO_BUF = ZEROES; GD_BLK_XFER (); MLCS1 = write; TIME_OUT_LOOP;		
22033 ;	9772	MLCS1 = Write;		
22035 :	9774	TIME_OUT_LOUP;		
22036 ;	9775	incr PLOG_SEL from 0 to 6 do	!TEST SYNDRONE DECODE	AT 7 DIFFERENT PLOG VALUES
22037 ;	9776	begin		
22038 :	9///	come DIOC SEL from 0 to 4 of	ISELECT A DLOC VALUE	
22040 :	9779	case .PLOG_SEL from 0 to 6 of set	SELECT A PLOG VALUE	
22041 :	9780	361		
22042 :	9781	[0] :	!POINTS TO CRC WORD 0	
22043 ;	9782	PLOG = 0;		
22045	9783	[1] :	IDUINTS TO COC LIDER 1	
22046	9739 9740 9741 9742 9743 9744 9745 9746 9749 9751 9752 9753 9754 9756 9757 9764 9763 9764 9765 9764 9765 9767 9776 9771 9772 9773 9776 9777 9778 9778 9778 9778 9778 9778	PLOG = 1;	!POINTS TO CRC WORD 1	
22047 ;	9786			

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION

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22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                       TOPS-20 Bliss-16 V2(212)
                                                                                                       PA: <NEALE>BL3ML4.BLI.2 (103)
9787
                  [2]:
                                                                       !POINTS TO CRC WORD 2
                       PLOG = 2:
                  [3] :
                                                                       !POINTS TO CRC WORD 3
                       PLOG = 3:
                 [4] :
PLOG = 4;
                                                                       !POINTS TO CRC WORD 4
                  [5] :
                                                                       !POINTS TO CRC WORD 5
                       PLOG = 5:
                                                                       !POINTS TO ALL 6 WORDS IN CRC GROUP
                       PLOG = 58:
                  tes:
             incr CHANNEL from 1 to 35 do
                                                                       !TEST 35 CHANNELS STARTING AT CHANNEL 1
                  begin
BGNSUB;
                                                                       START OF SCOPE LOOP CLEAR THE DRIVE
                  CLR_MBUS:
                  incr COUNT from 0 to 255 do
                                                                       !CLEAR FIRST CRC GROUP IN THE IO_BUF
                       IO_BUF [.COUNT] = ZEROES;
                 FORCE_REM (.PLOG, .CHANNEL);
CRC_A = .CRC_B;
GD_BLK_XFER ();
ECC_DIS = ZERO;
MLCS1 = read;
                                                                       !FORCE ERR AT THIS WORD AND CHANNEL !CRC_A EQL TO CRC_B CAUSES THE UNC ERROR !SET_UP A GOOD BLOCK TRANSFER
                                                                       !CLEAR ECC DISABLE
                                                                       DO A READ TRANSFER IS COMPLETE
                  TIME_OUT_LOOP:
                                                                       !SEE IF THE ERROR WAS DETECTED
                  if not ((.ECH_ERR) and (.UNC_ERR))
                  then
                      begin !REPORT ERROR
ERROF (138, SYNC, DUMPER);
PRINTB (FOR FMT, WRD 77, WRD 10, WRD 76, WRD 9);
PRINTB (FMT_21, .PLOG, .CHANNEL);
                                                                       !REPORT ERROR IF NOT DETECTED
                       end:
                  incr CNT from 0 to 12 do
                                                                      !SEE IF ANY ERROR CORRECTION WAS DONE
                       if .IO_BUF [.CNT] neg ZEROES
                                                                      !TEST THE IO_BUF FOR ALL ZEROES
                       then
                           begin

ERRDF (139, SYNC, DUMPER);

PRINTB (FIV_FMT, WRD_61, WRD_74, WRD_75, WRD_12, WRD_67);

PRINTB (FMT_21, .PLOG, .CHANNEL);
                                                                       !ERROR IF ANY BITS GOT FLIPPED
                  ENDSUB:
                                                                       !END OF SCOPE LOOP
                  end;
```

22105 : 22106 :	ML4							22-Dec-1980 09:24 22-Dec-1980 09:21	:31	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (103)</neale>	0.03
22107 22108 ; 22109 ; 22110 ;		9839 9840 9841	end ENDTST;								
22118 0 22119 0 22120 0 22121 0 22122 0)76464)76470)76476)76502)76506	004167 152777 005067 004767 012777 105777	105426 000010 113076 117354 000061 114504	114516 114440	\$T53:	JSR BISB CLR JSR MOV TSTB	R1,\$SAVE5 #10,aML.REG+40 IO.BUF PC,GD.BLK.XFER #61,aML.REG				9733 9769 9770 9771 9772
22124 0 22125 0 22126 0 22127 0 22128 0)76520)76522)76524)76526)76530	100375 005005 010504 006304 066407	076534		2\$:	BPL CLR MOV	#61,aML.REG aML.REG+50 1\$ R5 R5 R5,R4 R4 3\$(R4),PC	;	PLOG.	SEL,*	9775 9778
22129 0 22130 0 22131 0 22132 0 22133 0 22134 0 22135 0)76534)76536)76540)76542)76544)76546	000016 000022 000030 000036 000044 000052			3\$:	ADD .WORD .WORD .WORD .WORD .WORD .WORD	3\$ (R4) ,PC 4\$-3\$ 5\$-3\$ 6\$-3\$ 7\$-3\$ 8\$-3\$ 9\$-3\$				
22136 0 22137 0 22138 0)76552)76554)76556)7656	005003 000421 012703 000416	000001		4\$: 5\$:	CLR BR MOV BR	10s-3s R3 11s #1,R3 11\$		PLOG *,PLOG		9782 9778 9785
22141 0	76564 76570 76572	012703 000413 012703	000002		6\$: 7\$:	MOV BR MOV	#2,R3 11\$ #3,R3		*,PL00		9788 9778 9791
22144 0 22145 0 22146 0)76576)76600)76604)76606	000410 012703 000405 012703 000402	000004 000005		8\$: 9\$:	BR MOV BR MOV	11\$ #4,R3 11\$ #5,R3		*,PL00		9778 9794 9778 9797
22147 0 22148 0 22149 0 22150 0)76612)76614)76620	012703	000072 000001		10\$: 11\$: 12\$:	BR MOV MOV TRAP BISB	11\$ #72.R3 #1.R4		*,PLOC *,CHA		9782 9778 9785 9778 9778 9778 9778 9794 9778 9797 9778 9800 9803 9804 9805
22151 0 22152 0 22153 0	76626 76634 76640	152777 016702 042702	000040 114742 177770 000007 114336	114360	120.	BISB MOV BIC BICB BISB	#40, aml.REG+40 ML.DUT, R2 #177770, R2 #7, aml.REG+40 R2, aml.REG+40 R1	:			9805
22148 0 22149 0 22150 0 22151 0 22152 0 22153 0 22155 0 22156 0 22157 0 22158 0	076612 076614 076624 076624 076634 076634 076652 076652 076660	012704 104402 152777 016702 042702 142777 150277 005001 010102 006302	114336	114342	13\$:	BICB BISB CLR MOV ASL	#7,aML.REG+40 R2,aML.REG+40 R1 R1,R2 R2	;	COUNT		9808 9809

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 477	SEQ 0464
22160 22161 22162	;ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
22163 076664 005062 011600 22164 076670 005201 22165 076672 020127 000377	CLR INC CMP	IO.BUF (R2) R1 R1,#377 ; COUNT,*	9808
22166 076676 003770 22167 076700 010346 22168 076702 010446 22169 076704 004767 116672	CLR INC CMP BLE MOV MOV JSR MOVB	13\$ R3,-(SP) ; PLOG,* R4,-(SP) ; CHANNEL,*	9811
22169 076704 004767 116672 22170 076710 117702 114420 22171 076714 042702 177700 22172 076720 142777 000077 22173 076726 150277 114372	MOVB BIC BICB	amL.REG+160.R2 ; #177700.R2 #77,amL.REG+150	9812
22174 076732 004767 117124 22175 076736 142777 000002 22176 076744 012777 000071	114376 BICB BISB JSR 114330 BICB 114202 MOV	RS,-(SP) R4,-(SP) PC,FORCE.REM aML.REG+160,R2 #177700,R2 #77,aML.REG+150 R2,aML.REG+150 PC,GD.BLK.XFER #2,aML.REG+120 #71,aML.REG aML.REG+50 148 #100,aML.REG+60	9813 9814 9815
22177 076752 105777 114246 22178 076756 100375 22179 076760 132777 000100 22180 076766 001403 22181 076770 005777 114370	BPL	aML.REG+50 14\$ #100,aML.REG+60 ;	9818
22181 076770 005777 114370 22182 076774 100434 22183 076776 104455	TST	#100, aml.REG+60 15\$ aml.REG+210 16\$ 55 212 SYNC DUMPER #MRD.9,-(SP) #MRD.76,-(SP) #MRD.10,-(SP) #MRD.77,-(SP) #FOR.FMT,-(SP) #5,-(SP) SP,R0 14 R4.(SP) : CHANNEL.*	9821
22185 077002 010526 22186 077004 024052 22187 077006 012746 006374 22188 077012 012746 007340	15\$: TRAP .WORD .WORD .WORD MOV	SYNC DUMPER MURD. 9, -(SP)	9822
22189 077016 012746 006406 22190 077022 012746 007346 22191 077026 012746 006046	MOV MOV MOV MOV	MWRD.76,-(SP) MWRD.77,-(SP) MFOR.FMT,-(SP)	
22192 077032 012746 000005 22193 077036 010600 22194 077040 104414	MOV MOV TRAP	#5,-(SP) SP,R0 ; SP,*	
22195 077042 010416 22196 077044 010346 22197 077046 012746 005534 22198 077052 012746 000003	MOV MOV MOV	R4, (SP) R3,-(SP) #FMT.21,-(SP) #3,-(SP) SP,R0 14 #22,SP R1 P1, P2 CNT	9823
22199 077056 010600 22200 077060 104414 22201 077062 062706 000022	MOV TRAP ADD	SP.RO ; SP.*	9820
22202 077066 005001 22203 077070 010102 22204 077072 006302	16\$: CLR 17\$: MOV ASL	R1 R1,R2 ; CNT,*	9820 9826 9828
22205 077074 005762 011600 22206 077100 001436 22207 077102 104455 22208 077104 000213	BEQ TRAP .WORD	18\$ 55 213	9831
22194 077040 104414 22195 077042 010416 22196 077044 010346 22198 077052 012746 00003 22199 077056 010600 22200 077060 104414 22201 077062 062706 000022 22202 077066 005001 22203 077070 010102 22204 077072 006302 22205 077074 005762 011600 22206 077100 001436 22207 077102 104455 22208 077104 000213 22208 077104 000213 22209 077106 010526 22211 077112 012746 007240 22212 077116 012746 007330 22213 077122 012746 007316	MOV MOV TRAP MOV MOV MOV MOV TRAP ADD TRAP ADD TRAP 	R1 R1.R2 ; CNT * R2	9832

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 478		SEQ 0465
22216 22217 23318	ML4		22-Dec-1980 0 22-Dec-1980 0	9:24:31 TOPS 9:21:22 PA:<
22219 077132 012746 007174 22220 077136 012746 006062 22221 077142 012746 000006 22222 077146 010600 22223 077150 104414	MOV MOV MOV TRAP	#WRD.61,-(SP) #FIV.FMT,-(SP) #6,-(SP) SP,R0 14	; SP,*	
22224 077152 010416 22225 077154 010346 22226 077156 012746 005534 22227 077162 012746 000003 22228 077166 010600 22229 077170 104414	MOV MOV MOV MOV TRAP	R4.(SP) R3(SP) WFMT.21(SP) W3(SP) SP.R0	; CHANNEL,* ; PLOG,* ; SP,*	9833
22231 077176 005201 000024	18\$: INC CMP BLE CMP	R1 R1,#14	: CNT : CNT,*	9830 9826
22233 077204 003731 22234 077206 022626 22235 077210 104467	BLE CMP TRAP ROR	17\$ (SP)+,(SP)+ 67 R0 12\$ R4	;	9804 9834
22236 077212 006000 22237 077214 103603 22238 077216 005204 22239 077220 020427 000043 22240 077224 003002 22241 077226 000167 177372	TRAP ROR BLO INC CMP BGT JMP	R4.#43	: CHANNEL ,*	9803
22243 077234 020527 000006	CMb	12\$ R5 R5.#6 20\$ 2\$ PC	; PLOG.SEL,*	9775
22245 077242 000167 177256 22246 077246 000207	BGT JMP 20\$: RTS	PC PC	:	9733
22246 077246 000207 22247 22248 22249 22254 22255	; Routine Size ; Maximum stack	: 186 words k depth per invocation: 18 words		
22263 077250 22264 077250 004767 177210 22265 077254 104466 22266 077256 006000 22267 077260 103773 22268 077262 000207 22269	T53:: 1\$: JSR TRAP ROR BLO RTS	PC,\$T53 66 RO 1\$ PC		9839

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION

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SEQ 0466

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:ML4

: Routine Size: 6 words : Maximum stack depth per invocation: 0 words

9842 ! <BLF/PAGE>

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BLI.2 (104) BGNTST: TEST NUMBER: TST 54 TEST NAME: SINGLE BIT ERROR SYNDRONE GENERATION & DECODE TEST TEST DESCRIPTION: TEST SYNDRONE GENERATION, SYNDRONE DECODE AND ERROR CORRECTION FOR SINGLE BIT CHANNEL ERRORS BY: 1. CLEAR THE FIRST CRC GROUP IN THE IO_BUF AND GOOD BLOCK TO ZEROES 2. VIA DATA DIAGNOSTIC MODE FLIP A BIT TO A ONE 3. THEN DO A MASS BUS READ TRANSFER AND EXAMIN THE ID BUF FOR ZEROES INDICATING THE CORRECTION WAS PERFORMED 4. REPEAT WITH ALL BITS IN THE CRC GROUP ! IMPLICIT INPUTS: IO BUF A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITES TRANSFERS IS FOUND. PD TEMP A BIT VECTOR OF 16 BITS WHERE THE READ PROM DATA IS STORED AND ACCESSED FROM. local !DONE FLAG NIB_PAT : bitvector [4], STORAGE FOR SBE DATA GENERATED POINTER WHERE SBE IS TO BE WRITTEN COUNT OF WHERE GOOD 'NIB_SEL' ARE FOUND NIB_SEL, GD_ GRD_ CNT; incr WRD CNT from 0 to 5 do !FORCE SINGLE BIT ERRORS IN ONE CRC GROUP begin incr BIT_CNT from 0 to 35 do !FORCE SBE'S AT EACH BIT OF A WORD begin incr CNT from 0 to 255 do IO_BUF [.CNT] = ZEROES; !CLEAR FIRST CRC GROUP OF IO_BUF

MISCELLAMEOUS	CODING SECTION			35-
22340 :ML4 22341 :			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (104)</neale>
22343 22344 22345	9895 9896 9897	CLR MBUS; GD_BLK_XFER ();	CLEAR THE DRIVE	RANSFER
22346 22347 22348	9898 9899 9900	MLCS1 = write; TIME OUT_LOOP; CLR_MBUS;	!CLEAR THE DRIVE AGAIN	ER IS COMPLETE
22349 : 22350 : 22351 :	9901 9902 9903	DONE = ZERO; GD_WRD_CNT = -1; MLD1 = ZEROES;	CLEAR THE DONE FLAG PRESET THE COUNT LOAD THE DATA DIAG RE	G (BITS 0-38)
22352 22353 22354	9904 9905 9906	MLD2 = ZEROES; MLE2 = %0'060000'; NIB_SEL = .BIT_CNT/4;	LEROES PAT AND CI	WHERE THE SBE IS TO GO
22355 : 22356 : 22357 :	9907 9908 9909	DAT_DM_XFER (); MLC31 = write; DELAY (QNE_US);	SET UP A DATA DIAG MOI DO A WRITE TRANSFER GIVE THE PROM DATA TIM	
22358 22359	9910 9911 9912	do begin	FIND THE GOOD NIB AT	
22361 22362	9913 9914 9915	PD_TEMP = .MLPD; if .PD_TEMP [.NIB_SEL] IS_SET	!READ THE PROM DATA FOR	
22364 22365	9916 9917	DAT_CLK = ONE	!IF NOT THEN GET THE N	
22367 22368 22368	9919 9920 9921	else begin GD_WRD_CNT = .GD_WRD_CNT + 1;	!ELSE SEE IF WE ARE AT !UP THE COUNT !AND SEE IF WE ARE DON	THE TESTED WORD
22370 : 22371 :	9922 9923 9924	if .GD_WRD_CNT eql .WRD_CNT the		
22373 22374 22375	9925 9926 9927	end; end		
22376 : 22377 :	9928 9929 9930	until .DONE IS_SET;	PREPEAT UNTIL WE ARE DE	
22379 22380 22381	9931 9932 9933	NIB_PAT = ZEROES; NIB_PAT [.BIT_CNT mod 4] = ONE; D1_TEMP = ZEROES; D2_TEMP = ZerOES; E2_TEMP = %o'060000'; LD_LNG_WRD (.NIB_SEL, .NIB_PAT); WRT_LNG_WRD; DAT_CLK = ONE; DELAY (ONE_US); BGNSUB; CLR_MBUS; GD_BLK_XFER (); ECT_DIS = ZERO; MLCS1 = read; TIME_OUT_LOOP;	CLEAR THE SAVE LOCATION IN THE SAVE LOCATION IN THE SAVE IN THE CLEAR THE OTHER NIBBLE TO BE ZEROES PATTERN	IS NIBBLE ES IN THIS ARRAY WORD AND ZEROES CRC PATTERN
22379 22380 22381 22382 22383 22384 22385 22386 22387 22388 22389 22390 22391 22392 22393	9934 9935 9936 9937	LD_LNG_WRD (.NIB_SEL, .NIB_PAT); WRT_LNG_WRD; DAT_CLK = ONE:	LOAD THE SBE INTO THE SAND WRITE THE SBE INTO THE CLOCK THE SBE INTO THE	SAVE LOCATION O THE DATA DIAG RES'S E ARRAYS MEMORY
22386 22387 22388	9938	DELAY (ONE_US); BGNSUB; CLR_MBUS;	START OF SCOPE LOOP	INTO THE MEMORY AG MODE XFERR
22389 22390 22391	9940 9941 9942 9943 9944 9945 9946	GD_BLK_XFER (); ECT_DIS = ZERO; MLCS1 = read;	GIVE IT TIME TO WRITE START OF SCOPE LOOP CLEAR OUT THE DATA DIVISET UP A GOOD BLOCK TO CLEAR ECC DISABLE AND READ OUT THE SBE	RANSFER
22393 22394	9945 9946	incr CNT from 0 to 13 do	!SEE IF THE SBE WAS CO	RRECTED

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION 22451 22452 22453 22454 077442 012777 060000 113664 22455 077450 010246 22455 077450 010246 22455 077450 010246 22457 077452 012746 000004 22457 077452 010005 22458 077462 010005 22459 077464 004767 104270 22458 077464 004767 114472 22459 077467 012777 000061 113456 22460 077470 012777 000061 113456 22461 077476 012703 000001 22462 077502 001411 22463 077504 016704 102406 22465 077510 001604 22465 077516 005304 22466 077516 005304 22467 077520 001374 22468 077520 001374 22468 077520 001374 22468 077520 001374 22468 077520 001374 22468 077520 001374 22469 077530 00604 22467 077530 00604 22467 077530 00604 22467 077530 00604 22472 077530 00604 22472 077530 00604 22472 077530 00604 22473 077530 00604	
22455	SEQ 0470
22454 077442 012777 060000 113664 MOV #60000,aML.REG+160 ; 22455 077450 010246 MOV R2,-(SP) ; 22456 077452 012746 000004 MOV #4,-(SP) 22457 077456 004767 104270 JSR PC,BL\$DIV 22458 077462 010005 MOV R0,R5 ;*,NIB.SEL 22459 077464 004767 114472 JSR PC,DAT.DM.XFER 22460 077470 012777 000061 113456 MOV #61,aML.REG 22461 077476 012703 000001 MOV #1,R3 ;*,\$\$TMP2 22462 077502 001411 5\$: BEQ 8\$	09:24:31 TOPS 09:21:22 PA:<
22458 077462 010005	9905 9906
22463 077504 016704 102406 MOV 1 SDI V P4	9907 9908 9909
22464 077510 001404 BEQ 7\$	
22465 077512 005066 000012 6\$: CLR 12(SP) ; \$\$TMP 22466 077516 005304 DEC R4 ; \$\$TMP1 22467 077520 001374 BNE 6\$	
22468 077522 005303 7\$: DEC R3 ; \$\$TMP2	
22470 077526 010504 8\$: MOV R5,R4 ; NIB.SEL,* 22471 077530 006204 ASR R4 22472 077532 006204 ASR R4	9915
22473 077534 006204	9913 9915
22464 077510 001404 000012 6\$: CLR 12(SP)	
22483 077576 005300 DEC RO 22484 077600 001411 BEQ 10\$;	9917
22484 077600 001411 22485 077602 005266 000004 22486 077606 026601 000004 22487 077612 001004 22488 077614 012766 000001 000006 22489 077622 000403 22489 077624 152777 000020 113442 10\$: BISB #20,aml.Reg+120	9917 9920 9923
22488 077614 012766 000001 000006 MOV #1.6(SP) ; *,DONE 22489 077622 000403 BR 11\$	
22484 077600 001411 22485 077602 005266 000004 22486 077606 026601 000004 22487 077612 001004 22488 077614 012766 000001 000006 22489 077622 000403 22490 077624 152777 000020 113442 10\$: BISB #20.aml.REG+120 22491 077632 026627 000006 00001 11\$: CMP 6(SP),#1 22492 077640 001340 BEQ 10\$ INC 4(SF) INC 4(SF	9928
22493 077642 005066 000010 CLR 10(SP) : NIB.PAT 22494 077646 010246 MOV R2,-(SP) : BIT.CNT,*	9930 9931
22495 077650 012746 000004 MOV #4,-(SP) 22496 077654 004767 104104 JSR PC,BL\$MOD 22497 077660 010004 MOV R0,R4	9931
22486 077600 001411 22486 077602 005266 000004 22486 077606 026601 000004 22487 077612 001004 22488 077614 012766 000001 000006 BNE 108 22489 077622 000403 22499 077624 152777 000020 113442 108: BISB M20,aML,REG+120 22491 077632 026627 000006 000011 118: CMP 6(SP),M1 ; DONE,* 22492 077640 001340 22493 077642 005066 000001 CLR 10(SP) ; NIB,PAT ; BIF, CNT,* 22496 077650 012746 000004 BNE 98 22495 077650 012746 00004 MOV M2,-(SP) ; BIT.CNT,* 22498 077664 000004 ASR R0 22500 077666 006200 ASR R0 22501 077670 012703 000016 MOV M14,R3 22502 077676 060603 ADD SP.R3 ; NIB,PAT,* 22504 0777676 060603 ADD SP.R3 ; NIB,PAT,* 22505 0777702 010016 MOV R4,-(SP) 22505 0777702 010016 MOV R4,-(SP)	

TOPS 10 10 10 10 10 10 10 1	CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 484	SEQ 0471
22528 100016 005066 000026 13\$: CLR 26(SP) ; \$\$TMP 2 22530 100024 001374 BNE 13\$; \$\$TMP 2 22531 100030 000766 BR 12\$; \$\$TMP 2 22532 100030 000766 BR 12\$; \$\$TMP 2 22534 100032 104402 15\$: TRAP 2 22535 100042 016704 113534 MOV ML_DUT_R4 BIC W177770_R4 BIC W17770_R4 BIC W177770_R4 BIC W17770_R4 BIC W17770_R4 BIC W177770_R4 BIC W17770_R4 BIC W177		ML4		
22528 100016 005066 000026 13\$: CLR 26(SP) ; \$\$TMP 2 22530 100024 001374 BNE 13\$; \$\$TMP 2 22531 100030 000766 BR 12\$; \$\$TMP 2 22532 100030 000766 BR 12\$; \$\$TMP 2 22534 100032 104402 15\$: TRAP 2 22535 100042 016704 113534 MOV ML_DUT_R4 BIC W177770_R4 BIC W17770_R4 BIC W177770_R4 BIC W17770_R4 BIC W17770_R4 BIC W177770_R4 BIC W17770_R4 BIC W177	22510 077704 042716 177770 22511 077710 012746 000001 22512 077714 011646	MOV	#177770,(SP) #1,-(SP) (SP),-(SP)	
22528 100016 005066 000026 13\$: CLR 26(SP) ; \$\$TMP 2 22530 100024 001374 BNE 13\$; \$\$TMP 2 22531 100030 000766 BR 12\$; \$\$TMP 2 22532 100030 000766 BR 12\$; \$\$TMP 2 22534 100032 104402 15\$: TRAP 2 22535 100042 016704 113534 MOV ML_DUT_R4 BIC W177770_R4 BIC W17770_R4 BIC W177770_R4 BIC W17770_R4 BIC W17770_R4 BIC W177770_R4 BIC W17770_R4 BIC W177	22513 077716 004767 103454 22514 077722 005067 111224 22515 077726 005067 111222 22516 077732 012767 060000	JSR CLR CLR 111216 MOV	PC,BL\$PU2 D1.TEMP ; D2.TEMP ;	9932 9933 9934
22528 100016 005066 000026 13\$: CLR 26(SP) ; \$\$TMP 2 22530 100024 001374 BNE 13\$; \$\$TMP 2 22531 100030 000766 BR 12\$; \$\$TMP 2 22532 100030 000766 BR 12\$; \$\$TMP 2 22534 100032 104402 15\$: TRAP 2 22535 100042 016704 113534 MOV ML_DUT_R4 BIC W177770_R4 BIC W17770_R4 BIC W177770_R4 BIC W17770_R4 BIC W17770_R4 BIC W177770_R4 BIC W17770_R4 BIC W177	22517 077740 010516 22518 077742 016646 000022 22519 077746 004767 117152	MOV MOV	R5.(SP) ; NIB.SE 22(SP),-(SP) ; NIB.PA PC.LD.LNG.WRD	L,* 9935
22528 100016 005066 000026 13\$: CLR 26(SP) ; \$\$TMP1 22530 100024 001374 BNE 13\$; \$\$TMP2 22531 100030 000766 BR 12\$; \$\$TMP2 22532 100030 000766 BR 12\$; \$\$TMP2 22534 100032 104402 15\$: TRAP 2 22535 100042 016704 113534 MOV ML_DUT_R4 22536 100046 042704 177770 BICB #77770,R4 22537 100052 142777 000007 113134 BICB #7, 3ML_REG+40 22538 100060 150477 113130 BISB #4, 3ML_REG+40 22539 100060 0150477 113130 BICB #7, 3ML_REG+40 22539 100060 0150477 113130 BICB #7, 3ML_REG+40 22539 100070 142777 000002 113176 BICB #2, 3ML_REG+120 ; 9941 22541 100076 012777 000007 113050 MOV #71, 3ML_REG ; 9943 22542 100104 105777 113114 16\$: TSTB aML_REG+50 22543 100110 100375 CLR R3 ; CNT ; 9948 22545 100114 010304 178: MOV R3,R4 ; CNT ; 9948	22521 077760 016777 111170 22522 077766 016777 111164 22523 077774 152777 000020	113366 MOV 113340 MOV 113272 BISB	D2.TEMP.aML.REG+200 E2.TEMP.aML.REG+160 #20,aML.REG+120 ;	9937
22528 100016 005066 000026 13\$: CLR 26(SP) ; \$\$TMP 2 22530 100024 001374 BNE 13\$; \$\$TMP 2 22531 100030 000766 BR 12\$; \$\$TMP 2 22532 100030 000766 BR 12\$; \$\$TMP 2 22534 100032 104402 15\$: TRAP 2 22535 100042 016704 113534 MOV ML_DUT_R4 BIC W177770_R4 BIC W17770_R4 BIC W177770_R4 BIC W17770_R4 BIC W17770_R4 BIC W177770_R4 BIC W17770_R4 BIC W177	22524 100002 012703 000001 22525 100006 001411 22526 100010 016704 102102	12\$: BEQ MOV	#1,R3 ; *,\$\$TR 15\$ L\$DLY,R4 ; *,\$\$TR	
22538 100060 150477 113130 BISB R4, aML, REG+40 22539 100064 004767 115772 JSR PC, GD, BLK, XFER 22540 100070 142777 000002 113176 BICB W2, aML, REG+120 9942 22541 100076 012777 000071 113050 MOV W71, aML, REG 22542 100104 105777 113114 16\$: TSTB aML, REG+50 22543 100110 100375 BPL 16\$ 22544 100112 005003 CLR R3 CNT 9946 22545 100114 010304 17\$: MOV R3, R4 ; CNT, * 9948	22527 100014 001404 22528 100016 005066 000026 22529 100022 005304	13\$: CLR DEC	26(SP) : \$\$TMP	
22538 100060 150477 113130 BISB R4, aML, REG+40 22539 100064 004767 115772 JSR PC, GD, BLK, XFER 22540 100070 142777 000002 113176 BICB W2, aML, REG+120 9942 22541 100076 012777 000071 113050 MOV W71, aML, REG 22542 100104 105777 113114 16\$: TSTB aML, REG+50 22543 100110 100375 BPL 16\$ 22544 100112 005003 CLR R3 CNT 9946 22545 100114 010304 17\$: MOV R3, R4 ; CNT, * 9948	22530 100024 001374 22531 100026 005303 22532 100030 000766 22533 100032 104402	BR	R3 ; \$\$TMP2	
22538 100060 150477 113130 BISB R4, aML, REG+40 22539 100064 004767 115772 JSR PC, GD, BLK, XFER 22540 100070 142777 000002 113176 BICB W2, aML, REG+120 9942 22541 100076 012777 000071 113050 MOV W71, aML, REG 22542 100104 105777 113114 16\$: TSTB aML, REG+50 22543 100110 100375 BPL 16\$ 22544 100112 005003 CLR R3 CNT 9946 22545 100114 010304 17\$: MOV R3, R4 ; CNT, * 9948	22534 100034 152777 000040 22535 100042 016704 113534 22536 100046 042704 177770	113152 BISB MOV BIC	#40.aML.REG+40 ; ML.DUT.R4 #177770.R4	9939
22540 100076 012777 000071 113050 MOV W71,aML.REG 50 22542 100104 105777 113114 16\$: TSTB aML.REG+50 22543 100110 100375 BPL 16\$ 22544 100112 005003 CLR R3 : CNT 9946 22545 100114 010304 17\$: MOV R3,R4 : CNT,*	22538 100060 150477 113130 22539 100064 004767 115772	113134 BICB BISB JSR	#7,aML.REG+40 R4.aML.REG+40 PC,GD.BLK.XFER ;	9941
22544 100112 005003	22540 100070 142777 000002 22541 100076 012777 000071 22542 100104 105777 113114	113050 BICB 113050 MOV 16\$: TSTB	#2.aML.REG+120 #71.aML.REG aML.REG+50	9942 9943
22547 100120 005764 011600 TST IO.BUF (R4) 22548 100124 001432 BEQ 18\$ 22549 100130 000214	22544 100112 005003 22545 100114 010304 22546 100116 006304	17\$: MOV	R3 R3,R4 ; CNT,*	9946 9948
22550 100130 000214 22551 100132 010526 22552 100134 024052 22553 100136 012746 006374 22553 100142 012746 007340 22555 100146 012746 007340 22555 100152 012746 006034 22557 100156 012746 006004 22558 100162 010600 22559 100164 104414 22560 100166 010216 22560 100170 010146 MOV R2,(SP) RIT.CNT,* 9953	22547 100120 005764 011600 22548 100124 001432 22549 100126 104455	TST BEQ TRAP	IO.BUF (R4) 18\$ 55	9951
22554 100142 012746 007340 MOV MURD.76,-(SP) 22555 100146 012746 007614 MOV MPHR.12,-(SP) 22556 100152 012746 006034 MOV MTHR.FMT,-(SP) 22557 100156 012746 006004 MOV MV,-(SP) 22558 100162 010600 MOV SP,R0 ; SP,* 22559 100164 104414 TRAP 14 22560 100160 010216 MOV R2,(SP) ; BIT.CNT,* 22561 100170 010146 MOV R1,-(SP) ; WRD.CNT,*	22550 100130 000214 22551 100132 010526 22552 100134 024052	. WORD . WORD . WORD	214 SYNC DUMPER	
22557 100156 012746 000004 MOV M4,-(SP) 22558 100162 010600 MOV SP,R0 ; SP,* 22559 100164 104414 TRAP 14 22560 100166 010216 MOV R2,(SP) ; BIT.CNT,* 22561 100170 010146 MOV R1,-(SP) ; WRD.CNT,*	22554 100142 012746 006374 22554 100142 012746 007340 22555 100146 012746 007614 22556 100152 012746 006034	MOV MOV	#WRD.9,-(SP) #WRD.76,-(SP) #PHR.12,-(SP)	9952
22560 100166 010216 MOV R2, (SP) ; BIT.CNT,* 9953 22561 100170 010146 MOV R1,-(SP) ; WRD.CNT,*	22557 100156 012746 000004 22558 100162 010600 22559 100164 104414	MOV MOV TRAP	#4,-(SP) SP,R0 ; SP,*	
	22560 100166 010216 22561 100170 010146	MOV	R2,(SP) ; BIT.CN R1,-(SP) ; WRD.CN	T,* 9953 T,*

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE	Ę ₄₈₅ 5	SEQ 0472
22563 22564	:ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
22566 100172 012746 005606 22567 100176 012746 000003 22568 100202 010600	MOV #FMT.22,-(SI MOV #3,-(SP) MOV SP,RO TRAP 14	; SP,*	
22569 100204 104414 22570 100206 062706 000020 22571 100212 005203 22572 100214 020327 000015	MOV SP,RO TRAP 14 ADD #20,SP 18\$: INC R3	: CNT	9950 9946
22573 100220 003735 22574 100222 104467 22575 100224 006000	18\$: INC R3 CMP R3,#15 BLE 17\$ TRAP 67 ROR R0	; CNT,*	9954
22576 100226 103701 22577 100230 062706 000020 22578 100234 005202 22579 100236 020227 000043 22580 100242 003002	ADC #20,SP INC R3 CMP R3,#15 BLE 17\$ TRAP 67 ROR R0 BLO 15\$ ADD #20,SP INC R2 CMP R2,#43 BGT 19\$ JMP 2\$	BIT.CNT,*	9891 9890
22581 100244 000167 177030	BGT 19\$ JMP 2\$ 19\$: INC R1 CMP R1,#5 BGT 20\$; WRD.CNT ; WRD.CNT,*	9887
22582 100250 005201 22583 100252 020127 000005 22584 100256 003002 22585 100260 000167 177012 22586 100264 062706 000010 22587 100270 000207 22588	20\$: ADD #10.SP	; wkb.cm; ,*	9841
22587 100270 000207 22588 22589 22590 22595	RTS PC ; Routine Size: 259 words ; Maximum stack depth per i	nvocation: 26 words	
22596 22600 22604 100272 22605 100272 004767 176766	T54:: 1\$: JSR PC,\$T54		9959
22604 100272 22605 100272 004767 176766 22606 100276 104466 22607 100300 006000 22608 100302 103773 22609 100304 000207 22610 22611 22612	1\$: JSR PC,\$T54 TRAP 66 ROR RO BLO 1\$ RTS PC		7737
22610 22611 22612	; Routine Size: 6 words ; Maximum stack depth per in	nvocation: 0 words	

CZMLABO ML-11 LOGIC TEST MACRO M1113 22-DEC-80 12:16 PAGE 486 MISCELLANEOUS CODING SECTION

SEQ 0473

9962 ! (BLF/PAGE>

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22

TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (104)

TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BLI.2 (105) 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 BGNTST: ! TEST NUMBER: TST 55 TEST NAME: MULTIPLE BIT ERROR SYNDRONE GENERATION AND DECODE TEST ! TEST DESCRIPTION: TEST SYNDRONE GENERATION, SYNDRONE DECODE AND ERROR CORRECTION FOR MULTIPLE BIT CHANNEL ERRORS BY: 1. CLEAR THE FIRST CRC GROUP IN THE IO_BUF AND GOOD BLOCK TO ZEROES 2. VIA DATA DIAG MODE FLIP ALL BITS IN A CHANNEL TO ONES 3. THEN DO A MASS BUS READ TRANSFER AND EXAMIN THE ID BUF FOR ZEROES INDICATING THE CORRECTION WAS PERFORMED 4. REPEAT FOR ALL CHANNELS IN THE CRC GROUP ! IMPLICIT INPUTS: PD TEMP A BIT VECTOR OF 16 BITS WHERE THE READ PROM DATA IS STORED OAND ACCESSED FROM. IO_BUF A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READS AND WRITES TRANSFERS IS FOUND. NIB_PAT : bitvector [4], STORAGE FOR SBE TO BE GENERATED POINTER TO WHERE ERROR IS TO BE WRITTEN NIB SEL, GD WRD CNT; !COUNT OF WHERE GOOD 'NIB_SEL' ARE FOUND 10006 E2_TEMP<12, 3> = %b'110'; !LOAD DATA STRUCTURE WITH ZEROES CRC PATTERN 10007 10008 incr BIT_CNT from 0 to 35 do 10009 begin 10010 CLR_MBUS; 10011 10012 incr CNT from 0 to 255 do !TEST FOR MULTILPLE ERRORS AT 36 CHANNELS !CLEAR THE DRIVE incr CNT from 0 to 255 do IO_BUF [.CNT] = ZEROES; !CLEAR FIRST CRC GROUP IN THE IO_BUF

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22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (105)
                                GD_BLK_XFER ();
MLCS1 = write;
TIME OUT_LOOP;
GD_WRD_CRT = ZERO;
D2_TEMP = ZEROES;
NIB_PAT = ZEROES;
NIB_PAT = ZEROES;
NIB_PAT [.BIT_CRT/4;
NIB_PAT [.BIT_CR
                                                                                                                                                                                    !LOAD THIS CHANNEL WITH MULTIPLE ERRORS
                                               begin
PD_TEMP = .MLPD;
                                                                                                                                                                                   !READ THIS ARRAY WORDS PROM_DATA
                                              if .PD_TEMP [.NIB_SEL] IS_SET !SEE IF THIS A GOOD NIBBLE
                                                         DAT_CLK = ONE
                                                                                                                                                                           !CLOCK OUT ANOTHER ARRAY WORD IF BAD
                                                else
                                                             begin
                                                                                                                                                                             !ELSE CLOCK IN THE ERROR INTO A GOOD NIBBLE
                                                 GD_WRD_CNT = .GD_WRD_CNT + 1;
DAT_CLR = ONE;
                                end
until .GD_WRD_CNT eql 6; !REPEAT UNTIL 6 WORDS ARE WRITTEN
                                                                                                                                                                                 START OF SCOPE LOOP

CLEAR OUT THE DATA DISG MODE TRANSFER

SET UP A GOOD BLOCK TRANSFER

ENABLE ERROR CORRECTION

READ OUT THE ERRORS

WAIT FOR THE TRANSFER TO COMPLETE
                                  BGNSUB:
                                 CLR_MBUS;
GD_BLK_XFER ();
ECC_DIS = 0;
MLCS1 = read;
                                  TIME_OUT_LOOP;
                             incr WRD_CNT from 0 to 13 do
                                                                                                                                                                                  SEE IF THE CORRECTIONS WHERE DONE
                                               if .IO_BUF [.WRD_CNT] neg ZEROES !TEST THE IO_BUF FOR ALL ZEROES
                                                           begin !REPO
ERROF (141, SYNC, DUMPER);
PRINTB (THR_FMT, PHR_13, WRD_76, WRD_9);
PRINTB (FMT_22, .WRD_CNT, .BIT_CNT);
                                                                                                                                                                                   !REPORT THE ERROR IF ANY LOCATIONS ARE NOT ZEROES
                                 ENDSUB:
                                                                                                                                                                            !END OF SCOPE LOOP
                                  end:
10066 ENDTST;
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HISTORIAN CONTINU DECLIAN			354 0410
22735 :ML4 22736 : 22737		22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 22-Dec-1980 09:21:22 PA:	

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 490 5	eso.	0477
MISCELLANEOUS CODING SECTION				
22791	;ML4		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
22792 22793 100524 004767 102646 22794 100530 010416 22795 100532 016646 000016 22796 100536 004767 116362 22797 100542 016777 110404 22798 100550 016777 110400	JSR MOV MOV JSR 112574 MOV 112576 MOV 112550 MOV	PC.BLSPU2 R4.(SP) 16(SP)(SP) PC.LD.LNG.WRD D1.TEMP.aML.REG+170 D2.TEMP.aML.REG+200	; NIB.SEL,* ; NIB.PAT,*	1002
22800 100564 004767 110374 22800 100564 004767 113372 22801 100570 012777 000061 22802 100576 012701 000001 22803 100602 001411 22804 100604 016702 101306 22805 100610 001404 22806 100612 005066 000022	JSR MOV MOV JSR MOV 112576 112550 MOV JSR MOV MOV 4\$: BEQ MOV BEQ 5\$: CLR DEC BNE OS: DEC BR 7\$: MOV ASR ASR	D2.TEMP.aML.REG+200 E2.TEMP.aML.REG+160 PC.DAT.DM.XFER #61.aML.REG #1.R1 7\$ L\$DLY.R2 6\$ 22(SP)	: *,\$\$TMP2 : *,\$\$TMP1 : \$\$TMP : \$\$TMP1	1002 1002 1002
22805 100610 001404 22806 100612 005066 000022 22807 100616 005302 22808 100620 001374 22809 100622 005301 22810 100624 000766 22811 100626 010402 22812 100630 006202	6\$: DEC BNE BR 7\$: MOV	R2 5\$ R1 4\$ R4,R2	; \$\$TMP1 ; \$\$TMP2 ; NIB.SEL,*	1003
22813 100632 006202 22814 100634 006202 22815 100636 062702 013120 22816 100642 017767 112536 22817 100650 010246 22818 100652 010446 22819 100654 042716 177770 22820 100660 012746 000001 22821 100664 005046 22822 100666 004767 102246 22823 100672 062706 000010	112250 8\$: MOV MOV MOV BIC MOV CLR JSR	L\$DLY,R2 6\$ 22(SP) R2 5\$ R1 4\$ R4,R2 R2 R2 RPD.TEMP,R2 aML.REG+230,PD.TEMP R2,-(SP) R4,-(SP) M177770,(SP) M1,-(SP) -(SP) PC,BL\$GT2 M10,SP R0 9\$ R5 M20,aML.REG+120 R5,M6 8\$ 2 M40,aML.REG+40	; NIB.SEL,*	1003
22824 100676 005300 22825 100700 001401 22826 100702 005205 22827 100704 152777 000020	112362 9\$: BISB CMP BNE TRAP BISB MOV BIC BISB JSR BISB JSR BISB JSR BISB JSR BICB BISB JSR BICB BISB JSR BICB BISB JSR BICB TSTB	R0 9\$ R5 W20, aML.REG+120 R5, W6 8\$	GD.WRD.CNT,*	1003 1003 1004 1004
22831 100722 152777 000040 22832 100730 016702 112646 22833 100734 042702 177770 22834 100740 142777 000007 22835 100746 150277 112242 22836 100752 004767 115104 22837 100756 142777 000002 22838 100764 012777 000071 22839 100772 105777 112226	112264 10\$: TRAP BISB MOV BIC 112246 BICB RISB	#40,aML.REG+40 ML.DUT.R2 #177770,R2 #7,aML.REG+40 R2 aML.REG+40		1004
22828 100712 020527 000006 22829 100716 001351 22830 100720 104402 22831 100722 152777 000040 22832 100730 016702 112646 22833 100734 042702 177770 22834 100740 142777 000007 22835 100746 150277 112242 22836 100752 004767 115104 22837 100756 142777 000007 22838 100764 012777 000071 22839 100772 105777 112226 22840 100776 100375 22841 101000 005001 22842 101002 010102 22843 101004 006302	112310 JSR BICB MOV 112162 MOV TSTB BPL	#40.aml.REG+40 ML.DUT.R2 #177770.R2 #7.aml.REG+40 R2.aml.REG+40 PC.GD.BLK.XFER #2.aml.REG+120 #71.aml.REG aml.REG+50 11\$ R1 R1.R2 R2		1004 1004 1005
22841 101000 005001 22842 101002 010102 22843 101004 006302 22844 101006 005762 011600	12\$: MOV ASL TST	R1,R2 R2 R2 IO.BUF (R2)	; WRD.CNT,*	1005

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 491		SEQ	0478
22846 22847	ML4			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
22849 101012 001432 22850 101014 104455 22851 101016 000215 22852 101020 010526	BEQ TRAP . WORD . WORD . WORD	13\$ 55 215 SYNC	;		1005
22853 101022 024052 22854 101024 012746 006374 22855 101030 012746 007340 22856 101034 012746 007636 22857 101040 012746 006034	MORD MOV MOV MOV MOV	DUMPER MURD. 9, -(SP) MURD. 76, -(SP) MPHR. 13, -(SP) MTHR. FMT, -(SP) M4, -(SP) SP, RO 14	;		1005
22857 101040 012746 006034 22858 101044 012746 000004 22859 101050 010600 22860 101052 104414	MOV MOV TRAP	#4,-(SP) SP,R0	; SP,*		
22858 101044 012746 000004 22859 101050 010600 22860 101052 104414 22861 101054 010316 22862 101056 010146 22863 101060 012746 005606 22864 101064 012746 000003 22865 101070 010600 22866 101072 104414 22867 101074 062706 000020	MOV MOV MOV MOV	R3,(SP) R1,-(SP) WFMT.22,-(SP) W3,-(SP) SP,R0	: BIT.CNT,* : WRD.CNT,*		1006
22865 101070 010600 22866 101072 104414	MOV TRAP	SP.RO	; SP,*		
22868 101100 005201	13\$: ADD INC	#20,SP R1 R1,#15	WRD.CNT		1005 1005
22869 101102 020127 000015 22870 101106 003735 22871 101110 104467 22872 101112 006000 22873 101114 103701 22874 101116 062706 000020 22875 101122 005203 22876 101124 020327 000043	BLE TRAP ROR	12 \$ 67	;		1006
22873 101114 103701 22874 101116 062706 000020	BLO ADD	R0 10\$ #20,SP			1000
22874 101116 062706 000020 22875 101122 005203 22876 101124 020327 000043 22877 101130 003002	CMP	R3 R3,#43 14\$: BIT.CNT,*		1000
22877 101130 003002 22878 101132 000167 177174 22879 101136 022626 22880 101140 000207	BLO ADD INC CMP BGT JMP 14\$: CMP RTS	1\$ (SP)+,(SP)+ PC	:		9961
22880 101140 000207 22881 22882 22883 22888 22889	; Routine Size: ; Maximum stack	206 words depth per invocation: 24 words			
22897 101142 22898 101142 004767 177140 22899 101146 104466	T55:: 1\$: JSR TRAP	PC.\$T55 66	:		1006

10067 ! <BLF/PAGE>

SEQ 0479

22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<

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TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (106)
                                                                      22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
       BGNTST:
        ! TEST NUMBER: TST 56
          TEST NAME:
                                     ECC ERROR REGISTER TEST
         TEST DESCRIPTION:
TEST THE ECC ERROR REGISTER FOR
CLEARING AND LATCHING OF ECC ERROR
                  INFORMATION ON DETTECTION
                  OF ECC ERRORS BY:
                  1. THIS TEST IS TABLE DRIVEN. DATA TABLE (DT_1)
CONTAINS ERROR FORCING DATA AND EXPECTED
MLEE DATA AS A RESULT OF THE FORCED ERROR
                  2. THE TEST THEREFOR INDEXES INTO DT_1 TO FORCE
                  3. A MASS BUS READ TRANSFER IS DONE
                  4. DT_1 IS AGAIN INDEXED AND ITS CONTENTS IS COMPARED AGAINST THE RESULTING
                       MLEE REGISTER CONTENTS
                  5. A MASS BUS CLEAR IS DONE AN MLEE IS CHECKED FOR
                       ZEROES
                  6. THE ECH BIT IS ALSO EXAMINED FOR CORRECT
                       LATCHING INFORMATION
10104
10105 Local
10106 F:
10107 E
             FINISH,
                                                                       !VARIABLE ENDING CONDITION FOR LATCH_LOOP
             ERR_FLG;
                                                                       !ERROR FLAG
10108
10109 CLR MBUS;
10110 FINISH = 4;
                                                                       !CLEAR THE DRIVE
                                                                      !MAKE LATCH_LOOP DO 5 LOOPS AT FIRST
10111
10112 incr CLR_LOOP from 0 to 1 do
10113 !THE SECOND PASS TESTS MLEE FOR CLEARING
                                                                      !THE FIRST PASS TESTS MLEE FOR LATCHING
10114
             begin
10116
             incr LATCH_LOOP from 0 to .FINISH do
                                                                      !TEST MLEE FOR LATCHING BY USING DT_1 DATA
                  begin
BGNSUB;
                                                                       !START OF SCOPE LOOP
                 ERR_FLG = ZERO;
                                                                      !CLEAR THE ERROR FLAG
```

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22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                        TOPS-20 BLiss-16 V2(212)
                                                                                                        PA: CNEALE>BL3ML4.BL1.2 (106)
                                                                        !CLEAR THE ERROR REGISTER
!ENABLE ECC DIAG MODE
!GET PAR CRC WRD DATA FROM DT_1
!GET CRC_A FROM DT_1
!GET CRC_B FROM DT_1
                  MLER = ZEROES:
                  ECC_DM = ONE;
                  PAR_CRC_WRD = .DT 1 [.LATCH_LOOP, P0_5];
CRC_A = .DT 1 [.LATCH_LOOP, A0_5];
CRC_B = .DT 1 [.LATCH_LOOP, B0_5];
GD_BLK_XFER ();
ECT_DIS = ZERO;
MLCS1 = read;
                                                                        SET UP % GOOD BLOCK TRANSFER DISABLE ECC DIABLE READ THE ERROR AND LACTH MLEE
                  TIME_OUT_LOOP;
                  if .CRC_ERR neg .DT_1 [.LATCH_LOOP, CRC_DATA] then ERR_FLG = ONE;
                                                                                                                !TEST CRC_ERR
                  if .SGL_ERR neg .DT_1 [.LATCH_LOOP, SGL_DATA] then ERR_FLG = ONE;
                                                                                                                 !TEST SGL ERR
                  if .UNC_ERR neg .DT_1 [.LATCH_LOOP, UNC_DATA] then ERR_FLG = ONE;
                                                                                                                 !TEST UNC
                  if .BIT_IN_ERR neg .DT_1 [.LATCH_LOOP, E0_5] then ERR_FLG = ONE; !TEST BIT IN ERROR
                  if .CHAN_IN_ERR neg .DT_1 [.LATCH_LOOP, CO_5] then ERR_FLG = ONE;
                                                                                                                 !TEST CHANNEL IN ERROR
                  if .ECH_ERR neg .DT_1 [.LATCH_LOOP, ECH_DATA] !TEST IF ECH GOT SET
                  then
                       begin
ERRDF (123, ASYNC, DUMPER);
                                                                        !REPORT ERROR IF NOT SET
                       PRINTB (FOR_FMT, WRD_67, WRD_10, WRD_69, WRD_14);
                  if .ERR_FLG IS_SET
                                                                       !TEST IF THE ERROR FLAG GOT SET
                  then
                       !REPORT ERROR IF SET
ERROF (124, SYNC, DUMPER);
PRINTB (THR_FMT, REG_14, URD_52, PHR_11);
PRINTB (FMT_1, .DT_1 [.LATCH_LOOP, EE_DATA], .MLEE);
PRINTB (FMT_19, .DT_1 [.LATCH_LOOP, BO_5], .DT_1 [.LATCH_LOOP, AO_5], .DT_1 [.LATCH_LOOP, PO_5])
                  ENDSUB:
                                                                        !END OF SCOPE LOOP
                  end;
             CLR_MBUS;
                                                                       !CLEAR THE REGISTER
             if .MLEE neg ZERO
                                                                      "!SEE IF THE REGISTER GOT CLEARED
                  begin !REPURT E
ERROF (125, SYNC, DUMPER);
PRINTB (FOR_FMT, REG_14, WRD_52, FNC_7, WRD_14);
                                                                       !REPORT ERROR IF NOT CLEARED
            ECC_DM = ONE;
FINISH = ZERO;
                                                                        !ENABLE ECC DISABLE
                                                                        !MAKE LATCH_LOOP LOOP ONCE TO CLEAR MLEE AGAIN
             end;
10171
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MISCELLANEOUS CODING SECTION	CZMLABO ML-11 LOGIO MISCELLANEOUS CODIA	TEST IG SECTION	MACRO M1113	22-DEC-80	12:16	PAGE	B 6
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-	-	~	~-

23030 :ML4 23031 :				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (106)</neale>	
23033 ; 10172	ENDTST;					
23041 101156 004167 23042 101162 152777 23043 101170 016705 23044 101174 042705 23045 101200 142777	102734 000040 112024 112406 177770 000007 112006 112002 000004	ST56: JSR R BISB # MOV M BIC # BICB # BISB R	1,\$SAVE5 40,aML.REG+40 IL.DUT.R5 177770.R5 17,aML.REG+40 15,aML.REG+40 14,-(SP)	j		1006 1010
23048 101210 003040		IS: CLR R	Ze	: CLA	INISH R.LOOP ICH.LOOP	1011 1011 1011
23051 101226 010446 23052 101230 012746 23053 101234 004767 23054 101240 010005	000006 102266	1\$: CLR R JMP 1 2\$: MOV R MOV M JSR P MOV R	4,-(SP) 6,-(SP) C,BL\$MUL	; LAT	CH.LOOP,*	1012
23055 101242 012701 (23056 101246 060501 23057 101250 012702 (23058 101254 060502	013534 013536	MOV # ADD R MOV # ADD R	4,-(SP) 6,-(SP) C,BL\$MUL 0,R ^c DT.1,R1 5,R1 DT.1+2,R2	:		1013 1012
23059 101256 104402 23060 101260 005003 23061 101262 005077 23062 101266 152777	111746 000001 112000	CLR R	MI REG+60	ERF	R.FLG	1011 1011 1012 1012 1012
23064 101276 006300 23065 101300 006300 23066 101302 062700	140377 037400 112010 112004	MOV (I ASL R ASL R BIC #	1, aML.REG+120 R2),R0 0 10 140377,R0	50		1012
25069 101320 111200	112004 177700 000077 111770 111764	ASL RI ASL RI BIC W BIC W BIS RI MOVB (I BIC W BICB W BISB RI	140377,R0 137400,aML.REG+1 10,aML.REG+150 R2),R0 177700,R0 177700,R0 177700,R0 177700,R0 177700,R0 177,aML.REG+160 10,aML.REG+160 10,aML.REG+160 10,aML.REG+120 17,aML.REG+120 17,aML.REG+120 17,aML.REG+120 17,aML.REG+120 17,aML.REG+120 17,aML.REG+120	;		1012
23071 101326 142777 (23072 101334 150077 23073 101340 116500 (23074 101344 042700 23075 101350 142777 (111764 013540 177700 000077 111756	BISB RIMOVB DI BIC ME BICB	0, aml.REG+150 T.1+4(R5),R0 177700,R0 77, aml.REG+160			1012
23073 101340 116500 (23074 101344 042700 23075 101350 142777 (23076 101356 150077 23078 101366 142777 (23079 101366 142777 23080 101402 105777 23081 101406 100375 23082 101410 011146 23083 101412 042716	013540 177700 000077 111756 111752 114474 000002 111700 000071 111552	BIC NO BICB NO BISB ROUSE NO BICB NO MOV	O.aML.REG+160 C.GD.BLK.XFER 2.aML.REG+120 71.aML.REG			1012 1012 1012
23081 101406 100375 23082 101410 011146 23083 101412 042716	157777	4\$: TSTB at BPL 4: MOV (I BIC M	ML.REG+50 \$ R1),-(SP) 157777,(SP)	:		1013

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 496	SEQ 0483
23085 23086 23087	ML4		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
23088 101416 017700 111742 23089 101422 042700 157777 23090 101426 020026	MOV BIC CMP	amL.REG+210,R0 #157777,R0 RO,(SP)+	
23092 101432 012703 000001 23093 101436 011146 23094 101440 042716 137777 23095 101444 017700 111714 23096 101450 042700 137777 23097 101454 020026	S\$: MOV BIC CMP BEQ MOV BIC MOV BIC CMP BEQ MOV BIC CMP BEQ MOV BIC CMP BEQ MOV BIC MOV	aml.REG+210,R0 #157777,R0 R0.(SP)+ 5\$ #1,R3 : *,ERR.FLG (R1),-(SP) #137777,(SP) aml.REG+210,R0 #137777,R) R0.(SP)+ 6\$ #1,R3 : *,ERR.FLG : *,ERR.FLG	1013
23098 101456 001402 23099 101460 012703 000001 23100 101464 011146 23101 101466 042716 077777 23102 101472 017700 111666 23103 101476 042700 077777 23104 101502 020026	6\$: MOV BIC MOV BIC CMP	65 #1,R3 (R1),-(SP) #77777,(SP) aML.REG+210,R0 #77777,R0 R0,(SP)+	1013
23105 101504 001402 23106 101506 012703 000001 23107 101512 111146 23108 101514 042716 177700 23109 101520 117700 111640 23110 101524 042700 177700 23111 101530 020026	BIC CMP BEQ MOV 7\$: MOVB BIC MOVB BIC CMP	#1.R3 (R1),-(SP) #77777, (SP) aML.REG+210,R0 #77777,R0 R0,(SP)+ 7\$ #1.R3 (R1),-(SP) #177700,(SP) aML.REG+210,R0 #177700,R0 R0,(SP)+ 8\$ #1.R3 (R1),-(SP) #170077,(SP)	1013
23112 101532 001402 23113 101534 012703 000001 23114 101540 0111146 23115 101542 042716 170077 23116 101546 017700 111612 23117 101552 042700 170077 23118 101556 020026 23119 101560 001402	BIC CMP BEQ MOV BIC MOV BIC CMP BEQ	#1,R3 ; *,ERR.FLG (R1),-(SP) #170077,(SP) aML.REG+210,R0 #170077,R0 R0,(SP)+ 9\$	1013
23118 101556 020026 23119 101560 001402 23120 101562 012703 000001 23121 101566 011246 23122 101570 005046 23123 101572 032766 010000 23124 101600 001401 23125 101602 005216 23126 101604 005000 23127 101606 132777 000100 23128 101614 001401 23129 101616 005200 23130 101620 020026 23131 101622 001002 23133 101626 000425 23134 101630 005726 23135 101634 000173 23137 101636 010464 23138 101640 024052 23138 101640 024052 23139 101642 012746 006442	9\$: MOV CLR BIT BEQ INC CLR BIT BEQ INC CLR BITB BEQ INC CLR BITB BEQ INC CMP BNE TST BR TST TRAPWORDWORDWORD	#170077,(SP) aML.REG+210,R0 #170077,R0 R0,(SP)+ 9\$ #1,R3	1014
23125 101602 005216 23126 101604 005000 23127 101606 132777 000100 23128 101614 001401 23129 101616 005200	111420 10\$: CLR BITB BEQ INC	R0 #100,aML.REG+60 11\$ R0	
23131 101622 001002 23132 101624 005726 23133 101626 000425	11\$: CMP BNE TST BR	12\$ (SP)+ 13\$	
23132 101624 005726 23133 101626 000425 23134 101630 005726 23135 101632 104455 23136 101634 000173 23137 101636 010464 23138 101640 024052 23139 101642 012746 006442	12\$: TST TRAP .WORD .WORD	RO RO, (SP) + 12\$ (SP) + 13\$ (SP) + 55 173 ASYNC DUMPER WHRD.14,-(SP)	1014
23138 101640 024052 23139 101642 012746 006442	MOV	MURD.14,-(SP)	1014

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 497		SEQ 0484
23141	;ML4		22-Dec-198	0 09:24:31 TOPS 0 09:21:22 PA:<
23142 23143	,		22-Dec-198	0 09:21:22 PA:<
23144 101646 012746 007252 23145 101652 012746 006406 23146 101656 012746 007240 23147 101662 012746 006046 23148 101666 012746 000005	MOV	#WRD.69,-(SP) #WRD.10,-(SP) #WRD.67,-(SP) #FOR.FMT,-(SP) #5,-(SP) SP,R0 14 #14,SP R3,#1 14\$ 55 174 SYNC DUMPER #PHR.11,-(SP)		
23146 101656 012746 007240 23147 101662 012746 006046	MOV	WWRD.67,-(SP) WFOR.FMT,-(SP)		
23147 101662 012746 006046 23148 101666 012746 000005 23149 101672 010600	MOV	#5,-(SP) SP,R0	; SP,*	
25150 1016/4 104414	MOV TRAP ADD	14 #14.SP		1014
23151 101676 062706 000014 23152 101702 020327 000001 23153 101706 001062	13\$: CMP	R3,#1	: ERR.FLG,*	1014 1014
23154 101710 104455 23155 101712 000174	BNE TRAP .WORD .WORD .WORD	55	;	1015
23156 101714 010526 23157 101716 024052	. WORD	SYNC		
23158 101720 012746 007576 23159 101724 012746 007066 23160 101730 012746 010376	MOV	#PHR.11,-(SP)		1015
23160 101730 012746 010376	MOV	WREG.14,-(SP)		
23161 101734 012746 006034 23162 101740 012746 000004 23163 101744 010600	MOV	MPHR.11,-(SP) MURD.52,-(SP) MREG.14,-(SP) MTHR.FMT,-(SP) M4,-(SP) SP,R0 14	; SP,*	
23164 101746 104414 23165 101750 017716 111410	TRAP	14 2M PEG+210 (SP)		1015
23166 101754 011146 23167 101756 012746 004222 23168 101762 012746 000003	MOV	aML.REG+210,(SP) (R1),-(SP) WFMT.1,-(SP) W3,-(SP) SP,R0 14		1015
23166 101754 011146 23167 101756 012746 004222 23168 101762 012746 000003 23169 101766 010600	MOV	#3,-(SP)		
23170 101770 104414	MOV TRAP	14	; SP,*	1015
23172 101774 006200	MOV ASR ASR	(R2),R0 R0 R0		1015
23173 101776 006200 23174 102000 006200		RO		
23176 102004 006200	ASR	RO		
23174 102000 006200 23175 102002 006200 23176 102004 006200 23177 102006 006200 23178 102010 042700 177700 23179 102014 010016 23180 102016 111246 23181 102020 042716 177700	BIC	#177700,R0		
23179 102014 010016 23180 102016 111246 23181 102020 042716 177700	MOVB	(R2),-(SP)		
23182 102024 116546 013540	MOVB	DT.1+4(R5),-(SP)		
23181 102020 042716 177700 23182 102024 116546 013540 23183 102030 042716 177700 23184 102034 012746 005412 23185 102040 012746 000004 23186 102044 010600 23187 102046 104414	MOV	#177700,(SP) #FMT.19,-(SP)		
23185 102040 012746 000004 23186 102044 010600	MOV	#4,-(SP) \$P,R0	; SP,*	
23185 102040 012746 000004 23186 102044 010600 23187 102046 104414 23188 102050 062706 000030 23189 102054 104467 23190 102056 006000	TRAP	#30,SP		1014 1015
23189 102054 104467 23190 102056 006000	14\$: TRAP ROR	67 R0		1015
23191 102060 103002 23192 102062 000167 177170	BHIS	15\$ 3\$		
23193 102066 022626 23194 102070 005204	15\$: CMP INC	(SP)+,(SP)+ R4	: LATCH.LOOP	1011 1011
23195 102072 020466 000002	16\$: CMP	RO RO RO RO RO RO, (SP) (R2),-(SP) W177700, (SP) DT.1+4(R5),-(SP) W177700, (SP) WFMT.19,-(SP) SP,RO 14 W30,SP 67 RO 15\$ 3\$ (SP)+,(SP)+ R4 R4,2(SP)	; LATCH.LOOP, FINISH	

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 498	SEQ 0	1485
23197 23198 23198	ML4		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
23200 102076 003002 23201 102100 000167 177122 23202 102104 152777 000040 23203 102112 016705 111464 23204 102116 042705 177770 23205 102122 142777 000007	111102 17\$: BISB MOV BIC	17\$ 2\$ #40,aML.REG+40 ML.DUT.R5 #177770,R5		1015
23205 102122 142777 000007 23206 102130 150577 111060 23207 102134 005777 111224 23208 102140 001424	111064 BICB BISB TST BEQ	ML.DUT.R5 #177770.R5 #7.aML.REG+40 R5.aML.REG+40 aML.REG+210 18\$ 55 175 SYNC DUMPER		1016
23209 102142 104455 23210 102144 000175 23211 102146 010526	BEQ TRAP . WORD . WORD . WORD	55 175 SYNC		1016
23212 102150 024052 23213 102152 012746 006442 23214 102156 012746 010012 23215 102162 012746 007066 23216 102166 012746 010376 23217 102172 012746 006046 23218 102176 012746 000005 23219 102202 010600	WORD MOV MOV MOV MOV MOV MOV MOV MOV MOV	WFNC.7,-(SP) WWRD.52,-(SP) WREG.14,-(SP) WFOR.FMT,-(SP) W5,-(SP)	; SP,*	1016
23221 102206 062706 000014 23222 102212 152777 000001 23223 102220 005066 000002 23224 102224 005216 23225 102226 021627 000001 23226 102232 003002	111054 18\$: BISB CLR INC CMP BGT	14 #14,SP #1,aML.REG+120 2(SP) (SP) (SP),#1		1016 1016 1016 1011
23227 102234 000167 176760 23228 102240 022626 23229 102242 000207 23230	19\$: CMP RTS	1\$ (SP)+,(SP)+ PC		1006
23230 23231 23232 23237 23238		283 words depth per invocation: 22 words		
23246 102244 23247 102244 004767 176706 23248 102250 104466 23249 102252 006000 23250 102254 103773	T56:: 1\$: JSR TRAP ROR BLO	PC.\$T56 66 RO 1\$		1017

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION

MACRO M1113 22-DEC-80 12:16 PAGE 499

SEQ 0486

:ML4

22-Dec-1980 09:24:31 22-Dec-1980 09:21:22

TOPS PA:<

5 102256 000207

RTS PC

; Routine Size: 6 words ; Maximum stack depth per invocation: 0 words

10173 ! <BLF/PAGE>

23267 :ML4 23268 : 23269 : 23270 : 23271 : 23272 : 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212) PA: <NEALE>BL3ML4.BL1.2 (107) 10174 ! 10175 BGNTST; 10176 ! TEST NUMBER: TST 57 10178 10179 10180 | TEST NAME: ECC ERROR LOCATION REGISTER TEST TEST DESCRIPTION: TEST THE ECC ERROR LOCATION REGISTER FOR CLOCKING, BIT UNIQUENESS 10184 10185 CLEARING AND LATCHING OF DSA ADDRESSES ON DETECTION OF ECC ERROR BY: 10186 1. TEST FOR CLOCKING BY: 10187 A. DO A MASS BUS CLEAR FORCING REGISTER BITS TO ZEROES B. FORCE AN ECC ERROR AT THE LAST BLOCK AND DO A READ TRANSFER C. CHECK MLEL FOR LAST BLOCK ADDRESS 10188 10189 10191 2. TEST FOR BIT UNIQUENESS BY: A. WITH FORCED ECC ERRORS DO READ TRANSFERS AT VARIOUS DSA ADDRESSES AND CHECK MLEL FOR LATCHED DSA ADDRESSES B. DO CLEAR AND REPEAT 3. TEST FOR LATCHING AND CLEARING BY:
A. WITH A FORCED SINGLE BIT ERROR DO A READ TRANSFER AT THE
LAST BLOCK. (LATCHES MLEL WITH THE LAST BLOCK ADDRESS TO START THE TEST OFF)
B. AGAIN WITH A FORCED SINGLE BIT ERROR DO A READ TRANSFER AT DSA ZERO AND CHECK MLEL FOR NOT LATCHING. NO CLEAR DONE C. WITH FORCED UNCORRECTABLE ERRORS DO A READ TRANSFER AT DSA ZERO CHECK MLEL FOR LATCHED DSA ZERO NO CLEAR DONE D. WITH FORCED SINGLE BIT ERRORS DO A READ TRANSFER AT THE LAST BLOCK AND CHECK MLEL FOR NOT LATCHING NO CLEAR DONE E. WITH A FORCED UNCORRECTABLE ERROR DO A READ TRANSFER AT THE LAST BLOCK AN CHECK MLEL FOR NOT LATCHING
F. DO A MASS BUS CLEAR
G. WITH A FORCED SINGLE BIT ERROR DO A READ TRANSFER AT THE LAST BLOCK 23309 23310 23311 23312 23313 23314 23316 23316 23317 23318 23319 23320 23321 H. DO A MASS BUS CLEAR AGAIN AND CHECK MLEL FOR ZEROES 10215 ! IMPLICIT INPUTS: IO_BUF A VECTOR OF 256 WORDS WHERE DATA 10218 10219 FOR MBUS READS AND WRITES TRANSFERS IS FOUND. local SGL. **!SINGLE BIT ERROR PARRAMETER**

```
22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (107)
                                                                               !UNCORRECTABLE ERR PARRAMETER
!TEMPORY SAVE LOCATION
!SHIFTING DATA PATTERN SAVE LOCATION
!EXPECTED DSA PATTERN LATCHED IN MLEL
              SAVÉ,
SHF_DATA,
DSA_PAT;
                                                                               START OF SCOPE LOOP CLEAR THE MLEL REGISTER
10231 BGNSUB;

10232 CLR_MBUS;

10233 FOR_ECC_ERR (SGL = 1, UNC = 0);

10234 MLDA = .LST_BLK;

10235 MLWC = not 255;

10236 MLBA = IO_BUF;

10237 MLCS1 = read;

10238 TIME_OUT_LOOP;
                                                                               FORCE A SBE LATCH THE LAST BLK ADRS INTO THE MLEL
                                                                               !DO A READ AND LATCH THE MLEL
 10240 if .MLEL eql ZEROES
                                                                               !SEE IF THE MLEL GOT CLOCKED
10241 then
10242
              begin
ERRDF (126, SYNC, DUMPER);
PRINTB (THR_FMT, REG_15, WRD_52, PHR_11);
                                                                               !REPORT AN ERROR IF NOT CLOCKED
              EXIT_TST;
              end:
     48 ENDSUB;
                                                                            !END OF SCOPE LOOP
10249 SHF_DATA = .LST_ARR;
                                                                            LOAD THE SHIFT DATA
10251 incr LOOP from 0 to 16 do
                                                                            !FIRST TEST FOR 1'S AND 0'S THEN SHIFT DATA
              begin
BGNSUB;
                                                                               START OF SCOPE LOOP
               CLR MBUS:
              FOR_ECC_ERR (SGL = 1, UNC = 0);
                                                                               !FORCE A SBE
              selectone .LOOP of
                                                                               SELECT THE TEST PATTERN
                   set
                    : [0]
                                                                              !ZEROES PATTERN
                       begin
DSA_PAT = ZEROES;
MLDA = .DSA_PAT;
                    [1]:
                                                                              !MOSTLY ONES PATTERN
                         begin
DSA_PAT = .LST_BLK;
                         MLDA = .DSA_PAT;
                    [otherwise] :
                                                                         SHIFT PATTERN
                         begin
DSA_PAT = .SHF_DATA;
MLDA = .SHF_DATA;
SHF_DATA = .SHF_DATA^-ONE;
end;
```

```
22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 
22-Dec-1980 09:21:22 PA:<NEALE>BL3ML4.BLI.2 (1
                                                                                               PA: <NEALE>BL3ML4.BL1.2 (107)
                tes:
           MLWC = not 255:
                                                                 SET UP THE TRANSFER
            MLBA = IO_BUF;
            MLCS1 = read:
                                                                !DO THE TRANSFER AND LATCH MLEL
            TIME_OUT_LOOP:
            SAVE = . MLEL;
                                                                 !READ AND SAVE THE MLEL REGISTER
            if .SAVE neg .DSA_PAT
                                                                 !SEE IF SAVE IS WHAT WE EXPECTED
                begin
ERRDF (127, ASYNC, DUMPER);
PRINTB (TWO_FMT, REG_15, PHR 4);
PRINTB (FMT_2, .DSA_PAT, .SAVE, (.DSA_PAT xor .SAVE));
                                                                  !REPORT AN ERROR IF NOT THE SAME
           ENDSUB:
                                                                 !END OF SCOPE LOOP
           end:
                                                                 !CLEAR THE DRIVE !FORCE A SBE
   298 FOR ECC_ERR (SGL = 1, UNC = 0);
299 MLWC = not 255;
                                                                 SET UP A LAST BLOCK TRANSFER TO LATCH THE MLEL WITH T
10300 MLBA = IO BUF;
10301 MLDA = .LST_BLK;
10302 MLCS1 = read;
10303 TIME_OUT_LOOP;
10305 incr LOOP from 0 to 4 do
                                                                 !NOW TEST FOR CORRECT LATCHING
           begin
BGNSUB;
                                                                 START OF SCOPE LOOP
           MLER = ZEROES:
           case .LOOP from 0 to 4 of
                                                                 SELECT TEST DATA
                set
                [0]:
                                                                 !FORCE A SBE AND CHECK FOR THE LST BLK ADRS
                     begin
SGL = 1;
                     UNC = 0;
                     DSA_PAT = .LST_BLK;
                     MLDA = ZEROES:
                     end:
                [1] :
                                                                 !FORCE A UNC ERROR AND CHECK FOR ADRS ZERO
                     begin
SGL = 0;
UNC = 1;
                     DSA PAT = ZEROES:
                     MLDA = ZEROES:
                     end:
                [2]:
                                                                 !FORCE SBE AND CHECK FOR ADRS ZERO
```

!END OF SCOPE LOOP

ENDSUB:

end;

10381 ENDTST:

SEQ 0490

						K 6
CZMLABO ML-11	LOGIC TEST	MACRO M1113	22-DEC-80	12:16	PAGE	504
MICCEL I AMECIE	CODING SECTION					

	and the second second second
CEA	0/07

HISCELLANEOUS CODING SECTION				SEQ	0491
23491 :ML4 23492 23493			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (107)</neale>	
23501 102260 004167 101632 23502 102264 005746	\$157:	JSR R1, \$SAVE5	:		1017
23503 102266 104402	1\$:	TDAD 2			1022
23505 102276 0 6705 111300	110716	BISB #40,aML.REG+40 MOV ML.DUT.R5 BIC #177770,R5 BICB #7,aML.REG+40 BISB R5,aML.REG+40 MOV #1.R3 MOV R3,-(SP) CLR R1			1022 1023
23506 102302 042705 177770 23507 102306 142777 000007 23508 102314 150577 110674 23509 102320 012703 000001	110700	BIC #177770,R5			
23508 102314 150577 110674	110700	BICB W7.aML.REG+40 BISB R5.aML.REG+40			
23510 102324 010346		MOV #1.R3 MOV R3,-(SP)	; *,SGL ; SGL,* ; UNC		1023
23511 102326 005001 23512 102330 005046		CLR R1 CLR -(SP)	; UNC		
23513 102332 004767 113422	1104/0	CLR -(SP) JSR PC,FOR.ECC.ERR MOV LST.BLK,aML.REG-10 MOV #-400,aML.REG+10 MOV #10.BUF,aML.REG-	70		1007
23515 102344 012777 177400	110612	MOV #-400, aml. REG+10	30		1023
23512 102330 005046 23513 102332 004767 113422 23514 102336 016777 107230 23515 102344 012777 177400 23516 102352 012777 011600 23517 102360 012777 000071 23518 102366 105777 110632	110640 110612 110614 110566	MOV #10.BUF, aML.REG	-20		1023 1023 1023 1023
23513 102332 004767 113422 23514 102336 016777 107230 23515 102344 012777 177400 23516 102352 012777 011600 23517 102360 012777 000071 23518 102366 105777 110632 23519 102372 100375	2\$:	TSTB aML.REG+50			
23518 102366 105777 110632 23519 102372 100375 23520 102374 005777 110774 23521 102400 001025		CLR -(SP) JSR PC,FOR.ECC.ERR MOV LST.BLK,aML.REG- MOV #-400,aML.REG-10 MOV #10.BUF,aML.REG- TSTB aML.REG-50 BPL 2\$ TST aML.REG+220 BNE 3\$:		1024
23522 102402 104455		BNE 38 TRAP 55 . WORD 176	:		1024
23523 102404 000176 23524 102406 010526		TRAP 55 .WORD 176 .WORD SYNC .WORD DUMPER			
23525 102410 024052		.WORD DUMPER MOV WPHR.11,-(SP)			102/
23527 102416 012746 007066		MOV #WRD.52,-(SP)			1024
23526 102412 012746 007576 23527 102416 012746 007066 23528 102422 012746 010404 23529 102426 012746 006034 23530 102432 012746 000004		MOV WPHR.11,-(SP) MOV WHRD.52,-(SP) MOV WREG.15,-(SP) MOV WTHR.FMT,-(SP) MOV W4,-(SP)			
23331 102436 010600		MOV #4,-(SP)	; SP,*		
23532 102440 104414 23533 102442 104463 23534 102444 062706 000016		MOV SP.RO TRAP 14			
23535 102442 104463 23535 102444 062706 000016 23535 102450 000167 001006		TRAP 63 ADD #16,SP JMP 26\$ CMP (SP)+,(SP)+ TRAP 67	;		1024
23535 102450 000167 001006 23536 102454 022626 23537 102456 104467	3\$:	JMP 265 CMP (SP)+, (SP)+			1024 1024 1022 1024
23535 102450 000167 001006 23536 102454 022626 23537 102456 104467 23538 102460 006000 23539 102462 103701		TRAP 67	:		1024
23538 102460 006000 23539 102462 103701 23540 103444 016705 107104		ROR RO BLO 1\$			
23540 102464 016705 107106 23541 102470 005002		MOV LST.ARR,R5 CLR R2 TRAP 2	: *,SHF.	DATA	1024
23541 102470 005002 23542 102472 104402 23543 102474 152777 000040 23544 102502 016700 111074	110512 48:	ADD #16,SP JMP 26\$ CMP (SP)+,(SP)+ TRAP 67 ROR RO BLO 1\$ MOV LST.ARR,R5 CLR R2 TRAP 2 BISB #40,aml.REG+40 MOV ML.DUT,R0			1024 1025 1025 1025
23543 102474 152777 000040 23544 102502 016700 111074		MOV ML.DUT, RO			

CZMLABO ML-11 LOGIC TO MISCELLANEOUS CODING	ECTION	MACKU M	1113	22-051-01	12:16 PAGE 505	SE	0492
23546 23547			:ML4			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
23549 102506 04270 23550 102512 14277 23551 102520 15007 23552 102524 012703 23553 102530 010344 23554 102532 00500 23555 102534 00504	177770 000007 110470 000001	110474		BIC BICB BISB MOV CLR CLR JSR TST BNE CLR BR CMP BNE MOV MOV BR	#177770,R0 #7,aML.REG+40 R0,aML.REG+40 #1,R3 R3,-(SP) R1 -(SP)	; *,SGL ; SGL,* ; UNC	1025
23556 102536 00476 23557 102542 00570	113216			JSR TST	PC.FOR.ECC.ERR	; LOOP	1025
23559 102546 00500 23560 102550 000409	000001		58:	CLR BR CMP	PC.FOR.ECC.ERR R2 5\$ R4 6\$ R2.#1 7\$: DSA.PAT : LOOP,*	1026 1026 1025
23562 102556 00100: 23563 102560 01670 23564 102564 01047	107006 110414		68:	BNE MOV MOV	R4 - 201 - RFG+30	; *,DSA.PAT ; DSA.PAT,*	
23565 102570 00040 23566 102572 01050 23567 102574 01057 23568 102600 00620	110404		7\$:	BR MOV MOV ASR MOV	85 R5,R4 R5,aML.REG+30	SHF.DATA.DSA.PAT SHF.DATA.* SHF.DATA	1025 1027 1027
23569 102602 012777 23570 102610 012777 23571 102616 012777 23572 102624 105777	177400 011600 000071 110374	110354 110356 110330	8\$: 9\$:	MOV MOV TSTB	#10.BUF, aml.REG+10 #10.BUF, aml.REG+20 #71, aml.REG aml.REG+50	SHF.DATA	1026 1026 1027 1027 1027 1028 1028
23573 102630 100379 23574 102632 017766 23575 102640 02660	110536 000004	000004		MOV CMP	9\$ aml.REG+220,4(SP) 4(SP),R4	: *,SAVE : SAVE,DSA.PAT	1028 1028
23576 102646 10445 23577 102646 10445 23578 102650 00017 23579 102652 010464 23580 102654 02405				BEQ TRAP .WORD .WORD	10\$ 55 177 ASYNC		1028
23579 102652 010466 23580 102654 024052 23581 102656 012746 23582 102662 012746 23583 102666 012746 23584 102672 012746 23585 102676 010600 23586 102700 104416 23589 102710 046616 23590 102714 040466 23591 102720 052616 23592 102722 016646 23593 102726 010446 23595 102736 012746 23596 102736 012746 23597 102742 104416 23598 102744 062706 23599 102750 022626 23600 102752 104466	007454 010404 006024 000003			MORD MOV MOV MOV MOV MOV MOV BIC BIC BIS MOV MOV MOV MOV MOV MOV MOV	ASYNC DUMPER MPHR.4,-(SP) MREG.15,-(SP) MTWO.FMT,-(SP) M3,-(SP) SP,R0	;	1029
23586 102700 104414 23587 102702 016616	000014			TRAP	14 14(SP) (SP)	; SP,* ; SAVE,* ; DSA.PAT,*	1029
23589 102706 010446 23589 102710 046616 23590 102714 040466	000002			BIC	R4,-(SP) 2(SP),(SP) R4,2(SP)	; DSA.PAT,*	
23591 102720 052610 23592 102722 016640 23593 102726 010440	000014			MOV MOV	(SP)+, (SP) 14(SP),-(SP) R4,-(SP)	: SAVE * : DSA.PAT *	
23594 102730 012746 23595 102734 012746 23596 102740 010600	004266			MOV MOV	#FMT.2,-(SP) #4,-(SP) SP,R0	; SP.*	
23597 102742 104414 23598 102744 062700 23599 102750 022620 23600 102752 10446	000020		10\$:	TRAP ADD CMP TRAP	14 14(SP) (SP) R4,-(SP) 2(SP) (SP) R4,2(SP) (SP)+ (SP) 14(SP),-(SP) R4,-(SP) MFMT.2,-(SP) MFMT.2,-(SP) SP,RO 14 M20,SP (SP)+,(SP)+ 67		1028 1025 1025

CZMLABO ML-11 MISCELLANEOUS	CODING SE	CTION	MALKU M		22-051-80	12:16 PAGE 506			EQ 0493
23602 23603				ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
23605 102754 23606 102756	006000				ROR	RO 4\$			
23607 102760 23608 102762	005202	000020			INC	R2 .#20	; LOOP,*		1025
23610 102776	152777	000040 110600	110216		BISB	#40,aML.REG+40	:		1029
23612 103002 23613 103006	042705	177770 000007 110174 000001	110200		BICB	ML.DUT.R5 #177770.R5 #7.aML.REG+40 R5.aML.REG+40			
23615 103020 23616 103024	012703 010346	000001			WOA WOA B12R	#1.R5	; *,SGL ; SGL,* ; UNC		1029
23617 103026 23618 103030	005001 005046	442722			CLR	R3,-(SP) R1 -(SP)	; UNC		
23620 103036 23621 103044	012777	177400	110120		ROR BLO INC CMP BLE BISB MOV BICB BISB MOV MOV CLR CLR JSR MOV MOV MOV	#-400.aML.REG+10 #10.BUF.aML.REG+20	1		1029
23622 103052 23623 103060	016777 012777	011600 106514 000071 110132	110122 110124 110066	110.	MOV MOV TSTB	PC.FOR.ECC.ERR #-400.aML.REG+10 #10.BUF.aML.REG+20 LST.BLK.aML.REG+30 #71.aML.REG aML.REG+50 11\$			1030
23625 103072 23626 103074	100375	110132		115:	BPL CLR TRAP	RZ	; LOOP		1030
23628 103100 23628 103100 23629 103104	104402 005077 010205	110130		12\$:	TRAP CLR MOV	aml.REG+60	L00P,*		1030 1030 1030 1031
23630 103106 23631 103110	006305 066507	103114			ASL	R2,R5 R5 13\$(R5),PC	, Loor,-		1031
23633 103116 23634 103120	000012			13\$:	MORD WORD WORD WORD WORD MOV CLR MOV CLR CLR MOV CLR BR BR CLR BR BR CLR BR BR CLR MOV CLR CLR	R5 13\$(R5),PC 14\$-13\$ 15\$-13\$ 17\$-13\$ 18\$-13\$ 19\$-13\$ #1,R3 R1	1		
23635 103122 23636 103124 23637 103126 23638 103132 23639 103134 23640 103140 23641 103142 23642 103144 23643 103150 23645 103160 23647 103162 23649 103170 23650 103170 23651 103204 23653 103210 23654 103216 23655 103222 23656 103222	000044 000046 000056 012703 005001 016704 005003 012701 005004 005077 000430 005003 012701 000417 152777 016705 042705 142777 150577 012703 005001	000001			. WORD	18\$-13\$ 19\$-13\$			
23638 103132 23639 103134	005001	000001 106432		14\$:	CLR	R1 LST BLK P4	: *,SGL : UNC : *,DSA.PAT		1031 1031
23640 103140 23641 103142	000404 005003			15\$:	BR CLR	16\$ R3	:		1031 1032
23643 103150 23643 103150	005004	000001 110026		16\$:	CLR	#1,R1 R4 200 DEG430	SGL *,UNC DSA.PAT		1032 1032
23645 103156 23646 103160	000430	110020		17\$: 18\$:	BR BR	22\$ 20\$			1031 1033
23647 103164 23648 103164 23649 103170	005003 012701	000001		18\$:	MOV	LST.BLK,R4 16\$ R3 #1,R1 R4 aML.REG+30 22\$ 20\$ R3 #1,R1 21\$	SGL *,UNC		1031 1031 1031 1032 1032 1032 1033 1033
23650 103172 23651 103200	152777 016705	000040 110376 177770 000007 107772 000001	110014	19\$:	BISB	#40.aML.REG+40 ML.DUT.R5 #177770.R5 #7.aML.REG+40 R5.aML.REG+40 #1.R3 R1	:		1034
23652 103204 23653 103210 23654 103216	042705 142777 150577	177770 000007 107773	107776		BICB	#177770.R5 #7,aML.REG+40			
23655 103222 23656 103226	012703 005001	000001		20\$:	MOV	#1,R3 R1	; *,SGL ; UNC		1034 1034

LABO ML-11 LO CELLANEOUS CO RASA	J.110 J.			:ML4				22=0=c=1080		0494 TOP
3659				;				22-Dec-1980 22-Dec-1980	09:21:22	PA:
3661 103230 3662 103232 3663 103240 3664 103242 3665 103244 3666 103250 3667 103256	005004	106334	107744	21\$:	CLR	R4 LST_BLK_aML_REG+30	: DSA.PAT			103 103 103
3663 103240 3664 103242	010346			22\$:	MOV MOV JSR	R3,-(SP) R1,-(SP)	: SGL .*			103
3662 103232 3663 103240 3664 103242 3665 103244 3666 103250 3667 103256 3668 103276 3670 103276 3671 103300 3672 103304 3673 103306 3674 103314 3675 103320 3676 103324 3677 103332 3678 103356 3679 103344 3680 103350 3681 103352 3682 103354 3683 103356 3684 103360 3685 103362	004767	112510 177400 011600 000071 107726	107706		MOV	PC,FOR.ECC.ERR #-400,aML.REG+10	:			103
TALE TOTAL	012777	000071	107706 107710 107662	270	MOV	#10.BUF, aML.REG+20 #71, aML.REG	1			103 103 103
3670 103276	012777 012777 105777 100375 020227 001014	000004		23\$:	TSTB BPL	R4 LST.BLK.aML.REG+30 R3,-(SP) R1,-(SP) PC,FOR.ECC.ERR #-400,aML.REG+10 #10.BUF,aML.REG+20 #71,aML.REG aML.REG+50 238 R2,#4	. 1000 +			103
672 103304	001014		107700		BPL CMP BNE BISB	4/0 om occ./0	: LOOP,*			103 103 103
674 103314 675 103320	152777 016705 042705	110262	107700		MOV	ML.DUT.R5 #177770.R5				103
676 103324 677 103332	142777 150577	000007	107662		BICB	#7,aML.REG+40 R5,aML.REG+40				
3678 103336 3679 103344	017766	000040 110262 177770 000007 107656 110032 000010	000010	248:	MOV	ML.DUT.R5 M177770,R5 M7.aML.REG+40 R5.aML.REG+40 aML.REG+220,10(SP) 10(SP),R4 258 55 200 SYNC DUMPER MURD. 10 -(SP)	: *,SAVE ; SAVE,DSA.PAT			103 103
680 103350 681 103352	001433 104455				BEQ TRAP . WORD . WORD . WORD	25 \$ 55	:			103
683 103356	010526				. WORD	SYNC				
685 103362 103362	012746	006406			MOV		:			103
669 103272 670 103276 671 103300 672 103304 673 103306 674 103314 675 103320 676 103324 677 103332 678 103336 679 103344 680 103350 681 103352 682 103354 683 103356 684 103360 685 103366 687 103372 668 103376 669 103402 6690 103406	012746	010404			MOV	WURD.71(SP) WREG.15(SP) WTHR.FMT(SP) W4(SP) SP.R0				
3689 103402 3690 103406	012746	000004			MOV	#4(SP) SP.RO	; SP,*			
691 103410 692 103412	104414 016616 010446 012746	000022			MOV	14 22(SP) (SP)	: SAVE,* : DSA.PAT,*			103
691 103410 692 103412 693 103416 694 103420 695 103424 696 103430 697 103432 698 103434 699 103440 6700 103442	010446	004222 000003			MOV	14 22(SP),(SP) R4,-(SP) WFMT.1,-(SP) W3,-(SP) SP,R0 14	; DSA.PAT,*			
696 103430 697 103430	010600 104414	000003			MOV MOV TDAR	SP, RO	; SP,*			
3694 103420 3695 103424 3696 103430 3697 103432 3698 103434 3699 103440 3700 103442	062706	000020		25\$:	ADD	#20,SP (SP)+,(SP)+	:			103 103 103
5700 103442 5701 103444	104467				TRAP	67 R0	:			103
3702 103446 3703 103450	103613 005202				BLO	12\$ R2	: LOOP : LOOP,*			1030
3704 103452 3705 103456	020227	000004			BLE	R2,#4 12\$; LOOP,*			
3704 103452 3705 103456 3706 103460 3707 103462 3708 103464	022020 005726 000207			26\$:	MOV TRAP ADD CMP TRAP ROR BLO INC CMP BLE CMP TST RTS	14 #20 SP (SP)+, (SP)+ 67 R0 12\$ R2 R2,#4 12\$ (SP)+, (SP)+ (SP)+ PC	•			1017
3708 103464 3709 3710 3711	000207			. Pour		: 323 words k depth per invocation: 19 words				

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:1	6 PAGE 508
23720 23721 23725 23729 103466 23730 103466 004767 176566 23731 103472 104466 23732 103474 006000 23733 103476 103773 23734 103500 000207	T57:: 1\$: JSR PC. TRAP 66 ROR RO BLO 1\$ RTS PC	\$157
23736 23737 23742 23743 23744; 10382 ! SLF/P		words th per invocation: 0 words

SEQ 0495

1037

```
22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) 
22-Dec-1980 09:21:22 PA:<NEALE>BL3ML4.BLI.2 (108)
 10383 !
10384 BGNTST;
10385
10386 !++
10387 ! TEST N
             ! TEST NUMBER: TST 58
               TEST NAME: ECC HARD ERROR BIT TEST
               TEST DESCRIPTION:
                          VIA ECC_DM AND ECC_DIS TEST THE
ECH BIT FOR SETTING AND NOT SETTING BY:
                          1. VIA ECC_DM AND A WRITE TRANSFER FORCE ECC ERRORS INTO THE ML11'S GOOD BLOCK
                           2. WITH ECC_DM = 0 AND ECC_DIS = 0
                                  DO A READ TRANSFER AND CHECK ECH SET
10400 | 3. WITH ECC_D
10402 | DO A READ
10403 | IMPLICIT INPUTS:
10405 | IO_BUF
10406 | A VECTOR OF 2
10407 | FOR MBUS READS
10408 | IS FOUND.
10410 |
10411 |---
10412 | IOCAL
10415 | IOCAL
10416 | CLR_MBUS;
10417 | ECC_DM = ONE;
10418 | PAR_CRC_WRD = ZEROES;
10419 | CRC_A = ZEROES;
10420 | CRC_B = ZEROES;
10421 | BAI = ONE;
10423 | GD_BUK_XFER ();
10424 | MLCS1 = write;
10425 | TIME_OUT_LOOP;
10426 | IOCAL
10427 | IOCAL
10428 | BGNSUB;
10430 | CLR_MBUS;
10431 | GD_BUK_XFER ();
10431 | GD_BUK_XFER ();
                           3. WITH ECC_DM = 0 AND ECC_DIS = 1
DO A READ TRANSFER AND THECK ECH NOT SET
                          IO_BUF
A VECTOR OF 256 WORDS WHERE DATA
                           FOR MBUS READS AND WRITES TRANSFERS
                                                                                                      !EXPECTED ECH_ERR VALUE
                                                                                                      CLEAR THE DRIVE
                                                                                                      FORCE UNCORRECTABLE ERRORS INTO THE GOOD BLOCK
           incr LOOP from 0 to 1 do
                                                                                                      !TEST ECH BIT FOR SETTING / NOT SETTING
                                                                                                       !START OF SCOPE LOOP
                                                                                                      !CLEAR THE DRIVE
                   GD_BLK_XFER ();
                                                                                                      SET UP A GOOD BLOCK TRANSFER
                   if .LOOP eal ZERO
                                                                                                      SELECT THE TEST DATA
                    then
```

HIJCELL		ODING SE	CITON							DEW 0497
23802 23803	ML4							22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS-20 Bliss-16 V2(212) PA: <neale>BL3ML4.BLI.2 (108)</neale>	,
23805 23806 23807 23808	:	10435 10436 10437 10438		begin ECC_DIS EXPECTE end	D = 0;			!EXPECT ECH BIT TO SET		
23809 23810 23811 23812 23813		10439 10440 10441 10442 10443	els	begin ECC_DIS EXPECTE end;	D = 1;			!EXPECT ECH BIT NOT TO	SET	
23815 23816 23817		10445 10446 10447	MLC	S1 = rea E_OUT_LO	d; OP;			!READ THE ERROR OUT		
23818	:	10448	if	.ECH_ERR	neq .E	XPECTED		!IS ECH BIT WHAT WE EXP	ECTED	
23819		10448 10449 10450 10451 10452 10453 10454	the	begin				!ERROR IF NOT		
23821		10451		ERRDF (129, SY	NC, DUMP	ER); 7 WRD 10 WRD 69	WRD 14) ·		
23823		10453		PRINTB end;	(FMT_1,	.EXPECT	ER); 7, WRD_10, WRD_69 ED, .ECH_ERR);	, with 147,		
23825		10455								
23827	:	10455 10456 10457	end	SUB;				!END OF SCOPE LOOP		
23828 23829	:	10458 10459	ENDTST;							
23833 23837	103502 103506				\$T58:	JSR	R1.\$SAVE3	:		1038 1041
23839	103506 103514	004167 152777 016702	100352 000040 110062	107500		BISB	ML.DUT,R2	:		1041
23840 23841	103520 103524	042702	1////0	107462		BIC BICB BISB BISB BIC BICB BICB BICB	#177770,R2 #7,aML.REG+40			
23842	103532	150277	000007 107456 000001			BISB	DO SMI DECALO			10/1
23844 23845	103544	042777	000001 037400 000077 000077 000010 177777 112254 000061 107404	107530 107552 107544 107546 107420 105776		BIC	#37400, aml .REG+	150 :		1041 1041 1041
	103560	142777	000077	107546		BICB	#77, and .REG+160			1042
23848	103574	012767	177777	107420		PIUV	#10, aML.REG+40 #-1,10.BUF			1042 1042 1042 1042
23849 23850	103602	004767	112254	107340		JSR MOV TSTB	PC.GD.BLK.XFER			1042
23851	103614	105777	107404	10.510	1\$:	TSTB	#1, aML.REG+120 #37400, aML.REG+ #77, aML.REG+150 #77, aML.REG+160 #10, aML.REG+40 #-1, IO.BUF PC, GD.BLK.XFER #61, aML.REG aML.REG+50 1\$			1042
23846 23847 23848 23849 23850 23851 23852 23853 23854 23855	103622	152777 142777 142777 142777 152777 012767 004767 012777 105777 100375 005001 104402 152777			20	CLR	R1	; L00P		1042
23855	103544 103552 103560 103566 103574 103602 103614 103620 103624 103624	152777	000040	107360	2\$:	TRAP BISB	#40,aML.REG+40			1042 1042 1042

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80	12:16 PAGE 511	SEQ 0498
	;ML4		
23857 23858 23859	1.00		22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
23860 103634 016700 107742 23861 103640 042700 177770 23862 103644 142777 000007 23863 103652 150077 107336 23864 103656 004767 112200 23865 103662 005701	107342 MOV BIC BICB BISB JSR TST	ML.DUT.RO #177770.RO #7.aML.REG+40 RO.aML.REG+40 PC.GD.BLK.XFER R1 ; LOOP	1043 1043
23866 103664 001006 23867 103666 142777 000002 23868 103674 012702 000001 23869 103700 000404	JSR TST BNE BICB MOV BR 107364 3\$: BISB CLR 107234 4\$: MOV 5\$: TSTB	#2.aml.REG+120 : *.EXPECTED	1043 1043 1043 1044 1044
23870 103702 152777 000002	107364 3\$: BISB	4\$ #2.aml.REG+120	1044
23871 103710 005002 23872 103712 012777 000071 23873 103720 105777 107300 23874 103724 100375	107234 4\$: MOV 5\$: TSTB BPL	R2 W71,aML.REG aML.REG+50 SS R2,R3 : EXPECTED.*	1044
23875 103726 010203 23876 103730 005000 23877 103732 132777 000100 23878 103740 001401 23879 103742 005200	MOV CLR	R2,R3 ; EXPECTED,* R0 #100,aML.REG+60 6\$ R0 R0,R3	1044
23880 103744 020003 23881 103746 001441 23882 103750 104455 23883 103752 000201 23884 103754 010526 23885 103756 024052	BEQ INC 6\$: CMP BEQ TRAP .WORD .WORD	RO,R3 8\$ 55 201 SYNC DUMPER	1045
23886 103760 012746 006442 23887 103764 012746 007252 23888 103770 012746 006406 23889 103774 012746 007240	MOV MOV MOV	MURD.14,-(SP) MURD.69,-(SP) MURD.10,-(SP) MURD.67,-(SP) MFOR.FMT,-(SP) M5,-(SP)	1045
23892 104010 010600 23893 104012 104414	MOV	SP.R0 ; SP.*	
23890 104000 012746 006046 23891 104004 012746 000005 23892 104010 010600 23893 104012 104414 23894 104014 005016 23895 104016 132777 000100 23896 104024 001401 23896 104026 005216 23898 104030 010246	107210 CLR BITB BEQ	(SP) #100.aml.REG+60 7\$	1045
23898 104030 010246 23899 104032 012746 004222 23900 104036 012746 000003	7\$: MOV	(SP) R2,-(SP) #FMT.1,-(SP) #3 -(SP)	
23901 104042 010600	MOV	SP.RO : SP.*	
23903 104046 062706 000022 23904 104052 104467 23905 104054 006000	8\$: TRAP	#22.SP :	1045 1045
23905 104054 006000 23906 104056 103662 23907 104060 005201 23908 104062 020127 000001 23909 104066 003656 23910 104070 000207	107210 MOV MOV TRAP CLR BITB BEQ INC 7\$: MOV MOV MOV TRAP ADD TRAP ADD TRAP ADD TRAP ROR BLO INC CMP BLE RTS	14 (SP) #100,aML.REG+60 7\$ (SP) R2,-(SP) #FMT.1,-(SP) #3,-(SP) SP,R0 : SP,* 14 #22,SP : LOOP R1 R1,#1 : LOOP,*	1042
23910 104070 000207 23911	RTS	PC :	1038

CZMLABO ML MISCELLANE	-11 LOGIC TE	ST MACRO	M1113 22-DEC-	80 12:16 PAGE 512		SEG	0499
23913 23914			ML4			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:
23915 23916 23917 23922 23923			; Routine Si ; Maximum st	ze: 124 words ack depth per invocation	: 13 words		
23927 23931 10 23932 10 23933 10 23934 10 23935 10 23936 10	4072 4072 004767 4076 104466 4100 006000 4102 103773 4104 000207	177404	T58:: 1\$: JSR TRAP ROR BLO RTS	PC.\$T58 66 R0 1\$ PC	,		1045
23937 23938 23939 23944 23945 23946 ;	1046	0 ! <blf page=""></blf>	; Routine Si ; Maximum st	ze: 6 words ack depth per invocation	: 0 words		

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22-Dec-1980 09:24:31
22-Dec-1980 09:21:22
                                                                                                                            TOPS-20 Bliss-16 V2(212)
                                                                                                                            PA: <NEALE>BL3ML4.BL1.2 (109)
10461 !
10462 BGNTST;
10463
10464 !++
          ! TEST NUMBER: TST 59
            TEST NAME:
                                               DATA CHECK ERROR BIT TEST
          ! TEST DESCRIPTION:
                      VIA ECC_EN, ECC_DM AND ECC_DM TEST THE DCK BIT FOR SETTING AND NOT SETTING BY:
                      1. VIA ECC_DM AND A WRITE TRANSFER FORCE ECC ERRORS INTO THE ML11'S GOOD BLOCK
                      2. WITH DCK_EN = 1, ECC_DM = 0, ECC_DIS = 0
DO A READ TRANSFER AND CHECK THE DCK BIT SET
                      3. WITH DCK_EN = 0, ECC_DM = 0, ECC_DIS = 0
DO A READ TRANSFER AND CHECK THE DCK BIT NOT SET
                      4. WITH ECK_EN = 1, ECC_DM = 0, ECC_DIS = 1
DO A READ TRANSFER AND CHECK THE DCK BIT SET
A VECTOR OF 256 WORDS WHERE DATA
                      FOR MBUS READS AND WRITES TRANSFERS
                                                                                      !SINGLE BIT ERROR PARRAMETER
                                                                                      !UNCORRECTABLE ERROR PARRAMETER !EXPECTED DATA CHECK BIT VALUE
10498 EXPECTED;

10499

10500 CLR_MBUS;

10501 FOR_ECC_ERR (SGL = 1, UNC = 0);

10502 BAI = ONE;

10503 IO_BUF = ZEROES;

10504 GD_BLK_XFER ();

10505 MLCS1 = write;

10506 TIME_OUT_LOOP;

10507

10508 incr_LOOP from 0 to 2 do

10509 begin

10510 BGNSUB;
                                                                                      !CLEAR THE DRIVE
                                                                                      FORCE A SBE
                                                                                      SET UP A GOOD BLOCK TRANSFER
                                                                                      !WRITE THE DRIVE WITH THE ERROR
                                                                                     !TEST DCK BIT FOR 3 CONDITIONS
                begin
BGNSUB;
                                                                                      START OF SCOPE LOOP
                CLR_MBUS;
                GD_BLK_XFER ():
                                                                                      SET UP A GOOD BLOCK TRANSFER
```

```
22-Dec-1980 09:24:31 TOPS-20 Bliss-16 V2(212) PA:<NEALE>BL3ML4.BLI.2 (109)
           case .LOOP from 0 to 2 of
                                                             SELECT THE TEST DATA
                [O] :
                                                             !EXPECT THE DCK BIT TO SET
                    begin
DCK_EN = 1;
ECC_DIS = 0;
                    EXPECTED = 1:
               [1] :
                                                              !EXPECT THE DCK BIT NOT TO SET
                    begin
DCK_EN = 0;
                    ECC_DIS = 0;
                    EXPECTED = 0:
                    end:
               [2]:
                                                             !EXPECT THE DCK BIT TO SET
                    begin
DCK_EN = 1;
ECC_DIS = 1;
EXPECTED = 1;
                    end;
                tes:
           MLCS1 = read:
                                                            !READ THE ERROR AND CLOCK THE DCK BIT
           TIME OUT LOOP:
           if .DCK neg .EXPECTED
                                                             !IS THE DCK BIT WHAT WE EXPECTED
           then
               begin
                                                              !REPORT THE ERRRO IF NOT THE SAME
               if .LOOP neg ONE then ERRDF (130, SYNC, DUMPER) else ERRDF (130, ASYNC, DUMPER);
                                                              ! CHOOSE WHICH THE ERROR IS ON
               PRINTB (THR_FMT, WRD_72, PHR_5, WRD_10);
PRINTB (FMT_1, .EXPECTED, .DCK);
               end;
           ENDSUB:
                                                            !END OF THE SCOPE LOOP
           end;
10556 ENDTST:
```

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 2	-DEC-80 12:16 PAGE 515	SI	EQ 0502
24059 24060	ML4		22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
24062 104106 004167 077746 24063 104112 152777 000040 24064 104120 016702 107456 24065 104124 042702 177770	107074 \$159:	JSR R1.\$SAVE3 BISB #40.aML.REG+40 MOV ML.DUT.R2 BIC #177770.R2 BICB #7.aML.REG+40	:	1045 1049
24062 104106 004167 077746 24063 104112 152777 000040 24064 104120 016702 107456 24065 104124 042702 1777770 24066 104130 142777 000007 24067 104136 150277 107052 24068 104142 012701 000001 24069 104146 010146 24070 104150 005001 24071 104152 005046 24072 104154 004767 111600 24073 104160 152777 000010 24074 104166 005067 105406 24075 104172 004767 111664 24076 104176 012777 000061	107056	BISB RZ, and REG+40	; *,SGL ; SGL.* ; UNC	1050
24071 104152 005046 24072 104154 004767 111600 24073 104160 152777 000010 24074 104166 005067 105406 24075 104172 004767 111664 24076 104176 012777 000061 24077 104204 105777 107014	107026	MOV #1,R1 MOV R1,-(SP) CLR R1 CLR -(SP) JSR PC.FOR.ECC.ERR BISB #10,aml.REG+40 CLR IO.BUF JSR PC.GD.BLK.XFER MOV #61,aml.REG TSTB aml.REG+50		1050 1050 1050 1050
24076 104176 012777 000061 24077 104204 105777 107014 24078 104210 100375	106750	MOV #61.aML.REG TSTB aML.REG+50 BPL 1\$;	
24080 104214 104402 24081 104216 152777 000040 24082 104224 016700 107352 24083 104230 042700 177770 24084 104234 142777 000007 24085 104242 150077 106746 24086 104246 004767 111610	106770 2\$: 106752	BPL 18 CLR R1 TRAP 2 BISB #40,aml.REG+40 MOV ML.DUT,R0 BIC #177770,R0 BICB #7,aml.REG+40 BISB R0,aml.REG+40	: LOOP	1050 1050 1051
24087 104252 010100 24088 104254 006300 24089 104256 066007 104262	3\$:	JSR PC.GD.BLK.XFER MOV R1.R0	; LOOP.*	1051 1051
24091 104264 000024 24092 104266 000044 24093 104270 152777 000004 24094 104276 142777 000002	106776 4\$: 106770	ASL R0 ADD 3\$(R0),PC .WORD 4\$-3\$.WORD 5\$-3\$.WORD 6\$-3\$ BISB #4,aML.REG+120 BICB #2,aML.REG+120		1051
24094 104276 142777 000002 24095 104304 000416 24096 104306 142777 000004 24097 104314 142777 000002 24098 104322 005002 24099 104324 000410	106760 5\$: 106752	BICB #4.aML.REG+120 BICB #2.aML.REG+120	EXPECTED	1051 1052 1052 1052 1052 1053 1053 1053 1053
24099 104324 000410 24100 104326 152777 000004 24101 104334 152777 000002 24102 104342 012702 000001 24103 104346 012777 000071 24104 104354 105777 106644	106740 6\$: 106732 7\$:	CLR R2 BR 8\$ BISB #4.aml.REG+120 BISB #2.aml.REG+120 MOV #1.R2	*,EXPECTED	1051 1053 1053 1053
24103 104346 012777 000071 24104 104354 105777 106644 24105 104360 100375 24106 104362 010203 24107 104364 005000	7\$: 106600 8\$: 9\$:	MOV #71, aml. REG TSTB aml. REG+50 BPL 9\$; EXPECTED,*	1053
24090 104262 000006 24091 104264 000024 24092 104266 000044 24093 104270 152777 000004 24094 104276 142777 000002 24095 104304 000416 24096 104306 142777 000002 24098 104322 005002 24099 104324 000410 24100 104326 152777 000002 24101 104334 152777 000002 24102 104342 012702 000001 24103 104366 012777 000071 24104 104364 005000 24105 104360 100375 24106 104364 005000 24108 104366 005777 106644 24109 104374 005000 24110 104374 005000 24111 104376 020003 24112 104400 001446 24113 104402 020127 000001	100	MOV R2,R3 CLR R0 TST aML.REG+60 BPL 10\$ INC R0 CMP R0,R3 BEQ 14\$ CMP R1,#1		
24112 104400 001446 24113 104402 020127 000001	10\$:	BEQ 14\$ CMP R1,#1	; LOOP,*	1054

							2/442
22-Dec-1980 09:24:31 TO PA				ML4			24116 24116
		11\$ 55 202 SYNC DUMPER 12\$ 55 202 ASYNC DUMPER MWRD.10,-(SP) MPHR.5,-(SP) MHRD.72,-(SP) MTHR.FMT,-(SP) M4,-(SP) SP,R0 14 (SP)	BEQ TRAP .WORD .WORD .WORD BR TRAP .WORD .WORD .WORD .WORD	11\$:		001405 104455 000202 010526 024052 000404 104455 000202 010464	24118 104406 24119 104410 24120 104412 24121 104414 24122 104416 24123 104420 24124 104422 24125 104424 24126 104426
10	•	DUMPER MURD.10,-(SP) MPHR.5,-(SP) MURD.72,-(SP) MTHR.FMT,-(SP)	MOV	12\$:	006406 007472 007302 006034 000004	024052 012746 012746 012746 012746	24127 104430 24128 104432 24129 104436 24130 104442 24131 104446
	; SP.*	#4(SP) SP.RO	MOV MOV TRAP		000004	010600	24132 104456 24133 104456 24134 104460
10	•	aML.REG+60	CLR TST BPL INC		106544	005016 005777 100001 005216	24132 104452 24133 104456 24134 104460 24135 104462 24136 104464 24137 104470 24138 104472 24139 104474
	; EXPECTED,*	(SP) R2,-(SP) WFMT.1,-(SP) W3,-(SP) SP,R0 14	MOV MOV	13\$:	004222 000003	010246 012746 012746	24139 104474 24140 104476 24141 104502 24142 104506
10 10	; SP.*	14 #20.SP 67 R0	MOV TRAP ADD TRAP ROR	14\$:	000020	010600 104414 062706 104467	24143 104510 24144 104512 24145 104516 24146 104520
10	; LOOP,*	2\$ R1 R1,#2 2\$ (SP)+,(SP)+ PC	BLO INC CMP BLE CMP RTS		000002	006000 103634 005201 020127 003630	24147 104522 24148 104524
10	:	(SP)+,(SP)+ PC	CMP RTS			022626 000207	24149 104526 24150 104532 24151 104534 24152 104536 24153 24154 24155 24160 24161 24165

CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION	MACRO M1113 22-DEC-80 12:16 PAGE 517	SEQ 0504
24170 24171 24172	ML4	22-Dec-1980 09:24:31 TOPS 22-Dec-1980 09:21:22 PA:<
24173 104540 24174 104540 004767 1773 24175 104544 104466 24176 104546 006000 24177 104550 103773 24178 104552 000207	T59:: JSR PC,\$T59 TRAP 66 ROR RO BLO 1\$ RTS PC	1055
24180 24181 24186 24187 24188 : 10557 ! <bu< td=""><td>; Routine Size: 6 words ; Maximum stack depth per invocation: 0 words</td><td></td></bu<>	; Routine Size: 6 words ; Maximum stack depth per invocation: 0 words	

24190 :ML4 24191 : 24192 : 24193 : 24194 : 24195 : 24196 : 24197 : 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 TOPS-20 Bliss-16 V2(212)
PA: <NEALE>BL3ML4.BLI.2 (110) BGNTST; TEST NUMBER: TST 60 TEST NAME: PROM DATA TEST TEST DESCRIPTION: VERIFY THAT CHECK SUM VALUES FOR ALL PRESENT MEMORY ARRAY UV PROM LOCATIONS ARE ERROR FREE BY: 1. DOING MBUS TRANSFERS AT ALL PRESENT BLOCKS AND TESTING THE UNS BIT TO BE CLEARED. 10576 10577 ! IMPLICIT INPUTS: IO_BUF A VECTOR OF 256 WORDS WHERE DATA FOR MBUS READ AND WRITE FUNCTIONS IS FOUND. CHIP SIZ INDICATED THE SIZE OF THE ARRAY MODULES MOS RAMS. incr DSA_CNT from 0 to .LST_BLK do begin BGNSUB; !WRITE TO ALL PRESENT BLK'S AND CHECK UNS BIT DISABLE ECC LOAD WORD COUNT LOAD UBUS ADRS ECC_DIS = ON MLWC = not 255; MLBA = IO BUF; MLDA = .DSA_CNT; MLCS1 = write; TIME_OUT_LOOP; LOAD DSA !DO A WRITE FUNCTION if .UNS IS_SET !SEE IF XFERR CAUSED AN UNS ERROR then begin ERRDF (107, ARR_DAT, DUMPER); PRINTB (TWO_FMT, WRD_35, PHR_4); PRINTB (FMT_9, .DSA_CNT); !ERROR IF SET

MOV MOV

#3,-(SP)

1060

2.2-Dec-1980 09:24:31 2.301 2.302 2.303 2.303 2.303 2.303 2.303 2.303 2.303 2.304 2.305 2.306 2.305 2.306 2.306 2.306 2.306 2.306 2.307 2.306 2.307 2.	LABO ML-11 LOG CELLANEOUS COD	THO SECTION							050
24306 104730 010116 24307 104732 012746 000002 24308 104732 012746 000002 24308 104732 012746 000002 24308 104732 012746 000002 24309 104742 010600 24310 104744 104414 24311 104746 026727 104616 000100 24312 104734 001020 24313 104736 010100 24313 104736 000000 24315 104736 000000 24316 104736 000000 24316 104736 000000 24317 104736 000000 24318 104770 005200 24318 104770 005200 24318 104770 005200 24319 104774 010046 24321 105000 012746 004610 24322 105000 012746 004610 24323 105010 010600 24323 105010 010600 24324 105012 104414 24325 105014 000415 24327 105016 010100 382 800 383 800 383 800 384 80 385 800 385 800 386 800 387 800 388 800	4301 4302			ML4				22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	PA
24306 104730 010116	304 104724 0	10600			MOV	SP,RO	; SP,4		
24325 105014 000415 24326 105016 010100 3\$: MOV R1,R0 ; DSA.CNT,* 24327 105020 006200 ASR R0 24328 105032 000300	306 104730 0 307 104732 0	10116	4		MOV	R1,(SP) #FMT.9,-(SP)	; DSA.CNT,*		10
24325 105014 000415 BR 4\$ 24326 105016 010100 3\$: MOV R1,R0 ; DSA.CNT,* 24327 105020 006200 ASR R0	309 104742 0	10600)2		MOV	SP.RO	; SP.*		
24325 105014 000415 24326 105016 010100 3\$: MOV R1,R0 ; DSA.CNT,* 24327 105020 006200 ASR R0 24328 105032 000300	311 104746 0 312 104754 0	26727 10461	16 000100		CMP	CHIP.SIZ,#100			10
24325 105014 000415 24326 105016 010100 3\$: MOV R1,R0 ; DSA.CNT,* 24327 105020 006200 ASR R0 24328 105032 000300	313 104756 0 314 104760 0 315 104762 0 316 104764 0	10100 06200 06200 06200			MOV ASR ASR ASR	R1 .R0 R0 R0	; DSA.CNT,*		10
24325 105014 000415 24326 105016 010100 3\$: MOV R1,R0 ; DSA.CNT,* 24327 105020 006200 ASR R0 24328 105022 000300	317 104766 00 318 104770 00 3319 104774 01 330 104776 00	62700 17776 10046 05216	50		BIC	#177760,R0 R0,-(SP)			
24325 105014 000415 24326 105016 010100 3\$: MOV R1,R0 ; DSA.CNT,* 24327 105020 006200 SNAP R0	321 105000 01 322 105004 01 323 105010 01	12746 00461 12746 00000 10600	10		MOV MOV MOV	WFMT.8,-(SP) W2,-(SP) SP,R0	: SP.*		
24328 105022 000300 SWAB R0 24329 105024 042700 177760 BIC #177760,R0 24330 105030 010046 MOV R0,-(SP) 24331 105032 005216 INC (SP) 24332 105034 012746 004610 MOV #FMT 8 -(SP)	324 105012 10 325 105014 00 326 105016 01 327 105020 00	04414 00415 10100 06200		3\$:		14 4\$ R1,R0			1
24331 103032 003216 24332 105034 012746 004610 MOV MEMT 8 -(SP)	328 105022 00 329 105024 04 330 105030 01	00300 42700 17776 10046	50		SWAB BIC MOV	RO #177760,RO RO,-(SP)			
24333 105040 012746 000002 MOV #2(SP) 24334 105044 010600 MOV SP.R0 ; SP.*	332 105034 01 333 105040 01 334 105044 01	12746 00461 12746 00000	10		MOV	(SP) WFMT.8,-(SP) W2,-(SP) SP.RO			
24334 105044 010600 MOV SP,R0 ; SP,* 24335 105046 104414 TRAP 14 24336 105050 062706 000022 4\$: ADD #22,SP ; 24337 105054 104467 5\$: TRAP 67	335 105046 10 336 105050 0	04414 52706 00002	22	4\$: 5\$:	TRAP ADD TRAP	14 #22,SP 67	;		10
24338 105056 006000 ROR RO 24339 105060 103643 BLO 1\$	4338 105056 00 4339 105060 10 4340 105062 00	06000 03643 05201			ROR BLO INC	R0 1\$ R1	: DSA.CNT		10
24341 105064 020102 6\$: CMP R1,R2 ; DSA.CNT,*	4341 105064 07	20102 03640 00207		6\$:	BLE	R1,R2	; DSA.CNT;		10

MACRO M1113 22-DEC-- 12:16 PAGE 521 CZMLABO ML-11 LOGIC TEST MISCELLANEOUS CODING SECTION SEQ 0508 :ML4 22-Dec-1980 09:24:31 22-Dec-1980 09:21:22 T60:: 004767 177456 104466 PC,\$T60 66 R0 1\$ PC JSR TRAP ROR BLO RTS ; Routine Size: 6 words ; Maximum stack death per invocation: 0 words 10627 ! <BLF/PAGE>

TOPS PA:<

1062

10634 LASTAD; 10635 BGNSETUP (0); 10636 ENDSETUP;

ELLOWEOUS (OGIC TEST					EQ 0510
435 436 437		ML4			22-Dec-1980 09:24:31 22-Dec-1980 09:21:22	TOPS PA:<
439 105126 440 105130 441 105132 442	105132 000000 000000	BL\$LAS::.WORD .WORD T\$FREE::.WORD	T\$FREE < <t\$free-<bl\$las+4>>/2></t\$free-<bl\$las+4>			
443 444 445 446	105132 000000	L\$LAST== T\$PTHV==	BL\$LAS+4			
451 105134 452 105134	000207	SEND.LINK:: RTS	PC	;		1063
454 455 460		; Routine Size: ; Maximum stack	1 word depth per invocation: 0 words			
462 ; 463 ; 464 ; 468	10637 end 10638 10639 eludom					
469 470 471 472 473 474 475		; OTS external .GLOBL .GLOBL .GLOBL	references BL\$GT2, \$SAVE5, \$SAVE4, \$SAVE3 \$SAVE2, BL\$PU2, BL\$PU1, BL\$GT1 BL\$DIV, BL\$MOD, BL\$MUL			
76 77 78 79 80 81 82 83		; Size: ; Run Time: ; Elapsed Time: ; Memory Used: ; Compilation C	14698 code + 1916 data words 03:04.1 03:48.6 118 pages Complete			

SYMBOL T	TABLE			12110 17102	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		
LSETP LSEXP1 LSEXP5 LSHARD LSHIME LSHPCP LSHIME LSHPCP LSHIME LSHOP LSINIT LSLADP LSLAD LSLAN LSTEV LSPRIO MSGHS MSG	002376 G 002114 G 002014 G 002012 G 002374 002400 002476 002726 010630 013602 013600 013154 002512 002540 002563 002614 002672 002726	OSAPTS= 000001 OSAU = 000001 OSBGNS= 000001 OSBGNS= 000001 OSERRT= 000001 OSERRT= 000001 OSPOIN= 000001 OSSETU= 000001 PAR.DI 01566 PD.TEM 013120 PHR.1 = 007564 PHR.10= 007564 PHR.11= 007566 PHR.12= 007614 PHR.13= 007636 PHR.14= 007662 PHR.15= 007714 PHR.2 = 007404 PHR.3 = 007404 PHR.3 = 007404 PHR.3 = 007404 PHR.3 = 007404 PHR.9 = 007552 PHR.6 = 007504 PHR.9 = 007552 PHR.9 = 007552 PHR.9 = 007560 PRI00 = 000000 G PRI01 = 000000 G PRI02 = 000100 G PRI03 = 000100 G PRI04 = 000200 G PRI05 = 000240 G PRI05 = 000340 G PRI06 = 000300 G PRI07 = 000340 G PRI07 = 000340 G PRI08 = 0004400 PAAE 004430 PAAE 004660 PAAB 004661 PAAB 004661 PAAB 004661 PAAB 004676 PAAB 004676 PAAB 004676 PAAB 004676 PAAB 005034	P.AAV P.AAV P.AAV P.AAV P.AAV P.AAV P.AAV P.ABA P.ABB P.ABB P.ABB P.ABB P.ABI	005462 005534 005606 005746 005746 005746 006034 006034 006046 006120 006120 006142 006166 006214 006244 006276 006302 006314 006332 006330 006336 006336 006344 006426 006426 006456 006456 006456 006552 006506 006514 006522 006506 006574 006660	P.ACZ P.ADA P.ADB P.ADB P.ADB P.ADB P.ADB P.ADB P.ADB P.ADB P.ADB P.ADB P.ADB P.ADB P.ADB P.AEB	006732 006746 006754 006764 007016 007026 007036 007036 007074 007114 007122 007130 007156 007156 007156 007156 007210	P.AFD 010012 P.AFE 010022 P.AFF 010036 P.AFF 010036 P.AFG 010050 P.AFH 010060 P.AFJ 010112 P.AFJ 010112 P.AFJ 010136 P.AFM 010150 P.AFM 010150 P.AFM 010162 P.AFM 010204 P.AFM 010204 P.AFM 010204 P.AFM 010204 P.AFM 010230 P.AFS 010242 P.AFF 010230 P.AFS 010242 P.AFJ 010266 P.AFJ 010266 P.AFJ 010302 P.AFJ 010310 P.AFJ 010310 P.AFJ 010310 P.AFJ 010324 P.AGM 010332 P.AGM 010332 P.AGM 010362 P.AGM 010362 P.AGM 010364 P.AGM 010366 P.AGM 010464 P.AGM 010466 P.AGM 010672 P.AGM 010630 P.AGS 010672 P.AGM 010666

CZMLABO	O ML-11 LOGIC TABLE	TEST	MACRO M1113	22-DEC-80	12:16	PAGE	6 8 523-3	
RD.FF	021530	TST-LN	016146	133	06051	2 6	URD 3	0=

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SYMBOL TABLE	EST MACRO M1113	22-DEC-80 12:16 PAGE 5	523-3	
RD.EE 021530 RD.EL 021466 RD.ER 017710 RD.E1 020644 RD.E2 021064 RD.MR 020244 RD.PA 020436 RD.PD 021326 RD.REG 023462 REG.IN 013136 REG.1 = 010256 REG.10= 010346 REG.12= 010362 REG.13= 010370 REG.14= 010370 REG.14= 010376 REG.15= 010402 REG.16= 010412 REG.16= 010412 REG.16= 010430 REG.19= 010436 REG.2 = 010266 REG.20= 010444 REG.21= 010444 REG.21= 010444 REG.3 = 010310 REG.6 = 010310 REG.7 = 010324 REG.9 = 010340 REG.9 = 010340 REG.9 = 010340 REM.TB 013434 REZ 004144 REZ 004144 RES 00414 RES	TST.LN 016146 TWO.FM= 006024 T\$ARGC= 000003 T\$ERRN= 000000 T\$EXCP= 000000 T\$EXCP= 000000 T\$FREE 105132 G T\$GMAN= 000000 T\$HILI= 000000 T\$LOLI= 000000 T\$LOLI= 000000 T\$NEST= 177777 T\$NSO = 000000 T\$NS1 = 000000 T\$NS1 = 000000 T\$NS1 = 177777 T\$NSO = 000000 T\$NS1 = 000000 T\$SAML= 177777 T\$SEGL= 177777 T\$SEGL= 177777 T\$SEGL= 177777 T\$SLEN= 000000 T\$TTAGN= 010005 T\$TEST= 000000 T\$TSTM= 177777 T\$TSTAGN= 010005 T\$TSTM= 177777 T\$TSTSTS= 000000 T\$TSTM= 177777 T\$TSTSTS= 000000 T\$TSTM= 177777 T\$TSTSSM= 010002 T\$SHAR= 010002 T\$SHAR= 010002 T\$SHAR= 010000 T\$SSHAR= 010000 T\$SSHAR= 010000 T\$SSHAR= 010000 T\$SSOF= 010003 T\$SSW = 010001 T1 033014 G T10 033014 G T11 033360 G T13 035042 G T14 033610 G T15 033670 G T13 035042 G T14 036310 G T15 037556 G T16 040456 G T17 041652 G T18 042110 G T19 043260 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T21 044614 G T22 045262 G T23 045620 G T23 057500 G	T33	WRD .30= 006624 WRD .31= 006632 WRD .32= 006646 WRD .33= 006654 WRD .34= 006660 WRD .35= 006666 WRD .36= 006700 WRD .38= 006710 WRD .38= 006716 WRD .40= 006726 WRD .41= 006732 WRD .42= 006746 WRD .43= 006764 WRD .43= 006764 WRD .44= 006764 WRD .45= 006772 WRD .46= 007004 WRD .48= 007026 WRD .50= 007036 WRD .50= 007046 WRD .51= 006330 WRD .50= 007046 WRD .52= 007046 WRD .53= 007074 WRD .54= 007104 WRD .55= 007114 WRD .56= 007122 WRD .56= 007122 WRD .58= 007140 WRD .68= 007252 WRD .67= 007260 WRD .68= 007272 WRD .67= 007310 WRD .74= 007316 WRD .74= 007316 WRD .74= 007316 WRD .74= 007316 WRD .74= 007346 WRD .76= 007340 WRD .76= 007340 WRD .76= 007346 WRT .DS 022764 WRT .DS 022764	WRT.D2 022102 WRT.D3 022414 WRT.EE 021526 WRT.EL 021464 WRT.ER 017640 WRT.E1 020560 WRT.E2 020766 WRT.PA 020352 WRT.PA 020352 WRT.PA 021220 WRT.PA 013126 W.C.S.I 013122 XOR.LN 016642 XSALWA= 000000 XSFALS= 000040 XSFRUE= 000020 SEND.L 105134 G SSAVE3 00406 G SSAVE3 00406 G SSAVE3 00406 G SSAVE5 004116 G ST1 025670 ST10 032366 ST11 033030 ST12 033274 ST13 033704 ST14 035056 ST15 036324 ST16 037572 ST18 041666 ST19 042124 ST2 026072 ST18 041666 ST19 042124 ST2 026072 ST20 043274 ST18 041666 ST19 042124 ST2 026072 ST20 043274 ST31 044234 ST21 044234 ST22 04630 ST23 045276 ST24 045634 ST27 050712 ST28 051470 ST28 051470 ST29 052454 ST31 054362 ST31 054362 ST32 055756 ST31 054362 ST33 057514 ST34 060526 ST35 063152 ST38 064060 ST39 065064

CZMLAB	0 ML-11 LOGIC	TEST	MACRO M1113	22-DEC-80	12:16 PAGE	8 523-4				SEQ 0514
\$74 \$740 \$741 \$742 \$743 \$744	026544 065512 066500 067532 070304 071240	\$145 \$146 \$147 \$148 \$149 \$15	071450 071634 072332 072624 073516 027236	\$150 \$151 \$152 \$153 \$154	074222 074640 075712 076464 077264	\$155 \$156 \$157 \$158 \$159	100306 101156 102260 103502 104106	\$76 \$760 \$17 \$18 \$19	027660 104554 030316 031146 032132	

. ABS. 105136 000000 000 ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 34695 WORDS (136 PAGES)
DYNAMIC MEMORY: 21558 WORDS (82 PAGES)
ELAPSED TIME: 00:11:10
ML11,ML11/-SP/CR:SYM=SVC/ML,CZMLAB.DOC,ML2.P11,B16PG1.P11,B16PG2.P11,B16PG3.P11,B16PG4.P11,B16MUL.P11,B16SAV.P11,ML3.P11,ML4.P11

L11 YMBOL	CREATE CROSS REFE VALUE	RENCE			-DEC-80 AT	12:19	PAGE 1	8					SEQ 051
DR RR.DA	VALUE = 000020 = 010570	G	#84-21 #88-23 315-1	189 189 13685	204-8071 321-14034 519-24294	209-8320 322-14055	215-8617 322-14076	223-9002 323-14097	271-11447 323-14114	272-11511 334-14717	273-11587 390-17650	309-13418 396-17972	
RR.IN RR.16	011562 011574		REFERE #84-21 #88-23 315-1 419-1 #79-18	19216 180 16136	*186-7192 362-16154 *186-7182	*186-7212 364-16301 *186-7193	289-12395 365-16319 *186-7213	289-12436 412-18824 *186-7214	290-12442 412-18828 289-12437	332-14603	342-15126	342-15135	
SYNC	011574 = 000010 = 010464		4-13 #88-23 230-5 242-1 250-1 258-1 288-1 298-1 431-1 #79-19	387 9358 10007 10404 10844 11765 12377	4-13 196-7647 230-9378 243-10028 250-10439 259-10867 281-11997 289-12410 302-13025 431-19827 98-2891 101-2991	200-7860 233-9527 243-10063 251-10462 259-10891 282-12018 290-12456 328-14399 431-19843	204-8053 236-9704 244-10086 251-10486 264-11107 282-12036 293-12589 347-15410 434-19988		215-8597 237-9742 248-10330 257-10754 264-11142 283-12105 293-12621 366-16403 505-23579 *453-21006 *458-21246	216-8659 241-9954 249-10349 257-10771 265-11163 284-12151 294-12641 413-18898 516-24126 454-21036 458-21268	222-8989 242-9973 249-10366 257-10788 265-11181 288-12344 297-12800 429-19737	229-9334 242-9990 249-10383 258-10809 265-11199 288-12360 298-12823 430-19761	
.CAL .GEN !TO !TO0 !TO1 !TO2	013140 013146 = 000001 = 000002 = 000004	9999	431-1 #79-19 #79-19 #84-21 #84-21 #84-21	19804 904 907 173 163 162	431-19827 98-2891 101-2991	431-19843 *98-2892 101-3015	434-19988 +98-2893 +453-21003	496-23137 101-3015 454-21029	505-23579 *453-21006 *458-21246	516-24126 454-21036 458-21268	*458-21249	459-21279	
.CAL .GEN .ITO .ITO0 .ITO1 .ITO2 .ITO3 .ITO4 .ITO5 .ITO6 .ITO7 .ITO8 .ITO9 .IT1 .IT10 .IT11	= 000001 = 000002 = 000004 = 000010 = 000020 = 000000 = 000000	9999999	#84-21 #84-21 #84-21 #84-21	160 159 158 157 156									
1109 111 1110 1111 1112 1113	= 001000 = 000002 = 002000 = 004000 = 010000 = 020000	99999	#84-21 #84-21 #84-21 #84-21	172 153 152 151									
1712 1713 1714 1715 172 173 174 175	= 040000 = 100000 = 000004 = 000010 = 000020	99999	#84-21 #84-21 #84-21 #84-21	149 148 171 170 169									
117 118 119	= 000040 = 000100 = 000200 = 000400 = 001000	99999	#84-21 #84-21 #84-21 #84-21 #84-21 #84-21 #84-21 #84-21 #84-21	167 166 165 164	102-3086	483-22457	489-22772	523-24472					
LSDIV LSGT1 LSGT2	003016 003140	Ğ	#9-143 #15-15	53	396-17964 94-2707	523-24471			283-12066	284-12145	307-13305	309-13388	
			#9-143 #15-15 321-1 379-1 #523-2 #38-30	13988 17045	102-3086 396-17964 94-2707 334-14689 389-17624 523-24440 103-3094	483-22457 523-24471 98-2863 341-15084 390-17637 523-24444 483-22496	103-3112 347-15383 419-19205	281-11991 353-15715 448-20756	283-12066 355-15828 483-22481	284-12145 362-16179 490-22822	307-13305 365-16343 523-24470	309-13388 372-16699	
L\$LAS	105126 003764	G	#523-2	24439	523-24440	523-24444	489-22776	523-24472					

MBOL MBOL SPUT SPU2 SSHF	CREATE CROSS REFE VALUE 003526 003302 003376 003776	G	REFERENCES #34-128 #21-140 #28-183 9-155 16-207 #39-338 #84-2193 #79-1905 #79-1908 #97-2832 #79-1884	495-23053 453-20991 101-2990 9-158 21-143	523-24472 523-24471 101-2999 9-162 21-147	101-3008 9-166	283-12099 10-191 22-180	390-17671 16-175	484-22513 16-178 28-196	490-22793 16-182	523-24471 16-186 29-234	SEQ O
		G	16-207 #39-338 #84-2103	21-143	21-147	9-166 21-153	22-180	16-175 28-192	28-196	16-182 28-202	29-234	
ECAL GEN L.CR IP.S AU AUTO BRK BSEG BSUB CEFG CLCK CLEA CLOS CLP1 CVEC DCLN DODU	- 00000 013142 013150 014612 011570 = 000052 = 000061 = 000022 = 000045 = 000045 = 000035 = 000036 = 000044 = 000051		#79-1905 #79-1908 #97-2832 #79-1884 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13	98-2897 101-3000 453-21009 *186-7191	*98-2898 101-3017 458-21252 *186-7211	*98-2899 *453-21004 520-24311	101-3017 454-21030	*453-21007 *458-21247	454-21037 458-21269	*458-21250	459-21280	
AUTO BRK BSEB BSEB CLCA CLOS CLCA CLCA CLCA CLCA CLCA CLCA CLCA CLC	= 000400 013142 013150 014612 011570 = 000052 = 000061 = 000022 = 000045 = 000045 = 000035 = 000036 = 000051 = 000053 = 000055 = 000056 = 000056 = 000057 = 000056 = 000057 = 000057 = 000050 = 000050		#4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13	4-65								

ML.	BOL.	CREATED BY	MACRO	ON 2	2-DEC-80 AT	12:19	PAGE 4 CREF	. 8					SEQ 0518
ERF EVL ESE	BOL L.CH ND OAD	VALUE 015212 = 000004 G = 002100	#100- #84-2 #4-13	ENCES 2974 187	453-21010	458-21253							
ES.	TEM	= 002100 = 000035 = 011156	#4-13 #88-2 *371-	399 16683 22799	4-65 •94-2693 •378-17029	*328-14370 *389-17609	*333-14673 *419-19193	340-15017 *448-20750	*341-15071 *484-22516	*347-15371 484-22522	*353-15703 *489-22747	*355-15816 *489-22748	
FIN	D.C RST. /.FM	015430 016040 = 006062	#102- #108- #85-2 242- 250- 259- 282- 293- 407- #85-2	3081 3333 235 10014 10411 10860 12025 12612 18580 205	464-21514 241-9948 193-7503 243-10035 250-10451 259-10874 283-12079 294-12632 412-18840 201-7875	465-21604 248-10324 215-8624 243-10075 251-10469 259-10903 284-12116 294-12648 426-19530 497-23167	256-10729 237-9715 244-10093 251-10498 264-11115 288-12351 298-12811 431-19834 507-23694	297-12792 237-9732 244-10122 257-10746 264-11130 288-12368 298-12830 434-19995 511-23899	313-13613 237-9749 248-10337 257-10761 265-11153 289-12388 298-12851 466-21647 516-24140	327-14320 241-9961 249-10356 257-10778 265-11171 289-12417 328-14406 471-21926	327-14342 242-9980 249-10373 257-10795 265-11189 290-12463 348-15421 478-22220	354-15769 242-9997 249-10390 258-10816 273-11594 293-12596 379-17065	
FMT FMT FMT FMT FMT FMT	.10 .11 .12 .13 .14 .15 .16 .17 .18 .19 .20	= 004676 = 004750 = 005004 = 005034 = 005110 = 005150 = 005216 = 005306	#85-2 #85-2 #85-2 #85-2 #85-2 #85-2	214 215 216 217 218 219 220 221	308-13359 193-7508 298-12816 310-13455 366-16415 402-18313 205-8094 187-7243		298-12856	223-9018	271-11466	272-11530	274-11605		
		= 005342 = 005412 = 004266 = 005462 = 005534	#85-2 #85-2 #85-2 #85-2	222 223 206 224 225	454-21031 200-7824 454-21038 464-21531	459-21274 216-8669 459-21281 464-21555	497-23184 233-9544 466-21625		383-17293 471-21907			478-22226	
AY.	.22 .23 .24 .25	= 005606 = 005652 = 005722 = 005746 = 005764	#85-2	227 228 6928 229 230	483-22366 180-6861 180-6868 181-6935 226-9175 226-9185	180-6875 181-6942	180-6882 181-6949	180-6889 182-6961	180-6896 182-6970	181-6907 182-6977	181-6914	181-6921	
FMT FMT FMT FMT	.25 .26 .3 .4 .5 .6 .7 .8 .9 .1 .10 .11 .12 .13 .14 .15 .16 .17	= 005746 = 005764 = 004354 = 004430 = 004532 = 004562 = 004644 = 007726 = 010050 = 010060 = 010112 = 010136 = 010150 = 010162	#85-2 #85-2 #85-2 #85-2	207 208 209 210 211	277-11776 379-17072 419-19228 284-12164 520-24321 289-12422 233-9530 288-12347 288-12364 298-12807 353-15739 328-14401 342-15142 347-15413 364-16284		402-18320 430-19751	403-18355 430-19771					
FMI FNC FNC	.9 .1 .10	= 004644 = 007726 = 010050 = 010060		213 340 349 350	289-12422 233-9530 288-12347 288-12344	323-14131 520-24332 290-12468 288-12363 293-12592		413-18910 289-12413 293-12624	520-24307 290-12459 294-12644				
FNC FNC FNC	.12 .13 .14 .15	= 010100 = 010112 = 010122 = 010136	#87-2 #87-2 #87-2 #87-2 #87-2 #87-2 #87-2	351 352 353 354	298-12807 353-15739 328-14401 342-15142	288-12363 293-12592 298-12826 412-18839 364-16286	288-12380 293-12608 298-12847 425-19482 412-18844		425-19521				
FNC	.17	= 010150	#87-2 #87-2	356	347-15413 364-16284		412-18838	413-18903					

ML11 SYMBOL	CREATED BY CROSS REFERENCE	MACRO ON 22	P-DEC-80 AT	12:19	PAGE 5	1 8					SEQ 0519
FNC.18	VALUE = 010172	REFERENCES	390-17657	396-17979							
FNC.19 FNC.21 FNC.21	= 010204 = 007742 = 010216 = 010230	#87-2358 #87-2341 #87-2359	437-20111 237-9711 308-13352	237-9728	237-9745	282-12039	283-12075				
ML11 SYMBOL FNC.18 FNC.19 FNC.21 FNC.22 FNC.23 FNC.3 FNC.4 FNC.5	= 010242 = 007750 = 007756 = 007774	#87-2361 #87-2342 #87-2343 #87-2344 252-10519	190-7361 199-7806 241-9957 204-8076 257-10791	215-8620 200-7864 242-10010 210-8329 258-10812	273-11590 264-11111 243-10031 248-10333 259-10856 402-18271	265-11167 243-10071 249-10386 259-10870 402-18303	265-11185 244-10089 250-10407 259-10899 403-18342 260-10924	266-11210 244-10118 250-10447 271-11452 443-20485 271-11451	439-20252 444-20492 251-10465 272-11516	443-20483 251-10494 372-16734	
FNC.6	= 010004	#87-2345	193-7498	204-8075	210-8328	256-10738	260-10924	271-11451	272-11515	308-13347	
FNC.7 FNC.8	= 010012 = 010022 = 010036	383-17276 #87-2346 #87-2347	396-17973 264-11110 277-11767	407-18578 265-11166 277-11770	265-11184 282-12003	266-11209 282-12020	498-23214				
FNC.7 FNC.8 FNC.9 FORCE. FOR.EC	= 010012 = 010022 = 010036 015602 015760 = 006046	#87-2348 #104-3183 #106-3277 #85-2234 413-18904 471-21901	464-21507 504-23513 277-11771 425-19484 477-22191	465-21595 505-23556 282-12007 425-19506 497-23147	471-21883 506-23619 282-12043 431-19849 498-23217	477-22169 507-23665 302-13031 437-20115 511-23890	515-24072 302-13046 440-20287	310-13442 441-20326	315-13691 449-20780	366-16409 464-21549	
FSAUTO FSBGN FSCLEA FSDU FSEND	= 000015 = 000020 = 000040 = 000007 = 000016 = 000041	#4-13 #4-13 #4-13	4-39	5-226	5-274	5-305	5-337				
	= 000016 = 000041 = 000004	#4-13 #4-13 4-13 5-247 #4-13	4-13 4-13 5-288 5-226 4-119	4-13 4-13 5-337 5-247 4-139	4-13 4-13	4-13 4-13	4-13 4-13	4-13 4-13	4-13 4-13	4-13 4-39	
FSINIT FSJMP FSMCD	= 000006 = 000050 = 000000	#4-13 #4-13 #4-13	4-119	5-337							
F\$MSG F\$PROT F\$PUR	= 000011 = 000021 = 000017	#4-13 #4-13 #4-13	5-305	5-311							
FSHARD FSHW FSINIT FSIMP FSMOD FSMSG FSPROT FSRPT FSREG FSSOFT FSSEG FSSUB FSSW FSTEST GD.BLK	= 000006 = 000000 = 000011 = 000017 = 000012 = 000003 = 000005 = 000010 = 000002 = 000014 = 000001 016062	#4-13 #4-13 #4-13	5-274	5-288							
F\$SW	= 000002	#4-13 #4-13	4-150	4-162							
GD.BLK	= 000001 016062	#4-13 #109-3382 407-18540 443-20473 471-21884 511-23864 #79-1881	371-16645 407-18557 448-20720 476-22121 515-24075 91-2558	378-16995 418-19149 448-20725 477-22174 515-24086 109-3384	382-17240 424-19434 453-20999 482-22437	383-17261 424-19456 458-21242 484-22539	388-17568 440-20273 463-21480 489-22762	395-17920 440-20308 464-21508 490-22836	395-17937 441-20335 465-21596 495-23077	401-18242 443-20463 470-21835 510-23849	
GOOD.B G\$CNTO G\$DELM	011564 = 000200 = 000372	#79-1881 #4-13 #4-13	91-2558	109-3384	*186-7181	*341-15115					

ML11 SYMBOL SYMBOL GSDISP GSEXCP GSHILI GSLOLI	CREATED BY CROSS REFERENCE VALUE = 000003 = 000400	MACRO ON 22 REFERENCES #4-13 #4-13	2-DEC-80 AT	12:19	PAGE 6 CREF	N 8					SEQ 0520
CHILL	= 000002 = 000001 = 000000 = 000376 = 000001	#4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13	5-239 5-239 5-239 5-240 5-243	5-240 5-240 5-241	5-241 5-241 5-242	5-242 5-242 5-244	5-243 5-243	5-244 5-244	5-245 5-245	5-285 5-285	
GSMO GSOFFS GSOFSI GSPRMA GSPRMD CSRADA GSRADA GSRADD	= 000000 = 000140 = 000000 = 000040 = 000120 = 000020	04-13 04-13 04-13 04-13 04-13	5-243 5-243 5-243 5-239	5-245 5-245 5-245 5-240	5-242 5-285 5-285 5-241	5-244					
	= 000040 = 000120 = 000020 = 000004 = 000010 = 000000	#4-13 #4-13 #4-4 \$-228	5-239 4-8 5-249	5-240 4-30 5-276	5-241 4-48 5-291	5-242 4-67 5-313	5-243 4-105 5-330	5-244 4-121 5-340	5-245 4-152	5-285 #5-167	
HOE HW.OR. IBE IDU IER INTER	= 100000 G 011160 = 010000 G = 000040 G = 020000 G = 010672	#79-1874 #85-2201 #84-2190	+314-13632	314-13672							
INTER 10.BUF	011600	#88-2391 412-18833 #79-1889 339-14988 388-17524 407-18565 *417-19114 436-20096 *463-21479 *477-22163	340-15041 394-17878 4411-18786 4417-19115	252-10516 103-3098 362-16153 *395-17928 411-18799 *417-19116 *440-20292 464-21539 *482-22428 *515-24074	*103-3099 365-16318 396-17958 411-18808 *424-19433	108-3335 *371-16643 *401-18236 *412-18863 *424-19448	109-3385 *378-16994 *401-18237 412-18866 425-19474	353-15729 110-3434 *382-17239 *406-18530 *413-18881 425-19496 *453-20993 *471-21877 504-23516	364-16280 333-14623 «383-17257 «406-18531 413-18886 429-19728 «454-21051 471-21915 505-23570	373-16757 333-14647 383-17268 *407-18548 *417-19110 434-19982 *458-21238 *476-22120 506-23621	
ISR IXE ISAU ISAUTO ISCLN ISDU ISHRD ISHRD ISHRD ISHRSG ISPROT ISPTAB ISPWR ISRPT ISSEG ISSE TU ISSFT	= 000100 G = 004000 G = 000041 = 000041	*463-21479 *477-22163 507-23667 #84-2191 #62200 #4-13 #4-13 #4-13 #4-13 #4-13	#5-247				470 22044	304-23310	303-23310	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	
ISMSG ISPROT	= 000041 = 000040 = 000061	#4-13 #4-13 #4-13	4-39 #5-305	#4-39	5-337	#5-337					
ISPUR ISRPT ISSEG ISSETU ISSET	= 000041 = 000041 = 000041 = 000041 = 000041	#4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13 #4-13	#5-288								

PL11	CREATED BY	MACRO ON 22	2-DEC-80 AT	12:19	PAGE 7	9					
SYMBOL	CROSS REFERENCE	REFERENCES		(REF						SE
SYMBOL ISSRV ISSUB ISTST JSJMP	VALUE = 000041 = 000041 = 000167	#4-13 #4-13 #4-13									
JSJMP LAST.B LAU LAUTO	016114	#110-3431	352-15652 43-198 43-100 522-24416	352-15674	354-15787	430-19790					
LCLEAN LDU LD.LNG LINIT LOAD.S	004176 017124 024714 013606	#43-181 #43-83 #522-24397 #43-131 #121-3963 #185-7119 #88-2406	522-24416 43-148 339-15006 187-7270 94-2720	484-22519	490-22796						
LOE LOT LRPT LST.AR	= 040000 G = 000010 G 004152 011576	#88-2406 #85-2203 #84-2188 #43-32 #79-1888	43-40	*186-7198 412-18823	*186-7199 412-18827	*187-7225 504-23540	*187-7226	289-12394	289-12435	332-14602	
LST.BL	011572	#79-1888 342-15125 #79-1886 354-15757 506-23639 #79-1900	*186-7183 342-15134 110-3433 429-19715 507-23662	412-18823 *186-7184 430-19794 519-24274 *187-7239	*186-7199 412-18827 *186-7204 431-19813	*186-7205 434-19972	*186-7206 436-20097	*187-7233 504-23514	*187-7234 505-23563	*187-7235 506-23622	
LST.DU LSACP	013134 002110 G	#79-1900 #4-65	507-23662 182-6965	\$19-24274 *187-7239	*187-7241	193-7477					
L\$APT L\$AU	002036 G 004212 G	#4-65 4-65	#43-198								
L\$AUTO	002070 G 004166 G 002106 G 105116 G	4-65	#43-100								
L\$CCP L\$CLEA L\$CO	105116 G 002032 G	4-65 #4-65	#522-24415								
LSDEPO LSDESC LSDESP LSDEVP LSDISP LSDLY	002032 G 002011 G 002130 G 002076 G	#4-65 4-65 #4-65	#4-87								
L\$DISP L\$DLY	002060 G 002164 G 002116 G	#4-65 #4-65 #4-65 #4-65 #4-65 #4-65 #4-65 #4-65 #4-65 #4-65 #4-65 #4-65 #4-65 #4-65 #4-65 #4-65	#4-103 93-2641 258-10824 328-14361 347-15362 371-16662 419-19184	93-2667 264-11096 333-14629 352-15657 371-16674 448-20730	93-2680 301-13010 333-14652 352-15678 377-16973 448-20741	190-7347 307-13284 333-14664 353-15694 378-17008 483-22463	196-7636 314-13623 340-15022 354-15791 378-17020 484-22526	200-7841 314-13647 340-15045 355-15807 389-17589 490-22804	236-9693 314-13660 340-15058 362-16159 389-17600	243-10043 327-14325 346-15326 365-16324 418-19129	
L\$DTP L\$DTYP	002040 G 002034 G 004200 G	418-19165 #4-65 #4-65	419-19184	448-20730	448-20741	483-22463	484-22526	490-22804			
LSDUT LSDUTY	004200 G 002072 G	4-65 #4-65	#43-148								
LSEF	002122 G 002052 G	4-65 #4-65	#4-80								
LSENVI LSERRT LSETP LSEXP1 LSEXP4 LSEXP5	004200 G 004200 G 002072 G 002122 G 002052 G 002044 G 002152 G 002102 G 002046 G 002066 G	4-65 #4-65 #4-65 #4-65 #4-65 #4-65 #4-65	#4-96								

ML11	CREATED BY	MACRO ON 22	2-DEC-80 AT	12:19	PAGE 8	9					
SYMBOL	CROSS REFERENCE				CREF						S
SYMBOL SYMBOL LSHARD LSHIME LSHPCP LSHPTP	002402 G 002120 G 002016 G	REFERENCES 4-65 #4-65	5-226	#5-226							
LSHW	002022 G	#4-65 4-65	4-119	#4-119							
LSICP	002104 G 025660 G	#4-65 4-65	#187-7270								
LSLADP LSLAST LSLOAD LSLUN LSMREV LSNAME	= 105132 G 002100 G 002074 G 002050 G 002000 G	#4-65 #4-65 #4-65 #4-65 #4-65	#523-24444								
LSPRIO LSPROT LSPRT LSREPP	002042 G 002750 G 002112 G 002062 G	#4-65 #4-65 #4-65	#5-305								
LSLADP LSLAST LSLOAD LSLUN LSMREV LSPRIO LSPRIO LSPRIO LSPRIO LSPRIO LSPRIO LSPRIO LSPRIO LSPRIO LSPRIO LSPRIO LSSPIP LSSPIP LSSPIP LSSWI LSTEST	002010 G 004154 G 002720 G 002056 G 002020 G 002024 G	#4-65	#43-49 5-274	#5-274							
L\$STA L\$SW L\$TEST	002030 G 002376 G 002114 G	#4-65 #4-65	4-150	#4-150							
L\$TIML L\$UNIT L10000 L10001 L10002 L10003 MEM.AR	002014 G 002012 G 002374 002400 002476 002726 = 010630 013602	#4-65 #4-65 4-119 4-150 5-226 5-274	185-7113 #4-139 #4-162 #5-247 #5-288	185-7129	186-7165						
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ML11 CREATED BY SYMBOL CROSS REFERENCE	MACRO ON 22-DEC-80 A	12:19 PAG	GE 11 F	9					SEQ 0525
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PHR.1 PHR.2	= 007404	#87-2326 237-9 259-10858 259-10	74 226-9184 13 242-9995 901 282-12041	243-10073 283-12077	244-10120 284-12114	249-10371 288-12349	250-10449 293-12594	251-10496 298-12809	257-10776 431-19832	
PHR.3	3 = 007422 = 007454	#87-2328 190-7 273-11593 315-1	60 199-7808 687 328-14404	204-8078 348-15419	210-8331 353-15733	215-8623 372-16738	216-8661 373-16764	271-11454 402-18273	272-11518 402-18305	
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PHR.6 PHR.7 PHR.8 PHR.9 PNT PRIO0 PRIO1 PRIO2 PRIO5 PRIO5 PRIO7 PRSN PTBL.	= 007516 = 007540	#87-2328 190-7 273-11593 315-1 403-18344 407-1 #87-2329 193-7 250-10409 252-1 265-11169 265-1 429-19740 464-2 #87-2330 242-9 #87-2331 284-1 #87-2332 425-1 #87-2333 425-1 #87-2333 425-1 #84-2194 #84-2185 #84-2186 #84-2181 #84-2181 #84-2181 #84-2180 #84-2180		257-10756	264-11144	284-12157				
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ML11 SYMBOL	CREATED BY	MACRO ON 2	2-DEC-80 AT 12:19	PAGE 13 CREF	Н	9			SEQ (
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#65-1084 #65-1097 #65-1116 #65-1116 #65-1126 #66-1138 #66-1138 #66-1138 #67-1223 #67-1223 #67-1223 #67-1223 #67-1223 #67-1223 #67-1223 #67-1223 #67-1237 #68-1250 #68-1250 #68-1250 #68-1251 #68-1251 #68-1251 #68-1317 #69-1317 #69-1317 #69-1317 #69-1317 #69-1317 #69-1329 #69-1333 #69-1336 #69-1336 #69-1336 #69-1336 #69-1336 #69-1336 #69-1336 #69-1336 #69-1336

P.AAS P.AAT P.AAU P.AAV P.AAX P.AAY P.AAZ P.ABA P.ABB P.ABC P.ABD P.ABE P.ABF P.ABG P.ABH P.ABI P.ABJ P. ABK P.ABL P.ABM

P.ABN

P.ABO P.ABP P.ABQ

P.ABR P.ABS P.ABU P.ABV P.ABW P.ABW

P.ABY P.ABZ P.ACA

P.ACB P.ACC P.ACD P.ACE 0527

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.AEN	007504	#73-1558	87-2330				
AED AEP	007516	#73-1568	87-2331 87-2332				
.AEQ	007552	#73-1572	87-2333				
.AER	007564	#74-1584	87-2334 87-2335				
.AET	007614	#74-1589	87-2336				
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.AFF	010036	#75-1644	87-2348				
.AFG	010050	#75-1651	87-2350				
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.AFJ	010112	#75-1664	87-2353				
.AFL	010136	#75-1668	87-2354				
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-AFO	010172	#75-1679	87-2357				
AFQ	010216	#75-1687	87-2359				
AFR	010230	#75-1691	87-2360				
AFT	010256	#76-1703	87-2362				
AFU	010266	#76-1706	88-2367				
AFW	010302	#76-1710	88-2369				
AFK AFL AFM AFO AFP AFQ AFR AFS AFT AFV AFV AFY AFX AGA AGB AGC AGD AGE	010122 010136 010150 010162 010172 010204 010216 010230 010242 010256 010266 010274 010302 010310 010316 010332 010340 010346 010354 010362	#75-1661 #75-1664 #75-1668 #75-1672 #75-1676 #75-1679 #75-1687 #75-1687 #76-1699 #76-1708 #76-1708 #76-1710 #76-1710 #76-1716 #76-1716 #76-1716 #76-1720 #76-1720	87-2352 87-2353 87-2354 87-2355 87-2356 87-2357 87-2358 87-2359 87-2360 87-2361 87-2362 88-2367 88-2367 88-2368 88-2370 88-2371 88-2371 88-2373 88-2375 88-2376 88-2376				
.AFZ	010324	#76-1716	88-2372				
AGA	010332	#76-1718	88-2373 88-2374				
.AGC	010346	#76-1722	88-2375				
.AGD	010354	#76-1724	88-2376				

ML11 SYMBOL	CREATED BY	MACRO	ON 2	2-DEC-80 AT	12:19	PAGE 16	K 9					cı
SYMBOL SYMBOL P.AGF P.AGG P.AGH P.AGJ P.AGU P.AGN P.AGO P.AGO P.AGC P.AGC P.AGC P.AGC P.AGC P.AGC P.AGC P.AGC P.AGC P.AGC	CROSS REFERENCE VALUE 010370 010376 010404 010412 010420 010430 010436 010444 010454 010454 010526 010570 010630 010672 010772 011026 011060 011106	REFER	728	88-2378 88-2379 88-2380 88-2381 88-2382 88-2383 88-2385 88-2386 88-2387 88-2388 88-2389 88-2390 88-2391 88-2392 88-2393		CREF						SI
P.AGX P.CAL P.GEN RAS.IN RD.CS1	013144 013152 013124 017532	#78-11 #79-19 #79-11 #79-11	853 906 909 895 4179	88-2396 98-2885 101-2985 *186-7190 177-6706	*98-2886 101-3013 *186-7210 222-8945	*98-2887 *453-2100 341-1511	101-3013 5 454-21028 7 411-18787	*453-21008 *458-21248 411-18797	454-21035 458-21267	*458-21251	459-21278	
RD.DA RD.DAT	013130	#131- #79-18 *137- *170- #170- #157- #161-	4494 897 4826 5493 6369	177-6716 *125-4194 137-4827 *154-5625 170-6370	222-8959 125-4195 *140-4986 154-5626 204-8083	*128-4352 140-4987 *158-5810 210-8336	128-4353 *144-5170 158-5811 215-8628	*131-4509 144-5171 *162-5998 223-9011	131-4510 *148-5356 162-5999 271-11459	*134-4667 148-5357 *167-6211 272-11523	134-4668 *151-5492 167-6212 274-11602	
RD.DS RD.D1 RD.D2 RD.D3 RD.EE RD.EL RD.E1 RD.E2 RD.PA RD.PA RD.PA REG.10 REG.11 REG.12 REG.13 REG.14 REG.15	022766 021654 022166 022514 021530 021466 017710 020644 021064 020244 020436 021326	#170- #157- #166- #154- #151- #128- #140- #144- #134- #134- #134- #176- #79-19	5354 5781 5969 5179 5623 5490 4337 4970 5153 4652 4810 5339	178-6760 178-6765 178-6770 178-6775 177-6746 177-6751 177-6711 177-6731 177-6731 177-6731	222-8952 222-8973 222-8980 222-8966							
RD.REG REG.IN REG.1 REG.10	023462 013136 = 010256 = 010346 = 010354	#176-0 #79-19 #87-2 #88-2 #88-2	6679 902 362 375 376	177-6741 204-8044 157-5787 180-6867	209-8293 161-5975	215-8584 166-6186	271-11442 *273-11555	272-11506 *273-11577	273-11582 *274-11622			
REG. 12 REG. 13 REG. 14 REG. 15 REG. 16 REG. 17 REG. 18	020644 021064 020244 020436 021326 023462 013136 = 010256 = 010346 = 010354 = 010370 = 010376 = 010404 = 010412 = 010430	#88-2 #88-2 #88-2 #88-2 #88-2 #88-2	578 578 579 580 581 582 583	181-6948 182-6960 180-6895 180-6874	497-23160 504-23528 190-7362	498-23210 505-2358	507-23687					

NL11 SYMBOL	CREATED BY CROSS REFERENCE	MACRO ON 2	2-DEC-80 AT	12:19	PAGE 17	. 9				
SYMBOL REG. 19 REG. 2 REG. 20	VALUE = 010436 = 010266 = 010444	REFERENCES #88-2384 #88-2367 #88-2385	180-6881 181-6906 182-6969	282-12024	ner					
REG.21	= 010454 = 010274	#68-2366 #68-2368	182-6976 181-6913	266-11205						
REG.5 REG.6 REG.7	= 010302 = 010310 = 010316 = 0103324	#88-2370 #88-2371 #88-2372	181-6920 180-6888 181-6934	283-12107 199-7803 216-8662	284-12154 430-19765	431-19808	431-19829			
REG.9 REM. TE RE2 RE3	= 010340 013434 004144	#88-2374 #82-2080 42-66 42-74	181-6941 98-2883 #42-96	98-2889	98-2895	105-3204	105-3209	105-3213		
SYMBOL SYMBOL REG.19 REG.20 REG.21 REG.3 REG.4 REG.5 REG.6 REG.7 REG.8 REG.9 REM.TE RE2 RE3 RE4 RH.ADD RH.ERR RH.TYP RH.VEC SC.SE1 SEV.FM SFPTBL	004140 013572 = 011060 013574 013576	42-83 #83-2137 #88-2395 #83-2138 #84-2143	#42-94 *185-7137 190-7358 *185-7139 *185-7141	185-7148 193-7496						
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SIX.FR	= 006100	#84-2143 #88-2393 #85-2237 #4-150 #85-2236 284-12159 390-17658 #79-1891 402-18295 #79-1890 #93-2647 #4-13	199-7809 308-13353 396-17980 *90-2496 402-18318 88-2409 97-2836	204-8079 309-13428 402-18276 *90-2497 403-18335 *93-2653 401-18248	210-8332 322-14046 402-18308 *90-2509 403-18353 94-2712	252-10524 322-14063 403-18347 *90-2510	260-10929 322-14084 444-20493 97-2843	266-11211 323-14105	271-11455 323-14122	272-11519 373-16765
STACK	012612	#79-1891 402-18295	*90-2496 402-18318	*90-2497 403-18335	*90-2509 403-18353	*90-2510	97-2843	101-2984	401-18260	402-18282
STK.OF STRIPF	014214	#79-1890 #93-2647	88-2409 97-2836	*93-2653 401-18248		*94-2716				
SVCGBL	= 17////	#4-13 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65	#4-22 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65	4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65	4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65	4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65	4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65	4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65	4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65	4-65 4-65 4-65 4-65 4-65 4-65 4-65 4-65
		4-103 4-150	4-80 4-103 4-150	4-87 4-119 4-150	4-119 4-150	4-87 4-119 4-150	4-96 4-119 5-226	4-96 4-119 5-226	4-96 4-119 5-226	4-103 4-150 5-274
SVCINS	= 177777	5-274 #4-13 4-65 4-65 4-65 4-65	5-274 #4-19 4-65 4-65 4-65 4-65	5-305 4-65 4-65 4-65 4-65 4-65	5-305 4-65 4-65 4-65 4-65 4-65	5-305 4-65 4-65 4-65 4-65 4-65 4-80	4-65 4-65 4-65 4-65 4-65 4-80	4-65 4-65 4-65 4-65 4-87	4-65 4-65 4-65 4-65 4-87	4-65 4-65 4-65 4-65 4-65

L11 YMBOL	CREATED BY CROSS REFERENCE VALUE	MACRO ON 22 REFERENCES	2-DEC-80 AT	12:19	PAGE 18 CREF	M 9					SEQ 053
		4-103 4-103 4-103 4-103 4-103	4-103 4-103 4-103 4-103 4-103	4-103 4-103 4-103 4-103 4-103	4-103 4-103 4-103 4-103 4-103	4-103 4-103 4-103 4-103 4-103	4-103 4-103 4-103 4-103 4-103	4-103 4-103 4-103 4-103 4-103	4-103 4-103 4-103 4-103 4-103	4-103 4-103 4-103 4-103 4-103	
		4-103 4-103 5-239 5-241 5-242 5-245	4-103 4-103 5-239 5-241 5-243 5-245	4-103 4-103 5-239 5-241 5-243 5-245	4-103 4-103 5-239 5-241 5-243 5-247	4-103 4-103 5-240 5-241 5-244 5-274	4-103 4-103 5-240 5-242 5-244 5-285	4-103 4-119 5-240 5-242 5-244 5-285	4-103 4-150 5-240 5-242 5-244 5-285	4-103 5-226 5-240 5-242 5-244 5-288	
VCSUB	= 177777 = 177777	#4-13 #4-13 5-247	#4-21 #4-23 5-247	4-139 5-288	4-139 5-288	4-139 5-288	4-162	4-162	4-162	5-247	
VCTST	= 177777 = 010526	#4-13 #88-2388 259-10896 425-19478 464-21520 491-22852	#4-20 204-8062 302-13040 425-19500 464-21543 497-23154	209-8311 372-16731 425-19519 465-21610 498-23211	215-8608 379-17058 437-20109 466-21640 504-23524	243-10068 383-17273 440-20281 471-21895 502-23483	244-10115 401-18264 440-20316 471-21919 511-23884	250-10444 402-18300 449-20774 477-22185 514-24121	251-10491 403-18339 454-21019 477-22209	258-10849 407-18573 458-21258 484-22551	
SLSYM EN.FM HR.FM	= 010000 = 006214 = 006034	#4-13 #85-2240 #85-2233 353-15734	190-7363 355-15847 464-21525	#4-162 200-7865 412-18847	#5-247 223-9007 419-19223	230-9343 430-19746 491-22857	230-9363 430-19766	230-9383 431-19809 504-23529	334-14722 439-20253	342-15145 454-21024	
IME.O RBLE. ST.LN	= 011106 = 011026 016146	458-21263 #88-2396 #88-2394 #113-3569	464-21525 443-20481 328-14376	466-21619	419-19223 484-22556 341-15092		497-23161 353-15723		507-23688	516-24131 419-19213	
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SCODE	= 000130	4-65 #5-239 5-240 #5-242 5-243 #5-245	4-65 5-239 #5-240 5-242 #5-243 5-245 #5-285	#4-65 #5-239 5-240 #5-242 5-243 #5-245 5-285	4-65 5-239 #5-241 5-242 #5-244 5-245	4-65 #5-239 5-241 #5-242 5-244 #5-245	5-239 #5-241 5-242 #5-244 5-245	#5-240 5-241 #5-243 5-244 #5-285	5-240 #5-241 5-243 #5-244 5-285	#5-240 5-241 #5-243 5-244 #5-285	
EXCP	= 000000 = 000000	#4-13 #5-239 5-244	5-239	#5-240	5-240	#5-241	5-241	#5-242	5-242	#5-244	
FREE GMAN HILI	105132 G = 000000 = 000007	523-24439 #4-13 #5-239	523-24440 5-239	#523-24441 #5-240	5-240	#5-241	5-241	#5_242	5-242	#5-2//	
LAST	= 000000 = 000000	5-244 #4-13 #5-239	5-239	#5-240				#5-242	5-242	#5-244	
LSYM	= 010000 = 177777	5-244	4-13 4-39 #4-139 #5-226	4-139 #4-39 4-150 5-226	5-240 4-162 4-39 #4-150 5-247	#5-247 4-119 4-150 5-247	5-241 5-288 #4-119 4-162 5-247	#5-242 4-119 4-162 #5-247	5-242 4-139 4-162 5-274	4-139 #4-162 #5-274	

NL11 SYMBOL SYMBOL	CREATED BY CROSS REFERENCE VALUE	MACRO ON A	22-DEC-80 A1		PAGE 19 CREF	N 9					SEQ 053
ISNSO	= 000000	5-311	5-288 5-311 5-337	5-288 #5-311	5-288 5-337	#5-288 5-337	5-305 5-337	#5-305 #5-337	5-305	5-311	
SNS1	= 000000 = 000021	#4-119 5-311	4-139	#4-150	4-162	#5-226	5-247	#5-274	5-288	#5-305	
SPTHV SPTMU SSAVL SSEGL STAGL STAGN	= 000000 G = 000000 = 1777777 = 1777777 = 0000000 = 1777777 = 010005	4-65 #4-13 #4-13 #4-13 #4-13 #4-13	#523-24445 4-119 5-274	4-119 5-274	#4-119	4-150	4-150	#4-150	5-226	5-226	
STEMP		#5-226 #4-103		5-274 4-103	#4-119 #5-274 #4-103 #4-103	4-150 5-305 4-103	4-150 5-305 4-103	#5-305 #4-103	4-103 4-103	4-103 4-103	
		#5-226 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103	4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103	4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103	#4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103	4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103	4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103	#4-150 #5-305 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103 #4-103	4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103	4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103	
STEST STSTM STSTS SSHAR SSHA	= 000000 = 1777777 = 0000000 = 010002 = 010000 = 010004 = 010001 026056 G 033014 G	#4-103 #4-103 #5-239 5-241 #5-242 5-244 #5-245 5-285 #4-13 #4-13 #4-13 #4-13 #5-226 #4-119 #5-305 #5-274 #4-103 4-103 4-103	4-103 #4-139 5-239 #5-241 5-242 #5-245 #5-288	4-103 4-139 #5-240 5-241 #5-243 5-244 #5-247 5-288	#4-103 #4-162 5-240 #5-241 5-243 #5-244 5-247 #5-311	4-103 4-162 #5-240 5-241 #5-243 5-244 #5-285 5-311	4-103 #5-239 5-240 #5-242 5-243 #5-245 5-285 #5-337	#4-103 5-239 #5-240 5-242 #5-243 5-245 #5-285 5-337	4-103 #5-239 5-240 #5-242 5-243 #5-245 5-285	4-103 5-239 #5-241 5-242 #5-244 5-245 #5-285	
STEST STSTM STSTS SSHAR SSHAP SSPRO SSSOF SSSW 11 10 111 112	= 010004 = 010003 = 010001 026056 G 033014 G 033260 G 033670 G	#5-274 #4-150 4-103 4-103 4-103 4-103	5-274 4-150 #190-7390 #231-9421 #234-9579 #238-9785	5-288 4-162							

-80 AT 12:19	PAGE 20 CREF	B 10
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ML11 SYMBOL	CREATED BY CROSS REFERENCE	MACRO ON	22-DEC-80 AT 12:1
SYMBOL T13 T14	VALUE 035042 G 036310 G	4-103 4-103	#245-10159 #252-10553
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T17 T18 T19	042110 G 043260 G	4-103 4-103 4-103	#274-11646 #278-11815 #285-12199 #194-7546
T20 T21	026322 G 044220 G 044614 G	4-103 4-103 4-103	#194-7546 #291-12500 #294-12670
123	045262 G 045620 G	4-103 4-103	#299-12892 #303-13079
T25 T26	046774 G 047552 G 050676 G	4-103 4-103 4-103	#311-13492 #316-13741 #324-14171
T27 T28 T29	051454 G 052440 G 053552 G	4-103 4-103 4-103	#329-14436 #335-14768 #342-15171
T30	037556 G 040456 G 041652 G 042110 G 043260 G 026322 G 044220 G 044614 G 045620 G 046774 G 047552 G 050676 G 051454 G 053552 G 056530 G 054346 G 055742 G	4-103 4-103 4-103	#197-7684 #348-15447 #356-15896
T19 T20 T21 T22 T23 T24 T25 T26 T27 T28 T30 T31 T32 T33 T34 T35 T36 T37 T38 T37	057500 G 060512 G	4-103 4-103	#367-16449
T35 T36	062100 G 063136 G	4-103 4-103 4-103	#380-17121 #384-17337 #391-17715
T37 T38 T39	064044 G 065050 G 065476 G	4-103 4-103 4-103	#397-18028 #404-18406 #408-18630
T4 T40	027222 G 066464 G	4-103 4-103	#201-7908 #414-18939
T41 T42 T43	070270 6	4-103 4-103 4-103	#420-19279 #426-19559 #432-19884
T43 T44 T45 T46	071224 G 071434 G 071620 G 072316 G	4-103 4-103 4-103 4-103	#435-20027 #437-20137 #441-20360
T48	072610 G 073502 G	4-103 4-103	#444-20519 #450-20831 #455-31084
T5 T50	027644 G 074624 G	4-103 4-103	#205-8134 #459-21315
T52 T53	075676 G 076450 G 077250 G	4-103 4-103 4-103	#467-21689 #472-21972 #478-22263
149 15 150 151 152 153 154 155 156 157 158 159 16	071224 G 071434 G 071620 G 072316 G 072610 G 073502 G 074206 G 074624 G 075676 G 076450 G 077250 G 100272 G 101142 G 102244 G 103466 G 104072 G 104540 G 030302 G	4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103 4-103	#420-19279 #426-19559 #432-19884 #435-20027 #437-20137 #441-20360 #444-20519 #450-20831 #455-21086 #205-8134 #459-21315 #467-21689 #472-21972 #478-22263 #485-22604 #491-22897 #498-23246 #508-23729 #512-23931 #517-24173
T57 T58	103466 G 104072 G	4-103 4-103	#508-23729 #512-23931
16	104540 G 030302 G	4-103 4-103	#517-24173 #211-8391

ML11 SYMBOL	CREATED BY CROSS REFERENCE	MACRO ON 22	-DEC-80 AT		AGE 21	10					SEQ 0535
5YMBOL T60 T7 T8 T9 UAM	CROSS REFERENCE VALUE 105072 G 031132 G 032116 G 032352 G = 000200 G	REFERENCES 4-103 4-103 4-103 4-103	#521-24364 #217-8701 #224-9066 #227-9212								
WRD.1	= 000200 G = 006276	#85-2242 249-10369	237-9714	241-9960 251-10497	242-9976 257-10745	242-9993 257-10757	243-10074 257-10774	244-10121 259-10859	248-10336 259-10902	249-10352 264-11114	
WRD.10	= 006406	#86-2255 396-17977	223-9004 419-19220	308-13351 425-19480	334-14719 425-19502	242-9993 257-10757 298-12810 355-15844 434-19990	366-16405 444-20491	379-17062 449-20776	383-17279 471-21899	390-17655 477-22189	
WRD.11	= 006416	#86-2256 251-10495 283-12109	237-9712 259-10857 284-12156	237-9729 259-10871 288-12348	237-9746 259-10900 288-12365 234-10527	243-10072 264-11112 293-12593	244-10090 282-12004 293-12609	244-10119 282-12021 293-12625 434-19992	250-10448 282-12040 294-12645	251-10466 283-12076 298-12808	
	= 006426	#86-2257 248-10334 265-11186 308-13350 396-17976	193-7500 249-10387 271-11453 328-14403 402-18272	199-7807 250-10408 272-11517 348-15418 402-18304	204-8077 252-10520 273-11592 364-16285 403-18343	243-10072 264-11112 293-12593 431-19831 210-8330 256-10739 277-11768 372-16736 444-20490	215-8622 257-10792 288-12381 373-16762 466-21643	241-9958 258-10813 289-12414 379-17061 471-21922	242-10011 260-10925 290-12460 383-17279 477-22212	243-10032 265-11168 298-12848 390-17654	
WRD.13 WRD.14	= 006436 = 006442	#86-2258 #86-2259 431-19806	199-7804 200-7862 496-23139	266-11206 233-9529 498-23213	353-15732 266-11207 511-23886	342-15144	364-16288	412-18846	413-18900	430-19763	
WRD.15 WRD.16 WRD.17 WRD.18	= 006454 = 006462 = 006470 = 006502	265-11186 308-13350 396-17976 #86-2258 #86-2259 431-19806 #86-2260 #86-2261 #86-2261 #86-2263 #86-2264 244-10117	282-12006 282-12023 283-12108 294-12647	282-12042 289-12387 284-12155	283-12078 431-19830	284-12115					
WRD.19	= 006506	266-11208 293-12607	237-9710 248-10332 258-10811 282-12038 293-12623 390-17652	237-9727 249-10385 258-10851 283-12074 294-12643 396-17974	237-9744 250-10406 259-10869 288-12346 298-12806 401-18266	241-9956 250-10446 259-10898 288-12362 298-12825 402-18302	242-10009 251-10464 260-10923 288-12379 298-12846 403-18341	243-10030 251-10493 264-11109 289-12412 315-13688 407-18577	243-10070 252-10518 265-11165 290-12458 372-16733 425-19481	244-10088 256-10737 265-11183 293-12591 373-16759 425-19503	
WRD.2	= 006302	#85-2243 259-10873	242 -9979 264-11129	242 -9996 265-11152		249-10355	249-10372	251-10468	257-10760	257-10777	
WRD.20 WRD.21 WRD.22 WRD.23	= 006514 = 006522 = 006530 = 006544	#86-2265 #86-2266 #86-2267 #86-2268	242-9979 264-11129 252-10522 302-13027 328-14405 372-16735	242-9996 265-11152 260-10927 302-13030 348-15420 373-16761	302-13045 302-13042 372-16739 383-17277	402-18275	402-18307	403-18346	407-18579	440-20286	
WRD.24 WRD.25	= 006552 = 006560	#86-2269 #86-2270	322-14040 379-17063	322-14057 383-17280	322-14078	323-14099	323-14116	379-17064	383-17291	449-20778	
WRD.26 WRD.27	= 006574 = 006602 = 006610	#86-2271 #86-2272	437-20114 429-19741	431-19833	431-19845	431-19848					
WRD.30	= 006314 = 006624	#85-2244 #86-2274	237-9731 434-19991	242-10013 434-19994	249-10389	257-10794	265-11170	288-12367	293-12611	298-12829	
WRD.24 WRD.25 WRD.26 WRD.27 WRD.30 WRD.31 WRD.31 WRD.33 WRD.33	= 006552 = 006560 = 006574 = 006602 = 006610 = 006314 = 006624 = 006632 = 006654 = 006654	383-17275 426-19526 #85-2243 259-10873 #86-2265 #86-2266 #86-2268 441-20325 #86-2270 #86-2271 #86-2272 #86-2273 #85-2244 #86-2275 #86-2275 #86-2276 #86-2277	437-20114 429-19741 429-19739 237-9731 434-19991 430-19764 322-14043 322-14041 322-14045	242-10013 434-19994 431-19807 322-14060 322-14058 322-14062	322-14081 322-14079 322-14083	323-14102 323-14100 323-14104	323-14119 323-14117 323-14121				

ML11 SYMBOL C SYMBOL	CREATED BY	MACRO ON A	22-DEC-80 AT		PAGE 22	10					SEQ 053
WRD.35	VALUE = 006666 = 006674	#86-2279	308-13348 315-13689	315-13690	334-14721	519-24297					
WRD.37	= 006700 = 006710	#86-2281 #86-2282	199-7805	200-7863	223-9005		364-16283	366-16406	439-20250		
URD.39	= 006716 = 006322	#86-2283 #85-2245	366-16408 237-9748	402-18274 243-10034	402-18306 250-10410	403-18345 258-10815	407-18576 265-11188	289-12416	290-12462	294-12631	
WRD.40 WRD.41 WRD.42	= 006726 = 006732	#86-2284	309-13422	310-13438							
WRD.42 WRD.43 WRD.44	= 006746 = 006754 = 006764	#86-2286 #86-2287	309-13422 419-19222 309-13423 242-9977	310-13439 242-9994	249-10353	249-10370	257-10758	257-10775	264-11727	265-11150	
WRD .45 WRD .46 WRD .47 WRD .48 WRD .49	= 006772 = 007004 = 007016 = 007026	#86-2289 #86-2290 #86-2291 #86-2292	308-13349 309-13425 310-13440 347-15412	328-14402 310-13441	419-19221						
MRD.50 MRD.50	= 007036 = 006330 = 007046 = 007054	#85-2246 #86-2294	229-9338 355-15846 309-13424	322-14080 390-17656 355-15845 204-8074	323-14101 396-17978	323-14118 412-18837	454-21023 413-18902	458-21262			
WRD.52	= 007054	#86-2296 #86-2296	193-7499	204-8074	210-8327	215-8621	271-11450	272-11514	273-11591	497-23159	
RD.53 RD.54 RD.55 RD.56	= 007074 = 007104 = 007114	#86-2298	230-9393 309-13426								
MRD.56	= 007114 = 007122	#86-2300 412-1883	204-8073	210-8326	215-8619	271-11449	272-11513	273-11589	353-15738	364-16282	
MRD.57 MRD.58 MRD.59	= 007130 = 007140	#86-2301 #86-2302									
WRD.6	= 007152 = 006336	#86-2303 #85-2247	426-19529 230-9362	230-9382 413-18901	322-14042	322-14059	440-20285	441-20324			
MRD.60 MRD.61 MRD.62	= 007136 = 007174 = 007210	#86-2305 #86-2306	252-10523 193-7502	260-10928	464-21548	466-21646	471-21925	478-22219			
RD.63	= 007216 = 007224	#87-2311 #87-2312	449-20779	454-21022	458-21261						
IRD .63 IRD .64 IRD .65 IRD .67 IRD .68 IRD .69	= 00/232 = 007240 = 007244	#87-2313 #87-2314	466-21642	471-21900	471-21921	477-22211	497-23146	511-23889			
	= 007246 = 007252 = 006344	#87-2316 #85-2248	497-23144 229-9337	511-23887 230-9361	230-9381	230-9394	439-20251	440-20284	440-20319		
WRD.71	= 007200 = 007272 = 007302	#87-2318 #87-2319	507-23686 516-24130								
JRD. 70 JRD. 71 JRD. 72 JRD. 73 JRD. 74 JRD. 76 JRD. 76 JRD. 8 JRD. 8	= 007216 = 007224 = 007232 = 007240 = 007246 = 007252 = 006344 = 007260 = 007272 = 007310 = 007316 = 007340 = 007346 = 006360 = 006374	#86-2306 #87-2311 #87-2312 #87-2313 #87-2314 #87-2316 #87-2316 #87-2317 #87-2318 #87-2319 #87-2321 #87-2321 #87-2321 #87-2324 #87-2324 #87-2324 #87-2324 #87-2324	507-23686 516-24130 449-20777 464-21524 464-21523 471-21898 477-22190 230-9380 229-9336	464-21547 464-21546 477-22188	466-21618 465-21613 484-22554	466-21645 466-21644 491-22855	471-21924 471-21923	477-22214 477-22213			
MRD.8	= 006360 = 006374	#85-2249 #85-2250 491-2285	230-9380 229-9336	440-20318 230-9360	454-21021 372-16737	458-21260 373-16763	440-20283	471-21897	477-22187	484-22553	
		171 6607	•								

ML11 SYMBOL	CREATED BY CROSS REFERENCE	MACRO ON 22-DEC-80 AT 12:19 PAGE 23 CREF E 10	SEQ 0537
SYMBOL WRT.CS WRT.DA WRT.D1 WRT.D2 WRT.D3 WRT.EE WRT.EL WRT.ER WRT.ER WRT.ER WRT.PA WRT.PA WRT.RE WRT.RE	VALUE 017462 020016 022764 021570 022102 022414 021526 021464 017640 020560 020766 020174 020352 021220 023074 013126	REFERENCES #123-4082 173-6508 220-8875 221-8907 #129-4397 173-6518 220-8881 221-8915 #168-6268 174-6562 #155-5668 174-6567 #159-5858 174-6572 #163-6060 174-6577 #152-5542 173-6548	
WRT.EL WRT.ER WRT.E1 WRT.E2	021464 017640 020560 020766	#149-5407 174-6557 #126-4239 173-6513 220-8878 221-8911 #138-4870 173-6528 221-8891 221-8923 #141-5043 173-6533 221-8894	
WRT.PA	020352 021220	#135-4710 173-6538 221-8888 221-8919 #145-5228 173-6543 418-19147	
		#135-4710 173-6538 221-8888 221-8919 #145-5228 173-6543 418-19147 #172-6481 204-8039 209-8288 214-8562 271-11437 272-11501 273-11569 #79-1896 *125-4191 125-4195 *128-4349 128-4353 *131-4506 131-4510 *134-4664 134-4668 *137-4823 137-4827 *140-4983 140-4987 *144-5167 144-5171 *148-5353 148-5357 *151-5491 151-5493 *154-5624 154-5626 *158-5809 158-5811 *162-5997 162-5999 *167-6208 167-6212 *170-6366 170-6370 204-8084 210-8337 223-9012 271-11460 272-11524 #79-1893 *186-7189 *186-7209 411-18798 411-18807 413-18887 #117-3796 372-16708 379-17053 #4-13	
W.C.SI XOR.LN XSALWA XSFALS XSOFFS XSTRUE SEND.L SPATCH	013122 016642 = 000000 = 000040 = 000020 105134 G 002756 G 004044 G	#4-13 #4-13 #523-24451 #5-327	
\$SAVE2	004044 G	#523-24451 #5-327 34-128	7
\$SAVE3	004060 G	411-18785 443-20453 519-24273 523-24471 9-143 10-191 15-153 16-207 28-183 29-234 #42-68 88-2407 100-2975 104-3184 185-7119 226-9144 229-9318 233-9506 281-11971 288-12332 424-19426 510-23833	7
\$SAVE4 \$SAVE5	004076 G 004116 G	#42-76 117-3797 196-7624 203-8019 277-11747 429-19713 523-24470 35-196 39-371 #42-85 93-2648 97-2832 193-7475 199-7786 209-8271 214-8545 220-8863 270-11417 297-12778 306-13254 313-13604 320-13956 327-14305 332-14598 339-14973 346-15308 352-15638 361-16133 370-16607 377-16953 382-17228 387-17512 394-17865 401-18223 406-18519 417-19098 447-20675 453-20964 457-21208 463-21477 470-21832 476-22118 482-22423 489-22745 495-23041 504-23501 523-24470	5
\$T1 \$T10 \$T11 \$T12 \$T13 \$T14 \$T15 \$T16 \$T16 \$T17 \$T18 \$T19	025670 032366 033030 033274 033704 035056 036324 037572 040472 041666 042124	411-18785	