

# LPA11,AD11K

LPA/AD11-K DIAG TEST  
CRLPKB0

AH-B050B-MC  
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FICHE 1 OF 1

JAN 1980  
**digital**  
MADE IN USA

*(The following table is extremely faint and contains illegible data. It appears to be a diagnostic test grid with multiple columns and rows.)*

TEST NO.	TEST NAME	TEST RESULT	TEST UNIT	TEST DATE	TEST TIME	TEST OPERATOR	TEST COMMENTS
1	...	...	...	...	...	...	...
2	...	...	...	...	...	...	...
3	...	...	...	...	...	...	...
4	...	...	...	...	...	...	...
5	...	...	...	...	...	...	...
6	...	...	...	...	...	...	...
7	...	...	...	...	...	...	...
8	...	...	...	...	...	...	...
9	...	...	...	...	...	...	...
10	...	...	...	...	...	...	...
11	...	...	...	...	...	...	...
12	...	...	...	...	...	...	...
13	...	...	...	...	...	...	...
14	...	...	...	...	...	...	...
15	...	...	...	...	...	...	...
16	...	...	...	...	...	...	...
17	...	...	...	...	...	...	...
18	...	...	...	...	...	...	...
19	...	...	...	...	...	...	...
20	...	...	...	...	...	...	...
21	...	...	...	...	...	...	...
22	...	...	...	...	...	...	...
23	...	...	...	...	...	...	...
24	...	...	...	...	...	...	...
25	...	...	...	...	...	...	...
26	...	...	...	...	...	...	...
27	...	...	...	...	...	...	...
28	...	...	...	...	...	...	...
29	...	...	...	...	...	...	...
30	...	...	...	...	...	...	...
31	...	...	...	...	...	...	...
32	...	...	...	...	...	...	...
33	...	...	...	...	...	...	...
34	...	...	...	...	...	...	...
35	...	...	...	...	...	...	...
36	...	...	...	...	...	...	...
37	...	...	...	...	...	...	...
38	...	...	...	...	...	...	...
39	...	...	...	...	...	...	...
40	...	...	...	...	...	...	...
41	...	...	...	...	...	...	...
42	...	...	...	...	...	...	...
43	...	...	...	...	...	...	...
44	...	...	...	...	...	...	...
45	...	...	...	...	...	...	...
46	...	...	...	...	...	...	...
47	...	...	...	...	...	...	...
48	...	...	...	...	...	...	...
49	...	...	...	...	...	...	...
50	...	...	...	...	...	...	...

IDENTIFICATION

B 1

SEQ 0001

Product Code: AC-B049B-MC  
Product Name: CRLPKB0 LPA/AD11-K DIAG TEST  
Date: JAN 1979  
Revised: JULY 1979  
Maintainer: Diagnostic Group

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## 1.0 ABSTRACT

This diagnostic has two starting addresses: 200 for standard tolerances and 210 for tighter option test area tolerances.

This diagnostic tests the AD11K with or without a wraparound module (G5036).

When starting the diagnostic, a set of tests is listed and this statement is printed out: "Type the letter and carriage return of the desired test:". The following chart indicates which letter corresponds to which test:

- W: The entire Wraparound test (requires G5036 module)
  - a. Analog subtests
  - b. Noise test
  - c. Interchannel Settling test
  - d. Differential Linearity and Relative Accuracy test
- C: Calibration test only
- N: Noise test only
- S: Interchannel Settling only
- L: Logic Subtests only
- A: Auto test (requires G5036 module)
  - A. Logic subtests
  - B. Analog subtests
  - C. Noise Test
  - D. Interchannel Settling Test
  - E. Differential Linearity and Relative Accuracy Test

THIS PROGRAM IS A MODIFIED VERSION OF 'MD-11-DZADL-B' IT WAS MODIFIED TO ENABLE THE OPERATOR TO CHECK OUT THE AD 11K OPTION WHEN IT IS ON THE LPA11-KX I/O BUS. NO RECABLING IS NEEDED. SOME TEST DONE IN THE ORIGINAL DIAGNOSTIC SUCH AS ARBITRATION TEST, WERE DELETED AS THEY COULD NOT BE CHECKED. IF THIS DIAGNOSTIC DOESN'T FIND A SUSPECTED PROBLEM, YOU MAY HAVE TO RUN 'MD-11-DZADL-B' YOU SHOULD RUN 'MD-11-DRLPA' BEFORE RUNNING THIS DIAGNOSTIC. PLEASE READ SECTION 10.

## 2.0 REQUIREMENTS

### 2.1 Equipment

PDP-11 family computer with 8K of memory  
 Console terminal  
 AD11K Module installed in an LPA-11  
 Bit-map terminal <OPTIONAL>  
 G5036 Wraparound Module

## 2.2 Storage

This program uses all 8K of memory and is not "chainable" on an 8K CPU. The program is "chainable" on 12K or greater. The program will destroy "absolute loader" on an 8K CPU, if 'W' or 'A' is selected.

## 3.0 LOADING PROCEDURE

Procedure for loading normal binary tapes should be followed.

## 4.0 STARTING PROCEDURE

### 4.1 Control Switch Settings

Standard PDP-11 Format

SW15=1	Halt on error
SW14=1	Loop on test
SW13=1	Inhibit error typeouts
SW12=1	Halt for Bit map display
SW11=1	Inhibit iterations
SW10=1	Bell on error
SW9 =1	Loop on error
SW8 =1	Loop on test in SWR <7:0>

200 is the starting address of the diagnostic for standard tolerances. 204 is the restart address. 210 is the starting address of the diagnostic for the option test area's tighter tolerances. Starting address of the USER LINK loop is at 214.

## 5.0 OPERATING PROCEDURE

Start the diagnostic at 200 or 210. The program heading and the list of tests available, will be printed out followed by a message "Type the letter and carriage return for the desired test:". Then type the letter you want, according to the table listed and hit carriage return.

Two control characters, ^A and ^C, are set aside for interrupting a test and transferring control to either the beginning of the diagnostic (^C) or to the beginning of the specific test which was in progress (^A). During the logic tests while a reset is being performed, ^C or ^A will not be executed until after the reset has been completed, therefore hit ^C or ^A until it is successful.

For machines without a hardware switch register, location SWREG (176) is used as a software switch register. To modify the contents of SWREG, type ^G. The program responds with the current contents of SWREG and a slash. Type the desired new contents of SWREG followed by a carriage return.

If 'W' is typed, the program will type 'xx AD11K's FOUND'. Where xx is the number of AD11K's in octal. If the number is greater than 1, the test will be run successively on each AD11K. The program will run through the logic subtests, the Noise test on 8 edges, the Interchannel Settling test on 8 edges, and the Differential Linearity and Relative Accuracy test. A G5036 wraparound module is required. The program supports AD11K expansion beyond 16 channels. To run this test on a group of channels other than 0-17, load 20, 40, or 60 into location BASECH (1336) for channels 20-37, 40-57, 60-77.

If 'C' is typed, the program will run the calibration test and will loop on that test until the operator halts it. If a certain AD11K is to be tested, its status register address must be loaded into \$BASE (1250), and its vector address must be loaded into the low byte of \$VECT1 (1244) (the high byte containing the priority).

If 'N' is typed, the program will run the Noise test tagged 'BEGINN' and will loop on this test until the operator halts it. If a certain AD11K is to be tested its status register address must be loaded into \$BASE (1250), and its vector address must be loaded into the low byte of \$VECT1 (1244) (the high byte containing the priority).

If 'S' is typed, the program will run the Interchannel Settling test tagged 'BEGINS' and will loop on this test until the operator halts it. At the beginning of this test, the operator must respond to the statements asking for the 'FROM' channel and the 'TO' channel by typing in the channel value in octal and hitting carriage return. If a certain AD11K is to be tested its status register address must be loaded into \$BASE (1250), and its vector address must be loaded into \$VECT1 (1244) (the high byte containing the priority).

If 'A' is typed, the program will execute the logic tests, analog tests, noise, settle and differential linearity. At the beginning of the test the program will type 'XX AD11K's Found'. Where XX IS THE NUMBER OF AD11K's in octal. If the number is greater than 1, the test will be run successively on each AD11K. The program supports AD11K expansion beyond 16 channels. To run this test on a group of channels other than 0-17, load 20, 40, or 60 into location BASECH (1336) for channels 20-37, 40-57, 60-77.

If 'L' is typed, the program will execute the logic tests, printing 'END PASS' when it has completed an entire pass. At the beginning of the test the program will type 'XX AD11K's Found'. Where XX is the number of AD11K's in octal. If the number is greater than 1, the test will be run successively on each AD11K.

## 6.0 ERRORS

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This program uses the Diagnostic "SYSMAC" package for error reporting and typeout. The error information consists of the following:

ERRPC: Location at which an error was detected.  
STREG: Address of the status register.  
ADBUFF: Address of the buffer  
CHANL: Channel value  
NOMINAL: Expected correct data  
TOLERANCE: The acceptable deviation from the nominal  
ACTUAL: Actual data  
EXPECTED: Expected correct data

## 7.0 MISCELLANEOUS

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### 7.1 Execution Time

Execution time for each of the tests is:

Calibration:	8 conversions/5 seconds @ 110 baud
Wraparound Test:	17 minutes first pass; 35 minutes for successive passes
Settling Test:	1 minute
Noise Test:	1 minute
Logic Test:	1 minute
Auto Test:	18 minutes first pass, 36 minutes for successive passes

### 7.2 Status Register and Vector Addresses and Priority

When testing more than one AD11K, the difference in addresses is presently 40 for bus address and vector address. These values are in VADR (bus address) (1326) and VVCT (vector address) (1330). The first AD11K's status register address must be in \$BASE (1250), its vector address must be in the low byte of \$VECT1 (1244), and the priority must be in the high byte of \$VECT1.

### 7.3 AD11K Priority

If AD11K is set for a priority other than 6, the high byte of \$VECT1 (1244) must be adjusted accordingly (the low byte containing the vector address). If more than one AD11K is being tested, all must be set at the same priority.

#### 7.4 Switch Register

If a hardware switch register is present and the operator desires to use a software switch register and the ^G feature; it is necessary to load the starting address, set the hardware switch register to all ones (-1), and hit start. The program will then run with the software switch register.

#### 7.5 BIT-MAP Graphic Output

The screen display may be halted for examination by setting bit 12. And then just hit continue to complete the program's execution.

#### 7.6 USER LINK TO I/O DEVICE

A SPECIAL USER LINK HAS BEEN PROVIDED IN ORDER FOR THE OPERATOR TO EXAMINE OR MODIFY LOCATIONS ON THE LPA11-KX I/O BUS. (NOTE: THIS CANNOT BE DONE DIRECTLY.)

##### PROCEDURE:

- 1) START THE PROCESSOR AT LOCATION 214
- 2) THE DIALOG TO EXAMINE A LOCATION IS AS FOLLOWS:

```
E OR D      'E'
DEVICE ADDR= 'OCTAL ADDR'
XXXXXX
```

WHERE XXXXXX IS THE CONTENTS OF THE SPECIFIED LOC.

- 3) THE DIALOG TO MODIFY A LOCATION IS AS FOLLOWS:

```
E OR D      'D'
DATA=       'DATA TO BE DEPOSITED'
```

- 4) THE PROGRAM WILL STAY IN THIS LOOP UNTIL THE OPERATOR IS FINISHED. AT THIS TIME THE PROCESSOR SHOULD BE HALTED.

NOTE: THE OPERATORS RESPONSE IS ENCLOSED IN QUOTES.



8.0 RESTRICTIONS

SEQ 0008

- 8.1 A G5036 wraparound module must be present when running the auto test and the wraparound test.

Switch on G5036 must be in '0' position.

```
*****  
The wraparound (G5036) module must be connected as follows:  
AD11K TO BC08R CONNECTION A-A, VV-VV  
BC08R TO G5036 CONNECTION 'UPSIDE-DOWN' A-VV, VV-A  
*****
```

9.0 PROGRAM DESCRIPTION

## 9.1 Logic Tests

These 8 logic subtests run sequentially without further operator intervention after he/she has typed in the number of AD11K's to be tested. Its purpose is to check that each of the mux bits can be loaded and properly read back; that initialize clears the external start enable bit, the done bit, the interrupt enable bit, the overflow bit, the error flag, and the A/D start bit. It also checks that the A/D done flag sets at end of conversion and clears when the converted value is read. It checks the interrupt logic and the correct setting of the error flag.

## 9.2 Calibration Test

This test begins when the operator types 'C', it then loads the channel from the switch register bits 0-7 and does a conversion on that channel. If SWR bit 13 is down, it prints out the converted value on the teletype; otherwise, if SWR bit 13 is up, it puts the converted value in the display register. The operator may change the channel at any time during the test, however the new values from the new channel will not be printed until the next line of 8 values is printed. The 8 values on each line correspond to only one channel.

### 9.3 Differential Linearity

This test is to determine if a change in the input voltage represents a similar change in the resulting converted binary value.

### 9.4 Settling Test

The purpose of this test is to check that the time needed to settle and correctly report a new input value after switching channels does not exceed the expected amount of time for such a change.

### 9.5 Noise Test

This test measures the internal short-term repeatability noise within the A/D. RMS noise equals 1 standard deviation of the Gaussian curve, PEAK noise equals 2.3 standard deviation of the Gaussian curve.

### 9.6 Analog Tests

These 11 subtests check the channels and their output.

## 10. LPA11 (SYSTEM) DIAGNOSTIC SUMMARY

DIAGNOSTICS FOR THE LPA11 ARE WRITTEN AT THREE LEVELS: (1) TOTAL PDP-11 SYSTEM, (2) LPA11 SYSTEM; AND, (3) LPA11 OPTIONS.

LEVEL 1, IS DESIGNED TO ISOLATE A FAILURE TO THE LPA11 SYSTEM. ALL OPTIONS ON THE PDP-11 ARE EXERCISED.

LEVEL 2 DIAGNOSTICS ISOLATE A FAILURE TO THE INDIVIDUAL OPTION WITHIN THE LPA11. THE LEVEL 2 DIAGNOSTIC IS MD-11-DRLPA. WHEN THE USER RUNS DRLPA HE CAN GENERALLY TELL WHICH OPTION DIAGNOSTIC (LEVEL 3) TO RUN NEXT. M8254 AND M8200-YC ERRORS MAY 'LOOK' ALIKE AND DRLPA MAY NOT BE ABLE TO DISTINGUISH BETWEEN THEM. ARBITRATION ERRORS WILL NOT BE DETECTED BY THIS DIAGNOSTIC.

LEVEL THREE DIAGNOSTICS AID IN DETERMINING IF THE ERROR WAS IN FACT ON THE OPTION THE DRLPA SPECIFIED. THE USER MAY 'LOOP' ON THE ERROR. WITHIN LEVEL THREE, THERE ARE TWO GROUPS OF DIAGNOSTICS. THE FIRST GROUP REQUIRES NO 'EXTRA' WORK BY THE USER IN ORDER TO RUN. GROUP 'A' DIAGNOSTICS DO NOT CHECK ARBITRATION, AND REQUIRE EXTRA TIME FOR EXECUTION. THE SECOND GROUP (GROUP 'B') REQUIRES THAT THE USER RECONFIGURE THE PDP-11 SYSTEM. THIS RECONFIGURATION INVOLVES CABLING THE UNIBUS TO THE LPA'S I/O BUS.

THE DIAGNOSTIC FOR THE M8254 FALLS INTO THE GROUP 'B' CATEGORY.

## THE LPA11-KX DIAGNOSTIC KIT WILL INCLUDE:

K 1

SEQ 0010

<u>OPTION</u>	<u>GROUP</u>	<u>DIAG. #</u>	<u>DIAG. TITLE</u>
LPA11-KX	LEVEL 2	MD-11-DRLPA	LPA11-K SYSTEM DIAG.
M8254	'B'	MD-11-DRLPN	M8254 (IPBM) DIAG.
AA11-K	A	MD-11-DRLPB	AA11-K DIAG.
	B	MD-11-DZAAC	AA11-K DIAG.
AR11	A	MD-11-DRLPC	LPA/AR11 DIAG. #1
	A	MD-11-DRLPD	LPA/AR11 DIAG. #2
	A	MD-11-DRLPE	LPA/AR11 DIAG. #3
	B	MD-11-DZARA	AR11 DIAG. #1
	B	MD-11-DZARB	AR11 DIAG. #2
	B	MD-11-DZARC	AR11 DIAG. #3
DR11-K	A	MD-11-CRLPF	LPA/DR11-K DIAG.
	B	MD-11-DZDRG	DR11-K DIAG.
KW11-K	A	MD-11-CRLPG	LPA/KW11-K DIAG.
	B	MD-11-DZKWK	KW11-K DIAG.
LPS11	A	MD-11-DRLPH	LPA/LPS11 DIAG. #1
	A	MD-11-DRLPI	LPA/LPS11 DIAG. #2
	A	MD-11-DRLPJ	LPA/LPS11 DIAG. #3
	B	MD-11-DZLPC	LPS11 DIAG. #1
	B	MD-11-DZLPD	LPS11 DIAG. #2
	B	MD-11-DZLPI	LPS11 DIAG. #3
AD11-K	A	MD-11-CRLPK	LPA/AD11-K DIAG.
	B	MD-11-DZADL	AD11-K DIAG.
M8200-YC	B	MD-11-DZLPL	LPA/M8200-YC BASIC MICRO-CPU R/W TEST
	B	MD-11-DZLPM	LPA/M8200-YC JMP+ROM READ TEST

PRODUCT CODE: MAINDEC-11-DZADL-B  
 PRODUCT NAME: AD11-K PERFORMANCE TEST  
 DATE: DECEMBER 1976  
 MAINTAINER: DIANOSTIC GROUP

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PRODUCT CODE: MAINDEC-11-DRLPK-A  
 PRODUCT NAME: LPA/AD11-K PERFORMANCE TEST  
 DATE: JANUARY 1978  
 MAINTAINER: DIAGNOSTIC GROUP

REASON FOR DEVELOPMENT:

- 1) TO ENABLE THE OPERATOR TO CHECK OUT THE AD11-K OPTION WHEN IT IS ON THE LPA11-KX I/O BUS.

CHANGES MADE:

- 1) TOOK OUT CERTAIN TESTS FROM ORIGINAL DIAGNOSTIC (I.E. INTERRUPTS, TIME DEPENDENT CODE).
- 2) REPLACED DIRECT LINKS TO DEVICE WITH MACRO CALLS TO THE KMC-11 MICRO CODE. KMC-11 MICRO CODE (FILE:DRLPX2) HANDLES DIRECT COMMUNICATIONS WITH THE DEVICE.

FILE: DRLPA.MAC  
 CONTAINS MACRO LINKS BETWEEN PDP-11 CODE AND KMC-11 MICRO CODE. FILE: DRLPX2 NEEDS TO BE ASSEMBLED WITH DRLPK (SEE .CTL FILE).

FILE: DRLPX2  
 MICRO CODE FILE THAT GETS LOADED INTO THE KMC-11 VIA ROUTINES IN DRLPA.MAC.

DRLPX2.P11 IS ASSEMBLED WITH MACY11 (ONLY) AS ANY OTHER .P11 FILE. THE RESULTS OF ITS ASSEMBLY IS A .OBJ MODULE AS WAS THE RESULT OF THE ASSEMBLY OF THE DIAGNOSTIC .P11 FILE. BOTH .OBJ FILES GET LINKED WITH LNKX11 (ONLY).

FILE: DRLPK.CTL  
 THIS FILE EXPLAINS SEQUENCE OF ASSEMBLES AND LINKS. IT IS IN TOPS-20 FORMAT.

\*\*\*\*\*

PRODUCT CODE: AC-B049B-MC

DIAGNOSTIC CODE: MD-11-CRLPK-B  
PRODUCE NAME: CRLPKB LPA/AD11-K TEST  
DATE REVISED: JULY 1979  
MAINTAINER: DIAGNOSTIC GROUP

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THE 'B' VERSION WAS GENERATED TO REPAIR THE FOLLOWING PROBLEMS:

1. PROGRAM LISTING DID NOT AGREE WITH THE BINARY FILE AFTER LOC. 12064. THIS WAS DUE TO THE RELEASE ENGINEERING GROUP REASSEMBLING TO GET THE LISTING AND USING THE BINARY FILE SUPPLIED BY AUTHOR. (DEVELOPED WITH C2 SYSMAC - RELEASED WITH C3 SYSMAC)
2. WHEN SUBTEST 'A' OR 'W' WAS SELECTED, A 'MICRO-CODE LOAD ERROR' OCCURRED AT LOCATION 17612 ON THE 'THIRD PASS'. (DUE TO THE AUTHOR FORGETTING ABOUT WHERE THE MICRO-CODE 'HIDES' AT.)
3. 'TST11' COULD NOT BE LOOPED ON CORRECTLY. (ORIGIONAL PROGRAM USED A ABSOLUTE TAG FOR AT THAT TEST <<TST17>>)
4. AFTER A POWER FAILURE, THE PROGRAM APPEARED TO RECOVERY PROPERLY. BUT AFTER THE OPERATOR ENTERED THE TEST NUMBER THE PROGRAM REPORTED 'LPA FAULT' AND THEN HALTS. (PROGRAM DID A RESTART - IT MUST BE STARTED)

- .....
1. REASSELBLED THE FILE - <EASY AND FREE FIX WHEN WORKING ON PROBLEM 2-4
  2. PROTECT THE 'HIDDEN' SPACE THAT THE MICRO-CODE RESIDES AT.
  3. REMOVE INCORRECT TAG FROM 'TST11'
  4. BEACUSE THE KMC-11 IS A VOLIATLE DEVICE A COMPLETE PROGRAM START WAS NEEDED. JUST A ONE LOCATION PATCH IN THE POWER FAIL ROUTINE FIXES THE PROBLEM.

2936	BASIC DEFINITIONS
2937	OPERATIONAL SWITCH SETTINGS
2988	TRAP CATCHER
(1)	STARTING ADDRESS(ES)
2993	ACT11 HOOKS
2995	APT PARAMETER BLOCK
2996	COMMON TAGS
(2)	APT MAILBOX-ETABLE
(1)	ERROR POINTER TABLE
3036	MISCELLANEOUS, TEMPORARY, AND STORAGE LOCATIONS
3087	CONTROL A AND C DECODERS
3117	INITIAL START-UP, HOUSEKEEPING, AND DIALOGUE
3122	INITIALIZE THE COMMON TAGS
3128	DETERMINE IF VT55 TYPE TERMINAL IS PRESENT
3219	T1 FLOAT A ONE THRU MULTIPLEXER BITS
3228	T2 LOAD AND READ BACK INTERRUPT ENABLE BIT6
3234	T3 LOAD AND READ BACK CLOCK OVERFLOW START ENABLE BIT5
3239	T4 LOAD AND READ BACK EXTERNAL START ENABLE BIT4
3243	T5 LOAD AND READ BACK ERROR FLAG BIT15
3248	T6 TEST DONE FLAG SETS AND BIT0 CLEARS ON END OF CONV.
3259	T7 TEST A/D DONE FLAG CLEARS WHEN READ CONVERTED VALUE
3269	T10 TEST ERROR FLAG SETS IF 2ND CONVERSION ENDS BEFORE READING BUFFER
3297	WRAPAROUND TEST SECTION
3299	T11 TEST CH14 GROUND
3310	T12 TEST CONVERSION FROM EXT. START
3326	T13 TEST CH0 GROUND
3334	T14 TEST CH1 GROUND
3342	T15 TEST CH2 +1 VOLT
3351	T16 TEST CH3 +2.5 VOLTS
3359	T17 TEST CH4 -2.5 VOLTS
3367	T20 TEST VERNIER OFFSET DAC ON CH12
3414	T21 TEST CH13 +2.5 VOLTS
3421	T22 TEST CH17 +4V
3428	T23 OFFSET ON CH0
3455	T24 NOISE TEST ON 8 EDGES
3464	T25 SETTLE TEST ON 8 EDGES
3472	T26 DIFFERENTIAL LINEARITY AND RELATIVE ACCURACY TEST
3483	CALIBRATION TEST
3517	LOGIC TEST SECTION
3527	AUTO TEST
3545	WRAPAROUND TEST
4285	END OF PASS ROUTINE
4288	ASCII MESSAGES
4377	TTY INPUT ROUTINE
4379	READ AN OCTAL NUMBER FROM THE TTY
4381	SCOPE HANDLER ROUTINE
4382	ERROR HANDLER ROUTINE
4383	ERROR MESSAGE TYPEOUT ROUTINE
4385	TYPE ROUTINE
4386	APT COMMUNICATIONS ROUTINE
4388	BINARY TO OCTAL (ASCII) AND TYPE
4390	TRAP DECODER
(3)	TRAP TABLE
4392	POWER DOWN AND UP ROUTINES

.REM [

LPA.MAC

WELCOME, THIS DIAGNOSTIC IS ONE IN A SERIES OF DIAGNOSTIC  
DESIGNED IN ORDER TO AID YOU IN TESTING THE LPA-11XX OPTION.  
I HOPE THAT YOU HAVE READ THE DOCUMENTATION SECTION OF THIS  
DIAGNOSTIC. IF YOU HAVE, YOU KNOW ABOUT ALL OF THE DIAGNOSTICS  
THAT ARE AVAILABLE FOR TESTING THE LPA SYSTEM.

GOOD LUCK !

[  
.GLOBL DRLPX2

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156  
169  
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183  
415  
416  
457  
509  
608  
650  
697  
746

```
2935 .TITLE LPA-AD11K TEST MD-11-CRLPKB
(1)  ;*COPYRIGHT (C) 1979
(1)  ;*DIGITAL EQUIPMENT CORP.
(1)  ;*MAYNARD, MASS. 01754
(1)  ;*
(1)  ;*PROGRAM BY MODIFIED BY R. SHOOP
(1)  ;*
(1)  ;*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
(1)  ;*PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.
(1)  ;*
2936 .SBTTL BASIC DEFINITIONS
(1)
(1)  ;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
(1)  001100 STACK= 1100
(1)  .EQUIV EMT,ERROR      ;;BASIC DEFINITION OF ERROR CALL
(1)  .EQUIV IOT,SCOPE     ;;BASIC DEFINITION OF SCOPE CALL
(1)
(1)  ;*MISCELLANEOUS DEFINITIONS
(1)  000011 HT= 11      ;;CODE FOR HORIZONTAL TAB
(1)  000012 LF= 12      ;;CODE FOR LINE FEED
(1)  000015 CR= 15      ;;CODE FOR CARRIAGE RETURN
(1)  000200 CRLF= 200   ;;CODE FOR CARRIAGE RETURN-LINE FEED
(1)  177776 PS= 177776  ;;PROCESSOR STATUS WORD
(1)  .EQUIV PS,PSW
(1)  177774 STKLMT= 177774 ;;STACK LIMIT REGISTER
(1)  177772 PIRQ= 177772 ;;PROGRAM INTERRUPT REQUEST REGISTER
(1)  177570 DSWR= 177570 ;;HARDWARE SWITCH REGISTER
(1)  177570 DDISP= 177570 ;;HARDWARE DISPLAY REGISTER
(1)
(1)  ;*GENERAL PURPOSE REGISTER DEFINITIONS
(1)  000000 R0= %0      ;;GENERAL REGISTER
(1)  000001 R1= %1      ;;GENERAL REGISTER
(1)  000002 R2= %2      ;;GENERAL REGISTER
(1)  000003 R3= %3      ;;GENERAL REGISTER
(1)  000004 R4= %4      ;;GENERAL REGISTER
(1)  000005 R5= %5      ;;GENERAL REGISTER
(1)  000006 R6= %6      ;;GENERAL REGISTER
(1)  000007 R7= %7      ;;GENERAL REGISTER
(1)  000006 SP= %6      ;;STACK POINTER
(1)  000007 PC= %7      ;;PROGRAM COUNTER
(1)
(1)  ;*PRIORITY LEVEL DEFINITIONS
(1)  000000 PR0= 0      ;;PRIORITY LEVEL 0
(1)  000040 PR1= 40     ;;PRIORITY LEVEL 1
(1)  000100 PR2= 100    ;;PRIORITY LEVEL 2
(1)  000140 PR3= 140    ;;PRIORITY LEVEL 3
(1)  000200 PR4= 200    ;;PRIORITY LEVEL 4
(1)  000240 PR5= 240    ;;PRIORITY LEVEL 5
(1)  000300 PR6= 300    ;;PRIORITY LEVEL 6
(1)  000340 PR7= 340    ;;PRIORITY LEVEL 7
(1)
(1)  ;*'SWITCH REGISTER' SWITCH DEFINITIONS
(1)  100000 SW15= 100000
(1)  040000 SW14= 40000
(1)  020000 SW13= 20000
(1)  010000 SW12= 10000
```



(1)	004000	SW11=	4000
(1)	002000	SW10=	2000
(1)	001000	SW09=	1000
(1)	000400	SW08=	400
(1)	000200	SW07=	200
(1)	000100	SW06=	100
(1)	000040	SW05=	40
(1)	000020	SW04=	20
(1)	000010	SW03=	10
(1)	000004	SW02=	4
(1)	000002	SW01=	2
(1)	000001	SW00=	1
(1)		.EQUIV	SW09,SW9
(1)		.EQUIV	SW08,SW8
(1)		.EQUIV	SW07,SW7
(1)		.EQUIV	SW06,SW6
(1)		.EQUIV	SW05,SW5
(1)		.EQUIV	SW04,SW4
(1)		.EQUIV	SW03,SW3
(1)		.EQUIV	SW02,SW2
(1)		.EQUIV	SW01,SW1
(1)		.EQUIV	SW00,SW0

;\*DATA BIT DEFINITIONS (BIT00 TO BIT15)

(1)	100000	BIT15=	100000
(1)	040000	BIT14=	40000
(1)	020000	BIT13=	20000
(1)	010000	BIT12=	10000
(1)	004000	BIT11=	4000
(1)	002000	BIT10=	2000
(1)	001000	BIT09=	1000
(1)	000400	BIT08=	400
(1)	000200	BIT07=	200
(1)	000100	BIT06=	100
(1)	000040	BIT05=	40
(1)	000020	BIT04=	20
(1)	000010	BIT03=	10
(1)	000004	BIT02=	4
(1)	000002	BIT01=	2
(1)	000001	BIT00=	1
(1)		.EQUIV	BIT09,BIT9
(1)		.EQUIV	BIT08,BIT8
(1)		.EQUIV	BIT07,BIT7
(1)		.EQUIV	BIT06,BIT6
(1)		.EQUIV	BIT05,BIT5
(1)		.EQUIV	BIT04,BIT4
(1)		.EQUIV	BIT03,BIT3
(1)		.EQUIV	BIT02,BIT2
(1)		.EQUIV	BIT01,BIT1
(1)		.EQUIV	BIT00,BIT0

;\*BASIC "CPU" TRAP VECTOR ADDRESSES

(1)	000004	ERRVEC=	4	::TIME OUT AND OTHER ERRORS
(1)	000010	RESVEC=	10	::RESERVED AND ILLEGAL INSTRUCTIONS
(1)	000014	TBITVEC=	14	::'T' BIT
(1)	000014	TRTVEC=	14	::TRACE TRAP

(1) 000014  
(1) 000020  
(1) 000024  
(1) 000030  
(1) 000034  
(1) 000060  
(1) 000064  
(1) 000240

BPTVEC= 14 ;:BREAKPOINT TRAP (BPT)  
IOTVEC= 20 ;:INPUT/OUTPUT TRAP (IOT) \*\*SCOPE\*\*  
PWRVEC= 24 ;:POWER FAIL  
EMTVEC= 30 ;:EMULATOR TRAP (EMT) \*\*ERROR\*\*  
TRAPVEC=34 ;:TRAP TRAP  
TKVEC= 60 ;:TTY KEYBOARD VECTOR  
TPVEC= 64 ;:TTY PRINTER VECTOR  
PIRQVEC=240 ;:PROGRAM INTERRUPT REQUEST VECTOR

2937

.SBTTL OPERATIONAL SWITCH SETTINGS

(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)

```
.*
.*      SWITCH      USE
.*      -----
.*      15          HALT ON ERROR
.*      14          LOOP ON TEST
.*      13          INHIBIT ERROR TYPEOUTS
.*      12          HALT FOR BIT-MAP DISPLAY
.*      11          INHIBIT ITERATIONS
.*      10          BELL ON ERROR
.*      9           LOOP ON ERROR
.*      8           LOOP ON TEST IN SWR<7:0>
```

2938 170400  
2939 140340  
2940 00C300

ABASE= 170400  
AVECT1= 140340  
APRIOR= 300

2941  
2946  
2953  
2958  
2965  
2970  
2976  
2982  
2987  
2988

.SBTTL TRAP CATCHER

(1)  
(1) 000000  
(1)  
(1)  
(1)

.=0  
;\*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"  
;\*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS  
;\*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

(1) 000174 000174  
(1) 000174 000000  
(1) 000176 000000

.=174  
DISPREG: .WORD 0 ;:SOFTWARE DISPLAY REGISTER  
SWREG: .WORD 0 ;:SOFTWARE SWITCH REGISTER

(1) 000200 000137 001714  
2989 000204 000137 002404  
2990 000210 000137 001722  
2991 000214 000137 020550

.SBTTL STARTING ADDRESS(ES)  
JMP @#BEGIN ;:JUMP TO STARTING ADDRESS OF PROGRAM  
JMP @#BEG2 ;:RESTART ADDRESS  
JMP @#BEGIN2 ;:START ADDRESS FOR OPTION TEST AREA  
JMP @#\$UTK ;:STARTING ADDRESS FOR USER LINK

2993  
(1)  
(2)  
(1)  
(1) 000220  
(1) 000046  
(1) 000046 012100  
(1) 000052  
(1) 000052 000000  
(1) 000220  
2994 001000  
2995  
(1)  
(2)  
(1)  
(2)  
(1) 001000  
(1) 000024  
(1) 000024 000200  
(1) 000044  
(1) 000044 001000  
(1) 001000  
(2)  
(1)  
(1)  
(1)  
(1) 001000  
(1) 001000 000000  
(1) 001002 001174  
(1) 001004 002260  
(1) 001006 000764  
(1) 001010 003244  
(1) 001012 000031

```
.SBTTL ACT11 HOOKS

:*****
:HOOKS REQUIRED BY ACT11
      $SVPC=.          ;SAVE PC
      .=46
      $ENDAD          ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .$EOP
      .=52
      .WORD 0         ;;2)SET LOC.52 TO ZERO
      .= $SVPC        ;; RESTORE PC
.=1000
.SBTTL APT PARAMETER BLOCK

:*****
:SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
:*****
      .$X=.          ;;SAVE CURRENT LOCATION
      .=24          ;;SET POWER FAIL TO POINT TO START OF PROGRAM
      200           ;;FOR APT START UP
      .=44          ;;POINT TO APT INDIRECT ADDRESS PNTR.
      $APTHDR       ;;POINT TO APT HEADER BLOCK
      .=.$X         ;;RESET LOCATION COUNTER
:*****
:SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
:INTERFACE SPEC.

$APTHD:
$HIBTS: .WORD 0      ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
$MBADR: .WORD $MAIL  ;;ADDRESS OF APT MAILBOX (BITS 0-15)
$STMT:  .WORD 1200.  ;;RUN TIM OF LONGEST TEST
$PASTM: .WORD 500.   ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
$UNITM: .WORD 1700.  ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
      .WORD $ETEND-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)
```

2996  
(1)  
(2)  
(1)  
(1)  
(1)  
(1) 001100 001100  
(1) 001100 000000  
(1) 001102 000  
(1) 001103 000  
(1) 001104 000000  
(1) 001106 000000  
(1) 001110 000000  
(1) 001112 000000  
(1) 001114 000  
(1) 001115 001  
(1) 001116 000000  
(1) 001120 000000  
(1) 001122 000000  
(1) 001124 000000  
(1) 001126 000000  
(1) 001130 000000  
(1) 001132 000000  
(1) 001134 000  
(1) 001135 000  
(1) 001136 000000  
(1) 001140 177570  
(1) 001142 177570  
(1) 001144 177560  
(1) 001146 177562  
(1) 001150 177564  
(1) 001152 177566  
(1) 001154 000  
(1) 001155 002  
(1) 001156 012  
(1) 001157 000  
(1) 001160 000000  
(1) 001162 000000  
(1) 001164 177607 000377  
(1) 001170 077  
(1) 001171 015  
(1) 001172 000012  
(2)  
(2)  
(2)  
(3)  
(2)  
(2) 001174  
(2) 001174 000000  
(2) 001176 000000  
(2) 001200 000000  
(2) 001202 000000  
(2) 001204 000000  
(2) 001206 000000  
(2) 001210 000000

```
.SBTTL COMMON TAGS

*****
*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
*USED IN THE PROGRAM.

      . =1100
$CMTAG:      . =1100      ;; START OF COMMON TAGS
      .WORD      0
$STNM:      .BYTE      0      ;; CONTAINS THE TEST NUMBER
$ERFLG:     .BYTE      0      ;; CONTAINS ERROR FLAG
$I CNT:     .WORD      0      ;; CONTAINS SUBTEST ITERATION COUNT
$LPADR:     .WORD      0      ;; CONTAINS SCOPE LOOP ADDRESS
$LPERR:     .WORD      0      ;; CONTAINS SCOPE RETURN FOR ERRORS
$ERTTL:     .WORD      0      ;; CONTAINS TOTAL ERRORS DETECTED
$I T E M B:  .BYTE      0      ;; CONTAINS ITEM CONTROL BYTE
$ERMAX:     .BYTE      1      ;; CONTAINS MAX. ERRORS PER TEST
$ERRPC:     .WORD      0      ;; CONTAINS PC OF LAST ERROR INSTRUCTION
$GDADR:     .WORD      0      ;; CONTAINS ADDRESS OF 'GOOD' DATA
$BDADR:     .WORD      0      ;; CONTAINS ADDRESS OF 'BAD' DATA
$GDDAT:     .WORD      0      ;; CONTAINS 'GOOD' DATA
$BDDAT:     .WORD      0      ;; CONTAINS 'BAD' DATA
      .WORD      0      ;; RESERVED--NOT TO BE USED
      .WORD      0
$AUTOB:     .BYTE      0      ;; AUTOMATIC MODE INDICATOR
$I N T A G:  .BYTE      0      ;; INTERRUPT MODE INDICATOR
      .WORD      0
SWR:        .WORD      DSWR      ;; ADDRESS OF SWITCH REGISTER
DISPLAY:    .WORD      DDISP     ;; ADDRESS OF DISPLAY REGISTER
$TKS:       177560      ;; TTY KBD STATUS
$TKB:       177562      ;; TTY KBD BUFFER
$TPS:       177564      ;; TTY PRINTER STATUS REG. ADDRESS
$TPB:       177566      ;; TTY PRINTER BUFFER REG. ADDRESS
$NULL:      .BYTE      0      ;; CONTAINS NULL CHARACTER FOR FILLS
$FILLS:     .BYTE      2      ;; CONTAINS # OF FILLER CHARACTERS REQUIRED
$FILLC:     .BYTE     12      ;; INSERT FILL CHARS. AFTER A 'LINE FEED'
$TPFLG:     .BYTE      0      ;; 'TERMINAL AVAILABLE' FLAG (BIT<07>=0=YES)
$TIMES:     0          ;; MAX. NUMBER OF ITERATIONS
$ESCAPE:    0          ;; ESCAPE ON ERROR ADDRESS
$BELL:      .ASCIZ    <207><377><377> ;; CODE FOR BELL
$QUES:      .ASCII    /?/      ;; QUESTION MARK
$CRLF:      .ASCII    <15>     ;; CARRIAGE RETURN
$LF:        .ASCIZ    <12>     ;; LINE FEED

*****
.SBTTL APT MAILBOX-ETABLE

*****
.EVEN
$MAIL:      .WORD      .        ;; APT MAILBOX
$MSGTY:     .WORD      AMSGTY   ;; MESSAGE TYPE CODE
$FATAL:     .WORD      AFATAL   ;; FATAL ERROR NUMBER
$TESTN:     .WORD      ATESTN   ;; TEST NUMBER
$PASS:      .WORD      APASS    ;; PASS COUNT
$DEVCT:     .WORD      ADEVCT   ;; DEVICE COUNT
$UNIT:      .WORD      AUNIT    ;; I/O UNIT NUMBER
$MSGAD:     .WORD      AMSGAD   ;; MESSAGE ADDRESS
```

(2)	001212	000000	\$MSGLG: .WORD	AMSGLG	::MESSAGE LENGTH
(2)	001214		\$ETABLE:		::APT ENVIRONMENT TABLE
(2)	001214	000	\$ENV: .BYTE	AENV	::ENVIRONMENT BYTE
(2)	001215	000	\$ENVM: .BYTE	AENVM	::ENVIRONMENT MODE BITS
(2)	001216	000000	\$SWREG: .WORD	ASWREG	::APT SWITCH REGISTER
(2)	001220	000000	\$USWR: .WORD	AUSWR	::USER SWITCHES
(2)	001222	000000	\$CPUOP: .WORD	ACPUOP	::CPU TYPE,OPTIONS
(2)			:*		BITS 15-11=CPU TYPE
(2)			:*		11/04=01,11/05=02,11/20=03,11/40=04,11/45=05
(2)			:*		11/70=06,PDQ=07,Q=10
(2)			:*		BIT 10=REAL TIME CLOCK
(2)			:*		BIT 9=FLOATING POINT PROCESSOR
(2)			:*		BIT 8=MEMORY MANAGEMENT
(2)	001224	000	\$MAMS1: .BYTE	AMAMS1	::HIGH ADDRESS,M.S. BYTE
(2)	001225	000	\$MTYP1: .BYTE	AMTYP1	::MEM. TYPE,BLK#1
(2)			:*		MEM.TYPE BYTE -- (HIGH BYTE)
(2)			:*		900 NSEC CORE=001
(2)			:*		300 NSEC BIPOLAR=002
(2)			:*		500 NSEC MOS=003
(2)	001226	000000	\$MADR1: .WORD	AMADR1	::HIGH ADDRESS,BLK#1
(2)			:*		MEM.LAST ADDR.=3 BYTES,THIS WORD AND LOW OF "TYPE" ABOVE
(2)	001230	000	\$MAMS2: .BYTE	AMAMS2	::HIGH ADDRESS,M.S. BYTE
(2)	001231	000	\$MTYP2: .BYTE	AMTYP2	::MEM. TYPE,BLK#2
(2)	001232	000000	\$MADR2: .WORD	AMADR2	::MEM.LAST ADDRESS,BLK#2
(2)	001234	000	\$MAMS3: .BYTE	AMAMS3	::HIGH ADDRESS,M.S.BYTE
(2)	001235	000	\$MTYP3: .BYTE	AMTYP3	::MEM. TYPE,BLK#3
(2)	001236	000000	\$MADR3: .WORD	AMADR3	::MEM.LAST ADDRESS,BLK#3
(2)	001240	000	\$MAMS4: .BYTE	AMAMS4	::HIGH ADDRESS,M.S.BYTE
(2)	001241	000	\$MTYP4: .BYTE	AMTYP4	::MEM. TYPE,BLK#4
(2)	001242	000000	\$MADR4: .WORD	AMADR4	::MEM.LAST ADDRESS,BLK#4
(2)	001244	140340	\$VECT1: .WORD	AVECT1	::INTERRUPT VECTOR#1,BUS PRIORITY#1
(2)	001246	000000	\$VECT2: .WORD	AVECT2	::INTERRUPT VECTOR#2BUS PRIORITY#2
(2)	001250	170400	\$BASE: .WORD	ABASE	::BASE ADDRESS OF EQUIPMENT UNDER TEST
(2)	001252	000000	\$DEVN: .WORD	ADEVN	::DEVICE MAP
(2)	001254	000000	\$CDW1: .WORD	ACDW1	::CONTROLLER DESCRIPTION WORD#1
(2)	001256		\$ETEND:		
(2)			.MEXIT		

```

(1) .SBTTL ERROR POINTER TABLE
(1)
(1) :*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
(1) :*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
(1) :*LOCATION $ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
(1) :*NOTE1: IF $ITEMB IS 0 THE ONLY PERTINENT DATA IS ($ERRPC).
(1) :*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:
(1)
(1) :* EM ::POINTS TO THE ERROR MESSAGE
(1) :* DH ::POINTS TO THE DATA HEADER
(1) :* DT ::POINTS TO THE DATA
(1) :* DF ::POINTS TO THE DATA FORMAT
(1)
(1) $ERRTB:
(1) 001256
2998
2999
3000
3009 :ITEM 1
3010 001256 014257 EM1 :STATUS REG. ERROR
3011 001260 014417 DH1 :ERRPC STREG EXPECTED ACTUAL
3012 001262 014602 DT1 :$ERRPC, STREG, $GDDAT, $BDDAT
3013 001264 014642 DF1
3014
3015
3016 :ITEM 2
3017 001266 014305 EM2 :FAILED TO INTERRUPT
3018 001270 014540 DH3 :ERRPC STREG ACTUAL
3019 001272 014632 DT3 :$ERRPC, STREG, $BDDAT
3020 001274 014642 DF1
3021
3022 :ITEM 3
3023 001276 014335 EM3 :UNEXPECTED INTERRUPT
3024 001300 014540 DH3 :ERRPC STREG
3025 001302 014632 DT3 :$ERRPC, STREG
3026 001304 014642 DF1
3027
3028 :ITEM 4
3029 001306 014366 EM4 :ERROR ON A/D CHANNEL
3030 001310 014455 DH2 :ERRPC STREG CHAN NOMINAL TOL ACTUAL
3031 001312 014614 DT2 :$ERRPC, STREG, CHANL, $GDDAT, SPREAD, $BDDAT
3032 001314 014642 DF1
3033
3034

```

	.SBTTL	MISCELLANEOUS, TEMPORARY, AND STORAGE LOCATIONS
3036		
3037	001316 170400	STREG: ABASE ;ADDRESS OF STATUS REGISTER
3038	001320 170402	ADBUFF: ABASE+2 ;ADDRESS OF A/D BUFFER
3039	001322 000300	BASEBR: APRIOR ;INTERRUPT PRIORITY LEVEL
3040	001324 140342	VECTR1: AVECT1+2
3041	001326 000040	VADR: 40 ;INCREMENT FOR BUS ADDRESS
3042	001330 000040	VVCT: 40 ;INCREMENT FOR VECTOR ADDRESS
3043	001332 000000	BASECH: 0 ;BASE CHANNEL
3044	001334 000060	KBVECT: 60
3045	001336 000000	WIDE: 0 ;NO. OF WIDE STATES
3046	001340 000000	NARROW: 0 ;NO. OF NARROW STATES
3047	001342 000000	FIRST: 0
3048	001344 000000	SKIPST: 0 ;NO. OF SKIPPED STATES
3049	001346 000000	TEMP: 0 ;WORK AREA
3050	001350 000000	CH1: 0 ;FIRST CHANNEL
3051	001352 000000	CH2: 0 ;SECOND CHANNEL
3052	001354 000000	NBEXT: 0 ;NO. OF AD11K'S TO BE TESTED
3053	001356 000000	NMBEXT: 0 ;NO. OF AD11K'S TO BE TESTED
3054	001360 000000	DUMMY: 0 ;DUMMY CHANNEL
3055	001362 000000	CHANL: 0 ;CHANNEL VALUE
3056	001364 000000	TADDR: 0 ;TEST ADDRESS
3057	001366 000000	RNA: 0 ;RANDOM
3058	001370 000000	RNB: 0 ;NUMBER
3059	001372 000000	RNC: 0 ;VALUES
3060	001374 000000	RMS: 0 ;RMS NOISE VALUE
3061	001376 000000	PEAK: 0 ;PEAK NOISE VALUE
3062	001400 000000	FLAG: 0 ;VT55 FLAG
3063	001402 000000	SPREAD: 0 ;DEVIATION FROM THE NOMINAL
3064	001404 000000	DAC: 0 ;SAR VALUE
3065	001406 000000	DELAY: 0 ;TIME DELAY COUNTER
3066	001410 000000	EDGE: 0 ;EDGE VALUE
3067	001412 000000	BITPNT: 0
3068	001414 000000	MIN: 0 ;MIN VALUE
3069	001416 000000	WFTST: 0 ;OPTION TEST AREA FLAG
3070	001420 000000	MAX: 0 ;MAX VALUE
3071	001422 000000	PERCNT: 0 ;PERCENT FOR SAR ROUTINE
3072	001424 000000	OUT: 0
3073	001426 000000	MYTEMP: 0
3074	001430 000000	EDINT: 0
3075	001432 000000	\$TEMP1: 0
3076	001434 000000	\$TEMP2: 0

```

3078
3079      .: ADDRESS OF KMC-11 OF LPA-11      THE ADDR FOR KMAD0 MAY BE
(1)      .:                                  CHANGED BY THE USER TO REFLECT
(1)      .:                                  A DIFFERENT KMC-11 ADDR. THE
(1)      .:                                  REST OF THE ADDRESSES WILL
(1)      .:                                  BE CHANGED BY THE PROGRAM.
(1)      .:
(1)      .:
(1)      .:
(1) 001436      LPCI:
(1) 001436 170460      KMAD0: .WORD 170460      ;BASE KMC ADDR. MAY BE PATCHED BY USER.
(1)
(1) 001440      LPMR:
(1) 001440 170461      KMAD1: .WORD 170460+1      ;>DO NOT      <;KMC-CSR ADDR
(1) 001442      LPCO:
(1) 001442 170462      KMAD2: .WORD 170460+2      ;>PATCH      <:
(1) 001444      LPSO:
(1) 001444 170463      KMAD3: .WORD 170460+3      ;>THIS AREA      <
(1) 001446      LPADL:
(1) 001446 170464      KMAD4: .WORD 170460+4      ;
(1) 001450      LPADH:
(1) 001450 170465      KMAD5: .WORD 170460+5      ;>DO NOT      <
(1) 001452      LPMS1:
(1) 001452 170466      KMAD6: .WORD 170460+6      ;>PATCH      <
(1) 001454      LPMS2:
(1) 001454 170467      KMAD7: .WORD 170460+7      ;>THIS AREA      <
(1)
(1) 001456 000340      VECTOR: .WORD AVECT18777      ;BASE VECTOR OF KMC
(1) 001460 000344      VECTPS: .WORD 4+AVECT18777      ;VECTR ADDR.+2
(1)
(1) 001462 000004      VERSN: .WORD 4      ;CURRENT VERSION NUMBER OF MICROCODE.
(1)
(1) 001464 000000      .DVLS: .WORD 0      ;/DEVICE LIST OF I/O ADDR. DEFINED
(1) 001466 000020      .BLKW 16.      ;/BY INIT.
(1)
3080
3081 001526      UNEXP:
(1) 001526 012737 001542 001162      MOV #1$, $ESCAPE      ;;ESCAPE TO 1$ ON ERROR
3082 001534 005237 001103      INC $ERFLG
3083 001540 104003      ERROR 3
3084 001542 005037 001162      1$: CLR $ESCAPE      ;RETURN ESCAPE TO NORMAL
3085 001546 000002      RTI      ;UNEXPECTED INTERRUPT

```



```

3087          .SBTTL      CONTROL A AND C DECODERS
3088 001550 010046          ISERV:  MOV    RO,-(SP)          ;SAVE RO
3089 001552 017700 177370  MOV    @STKB,R0          ;GET CHARACTER
3090 001556 042700 177600  BIC    #177600,R0
3091 001562 120027 000003  CMPB   RO,#3          ;IS IT ^C?
3092 001566 001010          BNE    1$
3093 001570 104401 012250  TYPE   ,CMMSG         ;ECHO CHARACTER
3094 001574 012706 001100  MOV    #STACK,SP
3095 001600 004737 011362  JSR    PC,RST         ;RESET & SET INTRPT. EN.
3096 001604 000137 002404  JMP    BEG2
3097 001610 120027 000001  1$:  CMPB   RO,#1          ;IS IT ^A?
3098 001614 001010          BNE    2$
3099 001616 104401 012243  TYPE   ,AMSG         ;ECHO CHARACTER
3100 001622 012706 001100  MOV    #STACK,SP
3101 001626 004737 011362  JSR    PC,RST         ;RESET & SET INTRPT. EN.
3102 001632 000177 177526  JMP    @ADDR          ;RETURN TO TEST
3103 001636 120027 000007  2$:  CMPB   RO,#7          ;IS IT ^G?
3104 001642 001021          BNE    NONE
3105 001644 023727 001140 177570  CMP    SWR,#177570    ;HARDWARE SWREG?
3106 001652 001415          BEQ    NONE
3107 001654 104401 012255  TYPE   ,GMSG         ;ECHO CHARACTER
3108 001660 017746 177254  MOV    @SWR,-(SP)    ;;SAVE @SWR FOR TYPEOUT
(1)          ;;TYPE SWREG
(1) 001664 104403          TYPOS  ;;GO TYPE--OCTAL ASCII
(1) 001666 006          .BYTE 6          ;;TYPE 6 DIGITS
(1) 001667 001          .BYTE 1          ;;TYPE LEADING ZEROS
3109 001670 104401 012435  TYPE   ,SLASH
3110 001674 104407          RDOCT          ;READ NEW VALUE
3111 001676 012677 177236  MOV    (SP)+,@SWR    ;LOAD NEW SWREG VALUE
3112 001702 012600          POPRO: MOV    (SP)+,RO
3113 001704 000002          RETURN: RTI
3114 001706 104401 012241  NONE:  TYPE   ,QUEST  ;TYPE '?'
3115 001712 000773          BR     POPRO

```

```

3117 .SBTTL INITIAL START-UP,HOUSEKEEPING, AND DIALOGUE
3118 001714 005037 001416 BEGIN: CLR WFTST
3119 001720 000403 BR RBEG
3120 001722 012737 000001 001416 BEGIN2: MOV #1,WFTST
3121 001730 RBEG: ;RESET
3122 .SBTTL INITIALIZE THE COMMON TAGS
(1) ;:CLEAR THE COMMON TAGS ($CMTAG) AREA
(1) 001730 012706 001100 MOV #SCMTAG,R6 ;:FIRST LOCATION TO BE CLEARED
(1) 001734 005026 CLR (R6)+ ;:CLEAR MEMORY LOCATION
(1) 001736 022706 001140 CMP #SWR,R6 ;:DONE?
(1) 001742 001374 BNE -6 ;:LOOP BACK IF NO
(1) 001744 012706 001100 MOV #STACK,SP ;:SETUP THE STACK POINTER
(1) ;:INITIALIZE A FEW VECTORS
(1) 001750 012737 015240 000020 MOV #SCOPE,@IOTVEC ;:IOT VECTOR FOR SCOPE ROUTINE
(1) 001756 012737 000340 000022 MOV #340,@IOTVEC+2 ;:LEVEL 7
(1) 001764 012737 015516 000030 MOV #ERROR,@EMTVEC ;:EMT VECTOR FOR ERROR ROUTINE
(1) 001772 012737 000340 000032 MOV #340,@EMTVEC+2 ;:LEVEL 7
(1) 002000 012737 021302 000034 MOV #TRAP,@TRAPVEC ;:TRAP VECTOR FOR TRAP CALLS
(1) 002006 012737 000340 000036 MOV #340,@TRAPVEC+2 ;:LEVEL 7
(1) 002014 012737 021356 000024 MOV #SPWRDN,@PWRVEC ;:POWER FAILURE VECTOR
(1) 002022 012737 000340 000026 MOV #340,@PWRVEC+2 ;:LEVEL 7
(1) 002030 013737 012054 012046 MOV SENDCT,$EOPCT ;:SETUP END-OF-PROGRAM COUNTER
(1) 002036 005037 001160 CLR $TIMES ;:INITIALIZE NUMBER OF ITERATIONS
(1) 002042 005037 001162 CLR $ESCAPE ;:CLEAR THE ESCAPE ON ERROR ADDRESS
(1) 002046 112737 000001 001115 MOVB #1,$ERMAX ;:ALLOW ONE ERROR PER TEST
(1) 002054 012737 002054 001106 MOV #,$SLPADR ;:INITIALIZE THE LOOP ADDRESS FOR SCOPE
(1) 002062 012737 002062 001110 MOV #,$SLPERR ;:SETUP THE ERROR LOOP ADDRESS
(2) ;:SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
(2) ;:EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
(2) 002070 013746 000004 MOV @ERRVEC,-(SP) ;:SAVE ERROR VECTOR
(2) 002074 012737 002130 000004 MOV #64$,@ERRVEC ;:SET UP ERROR VECTOR
(2) 002102 012737 177570 001140 MOV #DSWR,SWR ;:SETUP FOR A HARDWARE SWICH REGISTER
(2) 002110 012737 177570 001142 MOV #DDISP,DISPLAY ;:AND A HARDWARE DISPLAY REGISTER
(2) 002116 022777 177777 177014 CMP #-1,@SWR ;:TRY TO REFERENCE HARDWARE SWR
(2) 002124 001012 BNE 66$ ;:BRANCH IF NO TIMEOUT TRAP OCCURRED
(2) ;:AND THE HARDWARE SWR IS NOT = -1
(2) 002126 000403 BR 65$ ;:BRANCH IF NO TIMEOUT
(2) 002130 012716 002136 64$: MOV #65$, (SP) ;:SET UP FOR TRAP RETURN
(2) 002134 000002 RTI
(2) 002136 012737 000176 001140 65$: MOV #SWREG,SWR ;:POINT TO SOFTWARE SWR
(2) 002144 012737 000174 001142 MOV #DISPREG,DISPLAY
(2) 002152 012637 000004 66$: MOV (SP)+,@ERRVEC ;:RESTORE ERROR VECTOR
(1)
(2) 002156 005037 001202 CLR $PASS ;:CLEAR PASS COUNT
(2) 002162 132737 000200 001215 BITB #APTSIZE,$ENVM ;:TEST USER SIZE UNDER APT
(2) 002170 001403 BEQ 67$ ;:YES,USE NON-APT SWITCH
(2) 002172 012737 001216 001140 MOV #SSWREG,SWR ;:NO,USE APT SWITCH REGISTER
(2) 002200 67$:

```

```

3124      ; THIS SECTION OF CODE HANDLES INITIALIZING LPA-11 FUNCTIONS
(1)      ;
(1)      ;
(1)      ;
(1) 002200 010046      MOV      R0,-(SP)
(1) 002202 010146      MOV      R1,-(SP)
(1) 002204 013700 001436  MOV      KMAD0,R0      ;GET KMC-11 ADDRESS.
(1) 002210 012701 001440  MOV      #KMAD1,R1     ;GET ADDR. OF ADDR. LIST.
(1)      ;
(1) 002214 005200      68$: INC      R0      ;UPDATE ADDR.
(1) 002216 010021      MOV      R0,(1)+      ;WRITE ADDR.
(1) 002220 020127 001456  CMP      R1,#KMAD7+2  ;DONE ALL ADDRESSES?
(1) 002224 001373      BNE      68$          ;NO - DO NEXT ADDR.
(1) 002226 005037 001464  CLR      .DVLS        ;CLR ADDR. LIST.
(1) 002232 012601      MOV      (SP)+,R1
(1) 002234 012600      MOV      (SP)+,R0
3125 002236 005037 001400  CLR      FLAG          ;CLEAR VT55 FLAG
3126 002242 005737 000042  TST      @#42          ;IS IT CHAINED?
3127 002246 001033      BNE      REST1
3128      ;
3129 002250 042777 000100 176666 .SBTTL  DETERMINE IF VT55 TYPE TERMINAL IS PRESENT
3130 002256 104401 013675      BIC      #100,@$TKS
3131 002262 004737 002656      TYPE     ,CO          ;TYPE ASCIZ STRING
3132 002266 020027 000033      JSR     PC,VTFLG     ;GET A CHARACTER
3133 002272 001017      CMP     R0,#33
3134 002274 004737 002656      BNE     NOVT55       ;NO VT55 PRESENT
3135 002300 020027 000057      JSR     PC,VTFLG     ;GET A CHARACTER
3136 002304 001012      CMP     R0,#57
3137 002306 004737 002656      BNE     NOVT55       ;NO VT55 PRESENT
3138 002312 020027 000103      JSR     PC,VTFLG     ;GET A CHARACTER
3139 002316 001403      CMP     R0,#103
3140 002320 020027 000105      BEQ     VT55         ;VT55 IS PRESENT
3141 002324 001002      CMP     R0,#105
3142 002326 005237 001400      BNE     NOVT55
VT55:    INC     FLAG

```

```

3144          :          DIALOGUE TO DETERMINE WHICH TEST TO RUN
3145 002332 104401 014040          : NOVT55: TYPE      ,HEAD1
3146 002336 004737 005376          : REST1: JSR      PC, FIXONE          ; INITIALIZE ADDRESSES
3147 002342 013700 001334          :          MOV      KBVECT, R0
3148 002346 012720 001550          :          MOV      #ISERV, (R0)+
3149 002352 012710 000340          :          MOV      #340, (R0)
3150 002356 012737 062341 001366          :          MOV      #62341, RNA          ; RANDOM NO, VARIABLES
3151 002364 012737 142315 001370          :          MOV      #142315, RNB
3152 002372 012737 127623 001372          :          MOV      #127623, RNC
3153 002400 004737 011650          :          JSR      PC, WFADJ          ; STANDARD OR OPTION TEST TOLERANCES?
3154 002404 012706 001100          : BEG2:  MOV      #STACK, SP          ; RESET STACK IN CASE RESTARTED
3155 002410 005737 000042          :          TST      @#42          ; IS IT CHAINED?
3156 002414 001402 005114          :          BEQ      1$
3157 002416 000137 005114          :          JMP      BEGL          ; GO TO LOGIC TESTS
3158 002422 104401 013503          : 1$:  TYPE      ,MSG71
3159 002426 104406 000100 176506          : TRYAG: RDLIN
3160 002430 052777 000100          :          BIS      #100, @STKS
3161 002436 005037 177776          :          CLR      PSW
3162 002442 012600          :          MOV      (SP)+, R0          ; READ ANSWER
3163 002444 142710 000040          :          BICB     #40, (R0)
3164 002450 121027 000101          :          CMPB     (R0), #'A
3165 002454 001002          :          BNE      1$          ; IS IT A?
3166 002456 000137 005156          :          JMP      BEGINA          ;;NO, TRY C
3167 002462 121027 000103          : 1$:  CMPB     (R0), #'C          ; GO TO AUTO TEST
3168 002466 001002          :          BNE      2$          ; IS IT C?
3169 002470 000137 004656          :          JMP      BEGINC          ;;NO, TRY L
3170 002474 121027 000114          : 2$:  CMPB     (R0), #'L          ; GO TO CALIBRATION TEST
3171 002500 001002          :          BNE      3$          ; IS IT L?
3172 002502 000137 005114          :          JMP      BEGL          ;;NO, TRY N
3173 002506 121027 000116          : 3$:  CMPB     (R0), #'N          ; GO TO LOGIC TESTS
3174 002512 001002          :          BNE      4$          ; IS IT N?
3175 002514 000137 005540          :          JMP      BEGINN          ;;NO, TRY S
3176 002520 121027 000123          : 4$:  CMPB     (R0), #'S          ; GO TO NOISE TEST
3177 002524 001002          :          BNE      5$          ; IS IT S?
3178 002526 000137 005610          :          JMP      BEGINS          ;;NO, TRY W
3179 002532 121027 000127          : 5$:  CMPB     (R0), #'W          ; GO TO SETTLE TEST
3180 002536 001002          :          BNE      6$          ; IS IT W?
3181 002540 000137 005250          :          JMP      BEGINW          ;;NO, TRY AGAIN
3182 002544 104401 012241          : 6$:  TYPE      ,QUEST          ; GO TO WRAPAROUND TEST
3183 002550 000726          :          BR       TRYAG          ; WAIT FOR CHARACTER

```

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3185
3186 ;SIZE AND REPORT THE NUMBER OF AD11K DETECTED
3187
3188 002552 013737 001250 001126 TESTAD: MOV $BASE,$BDDAT ;SETUP TO TEST FOR AD11K'S
3189 002560 005037 001464 CLR .DVLS
3190 002564 005037 001466 CLR .DVLS+2
3191 002570 005037 001354 CLR NBEXT ;CLEAR AD11K COUNTER
3192 002574 1$: ;ADDRESS AD11K
3193
(1) ;* MOV $GDDAT,@$BDDAT ;/ PUT DATA FROM $GDDAT TO DEVICE REG $BDDAT
3194 002604 005737 017454 TST $AERR ;DEVICE EXSIST? =0,YES
3195 002610 001006 BNE 2$ ;=1,NO.
3196
3197 002612 005237 001354 INC NBEXT ;INCREMENT AD11K COUNTER
3198 002616 063737 001326 001126 ADD VADR,$BDDAT ;GET NEXT AD11K
3199 002624 000763 BR 1$ ;:TRY NEXT AD11K
3200 002626 2$:
3201 002626 013746 001354 MOV NBEXT,-(SP) ;;SAVE NBEXT FOR TYPEOUT
(1) ;;TYPE NUMBER OF AD11K'S
(1) 002632 104403 TYPOS ;;GO TYPE--OCTAL ASCII
(1) 002634 002 .BYTE 2 ;;TYPE 2 DIGIT(S)
(1) 002635 000 .BYTE 0 ;;SUPPRESS LEADING ZEROS
3202 002636 104401 013043 TYPE ,MSG50
3203 002642 005337 001354 DEC NBEXT ;ADJUST AD11K COUNT
3204 002646 013737 001354 001356 MOV NBEXT,NMBEXT ;KEEP COUNT OF NUMBER
3205 002654 000207 RTS PC
3206
3207 002656 005000 VTFLG: CLR R0 ;TEST FOR PRESENCE
3208 002660 105777 176260 1$: TSTB @$TKS ;OF VT55
3209 002664 100404 BMI 2$ ;;VT55 RESPONDS WITH <33><57>[<103> OR <105>]
3210 002666 005300 DEC R0
3211 002670 001373 BNE 1$ ;;
3212 002672 005726 TST (SP)+ ;POP A WORD OFF STACK
3213 002674 000616 BR NOVT55 ;:NO VT55 PRESENT
3214 002676 017700 176244 2$: MOV @$TKB,R0
3215 002702 042700 177600 BIC #177600,R0 ;TEST VT55 CODE
3216 002706 000207 RTS PC

```

3218 002710  
3219  
(3)  
(3)  
(2) 002710 012737 002710 001106  
3220 002716 012737 002710 001110  
3221 002724 012737 000400 001124  
3222 002732 004737 003400  
3223 002736 104001  
3224 002740 006137 001124  
3225 002744 023727 001124 040000  
3226 002752 001367  
3227  
3228  
(3)  
(3)  
(2) 002754 000004  
3229 002756 012777 001526 176472  
3230 002764 012737 000100 001124  
3231 002772 004737 003400  
3232 002776 104001  
3233  
3234  
(3)  
(3)  
(2) 003000 000004  
3235 003002 012737 000040 001124  
3236 003010 004737 003400  
3237 003014 104001  
3238  
3239  
(3)  
(3)  
(2) 003016 000004  
3240 003020 012737 000020 001124  
3241 003026 004737 003400  
3242 003032 104001  
3243  
(3)  
(3)  
(2) 003034 000004  
3244 003036 012737 100000 001124  
3245 003044 004737 003400  
3246 003050 104001

```
BEGINL:
:*****
:*TEST 1      FLOAT A ONE THRU MULTIPLEXER BITS
:*****
TST1:  MOV      #TST1,$LFAVR
        MOV      #TST1,$LPERR
        MOV      #BIT8,$GDDAT      ;LOAD FIRST BIT
2$:    JSR      PC,TESTIT
        ERROR    1                  ;FAILED TO LOAD + READ BIT
1$:    ROL      $GDDAT              ;GET NEXT BIT
        CMP      $GDDAT,#BIT14     ;FINISHED?
        BNE     2$                  ;;NO,GO TO NEXT TEST
:*****
:*TEST 2      LOAD AND READ BACK INTERRUPT ENABLE BIT6
:*****
TST2:  SCOPE
        MOV      #UNEXP,@VECTOR    ;SETUP FOR UNEXPECTED INTERUPT
        MOV      #BIT6,$GDDAT      ;LOAD EXPECTED DATA
        JSR      PC,TESTIT
        ERROR    1                  ;FAILED TO LOAD + READ INTERRUPT ENABLE
:*****
:*TEST 3      LOAD AND READ BACK CLOCK OVERFLOW START ENABLE BITS
:*****
TST3:  SCOPE
        MOV      #BIT5,$GDDAT      ;LOAD EXPECTED DATA
        JSR      PC,TESTIT
        ERROR    1                  ;FAILED TO LOAD + READ CLOCK OVERFLOW START ENAB
:*****
:*TEST 4      LOAD AND READ BACK EXTERNAL START ENABLE BIT4
:*****
TST4:  SCOPE
        MOV      #BIT4,$GDDAT      ;LOAD EXPECTED DATA
        JSR      PC,TESTIT
        ERROR    1                  ;FAILED TO LOAD + READ EXT. START ENABLE
:*****
:*TEST 5      LOAD AND READ BACK ERROR FLAG BIT15
:*****
TST5:  SCOPE
        MOV      #BIT15,$GDDAT     ;LOAD EXPECTED DATA
        JSR      PC,TESTIT
        ERROR    1                  ;FAILED TO LOAD + READ ERROR FLAG
```

```

3248          ;:*****
(3)          ;*TEST 6      TEST DONE FLAG SETS AND BIT0 CLEARS ON END OF CONV.
(3)          ;:*****
(2) 003052 000004
3249 003054 012700 001000 TST6:  SCOPE
(2)          MOV      #BIT9,R0      ;STALL TIME COUNTER
(2)          ;*      MOV      @STREG,MYTEMP  ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
(1) 003070 005237 001426          INC      MYTEMP
(2)          ;*      MOV      MYTEMP,@STREG  ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
3251 003104 012737 000200 001124          MOV      #BIT7,$GDDAT  ;LOAD EXPECTED
3252 003112 005300          1$:    DEC      R0      ;STALL
3253 003114 001376          BNE     1$      ;TIME
3254          ;
(2)          ;*      MOV      @STREG,MYTEMP  ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
(1) 003126 042737 100000 001426          BIC     #BIT15,MYTEMP
(2)          ;*      MOV      MYTEMP,@STREG  ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
3255 003144 004737 003410          JSR     PC,TEST
3256 003150 104001          ERROR  1      ;A/D DONE FLAG FAILED TO SET;BIT0 FAILED TO CLEAR
3257          ;*      MOV      @ADBUFF,MYTEMP ;/READ DEVICE REG ADPUFF,PUT DATA IN MYTEMP.
(1) 003162 013700 001426          MOV     MYTEMP,R0  ;/PUT CONVERTED VALUE IN R0.
3258          ;
3259          ;:*****
(3)          ;*TEST 7      TEST A/D DONE FLAG CLEARS WHEN READ CONVERTED VALUE
(3)          ;:*****
(2) 003166 000004
3260 003170 012737 000001 001426 TST7:  SCOPE
(1)          MOV      #BIT0,MYTEMP
3261          ;*      MOV      MYTEMP,@STREG  ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
3262 003206 005037 001124          CLR     $GDDAT
3263 003212          1$:
(2)          ;*      MOV      @STREG,MYTEMP  ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
(1) 003222 105737 001426          TSTB   MYTEMP
3264 003226 100371          BPL     1$
3265          ;*      MOV      @ADBUFF,MYTEMP ;/READ DEVICE REG ADPUFF,PUT DATA IN MYTEMP.
(1) 003240 013700 001426          MOV     MYTEMP,R0  ;/PUT CONVERTED VALUE IN R0.
3266 003244 004737 003410          JSR     PC,TEST
3267 003250 104001          ERROR  1      ;DONE FLAG FAILED TO CLEAR

```

```
3269          ::*****  
(3)          :*TEST 10      TEST ERROR FLAG SETS IF 2ND CONVERSION ENDS BEFORE READING BUFFER  
(3)          :*****  
(2) 003252 000004          TST10: SCOPE  
(1) 003254 012737 000010 001160      MOV #10,$TIMES      ;;DO 10 ITERATIONS  
3270 003262 012737 000001 001426      MOV #BIT0,MYTEMP  
3271          :*      MOV MYTEMP,@STREG      ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG  
(1)          1$:  
3272 003300          :*      MOV @STREG,MYTEMP      ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.  
(2)          TSTB MYTEMP  
(2)          BPL 1$  
(1) 003310 105737 001426          2$:      MOV #BIT15!BIT7,$GDDAT ;LOAD EXPECTED VALUE  
3273 003314 100371          :*      MOV MYTEMP,@STREG      ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG  
3274 003316 012737 100200 001124      MOV #BIT9,R0      ;WAIT FOR 2ND  
3275 003324 012737 000001 001426      DEC R0      ;CONVERSION TO END  
3276          :*      MOV MYTEMP,@STREG      ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG  
(1) 003342 012700 001000          3$:      DEC R0  
3277 003346 005300          :*      MOV MYTEMP,@STREG      ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG  
3278 003350 001376          :*      MOV MYTEMP,@STREG      ;/ READ DEVICE REG STREG,PUT DATA IN MYTEMP.  
3279 003352 004737 003410          MOV MYTEMP,R0      ;/PUT CONVERTED VALUE IN R0.  
3280 003356 104001          :*      MOV @ADBUFF,MYTEMP      ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.  
3281          :*      MOV MYTEMP,R0      ;/PUT CONVERTED VALUE IN R0.  
3282          :*      MOV @ADBUFF,MYTEMP      ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.  
3283          :*      MOV MYTEMP,R0      ;/PUT CONVERTED VALUE IN R0.  
3284          :*      MOV @ADBUFF,MYTEMP      ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.  
3285          :*      MOV MYTEMP,R0      ;/PUT CONVERTED VALUE IN R0.  
3286          :*      MOV @ADBUFF,MYTEMP      ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.  
3287          :*      MOV MYTEMP,R0      ;/PUT CONVERTED VALUE IN R0.  
3288          :*      MOV @ADBUFF,MYTEMP      ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.  
3289          :*      MOV MYTEMP,R0      ;/PUT CONVERTED VALUE IN R0.  
3290 003400          :*      MOV @ADBUFF,MYTEMP      ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.  
(1)          :*      MOV MYTEMP,R0      ;/PUT CONVERTED VALUE IN R0.  
(1)          :*      MOV @ADBUFF,MYTEMP      ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.  
(1)          :*      MOV MYTEMP,R0      ;/PUT CONVERTED VALUE IN R0.  
3291 003410          :*      MOV @ADBUFF,MYTEMP      ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.  
(1)          :*      MOV MYTEMP,R0      ;/PUT CONVERTED VALUE IN R0.  
(1)          :*      MOV @ADBUFF,MYTEMP      ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.  
3292 003420 023737 001124 001126      :*      MOV @STREG,$BDDAT      ;/READ DEVICE REG STREG,PUT DATA IN $BDDAT.  
3293 003426 001002          CMP $GDDAT,$BDDAT      ;COMPARE RESULTS  
3294 003430 062716 000002          BNE RETERR      ;;ERROR RETURN  
3295 003434 000207          ADD #2,(SP)      ;BUMP RETURN ADDRESS TO GET AROUND ERROR  
          RETERR: RTS PC
```



3297  
3298 003436  
3299  
(3)  
(3)  
(2) 003436 000240  
(1) 003440 012737 000010 001160  
3300 003446 012737 000011 001102  
3301 003454 012737 003470 001110  
3302 003462 012737 003470 001106  
3303 003470 004537 011072  
3304 003474 000014  
3305 003476 004537 011314  
3306 003502 004000  
3307 003504 011726  
3308 003506 104004  
3309  
3310  
(3)  
(3)  
(2) 003510 000004  
(1) 003512 012737 000010 001160  
3311 003520 005737 001332  
3312 003524 001044  
3313 003526 012737 000020 001426  
3314  
(1)  
3315 003544 012700 001000  
3316 003550 012737 000220 001124  
3317 003556 012737 000200 001426  
3318  
(1)  
3319  
3320 003574 005300  
3321 003576 001376  
3322 003600 004737 003410  
3323 003604 104001  
3324  
(2)  
(1) 003616 013700 001426  
3325 003622 005037 001426  
(2)  
(2)  
3326  
(3)  
(3)  
(2) 003636 000004  
(1) 003640 012737 000010 001160  
3327 003646 004537 011072  
3328 003652 000000  
3329 003654 004537 011314  
3330 003660 004000  
3331 003662 011720  
3332 003664 104004

```
.SBTTL          WRAPAROUND TEST SECTION
WRAP:
:*****
:*TEST 11      TEST CH14 GROUND
:*****
TST11:  NOP
        MOV     #10,$TIMES      ;;DO 10 ITERATIONS
        MOV     #$TN-1,$TSTNM
        MOV     #1$,$LPERR
        MOV     #1$,$LPADR
1$:     JSR     R5,CONVRT        ;DO 8 CONVERSIONS
        14
        JSR     R5,COMPAR        ;COMPARE RESULTS
        4000                    ;NOMINAL
        V50                      ;TOLERANCE
        ERROR 4                  ;ERROR-CH14 NOT GROUND-AD11K MUST BE IN SINGLE-ENDED
;CONFIGURATION,G5036 WRAPAROUND MODULE MUST BE PRESENT,CHECK CONNECTION A-VV,VV-A
:*****
:*TEST 12      TEST CONVERSION FROM EXT. START
:*****
TST12:  SCOPE
        MOV     #10,$TIMES      ;;DO 10 ITERATIONS
        TST     BASECH          ;TESTING AN AM?
        BNE     TST13          ;;YES, GOTO NEXT TEST
        MOV     #BIT4,MYTEMP
;*      MOV     MYTEMP,@STREG   ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
        MOV     #BIT9,R0        ;TIME DELAY COUNTER
        MOV     #BIT7!BIT4,$GDDAT ;LOAD EXPECTED
        MOV     #200,MYTEMP
;*      MOV     MYTEMP,@ADBUFF ;/ PUT DATA FROM MYTEMP TO DEVICE REG ADBUFF
;WRAPAROUND MODULE PRESENT
1$:     DEC     R0
        BNE     1$
        JSR     PC,TEST
        ERROR 1                  ;FAILED TO DO CONVERSION FROM EXT. START
;*      MOV     @ADBUFF,MYTEMP ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.
        MOV     MYTEMP,R0      ;/PUT CONVERTED VALUE IN R0.
        CLR     MYTEMP
;*      MOV     MYTEMP,@STREG   ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
:*****
:*TEST 13      TEST CH0 GROUND
:*****
TST13:  SCOPE
        MOV     #10,$TIMES      ;;DO 10 ITERATIONS
        JSR     R5,CONVRT        ;CONVERT 8 TIMES
        0
        JSR     R5,COMPAR        ;COMPARE RESULTS
        4000                    ;NOMINAL
        V1                      ;TOLERANCE
        ERROR 4                  ;ERROR ON A/D CHANNEL
```

3334  
(3)  
(3)  
(2) 003666 000004  
(1) 003670 012737 000010 001160  
3335 003676 004537 011072  
3336 003702 000001  
3337 003704 004537 011314  
3338 003710 004000  
3339 003712 011724  
3340 003714 104004  
3341  
3342  
(3)  
(3)  
(2) 003716 000004  
(1) 003720 012737 000010 001160  
3343 003726 004537 011072  
3344 003732 000002  
3345 003734 004537 011314  
3346 003740 004632  
3347 003742 011726  
3348 003744 104004  
3349  
3350  
3351  
(3)  
(3)  
(2) 003746 000004  
(1) 003750 012737 000010 001160  
3352 003756 004537 011072  
3353 003762 000003  
3354 003764 004537 011314  
3355 003770 006000  
3356 003772 011734  
3357 003774 104004  
3358  
3359  
(3)  
(3)  
(2) 003776 000004  
(1) 004000 012737 000010 001160  
3360 004006 004537 011072  
3361 004012 000004  
3362 004014 004537 011314  
3363 004020 002000  
3364 004022 011734  
3365 004024 104004

```
*****
*TEST 14 TEST CH1 GROUND
*****
TST14: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
JSR R5,CONVRT ;CONVERT 8 TIMES
1 ;CHANNEL 1
JSR R5,COMPAR ;COMPARE RESULTS
4000 ;NOMINAL
V10 ;TOLERANCE
ERROR 4 ;ERROR ON A/D CHANNEL

*****
*TEST 15 TEST CH2 +1 VOLT
*****
TST15: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
JSR R5,CONVRT ;CONVERT 8 TIMES
2 ;CHANNEL 2
JSR R5,COMPAR ;COMPARE RESULTS
4632 ;NOMINAL
V50 ;TOLERANCE
ERROR 4 ;ERROR ON A/D CHANNEL
;AD11K MUST BE SET UP FOR +OR- 5V OR +OR- 5.12V

*****
*TEST 16 TEST CH3 +2.5 VOLTS
*****
TST16: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
JSR R5,CONVRT ;CONVERT 8 TIMES
3 ;CHANNEL 3
JSR R5,COMPAR ;COMPARE RESULTS
6000 ;NOMINAL
V240 ;TOLERANCE
ERROR 4 ;ERROR ON A/D CHANNEL

*****
*TEST 17 TEST CH4 -2.5 VOLTS
*****
TST17: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
JSR R5,CONVRT ;CONVERT 8 TIMES
4 ;CHANNEL 4
JSR R5,COMPAR ;COMPARE RESULTS
2000 ;NOMINAL
V240 ;TOLERANCE
ERROR 4
```

```

3367          ;*****
(3)          ;*TEST 20      TEST VERNIER OFFSET DAC ON CH12
(3)          ;*****
(2) 004026 000004          TST20: SCOPE
(1) 004030 012737 000001 001160 MOV #1,$TIMES      ;;DO 1 ITERATION
3368 004036 005037 001426 CLR MYTEMP
3369          ;*
(1)          ;* MOV MYTEMP,@ADBUFF ;/ PUT DATA FROM MYTEMP TO DEVICE REG ADBUFF
3370 004052 004737 004646 JSR PC,DAWAIT ;DELAY FOR DAC SETTLING
3371 004056 004537 011072 JSR R5,CONVRT ;CONV. CH12, DIRECT VERNIER DAC
3372 004062 000012 12
3373 004064 013704 001346 MOV TEMP,R4 ;SAVE VALUE IN R4
3374 004070 004537 011314 JSR R5,COMPAR ;COMPARE RESULTS
3375 004074 002376 2376 ;WITH -1.875 VOLTS
3376 004076 011732 V115 ;TOLERANCE OF 10%
3377 004100 104004 ERROR 4
3378 004102 005037 001420 CLR MAX
3379 004106 012702 000001 MOV #1,R2
3380 004112 010237 001426 1$: MOV R2,MYTEMP ;SET UP NEXT VERNIER DAC VALUE
3381          ;*
(1)          ;* MOV MYTEMP,@ADBUFF ;/ PUT DATA FROM MYTEMP TO DEVICE REG ADBUFF
3382 004126 004737 004646 JSR PC,DAWAIT ;DELAY FOR DAC SETTLING
3383 004132 004537 011072 JSR R5,CONVRT ;CONVERT IT
3384 004136 000012 12
3385 004140 005737 001420 TST MAX
3386 004144 001010 BNE 2$
3387 004146 023727 001346 004000 CMP TEMP,#4000
3388 004154 002404 BLT 2$
3389 004156 005237 001420 INC MAX
3390 004162 010237 001414 MOV R2,MIN
3391 004166 020227 000200 2$: CMP R2,#200
3392 004172 001003 BNE 3$
3393 004174 013737 001346 004266 MOV TEMP,4$
3394 004202 013703 001346 3$: MOV TEMP,R3 ;SAVE VALUE
3395 004206 160437 001346 SUB R4,TEMP ;TEMP=DIFF. BETWEEN VALUE&PREVIOUS
3396 004212 010304 MOV R3,R4 ;SET UP PREVIOUS VALUE FOR NEXT TIME THRU
3397 004214 004537 011314 JSR R5,COMPAR ;COMPARE RESULTS
3398 004220 000006 6 ;WITH 15 MILLIVOLTS(1 DAC LSB)
3399 004222 011736 V5
3400 004224 104004 ERROR 4
3401 004226 005202 INC R2
3402 004230 020227 000400 CMP R2,#400 ;DONE?
3403 004234 001326 BNE 1$ ;NO-DO NEXT VERNIER DAC VALUE
3404 004236 004737 020426 JSR PC,$RESET
3405 004242 052777 000100 174674 BIS #100,@$TKS
3406 004250 004737 004646 JSR PC,DAWAIT ;LET DAC SETTLE
3407 004254 004537 011072 JSR R5,CONVRT ;CONVERT IT
3408 004260 000012 12
3409 004262 004537 011314 JSR R5,COMPAR ;COMPARE RESULTS
3410 004266 000000 0
3411 004270 011722 V2
3412 004272 104004 ERROR 4

```

```
3414 (3) *****  
3415 (3) *TEST 21 TEST CH13 +2.5 VOLTS  
3416 (2) 004274 000004 *****  
3417 (1) 004276 012737 000010 001160 TST21: SCOPE  
3418 3415 004304 004537 011072 MOV #10,$TIMES ;;DO 10 ITERATIONS  
3419 3416 004310 000013 JSR R5,CONVRT ;CONVERT 8 TIMES  
3420 3417 004312 004537 011314 JSR R5,COMPAR ;COMPARE RESULTS  
3421 3418 004316 006000 6000 ;NOMINAL  
3422 3419 004320 011730 V144 ;TOLERANCE  
3423 3420 004322 104004 ERROR 4  
3424 (3) *****  
3425 (3) *TEST 22 TEST CH17 +4V  
3426 (2) 004324 000004 *****  
3427 (1) 004326 012737 000010 001160 TST22: SCOPE  
3428 3422 004334 004537 011072 MOV #10,$TIMES ;;DO 10 ITERATIONS  
3429 3423 004340 000017 JSR R5,CONVRT ;CONVERT 8 TIMES  
3430 3424 004342 004537 011314 JSR R5,COMPAR ;CHANNEL 17  
3431 3425 004346 007146 7146 ;COMPARE RESULTS  
3432 3426 004350 011734 V240 ;NOMINAL  
3433 3427 004352 104004 ERROR 4 ;TOLERANCE  
3434 (3) *****  
3435 (3) *TEST 23 OFFSET ON CH0 *****  
3436 (2) 004354 000004 *****  
3437 (1) 004356 012737 000001 001160 TST23: SCOPE  
3438 3429 004364 013737 001332 001362 MOV #1,$TIMES ;;DO 1 ITERATION  
3439 3430 004372 013737 001332 001360 MOV BASECH,CHANL ;LOAD CHANNEL  
3440 3431 004400 012737 004001 001410 MOV BASECH,DUMMY ;LOAD DUMMY  
3441 3432 004406 004537 006452 MOV #4001,EDGE  
3442 3433 004412 000062 JSR R5,SARSUB  
3443 3434 004414 013737 001404 001346 MOV 50.  
3444 3435 004422 004537 006452 JSR DAC,TEMP  
3445 3436 004426 000062 JSR R5,SARSUB  
3446 3437 004430 063737 001404 001346 ADD 50.  
3447 3438 004436 162737 000062 001346 SUB DAC,TEMP  
3448 3439 004444 013700 001414 MOV #62,TEMP  
3449 3440 004450 006300 ASL MIN,R0  
3450 3441 004452 160037 001346 SUB R0,TEMP  
3451 3442 004456 104401 013707 TYPE ,MOFSET ;TYPE ASCIZ STRING  
3452 3443 004462 013702 001346 MOV TEMP,R2  
3453 3444 004466 004737 011504 JSR PC,DECTYP  
3445 004472 104401 013722 TYPE ,MLSB ;TYPE ASCIZ STRING  
3446 004476 004537 011314 JSR R5,COMPAR ;IS RESULT WITHIN LIMITS?  
3447 004502 000000 0  
3448 004504 011740 V50D  
3449 004506 000401 BR OFFERR ;NO-ERROR  
3450 004510 000403 BR OFFOK ;YES-OK  
3451 004512 104401 012511 OFFERR: TYPE ,ERMSG  
3452 004516 000402 BR TST24 ;GO TO NEXT TEST  
3453 004520 104401 012500 OFFOK: TYPE ,OKMSG
```

3455  
(3)  
(3)  
(2) 004524 000004  
(1) 004526 012737 000001 001160  
3456 004534 012737 000116 001346  
3457 004542 004537 010664  
3458 004546 000015  
3459 004550 004537 010664  
3460 004554 000007  
3461 004556 004537 010664  
3462 004562 000016  
3463  
3464  
(3)  
(3)  
(2) 004564 000004  
(1) 004566 012737 000001 001160  
3465 004574 004537 006122  
3466 004600 000015  
3467 004602 000016  
3468 004604 012737 000116 001346  
3469 004612 004537 006122  
3470 004616 000016  
3471 004620 000015  
3472  
(3)  
(3)  
(2) 004622 000004  
(1) 004624 012737 000001 001160  
3473 004632 005737 001202  
3474 004636 001402  
3475 004640 004737 006750  
3476 004644 000207  
3477  
3478 004646 005000  
3479 004650 105300  
3480 004652 001376  
3481 004654 000207

```
*****  
:*TEST 24 NOISE TEST ON 8 EDGES  
*****  
TST24: SCOPE  
MOV #1,$TIMES ;:DO 1 ITERATION  
MOV #116,TEMP ;DAC VALUE  
JSR R5,NOI8 ;NOISE AT -FULL SCALE  
15  
JSR R5,NOI8 ;NOISE AT MID-RANGE  
7  
JSR R5,NOI8 ;NOISE AT +FULL SCALE  
16  
  
*****  
:*TEST 25 SETTLE TEST ON 8 EDGES  
*****  
TST25: SCOPE  
MOV #1,$TIMES ;:DO 1 ITERATION  
JSR R5,SET8 ;SETTLE-POSITIVE DIRECTION  
15  
16  
MOV #116,TEMP  
JSR R5,SET8 ;SETTLE-NEGATIVE DIRECTION  
16  
15  
  
*****  
:*TEST 26 DIFFERENTIAL LINEARITY AND RELATIVE ACCURACY TEST  
*****  
TST26: SCOPE  
MOV #1,$TIMES ;:DO 1 ITERATION  
TST $PASS ;FIRST TIME-SKIP DIFLIN  
BEQ LEND  
JSR PC,DIFLIN  
LEND: RTS PC ;RETURN TO TEST SECTION  
  
DAWAIT: CLR R0  
1$: DECB R0  
BNE 1$  
RTS PC
```

```

3483
3484 004656 012737 004656 001364 .SBTTL CALIBRATION TEST
3485 004664 005037 001426 BEGINC: MOV #BEGINC,TADDR ;TEST ADDRESS IN TADDR
(2) CLR MYTEMP
(2)
;* MOV MYTEMP,@STREG ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
3486 004700 104401 013617 TYPE ,HEAD5 ;TYPE OUT HEADING
3487 004704 005037 177776 CLR PSW
3488 004710 017700 174224 1$: MOV @SWR,R0 ;READ CHANNEL FROM SWITCH REG.
3489 004714 042700 177700 BIC #177700,R0 ;ISOLATE MUX BITS
3490 004720 032777 020000 174212 BIT #BIT13,@SWR ;IS BIT 13 SET?
3491 004726 001005 BNE 2$ ;;YES,SKIP TYPEOUT
3492 004730 104401 012323 TYPE ,CH
3493 004734 010046 MOV R0,-(SP) ;;SAVE R0 FOR TYPEOUT
(1) ;;TYPE CHANNEL
(1) 004736 104403 TYPOS ;;GO TYPE--OCTAL ASCII
(1) 004740 002 .BYTE 2 ;;TYPE 2 DIGIT(S)
(1) 004741 000 .BYTE 0 ;;SUPPRESS LEADING ZEROS
3494 004742 2$: SWAB R0 ;SWITCH BYTES
3495 004742 000300 MOV R0,MYTEMP
3496 004744 010037 001426
(2)
;* MOV MYTEMP,@STREG ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
3497 004760 012702 000010 MOV #10,R2 ;TYPEOUT COUNTER
3498 004764 3$:
(1)
(2)
;* MOV @STREG,MYTEMP ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
(1) 004774 005237 001426 INC MYTEMP
(2)
;* MOV MYTEMP,@STREG ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
3499 005010 30$:
3500
(2)
;* MOV @STREG,MYTEMP ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
(1) 005020 105737 001426 TSTB MYTEMP
3501 005024 100371 BPL 30$
3502
(2)
;* MOV @ADBUFF,MYTEMP ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.
(1) 005036 013700 001426 MOV MYTEMP,R0 ;/PUT CONVERTED VALUE IN R0.
3503 005042 032777 020000 174070 BIT #BIT13,@SWR ;IS BIT 13 SET?
3504 005050 001403 BEQ 4$ ;NOT SET, TYPE OUT LIST
3505 005052 010077 174064 MOV R0,@DISPLAY ;PUT VALUE IN DISPLAY FOR DISPLAY CONTRO
3506 005056 000714 BR 1$ ;REPEAT CONVERSION
3507 005060 104401 012326 4$: TYPE ,SPACE
3508 005064 010046 MOV R0,-(SP) ;;SAVE R0 FOR TYPEOUT
(1) ;;PRINT OCTAL CONVERTED VALUE
(1) 005066 104403 TYPOS ;;GO TYPE--OCTAL ASCII
(1) 005070 004 .BYTE 4 ;;TYPE 4 DIGIT(S)
(1) 005071 001 .BYTE 1 ;;TYPE LEADING ZEROS
3509 005072 012701 010000 5$: MOV #10000,R1
3510 005076 005301 DEC R1
3511 005100 001376 BNE 5$
3512 005102 005302 DEC R2 ;DECREMENT THE COUNTER
3513 005104 001327 BNE 3$ ;NO CARRIAGE RETURN
3514 005106 104401 001171 TYPE ,CRLF ;CARRIAGE RETURN
3515 005112 000676 BR 1$ ;REPEAT CONVERSION

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3517
3518 005114 012737 005114 001364 .SBTTL LOGIC TEST SECTION
3519 005122 005037 001430 BEGL: MOV #BEGL,TADDR ;TEST ADDRESS
3520 005126 004737 002552 CLR EDINT
3521 005132 004737 002710 1$: JSR PC,TESTAD ;NO OF ADDITIONAL AD'S
3522 005136 004737 005322 JSR PC,BEGINL ;LOGIC TESTS
3523 005142 000773 BR PC,BUMPAD ;MORE TO TEST?
3524 005144 012737 005132 012016 MOV 1$ ;TEST NEXT A/D
3525 005152 000137 012020 JMP #1$,AGTST ;ADDRESS FOR EOP
3526 $EOP ;TYPE END OF PASS
3527
3528 005156 012737 005156 001364 .SBTTL AUTO TEST
3529 005164 005037 001430 BEGINA: MOV #BEGINA,TADDR ;TEST ADDRESS
3530 005170 005037 001202 CLR EDINT
3531 005174 004737 002552 CLR $PASS ;CLEAR PASS COUNTER
3532 005200 004737 002710 1$: JSR PC,TESTAD ;NO. OF AD'S TO BE TESTED
3533 005204 104401 013001 JSR PC,BEGINL ;LOGIC TESTS
3534 005210 013746 001316 TYPE ,MEND ;TYPE END OF LOGIC TEST
3535 005214 104403 MOV STREG,-(SP) ;SAVE STREG FOR TYPEOUT
3536 005216 006 TYPOS ;TYPE OCTAL NUMBER
3537 005217 001 .BYTE 6 ;TYPE 6 DIGITS
3538 005220 104401 001171 .BYTE 1 ;TYPE LEADING ZEROS
3539 005224 004737 003436 TYPE ,$CR LF ;TYPE A CR,LF
3540 005230 004737 005322 JSR PC,WRAP
3541 005234 000761 JSR PC,BUMPAD ;TEST NEXT A/D
3542 005236 012737 005200 012016 BR 1$ ;TEST NEXT AD
3543 005244 000137 012020 MOV #1$,AGTST ;ADDRESS FOR EOP
3544 JMP $EOP ;TYPE END OF PASS
3545
3546 005250 012737 005250 001364 .SBTTL WRAPAROUND TEST
3547 005256 005037 001430 BEGINW: MOV #BEGINW,TADDR ;TEST ADDRESS
3548 005262 005037 001202 CLR EDINT
3549 005266 004737 002552 CLR $PASS ;CLEAR PASS COUNT
3550 005272 004737 003436 1$: JSR PC,TESTAD ;NO. OF AD'S TO BE TESTED
3551 005276 005037 001430 JSR PC,WRAP ;WRAPAROUND TESTS
3552 005302 004737 005322 CLR EDINT
3553 005306 000771 JSR PC,BUMPAD ;MORE A/D'S TO BE TESTED?
3554 005310 012737 005272 012016 BR 1$ ;YES-GO TEST NEXT AD11K
3555 005316 000137 012020 MOV #1$,AGTST
JMP $EOP ;INCREMENTS $PASS

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3557          ; DETERMINE IF MORE AD11K'S TO BE TESTED
3558 005322 005737 001354      ; BUMPAD: TST NBEXT ; ADDITIONAL AD'S?
3559 005326 001421              BEQ FIXADR ; NO-INITIALIZE ADDRESSES
3560 005330 063737 001326 001316 ADD VADR,STREG ; SET UP NEW ST. REG.
3561 005336 063737 001326 001320 ADD VADR,ADBUFF ; SET UP NEW BUFFER ADDRESS
3562 005344 063737 001330 001456 ADD VVCT,VECTOR ; SET UP NEW VECTOR
3563 005352 063737 001330 001324 ADD VVCT,VECTR1
3564 005360 005077 173740      CLR @VECTR1
3565 005364 005337 001354      DEC NBEXT ; ONE LESS AD11K
3566 005370 000441              BR BYPASS
3567 005372 062716 000002      FIXADR: ADD #2,(SP)
3568 005376 013737 001250 001316 FIXONE: MOV $BASE,STREG ; RELOAD INITIAL ADDRESSES
3569 005404 013737 001250 001320 MOV $BASE,ADBUFF
3570 005412 062737 000002 001320 ADD #2,ADBUFF
3571 005420 013737 001244 001456 MOV $VECT1,VECTOR
3572 005426 042737 170000 001456 BIC #170000,VECTOR
3573 005434 113737 001245 001322 MOVB $VECT1+1,BASEBR
3574 005442 105037 001323      CLRB BASEBR+1 ; CLEAR HIGH BYTE
3575 005446 013737 001456 001324 MOV VECTOR,VECTR1
3576 005454 062737 000002 001324 ADD #2,VECTR1
3577 005462 005077 173636      CLR @VECTR1
3578 005466 013737 001354 001354 MOV NMBEXT,NBEXT ; RESET COUNTER
3579          ;; LOAD .+2 AND HALT TRAP CATCH;;
3580 005474 012700 000216      BYPASS: MOV #216,R0 ; FILL .+2
3581 005500 012701 000214      MOV #214,R1 ; LOAD HALT
3582 005504 020137 001334      1$: CMP R1,KBVECT
3583 005510 001410              BEQ 2$
3584 005512 010021              MOV RO,(R1)+
3585 005514 005021              CLR (R1)+
3586 005516 010100              MOV R1,R0
3587 005520 005720              TST (R0)+
3588 005522 020027 001002      CMP RO,#1002
3589 005526 001366              BNE 1$
3590 005530 000207              RTS PC ; TEST NEXT A/D
3591 005532 022021      2$: CMP (R0)+,(R1)+
3592 005534 022021      CMP (R0)+,(R1)+
3593 005536 000762              BR 1$
3594
3595
3596          ; NOISE TEST, 1 EDGE
3597 005540 012737 005540 001364 ; BEGINN: MOV #BEGINN,TADDR ; TEST ADDRESS IN TADDR
3598 005546 104401 012132      TYPE ,NOIMSG ; ASK FOR CHANNEL
3599 005552 104401 013636      TYPE ,ASKCH
3600 005556 017737 173356 001350 1$: MOV @SWR,CH1 ; LOAD CHANNEL
3601 005564 042737 177700 001350 BIC #177700,CH1
3602 005572 012737 000200 001346 MOV #200,TEMP ; LOAD DAC VALUE
3603 005600 004537 010400      JSR R5,NOITST ; GO TO NOISE SUBROUTINE
3604 005604 001350      CH1
3605 005606 000763      BR 1$

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3607
3608 005610 012737 005610 001364 : INTERCHANNEL SETTLING TEST, 1 EDGE
3609 005616 104401 012152 : BEGINS: MOV #BEGINS,TADDR ;TEST ADDRESS IN TADDR
3610 005622 104407 : RDOCT ;ASK FOR CHANNELS
3611 005624 012637 001350 : MOV (SP)+,CH1
3612 005630 104401 012437 : TYPE ,TOMSG
3613 005634 104407 : RDOCT
3614 005636 012637 001352 : MOV (SP)+,CH2
3615 005642 012737 000200 001346 BK3: MOV #200,TEMP ;LOAD DAC
3616 005650 013737 001352 001362 : MOV CH2,CHANL
3617 005656 004737 006226 : JSR PC,GETEDG ;GET EDGE VALUES
3618 005662 005002 : CLR R2
3619 005664 004737 006060 : JSR PC,SET1A ;SCALING = .02 LSB
3620 005670 004737 006060 : JSR PC,SET1A ;MAKE IT .01 LSB
3621 005674 100001 : BPL POSR2
3622 005676 005402 : NEG R2
3623 005700 010204 : POSR2: MOV R2,R4
3624 005702 012737 000001 006450 : MOV #1,EDGFLG
3625 005710 004737 005716 : JSR PC,TYPSET
3626 005714 000752 : BR BK3
3627 005716 004737 011504 : TYPSET: JSR PC,DECTYP
3628 005722 104401 012333 : TYPE ,LSB
3629 005726 013746 001352 : MOV CH2,-(SP) ;;SAVE CH2 FOR TYPEOUT
(1) : ;;TYPE CH
(1) 005732 104403 : TYPOS ;;GO TYPE--OCTAL ASCII
(1) 005734 002 : .BYTE 2 ;;TYPE 2 DIGIT(S)
(1) 005735 000 : .BYTE 0 ;;SUPPRESS LEADING ZEROS
3630 005736 104401 013730 : TYPE ,MAT ;;TYPE ASCIIZ STRING
3631 005742 004737 006406 : JSR PC,TYPEDG
3632 005746 104401 012346 : TYPE ,SETCH
3633 005752 013746 001350 : MOV CH1,-(SP) ;;SAVE CH1 FOR TYPEOUT
(1) : ;;TYPE CH
(1) 005756 104403 : TYPOS ;;GO TYPE--OCTAL ASCII
(1) 005760 002 : .BYTE 2 ;;TYPE 2 DIGIT(S)
(1) 005761 000 : .BYTE 0 ;;SUPPRESS LEADING ZEROS
3634 005762 104401 012370 : TYPE ,ATMSG
3635 005766 013737 001350 006024 : MOV CH1,1$
3636 005774 163737 001332 006024 : SUB BASECH,1$
3637 006002 012737 000200 001426 : MOV #200,MYTEMP
3638 (1) :
(1) :
(1) :
3639 006020 004537 011072 : ;* MOV MYTEMP,@ADBUFF ;/ PUT DATA FROM MYTEMP TO DEVICE REG ADBUFF
3640 006024 000000 : JSR R5,CONVRT
3641 006026 013746 001346 : 1$: MOV TEMP,-(SP) ;;SAVE TEMP FOR TYPEOUT
(1) : ;;TYPE VALUE
(1) 006032 104403 : TYPOS ;;GO TYPE--OCTAL ASCII
(1) 006034 004 : .BYTE 4 ;;TYPE 4 DIGIT(S)
(1) 006035 001 : .BYTE 1 ;;TYPE LEADING ZEROS
3642 006036 020437 011746 : CMP R4,VSET
3643 006042 003003 : BGT ERR
3644 006044 104401 012500 : TYPE ,OKMSG
3645 006050 000207 : RTS PC

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3647 006052 104401 012511      ERR:  TYPE  ,ERMSG
3648 006056 000207              RTS    PC
3649
3650
3651
3652
3653 006060 013737 001352 001360  ;:SUBROUTINE FOR SETTLING TESTS::
3654 006066 004537 006452          SET1A: MOV    CH2,DUMMY      ;LOAD DUMMY
3655 006072 000062                    JSR    R5,SARSUB      ;DO SAR ROUTINE AT 50%
3656 006074 063702 001404          50.
3657 006100 013737 001350 001360  ADD    DAC,R2        ;ADD RESULT TO R2
3658 006106 004537 006452          MOV    CH1,DUMMY    ;CHANGE DUMMY VALUE
3659 006112 000062                    JSR    R5,SARSUB      ;DO SAR ROUTINE AT 50%
3660 006114 163702 001404          50.
3661 006120 000207                    SUB    DAC,R2        ;SUBTRACT RESULT FROM R2
3662                                RTS    PC            ;RETURN
3663 006122 012537 001350          SET8:  MOV    (R5)+,CH1      ;GET FIRST CHANNEL
3664 006126 012537 001352          MOV    (R5)+,CH2      ;GET SECOND CHANNEL
3665 006132 063737 001332 001350  ADD    BASECH,CH1
3666 006140 063737 001332 001352  ADD    BASECH,CH2
3667 006146 004737 006226          JSR    PC,GETEDG     ;GET EDGE VALUES
3668 006152 005002                    CLR    R2
3669 006154 012703 000010          MOV    #10,R3        ;SET UP COUNTER
3670 006160 004737 006060          SETAA: JSR    PC,SET1A    ;GET SETTLE VALUES
3671 006164 005237 001410          INC    EDGE
3672 006170 005303                    DEC    R3
3673 006172 001372                    BNE    SETAA        ;REPEAT 8 TIMES
3674 006174 162737 000010 001410  SUB    #10,EDGE
3675 006202 005702                    TST   R2
3676 006204 100001                    BPL   R2POS
3677 006206 005402                    NEG   R2
3678 006210 010204          R2POS: MOV    R2,R4
3679 006212 012737 000010 006450  MOV    #8.,EDGFLG
3680 006220 004737 005716          JSR    PC,TYPSET    ;TYPE OUT RESULTS
3681 006224 000205                    RTS    R5            ;RETURN
3682
3683
3684                                ;SUBROUTINE TO GET EDGE VALUE
3685                                ;CALL=JSR PC,GETEDG
3686                                ;CONVERSIONS ON A/D CHANNEL 'CHANL'
3687                                ;RESULT IN EDGE, USES R0
3688 006226          GETEDG:
3689 (1)
3689 (1)                                ;*  MOV    TEMP,@ADBUFF    ;/ PUT DATA FROM TEMP TO DEVICE REG ADBUFF
3690 006236 113700 001362          MOVB  CHANL,R0      ;GET CHANNEL
3691 006242 000300                    SWAB  R0            ;SET UP A.D STATUS REG.
3692 006244 010037 001426          MOV    R0,MYTEMP
3693 (2)
3694 (2)                                ;*  MOV    MYTEMP,@STREG   ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
3692 006260 012700 000100          MOV    #100,R0     ;DAC SETTLING DELAY
3693 006264 005300          1$:  DEC    R0
3694 006266 001376                    BNE   1$
3695 006270 005037 001410          CLR   EDGE
3696 006274 012700 000010          MOV   #10,R0
3697 006300          CONV:

```

```
(2)
(2)
(1) 006310 005237 001426      ;*   MOV   @STREG,MYTEMP   ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
(2)                               INC   MYTEMP
(2)
(2)                               ;*   MOV   MYTEMP,@STREG   ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
3698 006324      30$:
(2)
(2)                               ;*   MOV   @STREG,MYTEMP   ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
(1) 006334 105737 001426      TSTB  MYTEMP
3699 006340 100371      BPL   30$
3700
(2)
(2)                               ;*   MOV   @ADBUFF,MYTEMP  ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.
(1) 006352 063737 001426 001410  ADD   MYTEMP,EDGE
3701 006360 005300      DEC   R0
3702 006362 001346      BNE   CONV
3703 006364 006237 001410      ASR   EDGE
3704 006370 006237 001410      ASR   EDGE
3705 006374 006237 001410      ASR   EDGE
3706 006400 005537 001410      ADC   EDGE
3707 006404 000207      RTS   PC
3708      ;;SUBROUTINE TO TYPE EDGE VALUES;;
3709 006406 013703 001410  TYPEDG: MOV   EDGE,R3
3710 006412 010346      MOV   R3,-(SP)      ;;SAVE R3 FOR TYPEOUT
(1)                               ;;TYPE OCTAL VALUE OF EDGE
(1) 006414 104403      TYPOS      ;;GO TYPE--OCTAL ASCII
(1) 006416 004      .BYTE 4      ;;TYPE 4 DIGIT(S)
(1) 006417 001      .BYTE 1      ;;TYPE LEADING ZEROS
3711 006420 023727 006450 000001  CMP   EDGFLG,#1
3712 006426 001407      BEQ   RET
3713 006430 062703 000007      ADD   #7,R3
3714 006434 104401 013700      TYPE  ,C1      ;TYPE ASCII STRING
3715 006440 010346      MOV   R3,-(SP)      ;;SAVE R3 FOR TYPEOUT
(1)                               ;;TYPE EDGE VALUE
(1) 006442 104403      TYPOS      ;;GO TYPE--OCTAL ASCII
(1) 006444 004      .BYTE 4      ;;TYPE 4 DIGIT(S)
(1) 006445 001      .BYTE 1      ;;TYPE LEADING ZEROS
3716 006446 000207      RET:   RTS   PC
3717 006450 000000      EDGFLG: 0
```

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3719 ;SUBROUTINE TO DO SUCCESSIVE APPROXIMATION ROUTINE
3720 ;CALL=JSR R5,SARSUB
3721 ; XXX;XXX=PERCENT
3722 ;RESULT RETURNED IN 'DAC',USES R0,R1,R4
3723 006452 012537 001422 SARSUB: MOV (R5)+,PERCNT ;GET PERCENT
3724 006456 006337 001422 ASL PERCNT
3725 006462 006337 001422 ASL PERCNT
3726 006466 012737 000620 006746 MOV #400.,CNNO ;NO OF SAMPLES FOR SHORT PASS.
3727 006474 032777 004000 172436 BIT #BIT11,@SWR ;USER WANT SHORT PASS?
3728 006502 001010 BNE SAR1
3729 006504 000407 BR SAR1 ;ALWAYS USE SHORT SAMPLE COUNT.
3730 006506 012737 003100 006746 MOV #1600.,CNNO
3731 006514 006337 001422 ASL PERCNT ;RESCALE PERCENT FOR 1600.
3732 006520 006337 001422 ASL PERCNT ;POINTS PER BURST
3733 006524 012737 000200 001412 SAR1: MOV #200,BITPNT ;INITIALIZE BIT POINTER AT MSB
3734 006532 005037 001404 CLR DAC ;INITIALIZE DAC VALUE
3735 006536 004537 020744 JSR R5,$PUTS
3736 006542 001316 .WORD STREG
3737 006544 005000 TRY: CLR R0
3738 006546 063737 001412 001404 ADD BITPNT,DAC ;TRY BIT
3739 (1)
3740 006564 012737 000100 001406 ;* MOV DAC,@ADBUFF ;/ PUT DATA FROM DAC TO DEVICE REG ADBUFF
3741 006572 005337 001406 1$: MOV #100,DELAY
3742 006576 001375 DEC DELAY ;STALL TIME
3743 006600 013701 006746 BNE 1$
3744 006604 113737 001362 001435 MOV CNNO,R1 ;SET UP FOR 1600. OR 400. CONVERSIONS
3745 006612 052737 000001 001434 MOVB CHANL,$TEMP2+1
3746 006620 113737 001360 001433 BIS #1,$TEMP2
3747 006626 052737 000001 001432 MOVB DUMMY,$TEMP1+1
3748 006634 BIS #1,$TEMP1
3749 006634 013777 001432 172604 NXTCVT: MOV $TEMP1,@KMAD4
3750 006642 112777 000006 172572 $TMP: MOVB #6,@KMAD2
3751 006650 122777 000377 172564 10$: CMPB #377,@KMAD2
3752 006656 001374 BNE 10$
3753 006660 013777 001434 172560 MOV $TEMP2,@KMAD4
3754 006666 112777 000006 172546 MOVB #6,@KMAD2
3755 006674 122777 000377 172540 20$: CMPB #377,@KMAD2
3756 006702 001374 BNE 20$
3757 006704 027737 172536 001410 CMP @KMAD4,EDGE
3758 006712 002001 BGE 2$
3759 006714 005200 INC R0 ;COUNT RESULTS .LT. EDGE
3760 006716 005301 2$: DEC R1
3761 006720 001345 BNE NXTCVT
3762 006722 020037 001422 CMP R0,PERCNT
3763 006726 003003 BGT SHIFT
3764 006730 163737 001412 001404 SUB BITPNT,DAC ;TAKE THE BIT OUT
3765 006736 006237 001412 SHIFT: ASR BITPNT
3766 006742 001300 BNE TRY
3767 006744 000205 RTS R5
3768
3769 006746 000000 CNNO: .WORD 0

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3771      ::DIFFERENTIAL LINEARITY SUBROUTINE::
3772 006750 104401 013124  DIFLIN: TYPE      ,MSG20
3773 006754 005037 001424      CLR      OUT
3774 006760 012700 042300      MOV      #BUFFER,R0
3775 006764 012701 010000      MOV      #4096.,R1      ;4096 WORDS FOR HISTOGRAM
3776 006770 005020      CLEAR1: CLR      (R0)+      ;CLEAR BUFFER AREA
3777 006772 005301      DEC      R1
3778 006774 001375      BNE      CLEAR1
3779 006776 012700 021540      MOV      #DIST,R0      ;DISTRIBUTION BUFFER POINTER
3780 007002 012701 000310      MOV      #200.,R1      ;200. WORDS FOR DISTRIBUTION
3781 007006 005003      CLR      R3
3782 007010 005037 001424      CLR      OUT
3783 007014 005037 001336      CLR      WIDE
3784 007020 005037 001340      CLR      NARROW
3785 007024 005037 001342      CLR      FIRST
3786 007030 005037 001344      CLR      SKIPST
3787 007034 005020      CLEAR2: CLR      (R0)+      ;CLEAR DISTRIBUTION BUFFER AREA
3788 007036 005301      DEC      R1
3789 007040 001375      BNE      CLEAR2
3790 007042 012700 000011      CHANNL: MOV      #11,R0      ;CHANNEL 11
3791 007046 063700 001332      ADD      BASECH,R0
3792 007052 000300      SWAB     R0      ;LOAD MUX BITS
3793 007054 004537 020744      JSR      R5,$PUTS
3794 007060 001316      .WORD   STREG
3795 007062 010037 001426      MOV      R0,MYTEMP
(2)
(2)
3796 007076 010037 001432      ;*      MOV      MYTEMP,@STREG      ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
3797 007102 052737 000001 001432      MOV      R0,$TEMP1
3798 007110 012700 001440      BIS      #1,$TEMP1
3799 007114 012777 001704 172334      MOV      #800.,R0      ;NOMINAL STATE WIDTH - 1 LSB
3800 007122 012701 007776      AGAIN: MOV      #RETURN,@VECTOR
3801 007126 004737 011010      NEXT:  JSR      PC,RANDY      ;GET RANDOM NUMBER
3802 007132 013702 001366      MOV      RNA,R2
3803 007136 042702 177760      BIC      #177760,R2      ;MASK IT TO 4 BITS ONLY
3804 007142 001402      BEQ      CONVR
3805 007144 005302      DELAY3: DEC      R2      ;STALL
3806 007146 001376      BNE      DELAY3      ;TIME
3807 007150      CONVR:
3808 007150 013777 001432 172270      $TBF4: MOV      $TEMP1,@KMAD4
3809 007156 112777 000006 172256      MOV      #6,@KMAD2
3810 007164 122777 000377 172250      31$:  CMP      #377,@KMAD2
3811 007172 001374      BNE      31$
3812 007174 017702 172246      MOV      @KMAD4,R2
3813 007200 001413      BEQ      DELAY1      ;IGNORE IF =0
3814 007202 020227 007777      CMP      R2,#7777      ;IGNORE IF =7777
3815 007206 001413      BEQ      DELAY2
3816 007210 006302      ASL      R2
3817 007212 005262 042300      INC      BUFFER(R2)      ;MAKE HISTOGRAM
3818 007216 100013      BPL      OKAY
3819 007220 012762 077777 042300      MOV      #077777,BUFFER(R2)      ;PREVENT OVERFLOW
3820 007226 000407      BR      OKAY
3821 007230 020227 007777      DELAY1: CMP      R2,#7777      ;EQUALIZE LOOP TIME
3822 007234 001400      BEQ      DELAY2      ;WITH DUMMY INSTR.
3823 007236 005201      DELAY2: INC      R1
3824 007240 005263 001346      INC      TEMP(R3)

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3825 007244 100403  
3826 007246 005301  
3827 007250 001326  
3828 007252 000403  
3829 007254 005037 001346  
3830 007260 000772  
3831 007262 005300  
3832 007264 001316  
3833  
3834 007266 012700 007776  
3835 007272 012701 042302  
3836 007276 012102  
3837 007300 006202  
3838 007302 006202  
3839 007304 006202  
3840 007306 005502  
3841 007310 020227 000310  
3842 007314 002403  
3843 007316 005237 001424  
3844 007322 000423  
3845 007324 006302  
3846 007326 005262 021540  
3847 007332 006202  
3848 007334 020227 000062  
3849 007340 002007  
3850 007342 005237 001340  
3851 007346 005702  
3852 007350 001002  
3853 007352 005237 001344  
3854 007356 000405  
3855 007360 020227 000226  
3856 007364 003426  
3857 007366 005237 001336  
3858 007372 005737 001342  
3859 007376 001004  
3860 007400 005237 001342  
3861 007404 104401 012303  
3862 007410 010103  
3863 007412 162703 042302  
3864 007416 006203  
3865 007420 010346  
(1)  
(1) 007422 104403  
(1) 007424 004  
(1) 007425 001  
3866 007426 104401 012277  
3867 007432 004737 011504  
3868 007436 104401 012270  
3869 007442 005300  
3870 007444 001314  
3871 007446 112737 000177 014576  
3872 007454 013702 001344  
3873 007460 004737 011504  
3874 007464 104401 012526  
3875 007470 005737 001344  
3876 007474 001403

OKAY: BMI NOTOK  
DEC R1  
BNE NEXT  
BR AROUND  
NOTOK: CLR TEMP  
BR OKAY  
AROUND: DEC R0  
BNE AGAIN  
;DATA COLLECTION HAS NOW BEEN COMPLETED - WORK ON THE DATA COLLECTED  
MOV #4094.,R0  
MOV #BUFFER+2,R1  
READ: MOV (R1)+,R2 ;GET STATE WIDTH  
ASR R2 ;1 LSB = 800.  
ASR R2  
ASR R2  
ADC R2 ;1 LSB = 100.  
CMP R2,#200. ;OUT OF RANGE?  
BLT INRNGE  
INC OUT ;YES - INCREMENT COUNTER  
BR TYPBAD  
INRNGE: ASL R2  
INC DIST(R2) ;MAKE STATE WIDTH DISTRIBUTION  
ASR R2  
CMP R2,#50. ;IS IT 1/2 LSB?  
BGE NOTNAR  
INC NARROW  
TST R2 ;IS IT A SKIPPED STATE?  
BNE 31\$  
INC SKIPST  
BR TYPBAD  
NOTNAR: CMP R2,#150. ;IS IT 1.5 LSB?  
BLE LAST  
INC WIDE  
TYPBAD: TST FIRST  
BNE 60\$  
INC FIRST  
TYPE ,STATE  
60\$: MOV R1,R3  
SUB #BUFFER+2,R3  
ASR R3  
MOV R3,-(SP) ;:SAVE R3 FOR TYPEOUT  
;:TYPE STATE  
;:GO TYPE--OCTAL ASCII  
;:TYPE 4 DIGIT(S)  
;:TYPE LEADING ZEROS  
TYPOS  
.BYTE 4  
.BYTE 1  
TYPE ,DASH  
JSR PC,DECTYP  
TYPE ,LSBMSG  
LAST: DEC R0  
BNE READ  
MOVB #177,DECPNT  
MOV SKIPST,R2 ;:GET NO. OF SKIPPED STATES  
JSR PC,DECTYP ;:TYPE IT  
TYPE ,SKPMSG ;:TYPE MESSAGE  
TST SKIPST  
BEQ 1\$

3877	007476	104401	012511		TYPE	,ERMSG	;TYPE 'ERROR'
3878	007502	000402			BR	NAR	
3879	007504	104401	012500	1\$:	TYPE	,OKMSG	;TYPE #OK#
3880	007510	013702	001340	NAR:	MOV	NARROW,R2	;GET NO. OF NARROW STATES
3881	007514	004737	011504		JSR	PC,DECTYP	;TYPE IT
3882	007520	104401	012550		TYPE	,NARMSG	;TYPE MESSAGE
3883	007524	013702	001336		MOV	WIDE,R2	
3884	007530	063702	001424		ADD	OUT,R2	
3885	007534	004737	011504		JSR	PC,DECTYP	;TYPE NO. OF WIDE STATES
3886	007540	104401	012607		TYPE	,WIDMSG	;TYPE MESSAGE
3887	007544	013702	001424		MOV	OUT,R2	
3888	007550	004737	011504		JSR	PC,DECTYP	;TYPE NO. OF STATES OUTSIDE 2 LSB
3889	007554	104401	012646		TYPE	,OUTMSG	;TYPE MESSAGE
3890	007560	005737	001424		TST	OUT	
3891	007564	001403			BEQ	11\$	
3892	007566	104401	012511		TYPE	,ERMSG	;TYPE 'ERROR'
3893	007572	000402			BR	HALF	
3894	007574	104401	012500	11\$:	TYPE	,OKMSG	;TYPE 'OK'
3895	007600	013702	001340	HALF:	MOV	NARROW,R2	
3896	007604	063702	001336		ADD	WIDE,R2	
3897	007610	063702	001424		ADD	OUT,R2	
3898	007614	010200			MOV	R2,R0	
3899	007616	004737	011504		JSR	PC,DECTYP	;TYPE NO. OF STATES OUTSIDE LIMITS
3900	007622	112737	000056	014576	MOVB	#56,DECPNT	
3901	007630	104401	012701		TYPE	,HAFMSG	
3902	007634	020027	000051		CMP	R0,#41.	;COMPARE IT TO NOMINAL
3903	007640	003403			BLE	21\$	
3904	007642	104401	012511		TYPE	,ERMSG	;TYPE 'ERROR'
3905	007646	000402			BR	SWDIST	
3906	007650	104401	012500	21\$:	TYPE	,OKMSG	;TYPE 'OK'
3907	007654	005737	001400	SWDIST:	TST	FLAG	;VT55?
3908	007660	001426			BEQ	RELACC	
3909	007662	004737	010342		JSR	PC,DELCLR	;WAIT AWHILE, THEN CLEAR VT55
3910	007666	104401	013156		TYPE	,MSG16	
3911	007672	104401	013757		TYPE	,BUFF1	;TYPE BUFF1-PRINT GRID
3912	007676	012700	021540		MOV	#DIST,R0	;POINTER TO STATE WIDTH DISTRIBUTION
3913	007702	012701	000310		MOV	#200.,R1	;GO 200. TIMES UP TO 2 LSB
3914	007706	012002			MOV	(R0)+,R2	
3915	007710	004737	011402	NXTY1:	JSR	PC,LOADY	
3916	007714	005002			CLR	R2	
3917	007716	004737	011402		JSR	PC,LOADY	
3918	007722	005301			DEC	R1	
3919	007724	001370			BNE	NXTY1	
3920	007726	104401	013702		TYPE	,C2	;TYPE ASCIZ STRING
3921	007732	004737	010342		JSR	PC,DELCLR	
3922							

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3924          ;CHANGE HISTOGRAM ERROR TO RELATIVE ACCURACY ERROR
3925
3926 007736 005001          RELACC: CLR      R1          ;RUNNING ERROR = 0
3927 007740 005003          CLR      R3          ;MAXIMUM ERROR = 0
3928 007742 104401 013551  TYPE     ,MSG21
3929 007746 012700 042302  MOV     #BUFFER+2,R0
3930 007752 011002          NXTSTA: MOV     (R0),R2      ;STATE WIDTH = R2
3931 007754 162702 001440  SUB     #800.,R2      ;STATE WIDTH ERROR IN R2
3932 007760 060201          ADD     R2,R1        ;UPDATE RUNNING ERROR
3933 007762 010120          MOV     R1,(R0)+    ;SAVE IN BUFFER
3934 007764 010104          MOV     R1,R4        ;SAVE IN R4 ALSO
3935 007766 100001          BPL     PLUS        ;IS IT POSITIVE?
3936 007770 005404          NEG     R4          ;NO - MAKE IT POSITIVE
3937 007772 020403  PLUS:  CMP     R4,R3        ;CHECK AGAINST PREVIOUS MAX. ERROR
3938 007774 003405          BLE     NOTNEW      ;NOT A NEW MAXIMUM
3939 007776 010403          MOV     R4,R3        ;UPDATE MAXIMUM IN R3
3940 010000 010005          MOV     R0,R5
3941 010002 162705 042302  SUB     #BUFFER+2,R5
3942 010006 006205          ASR     R5          ;R5=EDGE VALUE AT MAX. RELACC
3943 010010 020027 062276  NOTNEW: CMP     R0,#BUFFER+8190. ;DONE?
3944 010014 001356          BNE     NXTSTA      ;NO - REPEAT
3945 010016 006203          ASR     R3          ;RESCALE FROM 1 LSB = 800. SCALING
3946 010020 006203          ASR     R3          ;TO 1 LSB = 100. SCALING
3947 010022 006203          ASR     R3
3948 010024 005503          ADC     R3
3949 010026 010302          MOV     R3,R2
3950 010030 004737 011504  JSR     PC,DECTYP
3951 010034 104401 013576  TYPE     ,LINEA
3952 010040 010546          MOV     R5,-(SP)    ;;SAVE R5 FOR TYPEOUT
(1)          ;;TYPE VALUE
(1) 010042 104403          TYPOS          ;;GO TYPE--OCTAL ASCII
(1) 010044 004          .BYTE 4          ;;TYPE 4 DIGIT(S)
(1) 010045 001          .BYTE 1          ;;TYPE LEADING ZEROS
3953 010046 104401 012435  TYPE     ,SLASH    ;PRINT '/'
3954 010052 005205          INC     R5
3955 010054 010546          MOV     R5,-(SP)    ;;SAVE R5 FOR TYPEOUT
(1)          ;;TYPE VALUE
(1) 010056 104403          TYPOS          ;;GO TYPE--OCTAL ASCII
(1) 010060 004          .BYTE 4          ;;TYPE 4 DIGIT(S)
(1) 010061 001          .BYTE 1          ;;TYPE LEADING ZEROS
3956 010062 020337 011750  CMP     R3,VLIN
3957 010066 003403          BLE     41$
3958 010070 104401 012511  TYPE     ,ERMSG
3959 010074 000402          BR     42$
3960 010076 104401 012500  41$:  TYPE     ,OKMSG
3961 010102 005737 001400  42$:  TST     FLAG      ;VT55?
3962 010106 001503          BEQ     L02
3963 010110 012700 042300  MOV     #BUFFER,R0
3964 010114 012701 010000  MOV     #4096.,R1

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3966 010120 011002          GETDAT: MOV      (R0),R2          ;GET RELATIVE ACCURACY ERROR SCALED 1LSB = 800.
3967 010122 006202          ASR      R2              ;RESCALE IT TO 1 LSB = 100.
3968 010124 006202          ASR      R2
3969 010126 006202          ASR      R2
3970 010130 005502          ADC      R2
3971 010132 062702 000166  ADD      #118.,R2          ;AND MOVE IT TO MID-SCREEN
3972 010136 010220          MOV      R2,(R0)+        ;PUT IT BACK INTO BUFFER
3973 010140 005301          DEC      R1
3974 010142 001366          BNE      GETDAT
3975 010144 012700 042300  MOV      #BUFFER,R0
3976 010150 012704 042300  MOV      #BUFFER,R4
3977 010154 012705 042302  MOV      #BUFFER+2,R5
3978 010160 012701 001000  MOV      #512.,R1
3979 010164 012702 000007  NXT8:   MOV      #7.,R2
3980 010170 012003          MOV      (R0)+,R3
3981 010172 010337 001414  MOV      R3,MIN          ;MINIMUM
3982 010176 010337 001420  MOV      R3,MAX          ;MAXIMUM
3983 010202 012003          NXTCMP: MOV      (R0)+,R3
3984 010204 020337 001414  CMP      R3,MIN
3985 010210 002002          BGE      MAXTST
3986 010212 010337 001414  MOV      R3,MIN          ;NEW MINIMUM
3987 010216 020337 001420  MAXTST: CMP      R3,MAX
3988 010222 003402          BLE      TST8
3989 010224 010337 001420  MOV      R3,MAX          ;NEW MAXIMUM
3990 010230 005302          TST8:   DEC      R2
3991 010232 001363          BNE      NXTCMP
3992 010234 013724 001414  MOV      MIN,(R4)+
3993 010240 013725 001420  MOV      MAX,(R5)+
3994 010244 022425          CMP      (R4)+,(R5)+    ;BUMP EACH ONCE MORE
3995 010246 005301          DEC      R1
3996 010250 001345          BNE      NXT8
3997 010252 104401 013064  TYPE     ,MSG18
3998 010256 104401 014005  TYPE     ,BUFF2          ;TYPE BUFF2
3999 010262 012700 042300  MOV      #BUFFER,R0
4000 010266 004737 010320  JSR      PC,LOAD
4001 010272 104401 013705  TYPE     ,C3              ;TYPE ASCIZ STRING
4002 010276 012700 042302  MOV      #BUFFER+2,R0
4003 010302 004737 010320  JSR      PC,LOAD
4004 010306 104401 013702  TYPE     ,C2              ;TYPE ASCIZ STRING
4005 010312 004737 010342  JSR      PC,DELCLR
4006 010316 000207          LO2:    RTS      PC
4007 010320 012701 001000  LOAD:   MOV      #512.,R1
4008 010324 012002          LOAD0: MOV      (R0)+,R2
4009 010326 005720          TST      (R0)+
4010 010330 004737 011402  JSR      PC,LOADY
4011 010334 005301          DEC      R1
4012 010336 001372          BNE      LOAD0
4013 010340 000207          RTS      PC

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4015 010342 005000          DELCLR: CLR      RO
4016 010344 012701 000020      MOV      #20,R1          ;DELAY BEFORE CLEANING SCREEN
4017 010350 005300          1$:      DEC      RO
4018 010352 001376          BNE      1$
4019 010354 005301          DEC      R1
4020 010356 001374          BNE      1$
4021 010360 032777 010000 170552  BIT      #BIT12,@SWR      ;TEST FOR HALT FOR DISPLAY
4022 010366 001401          BEQ      2$              ;:DON'T HALT FOR DISPLAY
4023 010370 000000          HALT
4024 010372 104401 014025      2$:      TYPE      ,VTINIT
4025 010376 000207          RTS      PC
4026
4027 010400 013537 001362      ;;NOISE SUBROUTINE;;
4028 010404 013737 001362 001360 NOITST: MOV      @(R5)+,CHANL      ;LOAD CHANNEL
4029 010412 004737 006226          MOV      CHANL,DUMMY      ;LOAD DUMMY CHANNEL
4030 010416 004737 010572          JSR      PC,GETEDG        ;GET EDGE VALUE
4031 010422 012737 000001 006450          JSR      PC,NOIA         ;GET RMS AND PEAK VALUES
4032 010430 004737 010436          MOV      #1,EDGFLG
4033 010434 000205          JSR      PC,TYPRP        ;TYPE RMS AND PEAK VALUES
4034
4035
4036
4037
4038
4039          ;;TYPE RMS AND PEAK VALUES;;
4040 010436 104401 012375      TYPRP: TYPE      ,NOI
4041 010442 005737 001374          TST      RMS
4042 010446 100002          BPL      POSRMS
4043 010450 005037 001374          CLR      RMS              ;RMS<0,SET RMS=0
4044 010454 005737 001376      POSRMS: TST      PEAK
4045 010460 100002          BPL      POSPEA
4046 010462 005037 001376          CLR      PEAK              ;PEAK<0,SET PEAK=0
4047 010466 013702 001374      POSPEA: MOV      RMS,R2
4048 010472 004737 011504          JSR      PC,DECTYP
4049 010476 104401 012750          TYPE      ,MESR
4050 010502 013702 001376          MOV      PEAK,R2
4051 010506 004737 011504          JSR      PC,DECTYP
4052 010512 104401 012763          TYPE      ,MESP
4053 010516 004737 006406          JSR      PC,TYPEDG
4054 010522 104401 012405          TYPE      ,CHAN
4055 010526 013746 001362          MOV      CHANL,-(SP)      ;;SAVE CHANL FOR TYPEOUT
(1)
(1) 010532 104403          ;;TYPE CHANL
(1) 010534 002          ;;GO TYPE--OCTAL ASCII
(1) 010535 000          ;;TYPE 2 DIGIT(S)
4056 010536 023737 001374 011742  CMP      RMS,VNR          ;;SUPPRESS LEADING ZEROS
4057 010544 003007          BGT      ER              ;WITHIN LIMITS?
4058 010546 023737 001376 011744  CMP      PEAK,VNP
4059 010554 003003          BGT      ER              ;WITHIN LIMITS?
4060 010556 104401 012500          TYPE      ,OKMSG
4061 010562 000207          RTS      PC
4062 010564 104401 012511      ER:      TYPE      ,ERMSG
4063 010570 000207          RTS      PC

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4065      ;;SUBROUTINES FOR NOISE TEST;;
4066 010572 005037 001374      NOIA: CLR RMS ;CLEAR RMS VLAUE
4067 010576 005037 001376      CLR PEAK ;CLEAR PEAK VALUE
4068 010602 004537 006452      NOI1: JSR R5,SARSUB ;DO SAR ROUTINE AT 16%
4069 010606 000020      16.
4070 010610 063737 001404 001374      ADD DAC,RMS ;ADD RESULT TO RMS
4071 010616 004537 006452      JSR R5,SARSUB ;DO SAR ROUTINE AT 84%
4072 010622 000124      84.
4073 010624 163737 001404 001374      SUB DAC,RMS ;SUBTRACT RESULT FROM RMS
4074 010632 004537 006452      JSR R5,SARSUB ;DO SAR ROUTINE AT 1%
4075 010636 000001      1
4076 010640 063737 001404 001376      ADD DAC,PEAK ;ADD RESULT TO PEAK
4077 010646 004537 006452      JSR R5,SARSUB ;DO SAR ROUTINE AT 99%
4078 010652 000143      99.
4079 010654 163737 001404 001376      SUB DAC,PEAK ;SUBTRACT RESULT FROM PEAK
4080 010662 000207      RTS PC ;RETURN
4081
4082 010664 012537 001362      NOI8: MOV (R5)+,CHANL ;GET CHANNEL VALUE
4083 010670 063737 001332 001362      ADD BASECH,CHANL
4084 010676 013737 001362 001360      MOV CHANL,DUMMY ;LOAD DUMMY CHANNEL
4085 010704 004737 006226      JSR PC,GETEDG ;GET EDGE VALUES
4086 010710 005037 001374      CLR RMS ;CLEAR RMS VALUE
4087 010714 005037 001376      CLR PEAK ;CLEAR PEAK VALUE
4088 010720 012737 000010 011006      MOV #10,10$ ;SET UP COUNTER
4089 010726 004737 010602      1$: JSR PC,NOI1 ;GET NOISE VALUES
4090 010732 005237 001410      INC EDGE
4091 010736 005337 011006      DEC 10$
4092 010742 001371      BNE 1$ ;REPEAT 8 TIMES
4093 010744 162737 000010 001410      SUB #10,EDGE
4094 010752 006237 001374      ASR RMS ;SCALE IT TO 1 LSB=100.
4095 010756 005537 001374      ADC RMS
4096 010762 006237 001376      ASR PEAK
4097 010766 005537 001376      ADC PEAK
4098 010772 012737 000010 006450      MOV #8.,EDGFLG
4099 011000 004737 010436      JSR PC,TYPRP ;TYPE RESULTS
4100 011004 000205      RTS R5 ;RETURN
4101 011006 000000      10$: 0 ;COUNTER
4102
4103
4104      ;;RANDOM NUMBER GENERATOR;;
4105 011010 063737 001370 001366      RANDY: ADD RNB,RNA
4106 011016 063737 001372 001366      ADD RNC,RNA
4107 011024 005537 001366      ADC RNA
4108 011030 063737 001366 001370      ADD RNA,RNB
4109 011036 063737 001372 001370      ADD RNC,RNB
4110 011044 005537 001370      ADC RNB
4111 011050 063737 001366 001372      ADD RNA,RNC
4112 011056 063737 001370 001372      ADD RNB,RNC
4113 011064 005537 001372      ADC RNC
4114 011070 000207      RTS PC

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4116          ;;ROUTINE TO AVERAGE 8 CONVERSIONS;;
4117 011072 012500          CONVRT: MOV      (R5)+,R0          ;GET CHANNEL VALUE
4118 011074 063700 001332      ADD      BASECH,R0
4119 011100 010037 001362      MOV      RO,CHANL
4120 011104 000300          SWAB     RO
4121 011106 005037 001346      CLR      TEMP
4122
4123 (1)          ;*      MOV      @ADBUFF,MYTEMP ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.
4124 (2) 011122 010037 001426      MOV      RO,MYTEMP
4125 (2)          ;*      MOV      MYTEMP,@STREG ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
4126 011136 012700 010000      MOV      #10000,R0
4127 011142 005300          2$:    DEC      R0
4128 011144 001376          BNE     2$
4129 011146 012777 001704 170302  MOV      #RETURN,@VECTOR ;LOAD VECTOR
4130 011154 012700 000010      MOV      #10,R0 ;SET UP COUNTER
4131 (1)          1$:
4132 (1)          ;*      MOV      @STREG,MYTEMP ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
4133 011170 052737 000001 001426  BIS      #1,MYTEMP
4134 (1)          ;*      MOV      MYTEMP,@STREG ;/ PUT DATA FROM MYTEMP TO DEVICE REG STREG
4135 011206 005001          CLR      R1
4136 011210 105201          10$:  INCB   R1
4137 011212 001007          BNE     11$
4138 011214 012737 000200 001124  MOV      #BIT7,$GDDAT ;EXPECT DONE TO SET BY NOW
4139 011222 013737 001426 001126  MOV      MYTEMP,$BDDAT
4140 011230 104001          ERROR  1 ;DONE FAILED TO SET ON A/D
4141 (1)          11$:
4142 (2)          ;*      MOV      @STREG,MYTEMP ;/READ DEVICE REG STREG,PUT DATA IN MYTEMP.
4143 (1) 011242 105737 001426      TSTB   MYTEMP
4144 011246 100360          BFL    10$
4145 (1)          ;*      MOV      @ADBUFF,MYTEMP ;/READ DEVICE REG ADBUFF,PUT DATA IN MYTEMP.
4146 011260 063737 001426 001346  ADD      MYTEMP,TEMP
4147          ;WAIT FOR CONVERSION
4148          ;READ BUFFER
4149          DEC      RO
4150          BNE     1$ ;DO 8 TIMES
4151          ASR     TEMP ;AVERAGE VALUE
4152          ASR     TEMP
4153          ASR     TEMP
4154          ADC     TEMP
4155          RTS    R5 ;RETURN

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4155      :COMPARE $GDDAT AND $BDDAT;;
4156 011314 012537 001124      COMPAR: MOV      (R5)+,$GDDAT      ;GET GOOD DATA
4157 011320 013537 001402      MOV      @(R5)+,SPREAD      ;GET SPREAD
4158 011324 013737 001346 001126      MCV      TEMP,$BDDAT      ;GET BAD(ACTUAL) DATA
4159 011332 013701 001126      MOV      $BDDAT,R1
4160 011336 013700 001124      MOV      $GDDAT,R0
4161 011342 160100      SUB      R1,R0      ;GET DIFFERENCE
4162 011344 100001      BPL      7$
4163 011346 005400      NEG      R0
4164 011350 020037 001402      7$:      CMP      R0,SPREAD      ;COMPARE IT TO SPREAD
4165 011354 003001      BGT      10$      ;GO TO ERROR PRINTOUT
4166 011356 005725      TST      (R5)+      ;BUMP RETURN POINTER AROUND ERROR CALL
4167 011360 000205      10$:    RTS      R5
4168
4169      :SUBROUTINE TO RESET & SET INTRPT. EN.;
4170 011362 004737 020426      RST:     JSR      PC,$RESET
4171 011366 052777 000100 167550      BIS      #100,@$TKS
4172 011374 005037 177776      CLR      PSW
4173 011400 000207      RTS      PC
4174
4175
4176
4177      :SUBROUTINE LOADY:
4178 011402 005702      LOADY:  TST      R2      ;ROUTINE TO LOAD VLAUE INTO R2
4179 011404 100001      BPL      PLUSR2      ;AS A VT55 Y-VALUE
4180 011406 005002      CLR      R2
4181 011410 020227 000353      PLUSR2: CMP      R2,#235.
4182 011414 002402      BLT      LESS
4183 011416 012702 000353      MOV      #235.,R2
4184 011422 010203      LESS:   MOV      R2,R3
4185 011424 042702 177740      BIC      #177740,R2
4186 011430 052702 000040      BIS      #40,R2
4187 011434 105777 167510      B10:    TSTB    @$TPS      ;PRINT CHARACTER
4188 011440 100375      BPL      B10
4189 011442 110277 167504      MOVB    R2,@$TPB
4190 011446 006203      ASR      R3
4191 011450 006203      ASR      R3
4192 011452 006203      ASR      R3
4193 011454 006203      ASR      R3
4194 011456 006203      ASR      R3
4195 011460 042703 177770      BIC      #177770,R3
4196 011464 052703 000040      BIS      #40,R3
4197 011470 105777 167454      B11:    TSTB    @$TPS      ;PRINT CHARACTER
4198 011474 100375      BPL      B11
4199 011476 110377 167450      MOVB    R3,@$TPB
4200 011502 000207      RTS      PC
4201
4202

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4204      ;:SUBROUTINE TO TYPE DECIMAL VALUE;;
4205      ;:IN R2 AS X.XX;;
4206 011504 005702      DECTYP: TST R2 ;TEST VALUE TO BE TYPED
4207 011506 100003      BPL POS
4208 011510 104401 012237      TYPE ,MINUS ;TYPE MINUS SIGN
4209 011514 005402      NEG R2
4210 011516 020227 001747      POS: CMP R2,#999. ;>999. REPLACE IT WITH 999.
4211 011522 003402      BLE OKAYD
4212 011524 012702 001747      MOV #999.,R2
4213 011530 105037 014600      OKAYD: CLRB ONES ;CLEAR ONES
4214 011534 105037 014577      CLRB TENS ;CLEAR TENS
4215 011540 105037 014575      CLRB HUNS ;CLEAR HUNS
4216 011544 005702      TESTR2: TST R2 ;CONVERT VALUE TO A DECIMAL VALUE
4217 011546 001424      BEQ TYP0UT
4218 011550 005302      DEC R2
4219 011552 105237 014600      INCB ONES
4220 011556 123727 014600 000012      CMPB ONES,#10.
4221 011564 001367      BNE TESTR2
4222 011566 105037 014600      CLRB ONES
4223 011572 105237 014577      INCB TENS
4224 011576 123727 014577 000012      CMPB TENS,#10.
4225 011604 001357      BNE TESTR2
4226 011606 105037 014577      CLRB TENS
4227 011612 105237 014575      INCB HUNS
4228 011616 000752      BR TESTR2
4229 011620 152737 000060 014575      TYP0UT: BISB #60,HUNS ;PREPARE FOR TYP0UT
4230 011626 152737 000060 014577      BISB #60,TENS
4231 011634 152737 000060 014600      BISB #60,ONES
4232 011642 104401 014575      TYPE ,HUNS ;TYPE VALUE
4233 011646 000207      RTS PC
4234
4235 011650 012701 011742      WFADJ: MOV #VNR,R1 ;SUBROUTINE TO SET UP LIMITS
4236 011654 005737 001332      TST BASECH ;TESTING AN AM11K?
4237 011660 001403      BEQ 1$ ;;
4238 011662 012702 011774      MOV #VARLT3,R2 ;BASECH NOT ZERO, USE AM11K LIMITS
4239 011666 000410      BR 3$ ;;
4240 011670 005737 001416      1$: TST WFTEST
4241 011674 001003      BNE 2$
4242 011676 012702 011754      MOV #VARLT1,R2 ;WFTEST=0,USE NORMAL LIMITS
4243 011702 000402      BR 3$
4244 011704 012702 011764      2$: MOV #VARLT2,R2 ;WFTEST=1,USE OPTION AREA LIMITS
4245 011710 012221      3$: MOV (R2)+,(R1)+
4246 011712 005711      TST (R1)
4247 011714 100375      BPL 3$
4248 011716 000207      RTS PC

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4250	011720	000001		V1:	1		:TOLERANCE VALUES FOR FUNCTIONAL TESTS
4251	011722	000002		V2:	2		
4252	011724	000010		V10:	10		
4253	011726	000050		V50:	50		
4254	011730	000144		V144:	144		
4255	011732	000115		V115:	115		
4256	011734	000240		V240:	240		
4257	011736	000005		V5:	5		
4258	011740	000062		V50D:	50.		
4259							
4260	011742	000000		VNR:	0		:RMS NOISE LIMIT
4261	011744	000000		VNP:	0		:PEAK NOISE LIMIT
4262	011746	000000		VSET:	0		:INTER-CHANNEL SETTling LIMIT
4263	011750	000000		VLIN:	0		:RELATIVE ACCURACY ERROR LIMIT
4264	011752	100000			BIT15		
4265							
4266	011754	000031		VARLT1:	25.		:.25 LSB, NORMAL LIMITS FOR SYSTEM
4267	011756	000310			200.		:2. LSB, INTEGRATION AND FIELD USE ON SPEC TESTS
4268	011760	000144			100.		:1 LSB
4269	011762	000144			100.		:1 LSB
4270							
4271	011764	000027		VARLT2:	23.		:.23 LSB, TIGHTER LIMITS FOR OPTION
4272	011766	000226			150.		:1.5 LSB, AREA USE ON SPEC TESTS
4273	011770	000132			90.		:.9 LSB
4274	011772	000132			90.		:.9 LSB
4275							
4276	011774	000062		VARLT3:	50.		:.5 LSB, LIMITS FOR AM11K TESTING
4277	011776	000310			200.		:2. LSB
4278	012000	000226			150.		:1.5 LSB
4279	012002	000226			150.		:1.5 LSB
4280							
4281	012004	052777	000100	AGATST:	BIS	#100,@\$TKS	
4282	012012	000177	000000		JMP	@AGTST	
4283	012016	001714		AGTST:	BEGIN		

4285

(1)

(2)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1) 012020

(2) 012020 000240

(1) 012022 005037 001102

(1) 012026 005037 001160

(1) 012032 005237 001202

(1) 012036 042737 100000 001202

(1) 012044 005327

(1) 012046 000001

(1) 012050 003017

(1) 012052 012737

(1) 012054 000001

(1) 012056 012046

(1) 012060 104401 012117

(1) 012064 104401 012114

(1) 012070 013700 000042

(1) 012074 001405

(1) 012076 000005

(1) 012100 004710

(1) 012102 000240

(1) 012104 000240

(1) 012106 000240

(1) 012110

(1) 012110 000137

(1) 012112 012004

(1) 012114 377 377 000

(1) 012117 015 042412 042116

(1) 012124 050040 051501 000123

4286

.SBTTL END OF PASS ROUTINE

\*\*\*\*\*

;\*INCREMENT THE PASS NUMBER (\$PASS)

;\*TYPE 'END PASS'

;\*IF THERES A MONITOR GO TO IT

;\*IF THERE ISN'T JUMP TO AGATST

;\*IF IT IS DESIRED TO HAVE A BELL INDICATE THE 'END OF PASS' LOCATION

;\*SENDMG CAN BE CHANGED TO 7.

\$EOP:

CLR \$TSTNM ;:ZERO THE TEST NUMBER

CLR \$TIMES ;:ZERO THE NUMBER OF ITERATIONS

INC \$PASS ;:INCREMENT THE PASS NUMBER

BIC #100000,\$PASS ;:DON'T ALLOW A NEG. NUMBER

DEC (PC)+ ;:LOOP?

\$EOPCT: .WORD 1

BGT \$DOAGN ;:YES

MOV (PC)+,@(PC)+ ;:RESTORE COUNTER

\$ENDCT: .WORD 1

\$EOPCT

TYPE \$SENDMG ;:TYPE 'END PASS'

TYPE \$ENULL ;:TYPE A NULL CHARACTER

\$GET42: MOV @#42,R0 ;:GET MONITOR ADDRESS

BEQ \$DOAGN ;:BRANCH IF NO MONITOR

RESET ;:CLEAR THE WORLD

\$ENDAD: JSR PC,(R0) ;:GO TO MONITOR

NOP ;:SAVE ROOM

NOP ;:FOR

NOP ;:ACT11

\$DOAGN:

JMP @(PC)+ ;:RETURN

\$RTNAD: .WORD AGATST

\$ENULL: .BYTE -1,-1,0 ;:NULL CHARACTER STRING

\$ENDMG: .ASCIZ <15><12>/END PASS/



```
4288
4289 012132 005015 047516 051511 .SBTTL ASCII MESSAGES
      012140 020105 042524 052123 NOIMSG: .ASCIZ <15><12>/NOISE TEST-- /
      012146 026455 000040
4290 012152 005015 042523 052124 SETMSG: .ASCIZ <15><12>/SETTLING TEST-- TYPE DESIRED 'FROM' CHANNEL & CR: /
      012160 044514 043516 052040
      012166 051505 026524 020055
      012174 054524 042520 042040
      012202 051505 051111 042105
      012210 023440 051106 046517
      012216 020047 044103 047101
      012224 042516 020114 020046
      012232 051103 020072 000
4291 012237 055 000 MINUS: .BYTE 55,0
4292 012241 077 000 QUEST: .BYTE 77,0
4293 012243 136 101 040 AMMSG: .BYTE 136,101,40,40,0
      012246 040 000
4294 012250 136 103 040 CMMSG: .BYTE 136,103,40,40,0
      012253 040 000
4295 012255 136 107 015 GMMSG: .BYTE 136,107,15,12,123,127,122,105,107,72,0
      012260 012 123 127
      012263 122 105 107
      012266 072 000
4296 012270 046040 041123 005015 LSBMSG: .ASCIZ / LSB/<15><12>
      012276 000
4297 012277 055 020055 000 DASH: .ASCIZ /-- /
4298 012303 123 040524 042524 STATE: .ASCIZ /STATE-- WIDTH/<15><12>
      012310 026455 053440 042111
      012316 044124 005015 000
4299 012323 103 000110 CH: .ASCIZ /CH/
4300 012326 020040 020040 000 SPACE: .ASCIZ / /
4301 012333 040 051514 020102 LSB: .ASCIZ / LSB ON CH/
      012340 047117 041440 000110
4302 012346 051440 052105 046124 SETCH: .ASCIZ / SETTLING FROM CH/
      012354 047111 020107 051106
      012362 046517 041440 000110
4303 012370 040440 020124 000 ATMSG: .ASCIZ / AT /
4304 012375 116 044517 042523 NOI: .ASCIZ /NOISE: /
      012402 020072 000
4305 012405 040 047117 041440 CHAN: .ASCIZ / ON CHANNEL /
      012412 040510 047116 046105
      012420 000040
4306 012422 020040 020040 047504 DONE: .ASCIZ / DONE/<15><12>
      012430 042516 005015 000
4307 012435 057 000 SLASH: .ASCIZ /#/
4308 012437 124 050131 020105 TOMSG: .ASCIZ /TYPE DESIRED 'TO' CHANNEL & CR: /
      012444 042504 044523 042522
      012452 020104 052047 023517
      012460 041440 040510 047116
      012466 046105 023040 041440
      012474 035122 000040
4309 012500 020040 020040 045517 OKMSG: .ASCIZ / OK/<15><12>
      012506 005015 000
```

```
4311 012511 040 025052 051105 ERMSG: .ASCIIZ / **ERROR**/<15><12>
      012516 047522 025122 006452
      012524 000012
4312 012526 051440 044513 050120 SKPMSG: .ASCIIZ / SKIPPED STATE(S)/
      012534 042105 051440 040524
      012542 042524 051450 000051
4313 012550 047040 051101 047522 NARMSG: .ASCIIZ # NARROW (< 1/2 LSB) STATE(S)#<15><12>
      012556 020127 036050 030440
      012564 031057 046040 041123
      012572 020051 052123 052101
      012600 024105 024523 005015
      012606 000
4314 012607 040 044527 042504 WIDMSG: .ASCIIZ # WIDE (> 1 1/2 LSB) STATE(S)#<15><12>
      012614 024040 020076 020061
      012622 027461 020062 051514
      012630 024502 051440 040524
      012636 042524 051450 006451
      012644 000012
4315 012646 051440 040524 042524 OUTMSG: .ASCIIZ / STATE(S) WIDER THAN 2 LSB/
      012654 051450 020051 044527
      012662 042504 020122 044124
      012670 047101 031040 046040
      012676 041123 000
4316 012701 040 052123 052101 HAFMSG: .ASCIIZ # STATE-WIDTH(S) OUTSIDE + OR - 1/2 LSB#
      012706 026505 044527 052104
      012714 024110 024523 047440
      012722 052125 044523 042504
      012730 025440 047440 020122
      012736 020055 027461 020062
      012744 051514 000102
4317 012750 046040 041123 051040 MESR: .ASCIIZ / LSB RMS, /
      012756 051515 020054 000
4318 012763 040 051514 020102 MESP: .ASCIIZ / LSB PEAK AT /
      012770 042520 045501 040440
      012776 020124 000
4319 013001 015 042412 042116 MEND: .ASCII <15><12>/END OF LOGIC TESTS/
      013006 047440 020106 047514
      013014 044507 020103 042524
      013022 052123 123
4320 013025 040 047117 040440 ONAD: .ASCIIZ / ON AD11K AT /
      013032 030504 045461 040440
      013040 020124 000
4321 013043 040 042101 030461 MSG50: .ASCIIZ / AD11K'S FOUND/<15><12>
      013050 023513 020123 047506
      013056 047125 006504 000012
4322 013064 005012 025412 027461 MSG18: .ASCII <12><12><12>#+1/2 LSB#<15><12><12><12><12><12><12><12><12><12><12><1
      013072 020062 051514 006502
      013100 005012 005012 005012
      013106 005012 005012 005012
4323 013114 030455 031057 051514 .ASCIIZ \-1/2LSB\
      013122 000102
4324
```

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4326
4327 013124 044504 043106 051105 MSG20: .EVEN
      013132 047105 044524 046101  .ASCIZ /DIFFERENTIAL LINEARITY:/<15><12>
      013140 046040 047111 040505
      013146 044522 054524 006472
      013154 000012
4328 013156 020040 020040 020040 MSG16: .ASCII / STATE-WIDTH DISTRIBUTION/<15><12><12><12>
      013164 020040 020040 020040
      013172 020040 020040 020040
      013200 020040 052123 052101
      013206 026505 044527 052104
      013214 020110 044504 052123
      013222 044522 052502 044524
      013230 047117 005015 005012
4329 013236 020040 020043 043117 .ASCII / # OF STATES/<12><12><12><12><12><12><12><12><12><12><12><12><12><12><12><
      013244 051440 040524 042524
      013252 005123 005012 005012
      013260 005012 005012 005012
      013266 005012 005012 005012
      013274 005012
4330 013276 020040 020040 020040 .ASCII / STATE WIDTH (LSB)/<15>
      013304 020040 020040 020040
      013312 020040 020040 020040
      013320 020040 020040 020040
      013326 020040 020040 020040
      013334 020040 020040 020040
      013342 020040 020040 020040
      013350 020040 020040 020040
      013356 051440 040524 042524
      013364 053440 042111 044124
      013372 024040 051514 024502
      013400 005015
4331 013402 030040 020040 020040 .ASCIZ # 0 1/2 1 1 1/2 2#
      013410 020040 020040 020040
      013416 020040 020040 027461
      013424 020062 020040 020040
      013432 020040 020040 020040
      013440 020040 020061 020040
      013446 020040 020040 020040
      013454 020040 030440 030440
      013462 031057 020040 020040
      013470 020040 020040 020040
      013476 020040 031040 000
4332 013503 015 052012 050131 MSG71: .ASCIZ <15><12>/TYPE LETTER & CR FOR DESIRED TEST: /
      013510 020105 042514 052124
      013516 051105 023040 041440
      013524 020122 047506 020122
      013532 042504 044523 042522
      013540 020104 042524 052123
      013546 020072 000
4333 013551 122 046105 052101 MSG21: .ASCIZ /RELATIVE ACCURACY:/<15><12>
      013556 053111 020105 041501
      013564 052503 040522 054503
      013572 006472 000012
4334 013576 046040 041123 046440 LINEA: .ASCIZ / LSB MAXIMUM AT /
      013604 054101 046511 046525
    
```

4335	013612	040440	020124	000					
	013617	015	041412	046101	HEAD5:	.ASCII	<15><12>/CALIBRATION--/		
	013624	041111	040522	044524					
4336	013632	047117	026455						
	013636	051440	052105	041440	ASKCH:	.ASCIZ	/ SET CHANNEL IN SWR LOW BYTE/<15><12>		
	013644	040510	047116	046105					
	013652	044440	020116	053523					
	013660	020122	047514	020127					
	013666	054502	042524	005015					
	013674	000							
4337	013675	033	000132		C0:	.ASCIZ	<33><132>		
4338	013700	000055			C1:	.ASCIZ	<55>		
4339	013702	031033	000		C2:	.ASCIZ	<33><62>		
4340	013705	112	000		C3:	.ASCIZ	<112>		
4341	013707	015	047412	043106	MOFSET:	.ASCIZ	<15><12>/OFFSET =/		
	013714	042523	020124	000075					
4342	013722	046040	041123	000040	MLSB:	.ASCIZ	/ LSB /		
4343	013730	040440	020124	000	MAT:	.ASCIZ	/ AT /		
4344	013735	015	020012	047105	METST:	.ASCIZ	<15><12>/ ENTERING TEST /		
	013742	042524	044522	043516					
	013750	052040	051505	020124					
	013756	000							
4345	013757	033	061	101	BUFF1:	.BYTE	33,61,101,61,111,62,114,41,60,45,63,51,66,55,71,61,74,110,41,40,112,0		
	013762	061	111	062					
	013765	114	041	060					
	013770	045	063	051					
	013773	066	055	071					
	013776	061	074	110					
	014001	041	040	112					
	014004	000							
4346	014005	033	061	101	BUFF2:	.BYTE	33,61,101,47,111,61,104,50,65,44,62,110,40,40,102,0		
	014010	047	111	061					
	014013	104	050	065					
	014016	044	062	110					
	014021	040	040	102					
	014024	000							
4347	014025	033	110	033	VTINIT:	.BYTE	33,110,33,112,33,61,101,40,33,62,0		
	014030	112	033	061					
	014033	101	040	033					
	014036	062	000						
4348	014040	005015	046412	026504	HEAD1:	.ASCII	<15><12><12>#MD-11-CRLPK-B AD11K/LPA-11 DIAGNOSTIC#<15><12>		
	014046	030461	041455	046122					
	014054	045520	041055	020040					
	014062	020040	042101	030461					
	014070	027513	050114	026501					
	014076	030461	042040	040511					
	014104	047107	051517	044524					
	014112	006503	012						
4349	014115	012	035101	040440		.ASCII	<12>/A: AUTO TEST/		
	014122	052125	020117	042524					
	014130	052123							
4350	014132	005015	035103	041440		.ASCII	<15><12>/C: CALIBRATION/		
	014140	046101	041111	040522					
	014146	044524	047117						
4351	014152	005015	035114	046040		.ASCII	<15><12>/L: LOGIC TEST/		
	014160	043517	041511	052040					

4352	014166	051505	124	020072								.ASCII <15><12>/N: NOISE TEST/
	014171	015	047012	020105								
	014176	047516	051511									
	014204	042524	052123									
4353	014210	005015	035123	051440								.ASCII <15><12>/S: SETTLE TEST/
	014216	052105	046124	020105								
	014224	042524	052123									
4354	014230	005015	035127	053440								.ASCIZ <15><12>/W: WRAPAROUND TEST/<15><12>
	014236	040522	040520	047522								
	014244	047125	020104	042524								
	014252	052123	005015	000								
4355	014257	015	051412	040524	EM1:							.ASCIZ <15><12>/STATUS REG. ERROR/<15><12>
	014264	052524	020123	042522								
	014272	027107	042440	051122								
	014300	051117	005015	000								
4356	014305	015	043012	044501	EM2:							.ASCIZ <15><12>/FAILED TO INTERRUPT/<15><12>
	014312	042514	020104	047524								
	014320	044440	052116	051105								
	014326	052522	052120	005015								
	014334	000										
4357	014335	015	052412	042516	EM3:							.ASCIZ <15><12>/UNEXPECTED INTERRUPT/<15><12>
	014342	050130	041505	042524								
	014350	020104	047111	042524								
	014356	051122	050125	006524								
	014364	000012										
4358	014366	005015	051105	047522	EM4:							.ASCIZ <15><12>#ERROR ON A/D CHANNEL#<15><12>
	014374	020122	047117	040440								
	014402	042057	041440	040510								
	014410	047116	046105	005015								
	014416	000										
4359	014417	105	051122	041520	DH1:							.ASCIZ /ERRPC STREG EXPECTED ACTUAL/<15><12>
	014424	051440	051124	043505								
	014432	042440	050130	041505								
	014440	042524	020104	041501								
	014446	052524	046101	005015								
	014454	000										
4360	014455	105	051122	041520	DH2:							.ASCIZ /ERRPC STREG CHANNEL NOMINAL TOLERANCE ACTUAL/
	014462	020040	052123	042522								
	014470	020107	020040	044103								
	014476	047101	042516	020114								
	014504	047040	046517	047111								
	014512	046101	020040	047524								
	014520	042514	040522	041516								
	014526	020105	040440	052103								
	014534	040525	000114									
4361	014540	051105	050122	020103	DH3:							.ASCIZ /ERRPC STREG ACTUAL/<15><12>
	014546	020040	020040	051440								
	014554	051124	043505	020040								
	014562	020040	041501	052524								
	014570	046101	005015	000								



4377

.SBTTL TTY INPUT ROUTINE

(1) :  
(2) :\*\*\*\*\*

(1) .ENABL LSB

(1) :  
(1) .DSABL LSB

(1) :  
(1) :  
(2) :\*\*\*\*\*

(1) :\*THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY

(1) :\*CALL:

(1) :\* RDCHR : INPUT A SINGLE CHARACTER FROM THE TTY  
(1) :\* RETURN HERE : CHARACTER IS ON THE STACK  
(1) : : WITH PARITY BIT STRIPPED OFF  
(1) :

(1) 014644 011646 \$RDCHR: MOV (SP), -(SP) : PUSH DOWN THE PC

(1) 014646 016666 000004 000002 MOV 4(SP), 2(SP) : SAVE THE PS

(1) 014654 105777 164264 1\$: TSTB @STKS : WAIT FOR

(1) 014660 100375 BPL 1\$ : A CHARACTER

(1) 014662 117766 164260 000004 MOV 4(SP), @STKB, 4(SP) : READ THE TTY

(1) 014670 042766 177600 000004 BIC #^C<177>, 4(SP) : GET RID OF JUNK IF ANY

(1) 014676 026627 000004 000023 CMP 4(SP), #23 : IS IT A CONTROL-S?

(1) 014704 001013 BNE 3\$ : BRANCH IF NO

(1) 014706 105777 164232 2\$: TSTB @STKS : WAIT FOR A CHARACTER

(1) 014712 100375 BPL 2\$ : LOOP UNTIL ITS THERE

(1) 014714 117746 164226 MOV 4(SP), @STKB, -(SP) : GET CHARACTER

(1) 014720 042716 177600 BIC #^C177, (SP) : MAKE IT 7-BIT ASCII

(1) 014724 022627 000021 CMP (SP)+, #21 : IS IT A CONTROL-Q?

(1) 014730 001366 BNE 2\$ : IF NOT DISCARD IT

(1) 014732 000750 BR 1\$ : YES, RESUME

(1) 014734 026627 000004 000140 3\$: CMP 4(SP), #140 : IS IT UPPER CASE?

(1) 014742 002407 BLT 4\$ : BRANCH IF YES

(1) 014744 026627 000004 000175 CMP 4(SP), #175 : IS IT A SPECIAL CHAR?

(1) 014752 003003 BGT 4\$ : BRANCH IF YES

(1) 014754 042766 000040 000004 BIC #40, 4(SP) : MAKE IT UPPER CASE

(1) 014762 000002 4\$: RTI : GO BACK TO USER

(2) :  
(1) :\*\*\*\*\*

(1) :\*THIS ROUTINE WILL INPUT A STRING FROM THE TTY

(1) :\*CALL:

(1) :\* RDLIN : INPUT A STRING FROM THE TTY  
(1) :\* RETURN HERE : ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK  
(1) :\* : TERMINATOR WILL BE A BYTE OF ALL 0'S  
(1) :

(1) 014764 010346 \$RDLIN: MOV R3, -(SP) : SAVE R3

(1) 014766 012703 015072 1\$: MOV #TTYIN, R3 : GET ADDRESS

(1) 014772 022703 015102 2\$: CMP #TTYIN+8, R3 : BUFFER FULL?

(1) 014776 101405 BLOS 4\$ : BR IF YES

(1) 015000 104405 RDCHR : GO READ ONE CHARACTER FROM THE TTY

(1) 015002 112613 MOV (SP)+, (R3) : GET CHARACTER

(1) 015004 122713 000177 10\$: CMPB #177, (R3) : IS IT A RUBOUT

(1) 015010 001003 BNE 3\$ : SKIP IF NOT

(1) 015012 104401 001170 4\$: TYPE \$QUES : TYPE A '?'

(1) 015016 000763 BR 1\$ : CLEAR THE BUFFER AND LOOP

(1) 015020 111337 015070 3\$: MOV (R3), 9\$ : ECHO THE CHARACTER

(1) 015024 104401 015070 TYPE .9\$

(1)	015030	122723	000015			CMPB	#15,(R3)+	::CHECK FOR RETURN
(1)	015034	001356				BNE	2\$	::LOOP IF NOT RETURN
(1)	015036	105063	177777			CLRB	-1(R3)	::CLEAR RETURN (THE 15)
(1)	015042	104401	001172			TYPE	,\$LF	::TYPE A LINE FEED
(1)	015046	012603				MOV	(SP)+,R3	::RESTORE R3
(1)	015050	011646				MOV	(SP),-(SP)	::ADJUST THE STACK AND PUT ADDRESS OF THE
(1)	015052	016666	000004	000002		MOV	4(SP),2(SP)	:: FIRST ASCII CHARACTER ON IT
(1)	015060	012766	015072	000004		MOV	#\$TTYIN,4(SP)	
(1)	015066	000002				RTI		::RETURN
(1)	015070	000			9\$:	.BYTE	0	::STORAGE FOR ASCII CHAR. TO TYPE
(1)	015071	000				.BYTE	0	::TERMINATOR
(1)	015072	000010			\$TTYIN:	.BLKB	8.	::RESERVE 8 BYTES FOR TTY INPUT
(1)	015102	052536	005015	000	\$CNTLU:	.ASCIZ	/^U/<15><12>	::CONTROL 'U'
(1)	015107	136	006507	000012	\$CNTLG:	.ASCIZ	/^G/<15><12>	::CONTROL 'G'
(1)	015114	005015	053523	020122	\$MSWR:	.ASCIZ	<15><12>/SWR = /	
(1)	015122	020075	000					
(1)	015125	040	047040	053505	\$MNEW:	.ASCIZ	/ NEW = /	
(1)	015132	036440	000040					









(1) 015706 000000  
(1) 015710  
(1) 015710 000002  
4383  
(1)  
(2)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1) 015712  
(1) 015712 104401 001171  
(1) 015716 010046  
(1) 015720 005000  
(1) 015722 153700 001114  
(1) 015726 001004  
(1)  
(2) 015730 013746 001116  
(2)  
(2) 015734 104402  
(1) 015736 000426  
(1) 015740 005300  
(1) 015742 006300  
(1) 015744 006300  
(1) 015746 006300  
(1) 015750 062700 001256  
(1) 015754 012037 015764  
(1) 015760 001404  
(1) 015762 104401  
(1) 015764 000000  
(1) 015766 104401 001171  
(1) 015772 012037 016002  
(1) 015776 001404  
(1) 016000 104401  
(1) 016002 000000  
(1) 016004 104401 001171  
(1) 016010 011000  
(1) 016012 001004  
(1) 016014 012600  
(1) 016016 104401 001171  
(1) 016022 000207  
(1) 016024  
(2) 016024 013046  
(2) 016026 104402  
(1) 016030 005710  
(1) 016032 001770  
(1) 016034 104401 016042  
(1) 016040 000771  
(1) 016042 020040 000  
(1) 016046

```
        HALT                ;;YES
6$:     RTI                  ;;RETURN
        .SBTTL  ERROR MESSAGE TIMEOUT ROUTINE

        ;*****
        ;*THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH
        ;*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" ($ERRTB),
        ;*AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.

$ERRTYP:
        TYPE      , $CRLF      ;;"CARRIAGE RETURN" & "LINE FEED"
        MOV       RO, -(SP)    ;;SAVE RO
        CLR       RO          ;;PICKUP THE ITEM INDEX
        BISB     @#$ITEMB, RO
        BNE      1$          ;;IF ITEM NUMBER IS ZERO, JUST
                               ;;TYPE THE PC OF THE ERROR
                               ;;SAVE $ERRPC FOR TYPEOUT
                               ;;ERROR ADDRESS
                               ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
                               ;;GET OUT
        MOV      $ERRPC, -(SP)
        TYPCC
        BR       6$
1$:     DEC       RO          ;;ADJUST THE INDEX SO THAT IT WILL
        ASL      RO          ;;      WORK FOR THE ERROR TABLE
        ASL      RO
        ASL      RO
        ADD      # $ERRTB, RO  ;;FORM TABLE POINTER
        MOV      (RO)+, 2$    ;;PICKUP "ERROR MESSAGE" POINTER
        BEQ     3$          ;;SKIP TYPEOUT IF NO POINTER
        TYPE    , $CRLF      ;;TYPE THE "ERROR MESSAGE"
        .WORD   0           ;;"ERROR MESSAGE" POINTER GOES HERE
2$:     TYPE    , $CRLF      ;;"CARRIAGE RETURN" & "LINE FEED"
        MOV     (RO)+, 4$    ;;PICKUP "DATA HEADER" POINTER
        BEQ     5$          ;;SKIP TYPEOUT IF 0
        TYPE    , $CRLF      ;;TYPE THE "DATA HEADER"
        .WORD   0           ;;"DATA HEADER" POINTER GOES HERE
3$:     MOV     (RO), RO     ;;"CARRIAGE RETURN" & "LINE FEED"
        BNE     7$          ;;PICKUP "DATA TABLE" POINTER
        MOV     (SP)+, RO    ;;GO TYPE THE DATA
        TYPE    , $CRLF      ;;RESTORE RO
        RTS     PC         ;;"CARRIAGE RETURN" & "LINE FEED"
        ;;RETURN
4$:     .WORD   0           ;;SAVE @(RO)+ FOR TYPEOUT
        TYPE    , $CRLF      ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
        MOV     (RO), RO    ;;IS THERE ANOTHER NUMBER?
        BNE     7$          ;;BR IF NO
        TYPE    , 8$        ;;TYPE TWO(2) SPACES
        BR     7$          ;;LOOP
5$:     MOV     @ (RO)+, -(SP)
        TYPCC
        TST     (RO)
        BEQ     6$
        TYPE    , 8$
        BR     7$
6$:     .ASCIZ  / /
        .EVEN
7$:     .ASCIZ  / /
        .EVEN
8$:     .ASCIZ  / /
        .EVEN
```

```
4385 .SBTTL TYPE ROUTINE
(1)
(2) ;*****
(1) ;*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
(1) ;*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
(1) ;*NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
(1) ;*NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
(1) ;*NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
(1) ;*
(1) ;*CALL:
(1) ;*1) USING A TRAP INSTRUCTION
(1) ;* TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
(1) ;*OR
(1) ;* TYPE
(1) ;* MESADR
(1) ;*
(1) (1) 016046 105737 001157 $TYPE: TSTB $TPFLG ;;IS THERE A TERMINAL?
(1) (1) 016052 100002 BPL 1$ ;;BR IF YES
(1) (1) 016054 000000 HALT ;;HALT HERE IF NO TERMINAL
(1) (1) 016056 000430 BR 3$ ;;LEAVE
(1) (1) 016060 010046 1$: MOV R0,-(SP) ;;SAVE R0
(1) (1) 016062 017600 000002 MOV @2(SP),R0 ;;GET ADDRESS OF ASCIZ STRING
(1) (1) 016066 122737 000001 001214 CMPB #APTENV,$ENV ;;RUNNING IN APT MODE
(1) (1) 016074 001011 BNE 62$ ;;NO,GO CHECK FOR APT CONSOLE
(1) (1) 016076 132737 000100 001215 BITB #APTPOOL,$ENVM ;;SPOOL MESSAGE TO APT
(1) (1) 016104 001405 BEQ 62$ ;;NO,GO CHECK FOR CONSOLE
(1) (1) 016106 010037 016116 MOV R0,61$ ;;SETUP MESSAGE ADDRESS FOR APT
(1) (1) 016112 004737 016336 JSR PC,$ATY3 ;;SPOOL MESSAGE TO APT
(1) (1) 016116 000000 61$: .WORD 0 ;;MESSAGE ADDRESS
(1) (1) 016120 132737 000040 001215 62$: BITB #APTCSUP,$ENVM ;;APT CONSOLE SUPPRESSED
(1) (1) 016126 001003 BNE 60$ ;;YES,SKIP TYPE OUT
(1) (1) 016130 112046 2$: MOVB (R0)+,-(SP) ;;PUSH CHARACTER TO BE TYPED ONTO STACK
(1) (1) 016132 001005 BNE 4$ ;;BR IF IT ISN'T THE TERMINATOR
(1) (1) 016134 005726 TST (SP)+ ;;IF TERMINATOR POP IT OFF THE STACK
(1) (1) 016136 012600 60$: MOV (SP)+,R0 ;;RESTORE R0
(1) (1) 016140 062716 000002 3$: ADD #2,(SP) ;;ADJUST RETURN PC
(1) (1) 016144 000002 RTI ;;RETURN
(1) (1) 016146 122716 000011 4$: CMPB #HT,(SP) ;;BRANCH IF <HT>
(1) (1) 016152 001430 BEQ 8$
(1) (1) 016154 122716 000200 CMPB #CRLF,(SP) ;;BRANCH IF NOT <CRLF>
(1) (1) 016160 001006 BNE 5$
(1) (1) 016162 005726 TST (SP)+ ;;POP <CR><LF> EQUIV
(1) (1) 016164 104401 TYPE ;;TYPE A CR AND LF
(1) (1) 016166 001171 $CRLF
(1) (1) 016170 105037 016324 CLRB $CHARCNT ;;CLEAR CHARACTER COUNT
(1) (1) 016174 000755 BR 2$ ;;GET NEXT CHARACTER
(1) (1) 016176 004737 016260 5$: JSR PC,$TYPEC ;;GO TYPE THIS CHARACTER
(1) (1) 016202 123726 001156 6$: CMPB $FILLC,(SP)+ ;;IS IT TIME FOR FILLER CHARS.?
(1) (1) 016206 001350 BNE 2$ ;;IF NO GO GET NEXT CHAR.
(1) (1) 016210 013746 001154 MOV $NULL,-(SP) ;;GET # OF FILLER CHARS. NEEDED
(1) (1) 016214 105366 000001 7$: DECB 1(SP) ;;AND THE NULL CHAR.
(1) (1) 016220 002770 BLT 6$ ;;DOES A NULL NEED TO BE TYPED?
(1) (1) 016222 004737 016260 JSR PC,$TYPEC ;;BR IF NO--GO POP THE NULL OFF OF STACK
(1) (1) 016226 105337 016324 DECB $CHARCNT ;;GO TYPE A NULL
;;DO NOT COUNT AS A COUNT
```

```
(1) 016232 000770 BR 7$ ::LOOP
(1)
(1) :HORIZONTAL TAB PROCESSOR
(1) 016234 112716 000040 8$: MOVB #' (SP) ::REPLACE TAB WITH SPACE
(1) 016240 004737 016260 9$: JSR PC,$TYPEC ::TYPE A SPACE
(1) 016244 132737 000007 016324 BITB #7,$CHARCNT ::BRANCH IF NOT AT
(1) 016252 001372 BNE 9$ ::TAB STOP
(1) 016254 005726 TST (SP)+ ::POP SPACE OFF STACK
(1) 016256 000724 BR 2$ ::GET NEXT CHARACTER
(1) 016260 105777 162664 $TYPEC: TSTB @$TPS ::WAIT UNTIL PRINTER IS READY
(1) 016264 100375 BPL $TYPEC
(1) 016266 116677 000002 162556 MOVB 2(SP),@$TPB ::LOAD CHAR TO BE TYPED INTO DATA REG.
(1) 016274 122766 000015 000002 CMPB #CR,2(SP) ::IS CHARACTER A CARRIAGE RETURN?
(1) 016302 001003 BNE 1$ ::BRANCH IF NO
(1) 016304 105037 016324 CLRB $CHARCNT ::YES--CLEAR CHARACTER COUNT
(1) 016310 000406 BR $TYPEX ::EXIT
(1) 016312 122766 000012 000002 1$: CMPB #LF,2(SP) ::IS CHARACTER A LINE FEED?
(1) 016320 001402 BEQ $TYPEX ::BRANCH IF YES
(1) 016322 105227 INCB (PC)+ ::COUNT THE CHARACTER
(1) 016324 000000 $CHARCNT: .WORD 0 ::CHARACTER COUNT STORAGE
(1) 016326 000207 $TYPEX: RTS PC
(1)
4386 .SBTTL APT COMMUNICATIONS ROUTINE
(1)
(2) ::*****
(1) 016330 112737 000001 016574 $ATY1: MOVB #1,$FFLG ::TO REPORT FATAL ERROR
(1) 016336 112737 000001 016572 $ATY3: MOVB #1,$MFLG ::TO TYPE A MESSAGE
(1) 016344 000403 BR $ATYC
(1) 016346 112737 000001 016574 $ATY4: MOVB #1,$FFLG ::TO ONLY REPORT FATAL ERROR
(1) 016354 $ATYC:
(3) 016354 010046 MOV R0,-(SP) ::PUSH R0 ON STACK
(3) 016356 010146 MOV R1,-(SP) ::PUSH R1 ON STACK
(1) 016360 105737 016572 TSTB $MFLG ::SHOULD TYPE A MESSAGE?
(1) 016364 001450 BEQ 5$ ::IF NOT: BR
(1) 016366 122737 000001 001214 CMPB #APTENV,$ENV ::OPERATING UNDER APT?
(1) 016374 001031 BNE 3$ ::IF NOT: BR
(1) 016376 132737 000100 001215 BITB #APTSPOOL,$ENVM ::SHOULD SPOOL MESSAGES?
(1) 016404 001425 BEQ 3$ ::IF NOT: BR
(1) 016406 017600 000004 MOV @4(SP),R0 ::GET MESSAGE ADDR.
(1) 016412 062766 000002 000004 ADD #2,4(SP) ::BUMP RETURN ADDR.
(1) 016420 005737 001174 1$: TST $MSGTYPE ::SEE IF DONE W/ LAST XMISSION?
(1) 016424 001375 BNE 1$ ::IF NOT: WAIT
(1) 016426 010037 001210 MOV R0,$MSGAD ::PUT ADDR IN MAILBOX
(1) 016432 105720 2$: TSTB (R0)+ ::FIND END OF MESSAGE
(1) 016434 001376 BNE 2$
(1) 016436 163700 001210 SUB $MSGAD,R0 ::SUB START OF MESSAGE
(1) 016442 006200 ASR R0 ::GET MESSAGE LNTH IN WORDS
(1) 016444 010037 001212 MOV R0,$MSGGLT ::PUT LENGTH IN MAILBOX
(1) 016450 012737 000004 001174 MOV #4,$MSGTYPE ::TELL APT TO TAKE MSG.
(1) 016456 000413 BR 5$
(1) 016460 017637 000004 016504 3$: MOV @4(SP),4$ ::PUT MSG ADDR IN JSR LINKAGE
(1) 016466 062766 000002 000004 ADD #2,4(SP) ::BUMP RETURN ADDRESS
(3) 016474 013746 177776 MOV 177776,-(SP) ::PUSH 177776 ON STACK
(1) 016500 004737 016046 JSR PC,$TYPE ::CALL TYPE MACRO
(1) 016504 000000 4$: .WORD 0
```



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(2)                                     ;CERTAIN TESTING.
(2) 016604 012737 016630 000004      MOV    #30$,4
(2) 016612 005237 170000              INC    170000
(3) 016616 104401 016624              TYPE   .65$      ;;TYPE ASCIZ STRING
(3) 016622 000401                      BR     64$      ;;GET OVER THE ASCIZ
(3)                                     ;;65$: .ASCIZ <?>##
(3) 016626                             64$:
(2) 016626 000401                      BR     31$
(2) 016630 022626                      30$: CMP    (SP)+,(SP)+
(2) 016632 012637 000004              31$: MOV    (SP)+,4      ;ALL THIS JUNK MUST BE REMOVED!!
(2) 016636 005037 017454              CLR    $AERR
(2) 016642 004537 017456              JSR    R5,$LOAD    ;LOAD MICRO-CODE.
(2) 016646 000000G                    .WORD  DRLPX2     ;FILE 'DRLPX2.OBJ'
(2) 016650 052777 040000 162560      BIS    #BIT14,@KMADO ;ISSUE KMC+DMC INIT.
(2) 016656                             1$:
(2)                                     ;'HANGS' HERE THEN KMC-11 ERROR.
(2) 016656 010146                      MOV    R1,-(SP)
(2) 016660 005001                      CLR    R1
(2) 016662 005201                      2$: INC    R1          ;STALL FOR DMC-UP
(2) 016664 001376                      BNE    2$
(2) 016666 012777 104000 162542      MOV    #BIT15!BIT11,@KMADO ;SET RUN, AND ENABLE ARBITRATION.
(2) 016674 105201                      25$: INCB  R1
(2) 016676 001376                      BNE    25$
(2) 016700 032777 000040 162530      BIT    #BIT5,@KMADO  ;SLAVE READY? (READING IPBM SR)
(2) 016706 001401                      BEQ    3$
(2)                                     ;FATAL LPA-11 ERROR SLAVE NOT READY.
(2) 016710 104000                      ERROR
(2) 016712 012777 000004 162522      3$: MOV    #4,@KMAD2    ;READ FAST PATH
(2) 016720                                     4$:
(3) 016720 004537 020366              JSR    R5,$TOUT    ;-TOUT-CHECK FOR TIMEOUT
(3) 016724 104000                      ERROR
(3)                                     ;/TIME-OUT ERROR
(3)                                     ;/WE FAILED TO COMPLETE
(3)                                     ;/CURRENT OPERATION.
(3)                                     ;/CONTINUES IN THIS LOOP
(3)                                     ;/WOULD MAKE US 'HANG' HERE
(3) 016726 000774                      BR     4$
(3)
(3)                                     ;/RETURNS HERE-FROM-TIMED OUT.
(2) 016730 122777 000377 162504      CMPB   #377,@KMAD2  ;WAIT TILL KMC DONE COMMAND.
(2) 016736 001370                      BNE    4$
(2) 016740 122777 000377 162500      CMPB   #377,@KMAD4  ;IF FAST PATH=377 THEN ERROR.
(2) 016746 001001                      BNE    35$
(2) 016750 104000                      ERROR
(2)                                     ;IPBM ERROR (SLAVE SIDE)
(2)                                     ;YOU MUST RUN IPBM DIAGNOSTIC.
(2) 016752 122777 000004 162466      35$: CMPB   #4,@KMAD4    ;IS THIS THE CORRECT VERSION OF MICRO-CODE?
(2) 016760 001543                      BEQ    5$          ;YES-CONTINUE.
(2) 016762 005227 177777              INC    #-1
(2) 016766 001140                      BNE    5$
(2) 016770 005227 177777              INC    #-1

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(2) 016774 001135          BNE      5$
(3) 016776 104401 017004   TYPE     ,67$          ;;TYPE ASCIZ STRING
(3) 017002 000440          BR       66$          ;;GET OVER THE ASCIZ
(3)          ;;67$: .ASCIZ <200>'W A R N I N G THIS PROGRAM WAS DESIGNED TO RUN WITH VERSION 4''
(3) 017104          66$:
(3) 017104 104401 017112   TYPE     ,69$          ;;TYPE ASCIZ STRING
(3) 017110 000430          BR       68$          ;;GET OVER THE ASCIZ
(3)          ;;69$: .ASCIZ <200>'MICRO-CODE. ANOTHER VERSION CODE WAS DETECTED.'"
(3) 017172          68$:
(3) 017172 104401 017200   TYPE     ,71$          ;;TYPE ASCIZ STRING
(3) 017176 000434          BR       70$          ;;GET OVER THE ASCIZ
(3)          ;;71$: .ASCIZ <200>'THIS MAY OR MAYNOT CAUSE FALSE ERROR TO BE REPORTED.'"<200><200>
(3) 017270          70$:
(2)
(2) 017270 112737 177777 017422 5$:   MOVB    #0-1,11$      ;DAC CODE FOR SLAVE.
(2) 017276 012501          MOV      (5)+,R1      ;GET NEXT DEVICE ADDR.
(2) 017300 021127 000000   6$:   CMP     (R1),#0      ;TERM REACHED?
(2) 017304 001444          BEQ     10$
(2) 017306 105237 017422   INCB    11$
(2) 017312 113777 017422 162126   MOVB    11$,@KMAD4    ;FIFO DATA
(2) 017320 004737 017424   JSR     PC,20$        ;ISSUE SEND
(2) 017324 112177 162116   MOVB    (R1)+,@KMAD4 ;SEND LOW BYTE OF DEVICE ADDR TO SLAVE.
(2) 017330 004737 017424   JSR     PC,20$        ;ISSUE SEND
(2) 017334 112177 162106   MOVB    (R1)+,@KMAD4 ;SEND HIGH BYTE OF DEVICE ADDR. TO SLAVE.
(2) 017340 004737 017424   JSR     PC,20$
(2)
(2) 017344 032777 000002 162064 7$:   BIT     #BIT1,@KMAD0 ;WAIT FOR FIFO DATA
(2) 017352 001374          BNE     7$            ;=1 NO DATA. =0 DATA.
(2) 017354 112777 000002 162060   MOVB    #2,@KMAD2    ;READ FIFO.
(2)
(2) 017362          8$:
(3) 017362 004537 020366   JSR     R5, $TOUT    ;--TOUT-CHECK FOR TIMEOUT
(3)
(3) 017366 104000          ERROR          ;/TIME-OUT ERROR
(3)          ;/WE FAILED TO COMPLETE
(3)          ;/CURRENT OPERATION.
(3)          ;/CONTINUES IN THIS LOOP
(3)          ;/WOULD MAKE US 'HANG' HERE
(3)
(3) 017370 000774          BR       8$
(3)
(2) 017372 122777 000377 162042   CMPB    #377,@KMAD2  ;/RETURNS HERE-FROM-TIMED OUT.
(2) 017400 001370          BNE     8$            ;WAIT FOR READ.
(2) 017402 105777 162040   TSTB    @KMAD4        ;WAS A ZERO RETURNED?
(2) 017406 001734          BEQ     6$            ;YES GET NEXT ADDR.
(2)          ;SLAVE WILL RETURN CODE 0 IF
(2) 017410 005237 017454   INC     $AERR         ;DEV PRESENT. ELSE
(2)          ;EXIT $AERR=1 IF SLAVE GIVES ERROR.
(2) 017414 005041          CLR     -(1)         ;GET RID OF REFERENCE TO BAD ADDR.
(2) 017416 012601          10$:  MOV     (SP)+,R1
(2) 017420 000205          RTS     R5           ;RETURN ALL ADDR. CHECKED.
(2)
(2) 017422 000000          11$:  .WORD   0           ;HOLDS DAC CODE PLUS OFFSET
(2)          ;TO SLAVES ADDR. TABLE.
(2)

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(2) 017604 005745 TST -(5)
(2) 017606 105204 INCB R4 ;UPDATE ERROR COUNTER.
(2) 017610 100324 BPL 1$ ;IF NOT TOO MANY, TRY AGAIN.
(2) 017612 000000 HALT ;MICRO CODE LOAD ERROR.
(2) 017614 000722 BR 1$ ;KMC-11 FAULT. YOU COULD TRY
;TO PRESS CONTINUE TO GIVE IT
;ANOTHER CHANCE, BUT I DOUBT
;THAT THAT WOULD WORK. SINCE I'VE
;ALREADY GIVEN IT 177 (OCTAL) CHANCES.
;TRY RUNNING THE KMC-11 DIAGNOSTIC.
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```
(2) ;*THIS ROUTINE ISSUES A WRITE COMMAND TO THE LPA-11
(2) ;*
(2) ;* CALL = JSR R5,$TLKW
(2) ;* .WORD 0 ;OFFSET OF DEVICE ADDR.
(2) ;* .WORD 0 ;DATA TO BE WRITTEN
(2) ;*
(2) 017616 010046 $TLKW: MOV R0,-(SP) ;SAVE R0
(2) 017620 012500 MOV (5)+,R0 ;GET DEVICE OFFSET
(2) 017622 052700 000340 BIS #340,R0 ;ADD WRITE CODE.
(2) 017626 004737 020100 JSR PC,$LPW ;WAIT FOR FAST PATH READY
(2) 017632 010037 017724 MOV R0,W1
(2) 017636 010077 161604 MOV R0,@KMAD4
(2) 017642 112777 000005 161572 MOVB #5,@KMAD2 ;ISSUE FAST PATH WRITE
(2) 017650 004737 020100 JSR PC,$LPW ;WAIT FOR RDY
(2) 017654 011537 017726 MOV (5),W2
(2) 017660 112577 161562 MOVB (5)+,@KMAD4 ;WRITE LOW BYTE DATA.
(2) ;*
(2) 017664 112777 000005 161550 MOVB #5,@KMAD2 ;FP WRITE
(2) 017672 004737 020100 JSR PC,$LPW
(2) 017676 111537 017730 MOVB (5),W3
(2) 017702 112577 161540 MOVB (5)+,@KMAD4 ;WRITE HIGH BYTE
(2) 017706 112777 000005 161526 MOVB #5,@KMAD2
(2) 017714 004737 020100 JSR PC,$LPW
(2) 017720 012600 MOV (SP)+,R0
(2) 017722 000205 RTS R5 ;EXIT DONE.
(2) ;*
(2) ;* THIS ROUTINE ISSUES A READ COMMAND TO THE LPA-11
(2) ;*
(2) ;* CALL = JSR R5,$TLKR
(2) ;* .WORD 0 ;OFFSET OF DEVICE
(2) ;* .WORD 0 ;RETURNS HERE
(2) ;* ;*DATA IN WORD $DATR
(2) ;*
(2) ;*
(2) 017732 010046 $TLKR: MOV R0,-(SP) ;SAVE R0
(2) 017734 012500 MOV (5)+,R0 ;GET OFFSET
(2) 017736 052700 000300 BIS #300,R0 ;ADD READ CODE
(2) 017742 004737 020100 JSR PC,$LPW ;WAIT TILL READY
(2) 017746 110077 161474 MOVB R0,@KMAD4
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(2) 017752 112777 000005 161462      MOVB   #5,@KMAD2      ;ISSUE WRITE FP
(2) 017760 004737 020100              JSR    PC,$LPW
(2) 017764 010037 020074              MOV    R0,RD1
(2) 017770              1$:
(3) 017770 004537 020366              JSR    R5,$TOUT      ;:-TOUT-CHECK FOR TIMEOUT
(3)
(3) 017774 104000              ERROR              ;/TIME-OUT ERROR
(3)                               ;/WE FAILED TO COMPLETE
(3)                               ;/CURRENT OPERATION.
(3)                               ;/CONTINUES IN THIS LOOP
(3)                               ;/WOULD MAKE US 'HANG' HERE
(3)
(3) 017776 000774              BR      1$
(3)
(3)                               ;/RETURNS HERE-FROM-TIMED OUT.
(2) 020000 032777 000040 161430      BIT    #BIT5,@KMAD0  ;FAST PATH GOT DATA?
(2) 020006 001370              BNE    1$
(2) 020010 112777 000004 161424      MOVB   #4,@KMAD2      ;ISSUE FAST PATH READ
(2) 020016 004737 020100              JSR    PC,$LPW
(2) 020022 117737 161420 020076      MOVB   @KMAD4,$DATR  ;GET LOW BYTE
(2) 020030              2$:
(3) 020030 004537 020366              JSR    R5,$TOUT      ;:-TOUT-CHECK FOR TIMEOUT
(3)
(3) 020034 104000              ERROR              ;/TIME-OUT ERROR
(3)                               ;/WE FAILED TO COMPLETE
(3)                               ;/CURRENT OPERATION.
(3)                               ;/CONTINUES IN THIS LOOP
(3)                               ;/WOULD MAKE US 'HANG' HERE
(3)
(3) 020036 000774              BR      2$
(3)
(3)                               ;/RETURNS HERE-FROM-TIMED OUT.
(2) 020040 032777 000040 161370      BIT    #BIT5,@KMAD0  ;FAST PATH READY?
(2) 020046 001370              BNE    2$
(2) 020050 112777 000004 161364      MOVB   #4,@KMAD2      ;ISSUE FAST PATH READ
(2) 020056 004737 020100              JSR    PC,$LPW
(2) 020062 117737 161360 020077      MOVB   @KMAD4,$DATR+1 ;SAVE HIGH BYTE
(2) 020070 012600              MOV    (SP)+,R0
(2) 020072 000205              RTS    R5
(2) 020074 000000              RD1: 0
(2) 020076 000000              $DATR: .WORD 0
(2)
(2)                               ;THIS ROUTINE WAITS FOR KMC-CODE TO BECOME READY AS WELL
(2)                               ;AS FAST PATH TO BE READ.
(2)
(2)                               ;
(2)                               ;      CALL = JSR    PC,$LPW
(2)                               ;
(2)                               ;IT WILL TIME OUT IF TOO MUCH TIME IS TAKEN BY
(2)                               ;THE MICRO-PROCESSORS AND REPORT AN ERROR, THEN HALT.
(2)
(2)
(2) 020100 010146              $LPW: MOV    R1,-(SP)      ;SAVE R1
(2) 020102 005001              CLR    R1
(2) 020104 122777 000377 161330 1$: CMPB   #377,@KMAD2    ;FINISHED INSTRUCTION?
(2) 020112 001403              BEQ    2$
(2) 020114 005201              INC    R1              ;TIME OUT?

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(2) 020116 001372          BNE      1$
(2) 020120 000411          BR       10$
(2)
(2) 020122 032777 000020 161306 2$:  BIT      #BIT4,@KMADC ;FAST PATH READ?
(2) 020130 001403          BEQ      3$
(2) 020132 005201          INC      R1           ;NO - TIME OUT?
(2) 020134 001372          BNE      2$
(2) 020136 000402          BR       10$          ;YES - REPORT AN ERROR
(2)
(2) 020140 012601          3$:  MOV     (SP)+,R1    ;RESTORE R1
(2) 020142 000207          RTS     PC           ;EXIT
(2)
(2) 020144                10$:
(3) 020144 104401 020152    TYPE     ,65$         ;;TYPE ASCIZ STRING
(3) 020150 000407          BR       64$         ;;GET OVER THE ASCIZ
(3)                ;;65$: .ASCIZ <200>#LPA-11 FAULT#
(3) 020170                64$:
(2)
(2) 020170 000000          11$:  HALT
(2) 020172 000776          BR       11$         ;LPA-11 FAULT RUN LPA-11
(2)                ;DIAGNOSTICS.
(2)
(2)
(2)                ;*
(2)                ;*THIS ROUTINE PROVIDES THE LINKAGE FROM USER CODE TO
(2)                ;*A DEVICE ADDRESS ON THE I/O BUSS FOR WRITE ONLY.
(2)                ;*
(2)                ;* FIRST WE WILL DETERMINE IF THE ADDRESS HAS BEEN USED
(2)                ;* BEFORE. IF NOT WE HAVE TO INITIALIZE THE LPA WITH
(2)                ;* THAT ADDRESS.
(2)                ;* WHEN THE ADDR. IS KNOWN BY THE LPA, DO THE OUTPUT BY
(2)                ;* $TLKW
(2)                ;*
(2)
(2) 020174 010046          $OUTLP: MOV    R0,-(SP) ;SAVE R0
(2) 020176 010146          MOV    R1,-(SP) ;SAVE R1
(2)
(2) 020200 012700 001464    MOV    #.DVLS,R0 ;PROGRAM DEFINED LIST.
(2) 020204 005001          CLR    R1
(2) 020206 005710          1$:  TST    (0)         ;TERMINATOR REACHED?
(2) 020210 001421          BEQ    10$         ;YES NEXT STEP.
(2) 020212 027520 000000    CMP    @ (5),(0)+ ;MATCH WITH ADDR IN LIST?
(2) 020216 001402          BEQ    2$
(2) 020220 005201          INC    R1
(2) 020222 000771          BR     1$
(2)
(2) 020224 010137 020242    2$:  MOV    R1,3$      ;SAVE OFFSET, DEVICE KNOWN.
(2) 020230 005725          TST    (5)+
(2) 020232 013537 020244    MOV    @ (5)+,4$   ;GET DATA TO BE WRITTEN
(2) 020236 004537 017616    JSR    R5,$TLKW   ;DO WRITE
(2) 020242 000000          3$:  .WORD  0        ;DEVICE OFFSET
(2) 020244 000000          4$:  .WORD  0        ;DATA TO BE WRITTEN.
(2) 020246 012601          MOV    (SP)+,R1
(2) 020250 012600          MOV    (SP)+,R0
(2) 020252 000205          RTS    R5
(2) 020254 017520 000000    10$:  MOV    @ (5),(0)+ ;SAVE ADDR.

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```
(2) 020260 005010 CLR (0)
(2) 020262 004537 016576 JSR R5,$LPAI
(2) 020266 001464 .WORD .DVLS
(2) 020270 000755 BR 2$
(2)
(2)
(2) ;* THIS ROUTINE PROVIDES THE LINKAGE FROM USER CODE
(2) ;* TO A DEVICE ADDR. ON THE I/O BUSS FOR READ ONLY.
(2) ;*
(2) ;* FIRST WE WILL DETERMINE IF THE ADDRESS HAS BEEN
(2) ;* USED BEFORE. IF NOT, WE HAVE TO INITIALIZE THE LPA
(2) ;* WITH THE NEW ADDR.
(2) ;* WHEN THE ADDR IS KNOWN WE CAN DO OUTPUT THROUGH
(2) ;* $TLKR
(2) ;* CALL THROUGH MOVEI DATA,ADDR.
(2) ;* WHICH EQUALS:
(2) ;* JSR R5,$INLP
(2) ;* .WORD XX ADDR OF DEVICE
(2) ;* .WORD YY ADDR TO STORE READ DATA.
(2)
(2) 020272 010046 $INLP: MOV R0,-(SP) ;SAVE R0
(2) 020274 010146 MOV R1,-(SP) ;SAVE R1
(2)
(2) 020276 012700 001464 MOV #.DVLS,R0 ;PROG DEFINED ADDR. LIST.
(2) 020302 005001 CLR R1
(2) 020304 005710 1$: TST (0) ;EOL REACHED?
(2) 020306 001420 BEQ 10$ ;YES - DEFINE NEW ADDR.
(2)
(2) 020310 027520 000000 CMP @ (5), (0)+ ;ADDR. MATCH?
(2) 020314 001402 BEQ 2$
(2) 020316 005201 INC R1
(2) 020320 000771 BR 1$
(2)
(2) 020322 010137 020334 2$: MOV R1,3$ ;SAVE LIST OFFSET
(2) 020326 005725 TST (5)+
(2) 020330 004537 017732 JSR R5,$TLKR ;GO READ DEVICE
(2) 020334 020334 $OFS=.
(2) 020334 000000 3$: .WORD 0 ;OFFSET OF DEVICE
(2)
(2) 020336 013735 020076 MOV $DATR,@ (5)+ ;STORE DATA.
(2) 020342 012601 MOV (SP)+,R1 ;RESTORE R1
(2) 020344 012600 MOV (SP)+,R0 ;RESTORE R2
(2) 020346 000205 RTS R5 ;EXIT
(2)
(2) 020350 017520 000000 10$: MOV @ (5), (0)+
(2) 020354 005010 CLR (0)
(2) 020356 004537 016576 JSR R5,$LPAI
(2) 020362 001464 .WORD .DVLS
(2) 020364 000756 BR 2$
(2)
(2) ;*
(2) ;* $TOUT ROUTINE USED TO WATCH IF
(2) ;* WE'RE IN A LOOP TOO-LONG
(2) ;* CALL= JSR R5, $TOUT
(2) ;* ERROR X ;RETURNS HERE ON TIMEOUT
(2) ;* BR
(2) ;* ;RETURNS HERE NO ERROR
```

```

(2)                                     ;*
(2)
(2) 020366 020537 020422 $TOUT:  CMP    R5,$SAD    ;SAME ADDR?
(2) 020372 001405          BEQ     1$          ;
(2) 020374 010537 020422          MOV    R5,$SAD    ;NO-SAVE THIS ADDR.
(2) 020400 005037 020424          CLR    $CNT       ;CLR CNT AT ADDR.
(2) 020404 000403          BR     2$          ;
(2) 020406 005237 020424 1$:     INC    $CNT       ;OVERFLOW?
(2) 020412 100402          BMI    3$          ;YES-ERROR RETURN
(2) 020414 062705 000004 2$:     ADD    #4,R5     ;NO-NON ERROR RETURN
(2) 020420 000205          3$:     RTS    R5      ;RETURN.
(2)
(2) 020422 000000          $SAD:  .WORD  0      ;CONTAINS LOOP ADDR.
(2) 020424 000000          $CNT:  .WORD  0      ;# OF TIMES AT ADDR.
(2)
(2)                                     ;*
(2)                                     ;* THIS ROUTINE REPLACES WHAT THE USER WOULD ORDINARILY
(2)                                     ;*USE FOR A RESET.  FIRST,WE DO A RESET INSTRUCTION.
(2)                                     ;*THEN WE CLR ".DVLST" WHICH FORCES US TO RESET BOTH THE
(2)                                     ;*KMC AND DMC AS SOON AS A DEVICE IS REFERENCED.
(2)                                     ;*
(2)                                     ;*      CALL=JSR      PC,$RESET      ;REPLACES 'RESET INSTRUCTION
(2)                                     ;*                                     ;RETURNS HERE.
(2)                                     ;*
(2) 020426 000005          $RESET: RESET      ;RESET THE WORLD.
(2)
(2)                                     ;*
(2) 020440 005737 017454          ;*      MOV    @2$,1$  ;/READ DEVICE REG 2$,PUT DATA IN 1$.
(2) 020444 001004          TST    $AERR       ;IF NO ERROR,LOOP
(2) 020446 062737 000002 020462 BNE    10$        ;THERE WAS AN ERROR.
(2)                                     ADD    #2,2$        ;UPDATE DEVICE ADDR.
(2)                                     ;YOU SEE ,WE HAVE TO PROTECT OUR SELF!
(2)                                     ;IF 2$ CONTAINED A VALID ADDR,WE
(2)                                     ;MUST KEEP TRYING UNTIL WE GENERATE
(2)                                     ;AN INVALID ADDR.
(2) 020454 000764          BR     $RESET
(2) 020456          10$:
(2) 020456 000207          RTS    PC
(2) 020460 000000          1$:     .WORD  0      ;JUNK LOC.
(2) 020462 160000          2$:     .WORD 160000 ;DUMB ADDR. FORCES INIT OF DMC/KMC.
(2)
(2)
(2)                                     ;
(2)                                     ;SDELAY- ROUTINE TO GIVE A MINOR DELAY.
(2)                                     ;IS NOT TIME DEPENDENT CODE SENCE
(2)                                     ;NOT USED TO GET SPECIFIC TIME BUT
(2)                                     ;JUST A LITTLE DELAY.
(2)
(2)                                     ;
(2)                                     ;THAT IS UNLESS A REAL TIME CLOCK IS PRESENT!
(2)                                     ;THEN WE'LL GENERATE A TIME BETWEEN 16MS TO 32 MS
(2)
(2)                                     ;
(2)                                     ;CALL= JSR PC, SDELAY
(2)
(2) 020464          SDELAY:
(2) 020464 005737 020546          TST    RTCCSR     ;CLOCK PRESENT?
(2) 020470 100016          BPL    10$

```





```

(3) 020666 000404             BR      66$          ;;GET OVER THE ASCIZ
(3)                          ;;:67$: .ASCIZ <200>#DATA= #
(3) 020700                    66$:
(2) 020700 104407             RDOCT
(2) 020702 012637 020720     MOV     (SP)+,13$
(2) 020706 004537 020174     11$: JSR     R5,$OUTLP       ;OUTPUT ROUTINE.
(2) 020712 020722           12$: .WORD  14$         ;DEVICE ADDR.
(2) 020714 020720             .WORD  13$         ;DATA
(2) 020716 000716             BR      21$

```

```

(2) 020720 000000           13$: .WORD  0
(2) 020722 000000           14$: .WORD  0
(2) 020724 100001 042504 044526 20$: .ASCIZ <1><200>#DEVICE ADDR= #
(2) 020732 042503 040440 042104
(2) 020740 036522 000040
(2)
(2)
(1)
(1)
(2)

```

.EVEN

```

; THIS ROUTINE LOOKS THROUGH CURENT .DVLS FOR A/D ADDR.
; IF UNFOUND,GENERATES IT. THIS ROUTINE'S WHOLE PURPOSE IS
; TO SET UP THE USER PROGRAM TO LINK TO FILE 'DRLPX2' FOR
; SAMPLE TAKEING PURPOSES.
; TO TAKE SAMPLES, THE USER PROGRAM MUST SET UP
; A/D CSR IN BSEL 4,AND 5.
; (2) HE MUST CALL THIS ROUTINE:
; JSR     R5,$PUTS          ;CALL SET UP ROUTINE.
; .WORD  ADCSR              ;ADDR. OF A/D CSR.
; RETURNS HERE ;KMC BSEL 3,6,7 PERMINENTLY SET UP
; ;(UNTILL ONE DOES A RESET)
;
; (3)THE USER MUST PUT CODE 006 INTO KMC REG 2 TO
; START CONVERSION CAUTION*DO WITH MOVB INSTR.!
; (4)MONITOR KMC REG 2 FOR CODE 377 (DRLPX2 IS DONE)
; (5)READ KMC REG 4,5 FOR A/D RESULT.
; (6) TO TAKE MORE SAMPLES,SIMPLY PUT A/D CSR INTO
; BSEL 4,5 AND CODE 6 INTO BSEL 2.

```

```

(2) 020744 012537 020754     $PUTS: MOV     (5)+,1$           ;GET ADDR OF ADDR. OF A/D
(2) 020750 004537 020272     JSR     R5,$INLP
(2) 020754 000000           1$: .WORD  0
(2) 020756 021052             .WORD  10$
(2) 020760 113777 020334 160464 MOVB    $OFS,@K MAD6
(2) 020766 113777 020334 160460 MOVB    $OFS,@K MAD7
(2) 020774 013737 020754 021014 MOV     1$,2$
(2) 021002 062737 000002 021014 ADD     #2,2$
(2) 021010 004537 020272     JSR     R5,$INLP
(2) 021014 000000           2$: .WORD  0
(2) 021016 021052             .WORD  10$
(2) 021020 113777 020334 160416 MOVB    $OFS,@K MAD3
(2) 021026 152777 000340 160416 BISB    #340,@K MAD6
(2) 021034 152777 000300 160412 BISB    #300,@K MAD7
(2) 021042 152777 000300 160374 BISB    #300,@K MAD3
(2) 021050 000205             RTS     R5

```

(2) 021052 000000

(2) 4388

10\$: .WORD 0  
.SBTTL BINARY TO OCTAL (ASCII) AND TYPE

(1)

```
::*****  
: *THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT  
: *OCTAL (ASCII) NUMBER AND TYPE IT.  
: *$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE  
: *CALL:  
: *      MOV      NUM,-(SP)      ::NUMBER TO BE TYPED  
: *      TYPOS    ::CALL FOR TYPEOUT  
: *      .BYTE   N              ::N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE  
: *      .BYTE   M              ::M=1 OR 0  
: *                               ::1=TYPE LEADING ZEROS  
: *                               ::0=SUPPRESS LEADING ZEROS
```

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1)

(1) 021054 017646 000000

(1) 021060 116637 000001

(1) 021066 112637 021301

(1) 021072 062716 000002

(1) 021076 000406

(1) 021100 112737 000001

(1) 021106 112737 000006

(1) 021114 112737 000005

(1) 021122 010346

(1) 021124 010446

(1) 021126 010546

(1) 021130 113704 021301

(1) 021134 005404

(1) 021136 062704 000006

(1) 021142 110437 021300

(1) 021146 113704 021277

(1) 021152 016605 000012

(1) 021156 005003

(1) 021160 006105

(1) 021162 000404

(1) 021164 006105

(1) 021166 006105

(1) 021170 006105

(1) 021172 010503

(1) 021174 006103

(1) 021176 105337 021300

(1) 021202 100016

(1) 021204 042703 177770

(1) 021210 001002

021277

021277

021301

021276

```
$TYPOS: MOV @ (SP),-(SP)      ::PICKUP THE MODE  
        MOVB 1(SP),$OFILL  ::LOAD ZERO FILL SWITCH  
        MOVB (SP)+,$SOMODE+1 ::NUMBER OF DIGITS TO TYPE  
        ADD #2,(SP)        ::ADJUST RETURN ADDRESS  
        BR $TYPON  
$TYPOC: MOVB #1,$OFILL     ::SET THE ZERO FILL SWITCH  
        MOVB #6,$SOMODE+1  ::SET FOR SIX(6) DIGITS  
$TYPON: MOVB #5,$SOCNT     ::SET THE ITERATION COUNT  
        MOV R3,-(SP)       ::SAVE R3  
        MOV R4,-(SP)       ::SAVE R4  
        MOV R5,-(SP)       ::SAVE R5  
        MOVB $SOMODE+1,R4  ::GET THE NUMBER OF DIGITS TO TYPE  
        NEG R4  
        ADD #6,R4          ::SUBTRACT IT FOR MAX. ALLOWED  
        MOVB R4,$SOMODE    ::SAVE IT FOR USE  
        MOVB $OFILL,R4     ::GET THE ZERO FILL SWITCH  
        MOV 12(SP),R5      ::PICKUP THE INPUT NUMBER  
        CLR R3             ::CLEAR THE OUTPUT WORD  
1$:    ROL R5              ::ROTATE MSB INTO 'C'  
        BR 3$             ::GO DO MSB  
2$:    ROL R5              ::FORM THIS DIGIT  
        ROL R5  
        ROL R5  
        MOV R5,R3  
3$:    ROL R3              ::GET LSB OF THIS DIGIT  
        DECB $SOMODE       ::TYPE THIS DIGIT?  
        BPL 7$            ::BR IF NO  
        BIC #177770,R3     ::GET RID OF JUNK  
        BNE 4$            ::TEST FOR 0
```

```
(1) 021212 005704          TST      R4          ;;SUPPRESS THIS 0?
(1) 021214 001403          BEQ      5$          ;;BR IF YES
(1) 021216 005204          4$: INC      R4          ;;DON'T SUPPRESS ANYMORE 0'S
(1) 021220 052703 000060  BIS      #'0,R3      ;;MAKE THIS DIGIT ASCII
(1) 021224 052703 000040  5$: BIS      #' ,R3      ;;MAKE ASCII IF NOT ALREADY
(1) 021230 110337 021274  MOVB     R3,8$        ;;SAVE FOR TYPING
(1) 021234 104401 021274  TYPE     ,8$         ;;GO TYPE THIS DIGIT
(1) 021240 105337 021276  7$: DECB    $OCNT      ;;COUNT BY 1
(1) 021244 003347          BGT      2$          ;;BR IF MORE TO DO
(1) 021246 002402          BLT      6$          ;;BR IF DONE
(1) 021250 005204          INC      R4          ;;INSURE LAST DIGIT ISN'T A BLANK
(1) 021252 000744          BR       2$          ;;GO DO THE LAST DIGIT
(1) 021254 012605          6$: MOV     (SP)+,R5    ;;RESTORE R5
(1) 021256 012604          MOV     (SP)+,R4      ;;RESTORE R4
(1) 021260 012603          MOV     (SP)+,R3      ;;RESTORE R3
(1) 021262 016666 000002 000004  MOV     2(SP),4(SP)   ;;SET THE STACK FOR RETURNING
(1) 021270 012616          MOV     (SP)+,(SP)
(1) 021272 000002          RTI
(1) 021274 000          8$: .BYTE  0          ;;RETURN
(1) 021275 000          .BYTE  0          ;;STORAGE FOR ASCII DIGIT
(1) 021276 000          .BYTE  0          ;;TERMINATOR FOR TYPE ROUTINE
(1) 021277 000          $OCNT: .BYTE 0       ;;OCTAL DIGIT COUNTER
(1) 021300 000000          $OFILL: .BYTE 0     ;;ZERO FILL SWITCH
                          $OMODE: .WORD 0                    ;;NUMBER OF DIGITS TO TYPE
```

4390

.SBTTL TRAP DECODER

(1)  
(2)  
(1)  
(1)  
(1)  
(1)  
(1)  
(1)

```

*****
;*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE 'TRAP' INSTRUCTION
;*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
;*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
;*GO TO THAT ROUTINE.
    
```

(1) 021302 010046  
(1) 021304 016600 000002  
(1) 021310 005740  
(1) 021312 111000  
(1) 021314 006300  
(1) 021316 016000 021336  
(1) 021322 000200

```

$TRAP:  MOV    R0,-(SP)      ;;SAVE R0
        MOV    2(SP),R0     ;;GET TRAP ADDRESS
        TST    -(R0)        ;;BACKUP BY 2
        MOVB   (R0),R0      ;;GET RIGHT BYTE OF TRAP
        ASL    R0           ;;POSITION FOR INDEXING
        MOV    $TRPAD(R0),R0 ;;INDEX TO TABLE
        RTS    R0          ;;GO TO ROUTINE
    
```

(1)  
(1)  
(1)  
(1)  
(1)  
(1)

;;THIS IS USE TO HANDLE THE 'GETPRI' MACRO

(1) 021324 011646  
(1) 021326 016666 000004 000002  
(1) 021334 000002

```

$TRAP2: MOV    (SP),-(SP)   ;;MOVE THE PC DOWN
        MOV    4(SP),2(SP)  ;;MOVE THE PSW DOWN
        RTI                    ;;RESTORE THE PSW
    
```

(3)  
(3)  
(3)  
(3)  
(3)  
(3)

.SBTTL TRAP TABLE

```

;*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
;*BY THE 'TRAP' INSTRUCTION.
    
```

(3) 021336 021324  
(3) 021340 016046  
(3) 021342 021100  
(3) 021344 021054  
(3) 021346 021114  
(1)  
(1)  
(3) 021350 014644  
(3) 021352 014764  
(3) 021354 015136

```

:      ROUTINE
:      -----
$TRPAD: .WORD  $TRAP2
        $TYPE  ;;CALL=TYPE      TRAP+1(104401)  TTY TYPEOUT ROUTINE
        $TYPOC ;;CALL=TYPOC    TRAP+2(104402)  TYPE OCTAL NUMBER (WITH LEADING ZEROS)
        $*POS  ;;CALL=TYPOS    TRAP+3(104403)  TYPE OCTAL NUMBER (NO LEADING ZEROS)
        $*PON  ;;CALL=TYPON    TRAP+4(104404)  TYPE OCTAL NUMBER (AS PER LAST CALL)

        $RDCHR ;;CALL=RDCHR    TRAP+5(104405)  TTY TYPEIN CHARACTER ROUTINE
        $RDLIN ;;CALL=RDLIN    TRAP+6(104406)  TTY TYPEIN STRING ROUTINE
        $RDOCT ;;CALL=RDOCT    TRAP+7(104407)  READ AN OCTAL NUMBER FROM TTY
    
```

4392

.SBTTL POWER DOWN AND UP ROUTINES

(1)  
(2)  
(1)  
(1)  
(1)  
(3)  
(3)  
(3)  
(3)  
(3)  
(3)  
(3)  
(3)  
(1)  
(1)  
(1)  
(1)  
(1)

021356 012737 021522 000024  
021364 012737 000340 000026  
021372 010046  
021374 010146  
021376 010246  
021400 010346  
021402 010446  
021404 C10546  
021406 017746 157526  
021412 010637 021526  
021416 012737 021430 000024  
021424 000000  
021426 000776

```

*****
:POWER DOWN ROUTINE
$PWRDN: MOV    #$ILLUP,@#PWRVEC ;;SET FOR FAST UP
        MOV    #340,@#PWRVEC+2 ;;PRIO:7
        MOV    R0,-(SP)        ;;PUSH R0 ON STACK
        MOV    R1,-(SP)        ;;PUSH R1 ON STACK
        MOV    R2,-(SP)        ;;PUSH R2 ON STACK
        MOV    R3,-(SP)        ;;PUSH R3 ON STACK
        MOV    R4,-(SP)        ;;PUSH R4 ON STACK
        MOV    R5,-(SP)        ;;PUSH R5 ON STACK
        MOV    @SWR,-(SP)      ;;PUSH @SWR ON STACK
        MOV    SP,$SAVR6      ;;SAVE SP
        MOV    #$PWRUP,@#PWRVEC ;;SET UP VECTOR
        HALT
        BR     -2              ;;HANG UP

```

(1)

(2)

(1)

021430 012737 021522 000024  
021436 013706 021526  
021442 005037 021526  
021446 005237 021526  
021452 001375  
021454 012677 157460  
021460 012605  
021462 012604  
021464 012603  
021466 012602  
021470 012601  
021472 012600  
021474 012737 021356 000024  
021502 012737 000340 000026  
021510 104401  
021512 021530  
021514 012716  
021516 001714  
021520 000002  
021522 000000  
021524 000776  
021526 000000  
021530 005015 047520 042527  
021536 000122

```

*****
:POWER UP ROUTINE
$PWRUP: MOV    #$ILLUP,@#PWRVEC ;;SET FOR FAST DOWN
        MOV    $SAVR6,SP      ;;GET SP
        CLR    $SAVR6        ;;WAIT LOOP FOR THE TTY
1$:     INC    $SAVR6        ;;WAIT FOR THE INC
        BNE    1$           ;;OF WORD
        MOV    (SP)+,@SWR    ;;POP STACK INTO @SWR
        MOV    (SP)+,R5     ;;POP STACK INTO R5
        MOV    (SP)+,R4     ;;POP STACK INTO R4
        MOV    (SP)+,R3     ;;POP STACK INTO R3
        MOV    (SP)+,R2     ;;POP STACK INTO R2
        MOV    (SP)+,R1     ;;POP STACK INTO R1
        MOV    (SP)+,R0     ;;POP STACK INTO R0
        MOV    #$PWRDN,@#PWRVEC ;;SET UP THE POWER DOWN VECTOR
        MOV    #340,@#PWRVEC+2 ;;PRIO:7
        TYPE   $POWER        ;;REPORT THE POWER FAILURE
$PWRMG: .WORD  $POWER        ;;POWER FAIL MESSAGE POINTER
        MOV    (PC)+,(SP)    ;;RESTART AT BEGIN
$PWRAD: .WORD  BEGIN        ;;RESTART ADDRESS
        RTI
$ILLUP: HALT
        BR     -2              ;;THE POWER UP SEQUENCE WAS STARTED
        ;; BEFORE THE POWER DOWN WAS COMPLETE
        ;; PUT THE SP HERE
$SAVR6: 0
$POWER: .ASCIZ <15><12>'POWER'
        .EVEN

```

4393

4394

4395

4396

4397

4398

4399

4400

4401

4402

021540 000310  
042000  
042300  
042300 010000  
000001

```

.EVEN
DIST:  .BLKW 200.           ;STATE-WIDTH DISTRIBUTION
        .=42000
;THE MICRO-CODE FOR THIS PROGRAM RESIDES HERE.
        .=42300
BUFFER: .BLKW 4096.        ;BUFFER AREA
.END

```





CHANL	001362	3055#	3429*	3616*	3689	3744	4027*	4028	4055	4082*	4083*	4084	4119*	4370
CHANNEL	007042	3790#												
CH1	001350	3050#	3600*	3601*	3604	3611*	3633	3635	3657	3663*	3665*			
CH2	001352	3051#	3614*	3616	3629	3653	3664*	3666*						
CLEAR1	006770	3776#	3778											
CLEAR2	007034	3787#	3789											
CLKINT	020540	4387#												
CMSG	012250	3093	4294#											
CNNO	006746	3726*	3730*	3743	3769#									
COMPAR	011314	3305	3329	3337	3345	3354	3362	3374	3397	3409	3417	3424	3446	4156#
CONV	006300	3697#	3702											
CONVR	007150	3804	3807#											
CONVRT	011072	3303	3327	3335	3343	3352	3360	3371	3383	3407	3415	3422	3639	4117#
CR =	000015	2936#	4385											
CRLF =	000200	2936#	4385											
CO	013675	3130	4337#											
C1	013700	3714	4338#											
C2	013702	3920	4004	4339#										
C3	013705	4001	4340#											
DAC	001404	3064#	3434	3437	3656	3660	3734*	3738*	3739	3764*	4070	4073	4076	4079
DASH	012277	3866	4297#											
DAWAIT	004646	3370	3382	3406	3478#									
DDISP =	177570	2936#	2996	3122										
DECPNT	014576	3871*	3900*	4364#										
DECTYP	011504	3444	3627	3867	3873	3881	3885	3888	3899	3950	4048	4051	4206#	
DELAY	001406	3065#	3740*	3741*										
DELAY1	007230	3813	3821#											
DELAY2	007236	3815	3822	3823#										
DELAY3	007144	3805#	3806											
DELCLR	010342	3909	3921	4005	4015#									
DF1	014642	3013	3020	3026	3032	4373#								
DH1	014417	3011	4359#											
DH2	014455	3030	4360#											
DH3	014540	3018	3024	4361#										
DIFLIN	006750	3475	3772#											
DISPLA	001142	2996#	3122*	3505*	4381*	4382*								
DISPRE	000174	2988#	3122											
DIST	021540	3779	3846*	3912	4394#									
DONE	012422	4306#												
DRLPX2=	***** G	52#	4387											
DSWR =	177570	2936#	2996	3122										
DT1	014602	3012	4369#											
DT2	014614	3031	4370#											
DT3	014632	3019	3025	4371#										
DUMMY	001360	3054#	3430*	3653*	3657*	3746	4028*	4084*						
EDGE	001410	3066#	3431*	3671*	3674*	3695*	3700*	3703*	3704*	3705*	3706*	3709	3757	4090*
		4093*												
EDGFLG	006450	3624*	3679*	3711	3717#	4031*	4098*							
EDINT	001430	3074#	3519*	3529*	3547*	3551*								
EMTVEC=	000030	2936#	3122*											
EM1	014257	3010	4355#											
EM2	014305	3017	4356#											
EM3	014335	3023	4357#											
EM4	014366	3029	4358#											
ER	010564	4057	4059	4062#										
ERMSG	012511	3451	3647	3877	3892	3904	3958	4062	4311#					



ERR	006052	3643	3647#							
ERRVEC=	000004	2936#	3122*	4381*						
FIRST	001342	3047#	3785*	3858	3860*					
FIXADR	005372	3559	3567#							
FIXONE	005376	3146	3568#							
FLAG	001400	3062#	3125*	3142*	3907	3961				
GETDAT	010120	3966#	3974							
GETEDG	006226	3617	3667	3688#	4029	4085				
GMSG	012255	3107	4295#							
GNS =	***** U	2988	4387	4390						
HAFMSG	012701	3901	4316#							
HALF	007600	3893	3895#							
HEAD1	014040	3145	4348#							
HEAD5	013617	3486	4335#							
HT =	000011	2936#	4385							
HUNS	014575	4215*	4227*	4229*	4232	4363#				
INRNGE	007324	3842	3845#							
IOTVEC=	000020	2936#	3122*							
ISERV	001550	3088#	3148							
KBVECT	001334	3044#	3147	3582						
KMAD0	001436	3079#	3124	4387*						
KMAD1	001440	3079#	3124							
KMAD2	001442	3079#	3750*	3751	3754*	3755	3809*	3810	4387*	
KMAD3	001444	3079#	4387*							
KMAD4	001446	3079#	3749*	3753*	3757	3808*	3812	4387*		
KMAD5	001450	3079#								
KMAD6	001452	3079#	4387*							
KMAD7	001454	3079#	3124	4387*						
LAST	007442	3856	3869#							
LEND	004644	3474	3476#							
LESS	011422	4182	4184#							
LF =	000012	2936#	4385							
LINEA	013576	3951	4334#							
LOAD	010320	4000	4003	4007#						
LOADY	011402	3915	3917	4010	4178#					
LOAD0	010324	4008#	4012							
LO2	010316	3962	4006#							
LPADH	001450	3079#								
LPADL	001446	3079#								
LPCI	001436	3079#								
LPC0	001442	3079#								
LPMR	001440	3079#								
LPMS1	001452	3079#								
LPMS2	001454	3079#								
LPS0	001444	3079#								
LSB	012333	3628	4301#							
LSBMSG	012270	3868	4296#							
MAT	013730	3630	4343#							
MAX	001420	3070#	3378*	3385	3389*	3982*	3987	3989*	3993	
MAXTST	010216	3985	3987#							
MEND	013001	3533	4319#							
MESP	012763	4052	4318#							
MESR	012750	4049	4317#							
METST	013735	4344#								
MIN	001414	3068#	3390*	3439	3981*	3984	3986*	3992		
MINUS	012237	4208	4291#							



















ADDM	2971#	3700													
BICM	2959#	3254													
CLRM	2954#	3325	3485												
CMPM	2977#														
COMMEN	2936#														
DUMWRN	3001#														
ENDCOM	2936#														
ERROR	2936#	3083	3223	3232	3237	3242	3246	3256	3267	3281	3308	3323	3332	3340	3348
	3357	3365	3377	3400	3412	3420	3427	4138	4387						
ESCAPE	2936#	3081													
GETPRI	2936#														
GETSWR	2936#														
INCRM	2947#	3250	3498	3697											
MOVE I	170#	3250	3254	3257	3263	3265	3272	3283	3291	3324	3498	3500	3502	3697	3698
	3700	4122	4129	4141	4143	4387									
MOVEM	157#	3193	3250	3254	3261	3271	3276	3290	3314	3318	3325	3369	3381	3485	3496
	3498	3638	3688	3691	3697	3739	3795	4123	4131						
MOVEMR	2983#	3496	3691	3795	4123										
MOVERO	2942#	3257	3265	3283	3324	3502									
MULT	2936#														
NEWTST	2936#	3219	3228	3234	3239	3243	3248	3259	3269	3299	3310	3326	3334	3342	3351
	3359	3367	3414	3421	3428	3455	3464	3472							
POP	2936#	4379	4386	4392											
PUSH	2936#	4379	4386	4392											
REPORT	2936#														
SCOPE	2936#	3228	3234	3239	3243	3248	3259	3269	3285	3310	3326	3334	3342	3351	3359
	3367	3414	3421	3428	3455	3464	3472								
SETPRI	2936#														
SETTRA	4390#														
SETUP	2936#	3122													
SKIP	2936#	3165	3168	3171	3174	3177	3180	3199	3209	3211	3213	3226	3293	3312	3452
	3491	4022	4237	4239											
SLASH	2936#														
SPACE	2936#														
STARS	2936#	2993	2995	2996	3219	3228	3234	3239	3243	3248	3259	3269	3299	3310	3326
	3334	3342	3351	3359	3367	3414	3421	3428	3455	3464	3472	4285	4377	4379	4381
	4382	4383	4385	4386	4388	4390	4392								
SWRSU	2936#	3122#													
TOUT	3079#	4387													
TRMTRP	4390#														
TSTBM	2966#	3263	3272	3500	3698	4141									
TYPBIN	2936#														
TYPDEC	2936#														
TYPNAM	2936#														
TYPNUM	2936#														
TYPOCS	2936#	3108	3201	3493	3508	3629	3633	3641	3710	3715	3865	3952	3955	4055	
TYPOCT	2936#	4383	4387												
TYPTXT	2936#	4387													
\$CAL	747#	4387													
\$DMAST	1792#														
\$DMDT	2821#														
\$MMAST	761#														
\$SCMRE	2996#														
\$SCMTM	2996#														
\$SESCA	2936#														
\$SNEWT	2936#	3219	3228	3234	3239	3243	3248	3259	3269	3299	3310	3326	3334	3342	3351

	3359	3367	3414	3421	3428	3455	3464	3472
\$\$SET	4390#							
\$\$SETM	3122#							
\$\$SKIP	2936#	3312	3452					
.EQUAT	2930#	2936						
.HEADE	2930#	2935						
.KMADR	55#	3079						
.KSIS	184#	3124						
.LOADL	458#	4387						
.LPAIN	209#	4387						
.PUTCS	417#	4387						
.RESET	328#	4387						
.SETUP	2932#	2997						
.SWRHI	2932#	2937						
.SWRLO	2937#							
.UTK	698#	4387						
.\$ACT1	2933#	2993						
.\$APT8	2933#	2996#						
.\$APTH	2933#	2995						
.\$APTY	2933#	4386						
.\$CATC	2930#	2988						
.\$CMTA	2930#	2996						
.\$EOP	2930#	4285						
.\$ERRO	2931#	4382						
.\$ERRT	2932#	4383						
.\$INLP	651#	4387						
.\$MMAC	141#							
.\$OUTL	609#	4387						
.\$PARM	2931#							
.\$POWE	2931#	4392						
.\$RAND	2933#							
.\$RDOC	2933#	4379						
.\$READ	2931#	4377						
.\$SAVE	2931#							
.\$SCOP	2931#	4381						
.\$SPAC	2932#							
.\$SWDO	2932#							
.\$TLKW	510#	4387						
.\$TOUT	3079#	4387						
.\$STRAP	2932#	4390						
.\$TYPD	2933#							
.\$TYPE	2932#	4385						
.\$TYPO	2931#	4388						

. ABS. 062300 000 CON RW ABS GBL D  
 000000 001 CON RW REL LCL I

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0

CRLPKB,CRLPKB/CRF=DRLPA.MAC,CRLPKB  
 RUN-TIME: 30 17 1 SECONDS  
 RUN-TIME RATIO: 163/49=3.3  
 CORE USED: 40K (79 PAGES)

LPA-AD11K TEST MD-11-CRLPKB  
CRLPKB.P11 08-AUG-79 10:18

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CROSS REFERENCE TABLE -- MACRO NAMES

SEQ 0099