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FIELD MAINTENANCE PRINT SET

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B-DD-LO109-0 (COMPLETE)	PACKET BUFFER BOARD

UNIT VARIATIONS COVERED BY THIS PRINT SET

CI20-AA
CI20-AB

FIELD MAINTENANCE
PRINT SET

DIGITAL EQUIPMENT
CORPORATION

PRINT SET PART
NUMBER MP01903

REVISION HISTORY	DATE	ECO NUMBER	REV.	DRN.	DATE	TITLE	digital	SIZE CODE	NUMBER	REV.
			A	J.F. BROWN	18APR84					
			B	G. F.	9JUL84					
				P. CAPPABIANCA	9JUL84					
			P. CAPPABIANCA	9JUL84						
				FIELD SERVICE	DATE					
				CHRIS DEMOS	9JUL84					
				TOP DOC.						
				B-DD-CI20-A-DBU						
										SHEET 1 OF 2

REV. B
SIZE CODE B TC
NUMBER C120-A-DBU

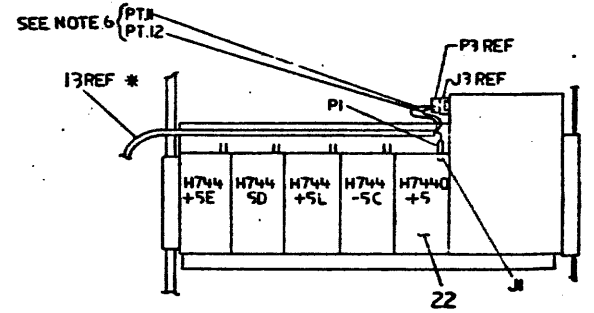
A-PS-9107673-0-0	POWER CORD
D-AD-7011432-0-0	POWER CORD EXT.
K-PL-7011432-0-DBP	POWER CORD EXT.
D-IA-7020352-0-0	HARNESS DC VOLTAGE MON.
K-PL-7020352-0-DBP	HARNESS DC VOLTAGE MON.
D-IA-7019862-0-0	HARNESS VANE SWITCH
K-PL-7019862-0-DBP	HARNESS VANE SWITCH
B-DD-5414506-0 (COMPLETE)	VOLTAGE MONITOR BOARD
D-IA-7019270-0-0	CABLE M-BUS
K-PL-7019270-0-DBP	CABLE M-BUS
D-UA-BNCI-0-0	CABLE, BNCI
K-PL-BNCI-0-DBP	CABLE, BNCI
B-DD-5414793-0 (COMPLETE)	BACKPANEL CI20

TITLE:	CI20-A	SIZE	CODE	NUMBER	REV.
	TABLE OF CONTENTS	SHEET 2 OF 2	B TC	CI20-A-DBU	B

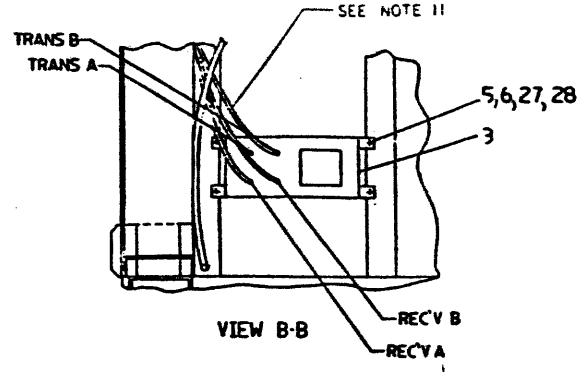
LEGEND		
PART NO	VARIATION	REV
C120-AS	157-52/51-72	33
C120-AB	232V 50/60HZ	83

- NOTES: FOR ALL HARNESS AND CABLE CONNECTIONS SEE HARNESS OR CABLE CHARTS ON SHEET 3.
- ATTACH CABLE TIES (ITEM 20) APPROX 6" APART EXCEPT WHERE SHOWN.
 - ITEM 16 OCCUPIES SLOTS 16, 17, 18.
 - MODULE M 3001 ITEM 19 IS LOCATED IN RM20 LOGIC ASSY SLOT 19. MODULE M 3002 ITEM 17 IS LOCATED IN SLOT 14. MODULE M 3003 ITEM 18 IS LOCATED IN SLOT 15.
 - PLACE ITEM 34 (DECAL AIR FLOW) ON POWER CONTROLLER 863 NEXT TO AIR FLOW CPU LED.
 - CONNECT TO ANY AVAILABLE SWITCHED OUTLET.
 - CONNECT POINTS 11 & 12 TO H7420, 9 PIN MATE-N-LOCK, TO AVAILABLE POSITION 1&2, OR 3&4, OR 5&6, OR 7&8 (19-30 VAC).
 - P2 ON ITEM 30 (HARNESS, VANE SWITCH) CONNECTS WITH EXISTING VANE SWITCH CONNECTOR. SEE VIEW D-D ON SHEET 2 OF 3.
 - J1 ON ITEM 30 (HARNESS, VANE SWITCH) CONNECTS WITH EXISTING CONNECTOR P4. SEE VIEW D-D ON SHEET 2 OF 3.
 - ITEM 39 (SUPPORT, CABLE) NOT USED IF SYSTEM IS EQUIPPED WITH A MAZ0 CORE MEMORY.
 - SWITCH POSITIONS ON VOLTAGE MONITOR BOARD: SWITCH 1 - ON SWITCH 2 - OFF SWITCH 3 - OFF SWITCH 4 - OFF

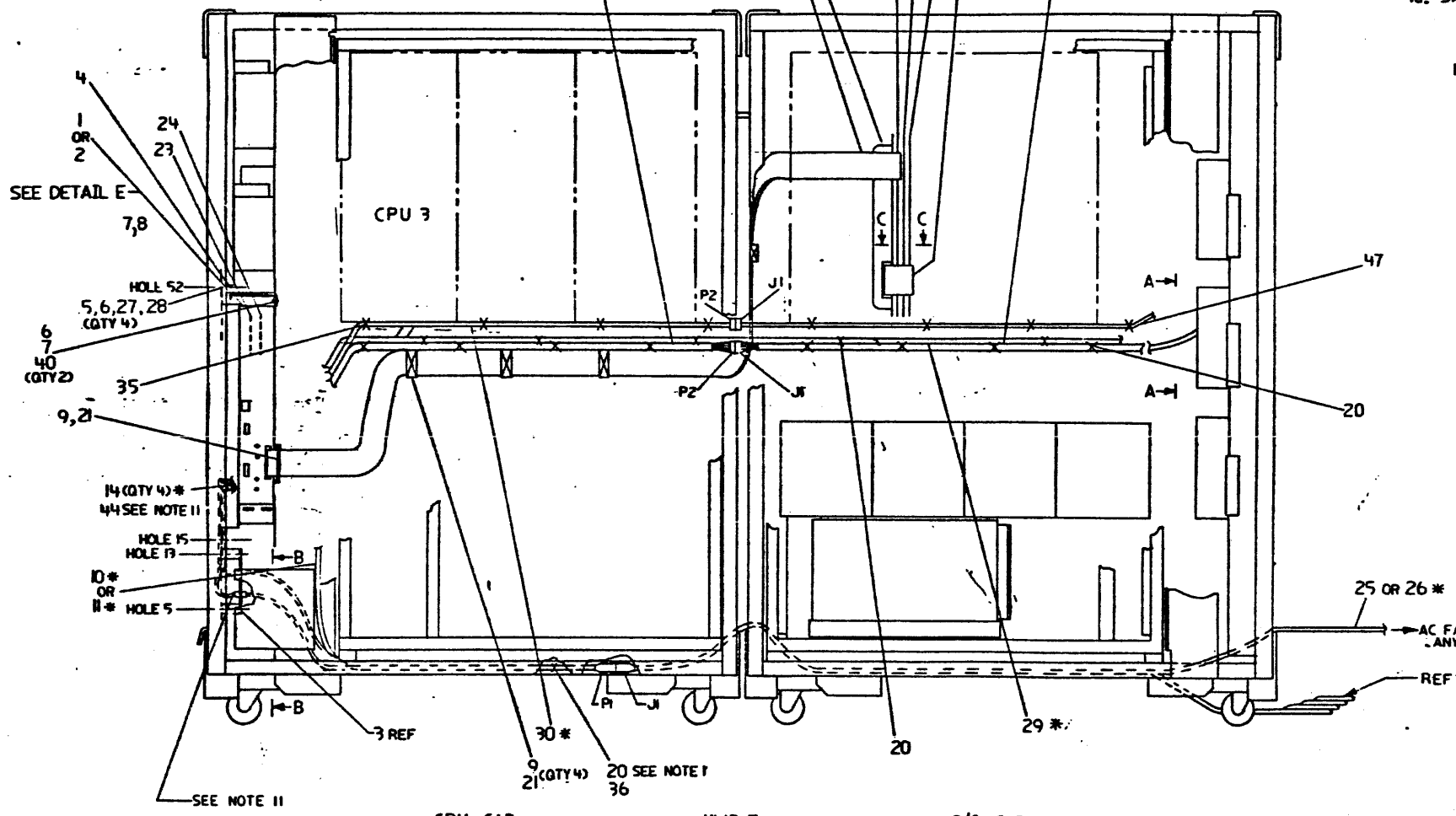
NOTES: CONTINUED ON SHEET 2



VIEW A-A
SHOWING P5 H7420
WITH DC+5V CONNECTION



VIEW B-B



CPU CAB
I/O CAB
KL10-E
REAR VIEW
DEC 20

25 OR 26 *
AC FAN HARNESS TO POWER CONTROLLER 861
(ANY AVAILABLE SWITCHED OUTLET)

REF: BNCIA-20

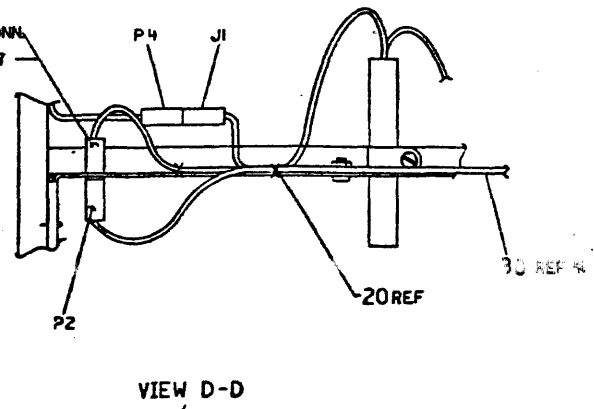
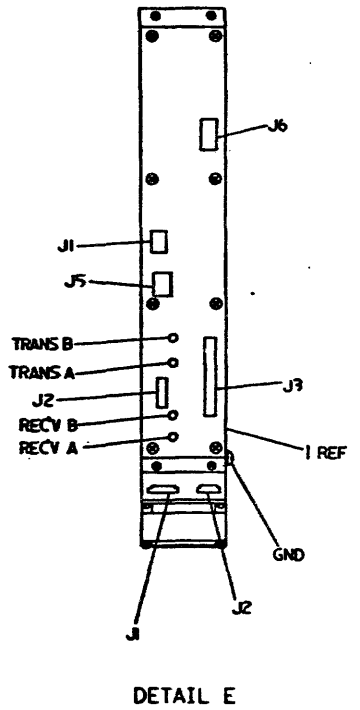
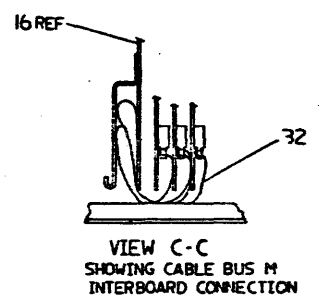
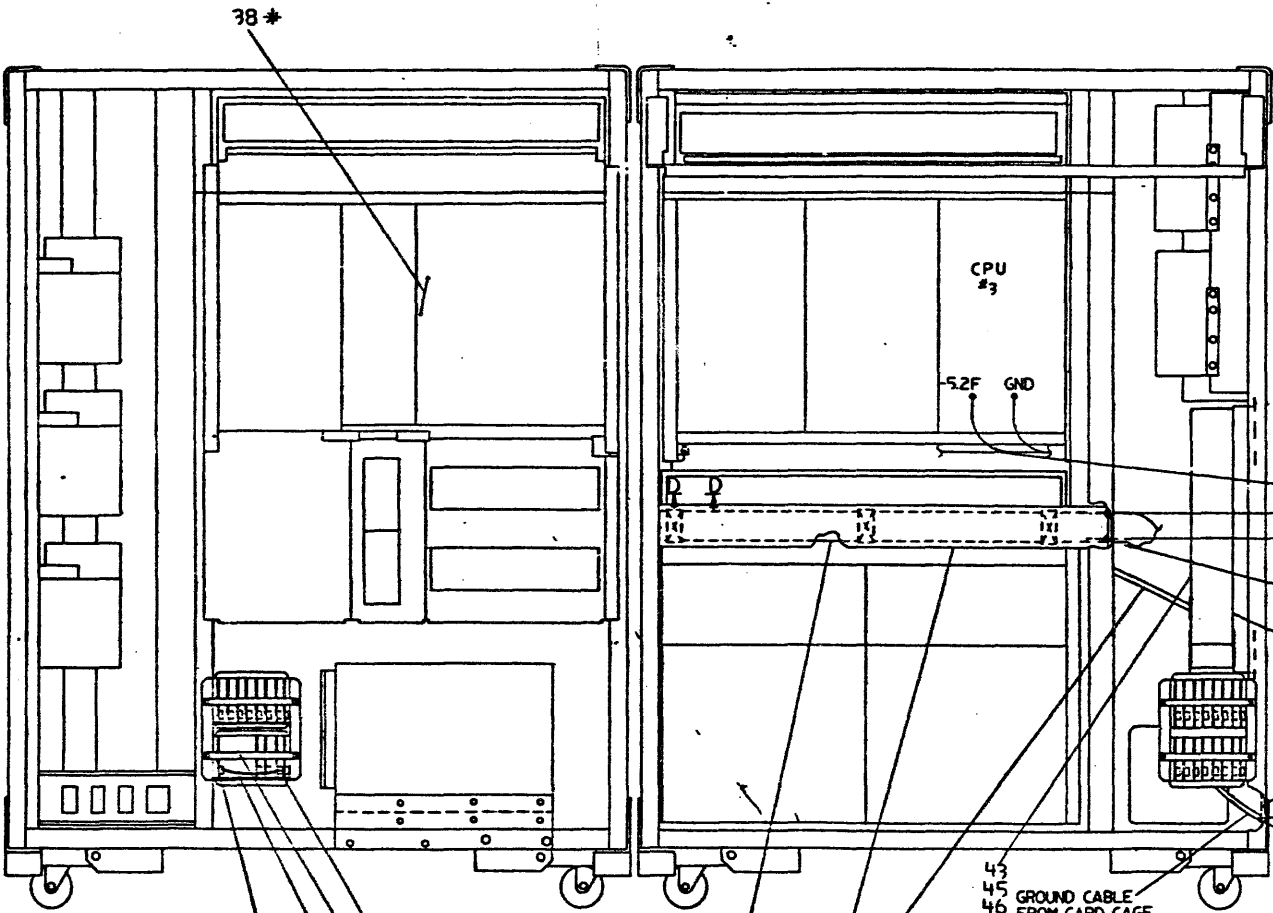
CAUTION: OFF SHEET PARTS LIST SEE K-PL-C120-A-DBP.

DATE	BY	CHKD	APP'D

QUANTITY & UNIT		DESCRIPTION		DRAWING NO.		PART NO.	
1	1	ASSY	C120				
1	1	ASSY	C120-A-0				

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY PER DEC STD 115	DATE	TITLE
	14 MAR 73	C120 ASSY
THIS ANGLE PROJECTION	DATE	DOCUMENT NUMBER
DO NOT SCALE DRAWING	25 APR 73	EL-C120-A-0
REMOVE BURRS AND BREAK SHARP CORNERS	25 APR 73	
SEE PARTS LIST		

NOTES: 1. MARK CABLES ITEM #4 USING ITEM #44 MARKING BOTH ENDS THE SAME, WITH TA, TB, RA, RB. ALSO MARK TA, TB, RA, RB ON THE CI20 BACKPANEL PROTECTION S-SIELD.



FRONT VIEW

VIEW D-D

CABLE CONNECTION CHART

ITEM NO.	FROM UNIT	LOCATION	REF DESIG	TO UNIT	LOCATION	REF DESIG	REMARKS
00R	CARD CAGE	GND	GND	25 OR 26	J1	P1	
	FAN BKLT	J2	P2				
14	C120 BP	TRANS A	P1	INTERFACE	TRANS A	P2	
		RECV A			RECV A		
		TRANS B			TRANS B		
		RECV B			RECV B		
14			P1	INTERFACE	RECV B	P2	
15	C120 BP	J3	P1 STRIP DOWN	RH20 DTE (CMR33)	J2	P2 STRIP UP	
32	M3001	J1	P3	M3002	J1	P2	
		J1	P1				
29	ITEM I/O CR II	P1		851 PC		SEE NOTE 5	
1/2	CARD CAGE	GND		CAB RAIL		HOLE #2	
39	RH20 BP	B10N1		RH20 BP	B13J1		
		B13B1			B13B1		
		C11L2			B13B2		
		B19B2			B19B2		
		C10K2			B19J1		
		B19J1			B19J1		
		C10T2			C19B1		
		C13B1			C19B1		
		C12H2			C19N1		
		C13N1			C19N1		
		C12L1			C19B2		
		C13B2			C19B2		
		C14H2			A15R2		
		C14F1			F15A1		
		A14J2			A15E1		
		C14P1			A15D2		
		C14K2			A15S2		
		B14J1			B15A1		
		C20H2			A21R2		
		C23F1			F21A1		
		A21J2			A21E1		
		C20P1			A21D2		
		C20K2			A21S2		
39	RH20 BP	B20J1		RH20 BP	B21A1		

WIRING CONNECTION CHART

ITEM NO.	WIRING TERM	CONNECTION	WITH	REMARKS
12	P1	C120 BP J2		
	P3	C120 BP J1		
5		25-26 GND		
6		C120 BP-52F		
12	P2	SECTION-2 J1		ITEM #3
13	P1	M7443 J1		SEE VIEW A-A
15		SEE NOTE 6		
15	12			
30	P1	FAN SKT J1		
30	P2	SEE NOTE 7		
30	J1	SEE NOTE 8		
30	P3	C120 BP J6		
29	P2	C120 BP J5		
29	P1	MON 80 J1		ITEM #11
29	6	+5V MON 80 J1-5		SEE FRONT VIEW

DATE: _____
 RELEASED: _____

TITLE		DOCUMENT NUMBER	
C120-A ASSY		EUA C120-A-0	
SCALE	1	DATE	MRO

EUA C120-A-0

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION	
						AA	AB
						B3 B3	
1	1	E-AD-7019268-0-0	7019268-00		CARD CAGE ASSY IPA-20-L	1	-
2	2	E-AD-7019268-0-0	7019268-01		CARD CAGE ASSY CI20	-	1
3	3	D-IA-7428312-0-0	7428312-01	A	BRACKET, INTERFACE	1	1
4	4	C-IA-7428222-0-0	7428222-01		BAFFLE, AIR	1	1
5	5		9007786-00		RETAINER, U-NUT 10-32X	13	13
6	6		9006073-01		SCREW, MACH PAN PHIL 10-	13	13
7	7		9006022-01		SCREW, MACH PAN PHIL 6-	4	4
8	8		9006633-00		WASHER, LOCK INTERNAL STEEL	4	4
9	9		1213716-00		SPACER, FOAM POLYU 1/2	4	4
10	10	D-IA-7020539-0-0	7020539-06		CABLE, FAN AC	-	1
11	11	D-IA-7019274-0-0	7019274-06		CABLE, FAN AC	1	-
12	12	D-IA-7019272-0-0	7019272-00		HARNESS DC-5.2 SECT N1-1 DC+5	1	1
13	13	D-IA-7019273-0-0	7019273-00		HARNESS DC-5.2 SECT N1-2 DC+5	1	1
14	14	D-IA-7018526-0-0	7018526-3A		CABLE ASSY (CI780)	4	4
15	15		BC06R-08		BC06R I/O CABLE	1	1
16	16	D-AD-7019266-0-0	7019266-00		MODULE BLANK ASSY	1	1
17	17		M3002-00		CI20 MICROPROCESSOR, MULTIWIRE HE	1	1
18	18		M3003-00		CI20 C-BUS/PLI INTERFACE, MULTIWI	1	1
19	19		M3001-00		CI20 E-BUS INTERFACE, MULTIWIRE H	1	1
20	20		9007032-00		TIE, CABLE BUNDL. DIA 0-1-3/4"=101	A/R	A/R
21	21		1213715-00		CLIP, FLAT CABLE W/ADHESIVE BK	4	4
22	22		H7440-00		POA1 H7440	1	1
23	23		L0100-00		ILI (CI LINK INTERFACE), HEX 12	1	1
24	24		L0109-00		HSC50 PILA (PACKET BUFFER BOARD),	1	1
25	25		9107673-06		PWR CORD, TERM 3-14 SJT 115	1	-
26	26	D-AD-7011432-0-0	7011432-02		POW CORD EXTENSION 50HZ	-	1
27	27		9007651-00		WASHER, LOCK EXTERNAL STEEL	14	14
28	28		9006664-00	-	WASHER, FLAT SST	12	12
29	29	D-IA-7020352-0-0	7020352-00		HARNESS, D.C. VOLTAGE MONITOR	1	1
30	30	D-IA-7019862-0-0	7019862-00		HARNESS, VANE SWITCH	1	1

REVISION HISTORY			BASIC PART NO: CI20		DRN:	J. F. BROWN	DATE:	30-NOV-82	D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF A		CHK'D:	G. F.	DATE:	14-APR-83	TITLE PARTS LIST			
---	INITIAL	E	SECTION, VARIATION INDEX		DES. ENG:	P. CAPPABIANCA	DATE:	25-APR-83	DOCUMENT NUMBER			
JT	CI20-MR001	F	(A)AA, AB		RESP. ENG.:	P. CAPPABIANCA	DATE:	25-APR-83	SIZE	CODE	NUMBER	REV
			(B)		MFG. ENG.:	P. DUPONT	DATE:	9-JUL-84	K	PL	CI20-A-DBP	F
			(C)					RELEASE DATE: 22-JAN-85				
			(D)									
			(E)									
			(F)									
			ASSEMBLY NUMBER:			TOP DOCUMENT NUMBER:			FILE NAME:		EDIT #	
			E-UA-CI20-A-0			B-DD-CI20-A-DBU			Z5905F.PLS		34	

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LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION	
						AA	AB
					VARIATION REVISION LEVEL:	B3	B3
31	31		5414506-01		VOLTAGE, MONITOR BOARD	1	1
32	32	D-IA-7019270-0-0	7019270-1J		BUS, CABLE, M ASSY.	1	1
33	33		3621499-02		LABEL,DCV MONITOR/NI CI 20	1	1
34	34		3613272-00		LABEL,ADH BACK,MYLAR CAP	1	1
35	35		9007031-00		TIE,CABLE BUNDL.DIA 0- 3/4"=101	36	36
36	36		9008264-00		MOUNT, CABLE TIE, ADHESIVE BACKE	A/R	A/R
37	37		3621498-02		LABEL,AIRFLOW CPU/NI CT 20	1	1
38	38	SEE NOTE 1	9105740-55		WIRE(WRAP) 30AWG KYNAR UL14	A/R	A/R
39	39	D-IA-7428311-0-0	7428311-01		SUPPORT,CABLE	1	1
40	40		9006659-00	A	WASHER,FLAT S/PAS	2	2
41	41		3700717-01		PKG. KIT OPTION CI20-A CUS.	1	1
42	42		3700717-02		PKG. KIT OPTION GI20-A CUS.	1	1
43	43		3617674-00		LABEL,SERIAL/POWER W/O UL + CSA	-	1
44	44		9008356-00		WIRE MARKER,ALPHA,A-Z	A/R	A/R
45	45		3617674-01		LABEL,SERIAL/POWER W UL & CSA	1	-
46	46		3617880-09		LABEL,CLASS "A" SUBASSEMBLY	1	-
47	47	D-IA-7021448-0-0	7021448-5C		CABLE DC VOLTAGE MONITOR SECT. 1	1	1

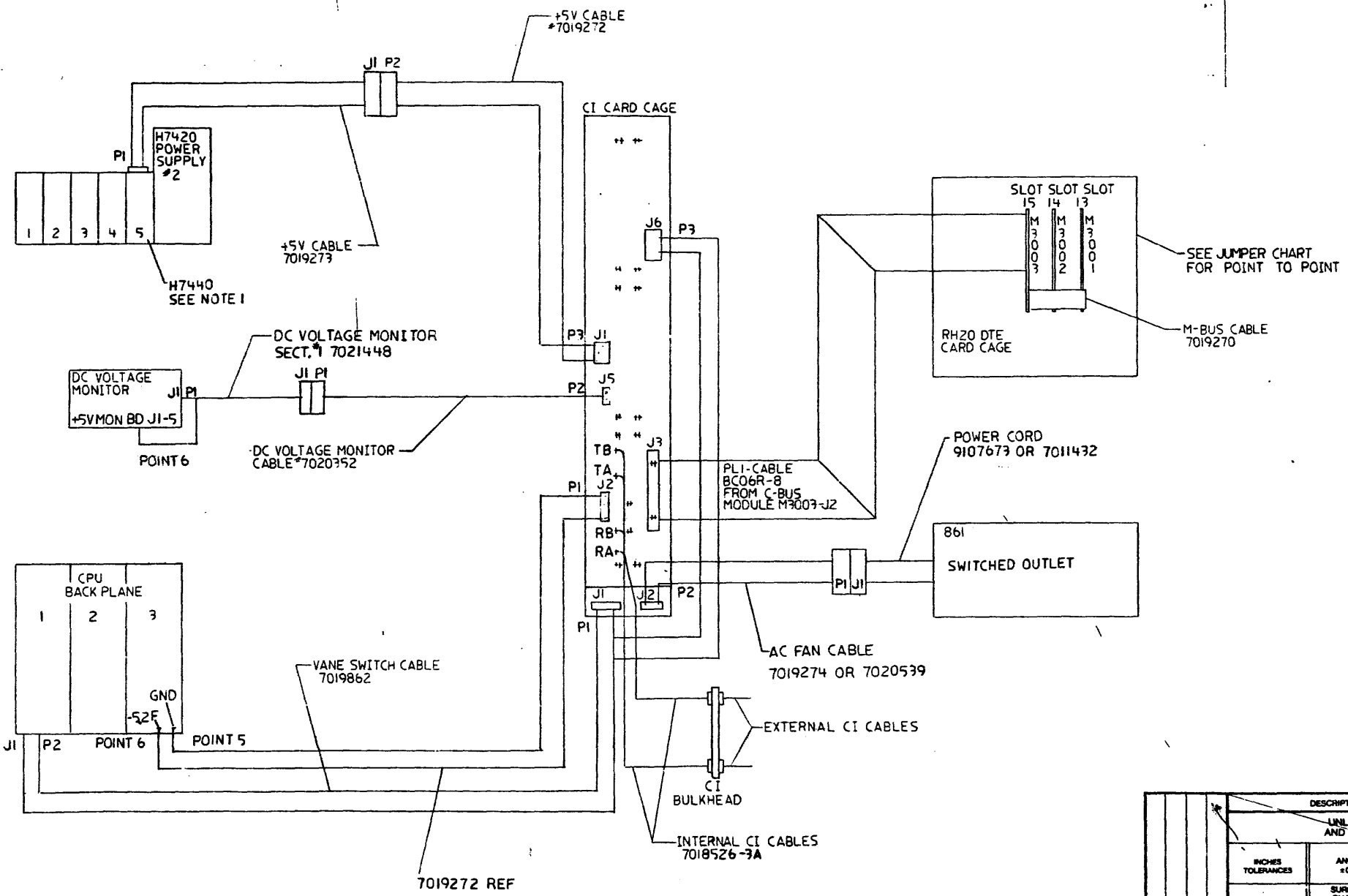
48 NOTE: 1. 12 FEET REQUIRED

D I G I T A L	TITLE	CI20 ASSY	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
				K	PL	CI20-A-DBP	F

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NOTES:

1. CONNECT 2 POSITION CONNECTOR P1 TO H7440-1J1 CONNECT POINTS 11&12 TO H7440, 9PIN MAT-IN LOK, TO AVAILABLE POSITION 1&2 3&4 5&6 OR 7&8 (19-30 VAC)



JUMPER CHART

FROM	TO
RH20BF B19B1	B19B1
B19B1	B19B1
C10L2	B19B2
B19B2	B19B2
C10K2	B19U1
B19U1	B19U1
C10T2	C13B1
C13B1	C19B1
C12H2	C13N1
C13N1	C19N1
C12L1	C13B2
C13B2	C19B2
C14H2	A15R2
C14F1	F15A1
A14J2	A15E1
C14P1	A15D2
C14K2	A15S2
B14J1	B15A1
C20H2	A21R2
C20F1	F21A1
A20J2	A21E1
C20P1	A21D2
C20K2	A21S2
RH20BF B20J1	RH20BF B21A1

DESCRIPTION	DRAWING NO.	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEG-STD 114)			
DIMENSION RANGE IN INCHES			
INCHES TOLERANCES	ANGLES	APPLICABLE DIMENSION RANGE	
X = ± .1	± 0°30'	OVER 0 TO 0.2	± .02
XX = ± .02	SURFACE QUALITY	OVER 0.2 TO 1.2	± .03
XXX = ± .008	MICROINCHES	OVER 1.2 TO 4.0	± .04
		OVER 4.0 TO 12.0	± .06
		OVER 12.0 TO 40.0	± .08
		OVER 40.0 TO 80.0	± .10
QUANTITY & VARIATION		± .004	± .008
		± .012	± .024
		± .016	± .032
		± .024	± .048

THIRD ANGLE PROJECTION

DO NOT SCALE DRAWING

REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DATE 17 JAN 73

DATE 14 FEB 73

DATE 15 APR 73

DATE 29 JUN 73

DATE 29 JUN 74

TITLE

INTER CONNECT DIAGRAM CI20-A

DOCUMENT NUMBER

DIC CI20-A-1

SCALE 1 OF 1

REVISION HISTORY

REV. NO.

RELEASED

ENGINEERING SPECIFICATION

DATE 29 June 84

TITLE CI20 REFERENCE MANUAL

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
A	Originated					
B	ECO			1-28 85	<i>J. Eichmann</i>	2/18/85

ENG	APPD	SIZE	CODE	NUMBER	REV
S. SCHULTZ	<i>John V. Turner</i>	A	SP	CI20-A-3	B

DEC 16-(392)-1079-N971
DRA 107

MRO

TITLE CI20 Reference Manual

CHAPTER 3
INSTALLATION

3.1 OVERVIEW

This chapter describes the installation of the CI20 in an existing system. A system containing a CI20 is limited to a maximum of four RH20's. RH20 slots 6 and 7 are used for the CI20. The brackets used to mount the CI20 modules are designed to prevent plugging any modules into slot 6. Slots 4 and 5 are reserved for future expansion to accommodate an NIA20. Refer to figures 3-1 and 3-2.

Installation of the CI20 in an existing system requires the following procedures:

1. Unpacking and checkout of installation kit
2. Pre-installation checkout
3. Backplane wire adds
4. Installation of port modules
5. Installation of power supply regulator

- 2 -

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

DEC FORM NO EN-01022-16-N370-(381)
DRA 108

SHEET 2 OF 33 MRO

TITLE CI20 Reference Manual

6. Installation of CI card cage
7. Installation of CI bulkhead
8. Installation of DC power harness
9. Installation of vane switch harness
10. Installation of DC voltage monitor harness and module
11. Installation of PLI bus
12. Installation of fan harness
13. Installation of CI cables
14. Installation of Link/Front End and Packet Buffer modules
15. Checkout

The following paragraphs provide detailed instructions for performing these steps.

- 3 -

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

DEC FORM NO EN-01022-16-N370-(381)

SHEET 3 OF 33 MRO

TITLE KL10-E INSTALLATION

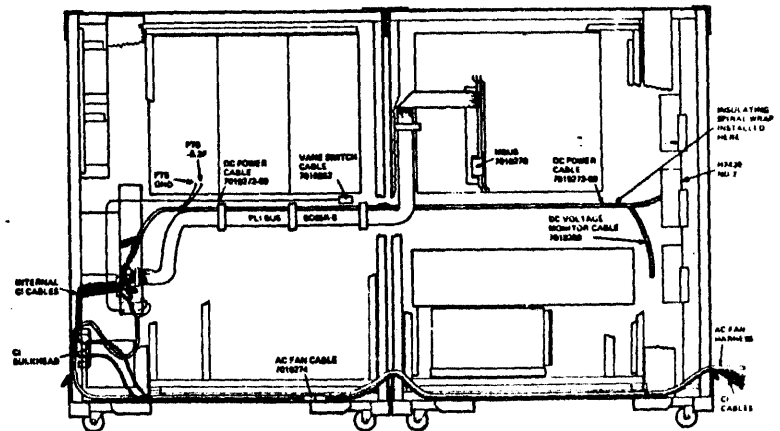


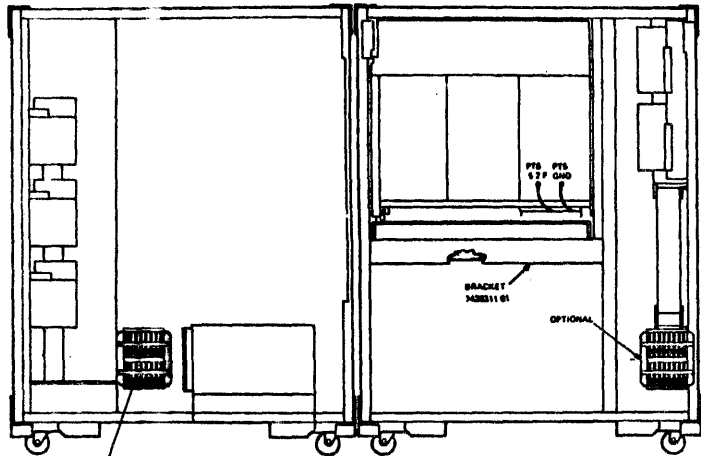
Figure 3-1. CI20 in KL10-E, Rear View

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

DEC FORM NO EN-01022-16-N370-(381)
DRA 108

SHEET 4 OF 33 MRO

TITLE KL10-E Installation



CI20 VOLTAGE MONITOR BOARD

Figure 3-2. CI20 in KL10-E, Front View

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

DEC FORM NO EN-01022-16-N370-(381)
DRA 108

SHEET 5 OF 33 MRD

TITLE CI20 Reference Manual

3.2 UNPACKING AND CHECKOUT

Before unpacking any equipment, move all boxes into the computer area. Check the shipment against the packing list to be sure that all boxes were sent. If any boxes are missing, contact the customer and the branch field service manager. Check that all boxes are sealed, and there is no sign of external damage, such as dents, holes, or damaged corners. If any boxes are open or damaged, document it on the installation or field service report and inform the customer. Open the boxes one at a time, starting with the READ ME FIRST box, and find the packing slip. Check the contents of the box against the packing slip and examine each item for damage. Note missing or damaged items on the installation report or field service report. This completes the unpacking and checkout phase. Advise the branch field service manager of any problems during this phase. If any items are damaged, the branch field service manager may want the customer to file an insurance claim. For missing items, the branch field service manager should get a short-ship request.

- 6 -

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

DEC FORM NO EN-01022-16-N370-(381)
DRA 108

SHEET 6 OF 33 MRD

TITLE CI20 Reference Manual

3.3 EQUIPMENT NEEDED FOR INSTALLATION AND CHECKOUT

The following equipment is required for installation and checkout of the CI20:

1. wire wrap tool, No. 30 AWG, DEC part No. 29-18301
2. wire unwrapping tool, No. 30 AWG, DEC part No. 29-13513
3. right-angle Phillips screwdriver
4. Tektronix 475 oscilloscope or equivalent (100 Mhz)
5. KLAD pack

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SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

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3.4 INSTALLATION PROCEDURE

3.4.1 Pre-Installation Checkout

Before performing the installation, verify that the system is operating properly. This is to preclude the possibility of system problems unrelated to the CI20 after the installation.

1. Remove all customer media, to minimize the possibility of corrupting customer data.
2. Mount the supplied KLAD pack, bring up the diagnostic monitor, and run the "B" string to verify that the system is working properly.
3. Verify that the system has an M8532-YA board in it. If not, replace the M8532 with an M8532-YA.

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SIZE	CODE	NUMBER	REV
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4. Power down the system.
5. RH20 positions 6 and 7 will be used for the CI20. If there is an RH20 in position 6, remove it. If there is an RH20 in position 7, leave it in place temporarily so that the diagnostic can be run to verify the backplane wiring. If there is no RH20 in position 7, remove one from one of the other positions and install it in position 7. If slots were unused, deskewing may have to be performed.
6. Power up the system and run diagnostic DFRHB. This is to verify that the backplane wiring of RH20 position 7 is functional. Power down the system and reinstall the RH20 in its original position.
7. Since the backplane will also be modified in slots 4 and 5, these slots must also be verified. Install an RH20 in position 5, and run diagnostic DFRHB.
8. Power down the system and reinstall the RH20 in its original position.

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TITLE CI20 Reference Manual

3.4.2 Backplane Wire Adds.

Add 24 new wires to RH20 backplane slots No.5 and No.7 as listed below. Strip approximately 1 inch of insulation from the wire to allow sufficient turns on the wire wrap post. After each wire is added, check the blank space adjacent to the wire listing.

Signal Name	From	To/From To	Check
EBUS D11 L	B10N1	B13B1	B19B1
EBUS D12 L	C10L2	B13B2	B19B2
EBUS D13 L	C10K2	B13U1	B19U1
EBUS PARITY L	C10T2	C13B1	C19B1
EBUS P100 L	C12H2	C13N1	C19N1
EBUS PARITY ACTIVE L	C12L1	C13B2	C19B2
MPR7 MWBUSCTFLD01 H	C14H2	A15R2	
MPR7 MWGCFD08 H	C14F1	F15A1	
MPR7 MWTMEFLD H	A14J2	A15E1	
CB11 CLK2 L	C14P1	A15D2	
CB12 CLK4 L	C14K2	A15S2	
CB12 CCCHANERR L	B14J1	B15A1	
MPR7 MWBUSCTFLD01 H	C20H2	A21R2	
MPR7 MWGCFD08 H	C20F1	F21A1	
MPR7 MWTMEFLD H	A20J2	A21E1	
CB11 CLK2 L	C20P1	A21D2	
CB12 CLK4 L	C20K2	A21S2	
CB12 CCCHANERR L	B20J1	B21A1	

After the wire wrap adds are completed, verify proper wiring by an ohmmeter check of each new wire. It is preferred that the person doing this test be someone other than the wire installer.

SIZE A	CODE SP	NUMBER CI20-A-3	REV B
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TITLE CI20 Reference Manual

3.4.3 Installation Of Port Modules.

Protective backing is placed on the lower third non-component side of each port module, and the upper third of the non-component side of the M3002. As the boards are inserted or removed, the protective backing protects the MBUS cable. Insert the port modules as follows:

1. Connect MBUS cable, DEC part number 70-19270-J1. Be sure to orient the cable so that the flat wire comes out of the cable header away from the board, as shown in the overhead view, figure 3-3.
2. Insert the EBus Interface/Port ALU module, M3001, in the rightmost slot of RH20 position number 7 (slot 13, looking at the backplane from the module side).
3. Connect the MBUS cable to the M3002 Module as shown in figure 3-3.
4. Insert the Port Microprocessor Control Module, M3002, in slot 14, to the left of the M3001, as shown in figure 3-3.

SIZE A	CODE SP	NUMBER CI20-A-3	REV B
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TITLE CI20 Reference Manual

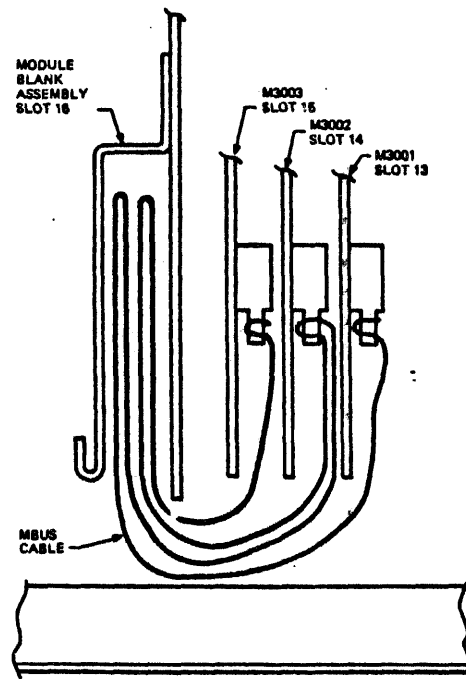


Figure 3-3. MBUS Cable Interboard Connection, Top View

SIZE A	CODE SP	NUMBER CI20-A-3	REV B
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TITLE CI20 Reference Manual

5. Connect the MBus cable to the M3003 module as shown in figure 3-3.
6. Install the CBUS/PLI Interface Module, M3003, in slot 15, to the left of the M3002.
7. Install the Module Blank Assembly, DEC part Number 19266-00, in RH20 position 6, slot 16. This assembly blocks slots 16, 17, and 18. It prevents modules from being plugged into this position, and provides a baffle for airflow.
8. Fold the MBus cable into the module blank assembly, as shown in figure 3-3.
9. Close the module door.
10. Power up the KL10.

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

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11. Readjust the +5 volt power supply to 5.0 +/- 0.25 V. This adjustment is located on H7420 number 2, in H744 number 1. This regulator is located furthest away from the power supply. The voltage is monitored at +5E, between PT15U and ground.
12. With KLDCP loaded and running, type MR (CR), then FX1 (CR) in response to the command prompt, as shown below:
>. MR (CR)
>. FX1 (CR)
13. Deskew the port modules by performing the following steps. A Tektronix 475 or equivalent (100 Mhz min) oscilloscope is required. Figure 3-4 illustrates the CI20 Deskew Timing.
14. Connect channel 1 of the oscilloscope to MTR MBOX CLK H, 4D33P1, on the CPU backplane. Use a ground clip.
15. Set the time base to 20 ns.
16. Set channel 1 vertical gain to 0.5 V/division. Set the ground reference to 1.3 volts above horizontal center level of oscilloscope. (MTR MBOX CLK H is an ECL signal.)
17. Set the oscilloscope sync to positive external.
18. Connect external sync input to CHTO H, 4B09K1, on the CPU backplane. Use a ground clip.

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

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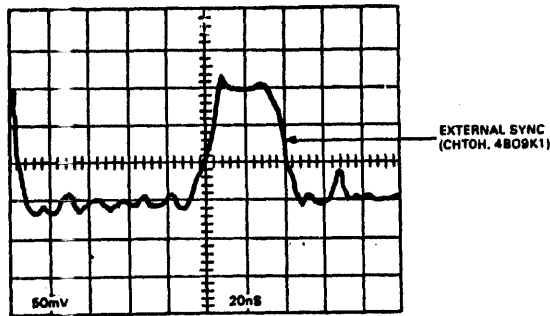


Figure 3-4A. External Sync (CHTO H)

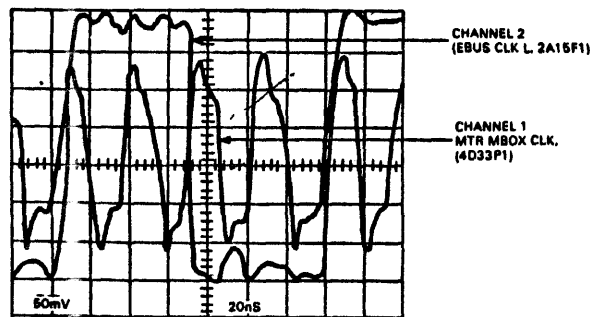


Figure 3-4B. EBUS CLK L and MTR MBOX CLK

FIGURE 3-4. CI20 Deskew Timing

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

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19. Connect channel 2 to CDS1, EBUS CLK L, 2A15F1, on the I/O backplane. Set the channel 2 vertical gain to 0.5 V/division. Use ground clip. To measure TTL voltages, set the ground reference to 1.5 volts below horizontal center line of oscilloscope.
20. Push the Trigger View Switch of the oscilloscope and display the external sync. Adjust the display, so that the rising edge of the external sync aligns with the vertical center line of the oscilloscope.
21. Display MBOX CLK H, channel 1. Identify the rising edge of MBOX CLK H that occurs prior to the vertical center line of the oscilloscope. Display channel 1 and channel 2.
22. Put the KL10-E in the override fault state. Remove the I/O rear door to access the I/O backplane.
23. In slot 12 of the I/O backplane, locate the bottom potentiometer on the clock module. Using this potentiometer, adjust the FALLING edge of channel 2, EBUS CLK L so that it crosses the RISING edge of MBOX CLK H. This crossing occurs on the horizontal center line of the oscilloscope.
24. Disconnect all probes.
25. Mount the KLAD pack on the front end RP06.
26. Load and run diagnostic DFPTA to verify proper functioning of the port modules. If the modules fail, troubleshoot as directed by the diagnostic. If the modules are functioning properly, proceed with the installation.

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

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3.4.4 Power Supply Regulator Installation.

Install the +5 volt regulator as follows:

1. Remove the spare slot filler panel from slot 5 of the H7420 number 2 power supply.
2. Take the new H7440 regulator from kit and install in slot 5 of H7420 number 2. Note that some systems may use H744 and/or H7440 regulators.

3.4.5 Installation Of CI Card Cage

Install the CI Card cage (see figure 3-5) as follows:

1. Install 8 U-Nuts (Tinnerman nuts) DEC part no. 9007786-00, on left side frame member of the CPU cabinet, as viewed from the rear. These nuts go into holes 5, 11, 15, and 52, counting up from the bottom of the cabinet. Four nuts are inserted into each frame member.
2. Locate the four 3-foot internal CI cables. Verify that the cables are identified at each end. If not, mark them TRANS A, TRANS B, RECV A, and RECV B, as appropriate, with the labels provided.
3. On the plexiglass cover at the rear of the CI card cage, verify and if necessary, label the coaxial connectors TRANS B, TRANS A, RECV B, and RECV A, as shown in figure 3-5.

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SIZE A	CODE SP	NUMBER CI20-A-3	REV B
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4. Route 3-foot internal CI cables through the white plastic strain relief on the CI card cage. Leave some slack to route the cables to the CI card cage backplane, and tighten the strain relief.
5. Connect the internal CI cables to the coaxial connectors on the rear of the CI card cage, as shown in figure 3-5. The connectors will engage a detent when they are seated properly.
6. Using 4 10/32 screws, external lockwashers, and flat washers, install the CI card cage in holes 15 and 52 of the right side frame members as shown in figure 3-2. Be careful not to damage the CI cables where they connect to the rear of the CI card cage. Hang the CI card cage on the top two screws, then install the bottom two screws.
7. Install an air deflection plate on top of the CI card cage using two screws and star washers.

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SIZE A	CODE SP	NUMBER CI20-A-3	REV B
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TITLE KI10-B Installation

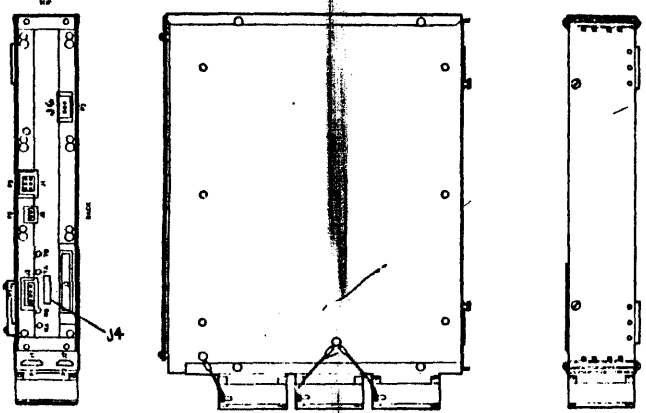


Figure 3-5. CI Card Cage, Location Drawing

SIZE A	CODE SP	NUMBER CI20-A-3	REV B
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3.4.6 Installation Of CI Cable Bulkhead

Install the CI cable bulkhead as follows:

1. Install the free ends of the internal 3-foot CI cables on CI bulkhead. Remove the nut from the free end of the coaxial connector, route the connector through the hole in the CI bulkhead from the side without lettering, and secure the cable by tightening the nut on the lettered side. Ensure that TRANS A, TRANS B, RECV A, AND RECV B cables from the CI card cage are connected to the corresponding holes in the CI bulkhead, as shown in figure 3-5.
2. Install the CI cable bulkhead in holes 5 and 11 of the left side frame members, from the rear, using 4 each No. 10-32 screws, external lockwashers, and flat washers as shown in figure 3-1.

3.4.7 Harness Installation

The harnesses to be installed at this stage are the DC power harness, which is in 2 sections, vane switch cable, DC voltage monitor cable, PLI bus, fan harness, and CI cables. Install these harnesses as follows:

1. Attach one half of DC power cable, DEC part number 7019272-00, (figure 3-6) to CI card cage J1 and J2 (figure 3-5). Locate the black and blue wires labelled PT5 and PT6 on this cable. Connect the black wire to -5.2 ground, and the blue wire to -5.2 F in the CPU bay, as shown in figure 3-2.

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SIZE A	CODE SP	NUMBER CI20-A-3	REV B
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2. Locate the other half of the DC power cable, DEC part number 70-19273-00, shown in figure 3-6 and join the end labelled J1 to P2 on the other half of the cable. Tie wrap the new harness to existing KL CPU power harness and continue across into and under the I/O card cage as shown in figure 3-1. Install spiral wrap provided in kit around this cable at the point where it contacts the side of the I/O card cage frame member nearest the H7420's. Refer to figure 3-1.
3. At the other end of the harness, locate the red and white wires labelled PT11 and PT12. Disconnect P3 from H7420 number 2, then connect PT11 and PT12 to pins 3 and 4 of P3 on the H7420 power supply as shown in figure 3-6. Reconnect P3 to the H7420.
4. Connect P1 of the harness to J1 of the H7440 regulator.
5. Locate vane switch cable, figure 3-7, DEC part number 70-19862-01 and connect the P1 end to J1 on the bottom of the CI card cage, as shown in figure 3-5. Connect P3 to J6 on the CI card cage, as shown in figure 3-5.
6. At the other end of the cable, remove P4 from the original KL CPU vane switch connector and connect P4 to J1 of the new vane switch harness. Connect P2 of the new vane switch harness to the original KL CPU vane switch assembly as shown in figure 3-8.
7. Overlay the CPU/CI air flow fault decal over the existing CPU air flow message decal on the 863 Fault Switch.

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SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

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TITLE KL10-E Installation

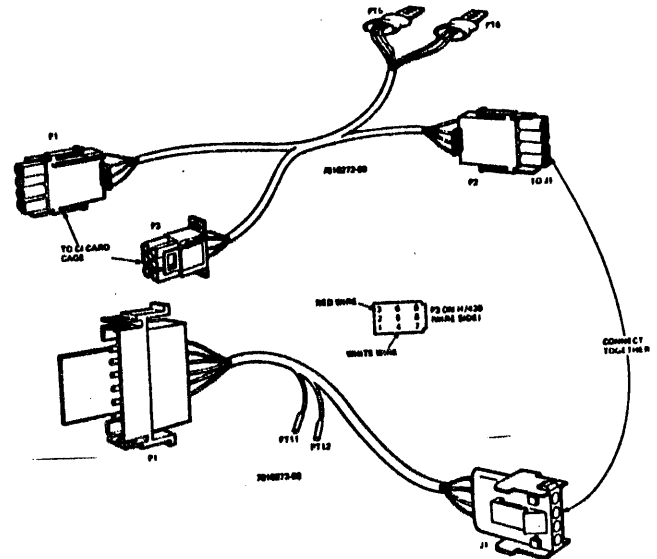


Figure 3-6. DC Power Cable

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

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TITLE KL10-E Installation

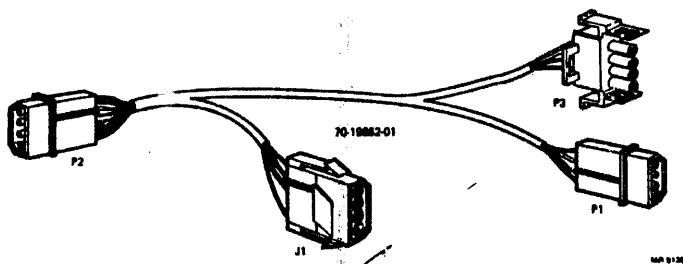


Figure 3-7. Vane Switch Cable

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

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TITLE KL10-E Installation

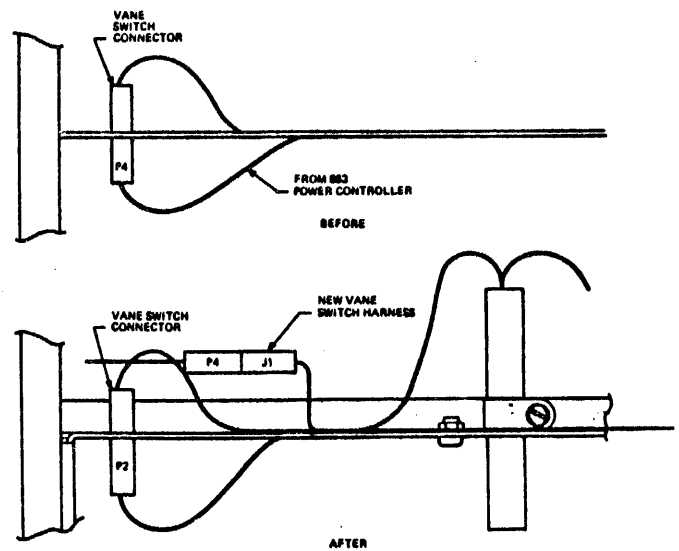


Figure 3-8. Vane Switch Harness Installation

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

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8. Locate the DC Voltage Monitor Cables, DEC part numbers 7020352-00 and 7021448-5C, as shown Figure 3-9. Connect P2 to J5 on the CI card cage, and route the other end to the DC voltage monitor panel. Locate the switches on the DC Voltage Monitor board. Switch 1 should be on and all other switches should be off. Plug the DC voltage monitor board, DEC part number 5414506-00, into the +5 volt slot as shown in figure 3-2.
9. The other end of the DC voltage monitor cable requires two connections. Connector P1 plugs into the new CI DC voltage monitor board. The remaining single orange wire should be connected adjacent to the existing orange wire on connector P1 of the DC voltage monitor board for sense +5V. Attach the monitor panel decal.
10. Tie wrap the DC voltage monitor and sense switch harnesses to the DC power cable. Use square adhesive-backed mounts to support the harness on the side of the CI card cage.
11. Locate the fan harness, Figure 3-10, DEC part number 7019274-06, (120 vac 60 Hz) or 7020379-06 (240 vac 50 Hz), and connect P2 to J2 on the CI card cage. Connect the fan harness and fan AC cable (figure 3-10) together. Connect the other end of the fan AC cable to any available switched outlet on the 861 power controller. Connect the ground wire to the adjacent ground screw on the CI card cage. Use a star washer to ensure a good electrical connection.
12. Install a Tinnerman nut in hole 13 on the frame and attach the ground cable from the CI card cage to the frame. Use two star washers to ensure a good electrical connection.

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A	SP	CI20-A-3	B

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13. If the system has core memory, dress the PLI cable as shown in figure 3-1. If the system does not have core memory, install bracket (smooth side facing the rear) DEC part number 7428311-01, using 4 of each: clips, screws, flat washers, and lock washers in holes 36 and 39 (figure 3-2). Connect one end of the PLI cable to module M3003, (red line is at the top of the cable). Connect the other end of the PLI cable to J3 (through PLI cable relief) on the CI card cage (red line is at the bottom). To secure the PLI cable, install adhesive foam within each of the 4 flat cable clamps. Install one cable clamp on the side of the CPU card cage, and 3 across memory fan assembly or bracket assembly.
14. Replace the I/O module door.
15. Route the external CI cables along the bottom of the CPU and I/O cabinets, and out the bottom of the I/O cabinet as shown in figure 3-5. Mark the cables TRANS A, TRANS B, RECVA, and RECVB, at both ends of the cables. Connect the cables to the corresponding connectors on the CI bulkhead and Star Coupler (see figures 3-11 and 3-12). The maximum length of these cables is 45 meters.
16. Install tie wraps approximately 8" apart on all harnesses.

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SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

TITLE KL10-E INSTALLATION

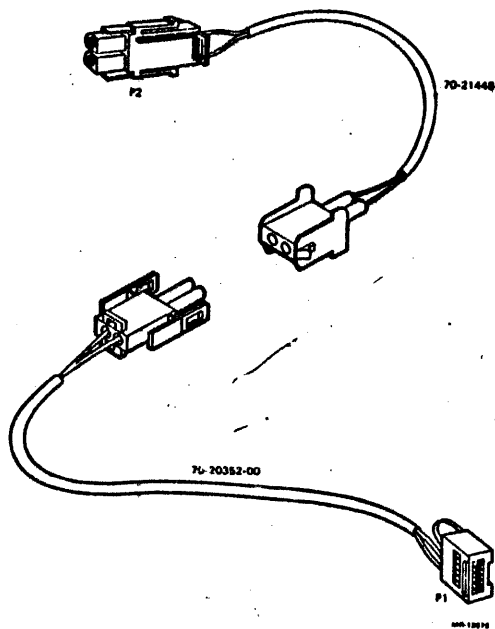


Figure 3-9. DC Voltage Monitor Cable

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

TITLE KL10-E Installation

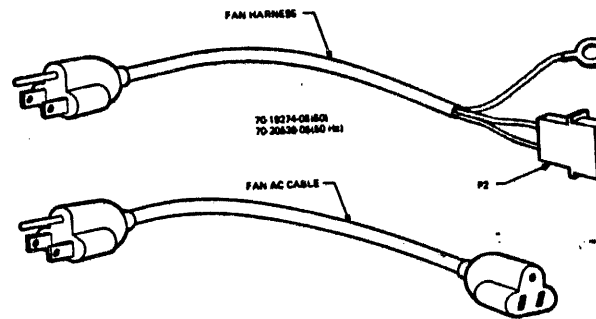


Figure 3-10. Fan AC Cable

SIZE	CODE	NUMBER	REV
A	SP	CI20-A-3	B

TITLE KL10-E Installation

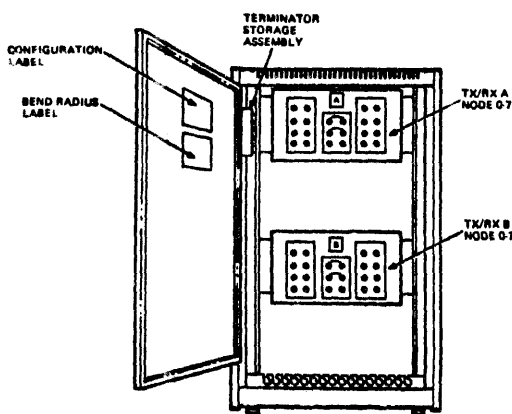


Figure 3-11. Star Coupler

SIZE CODE NUMBER REV
A SP CI20-A-3 B

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TITLE KL10-E Installation

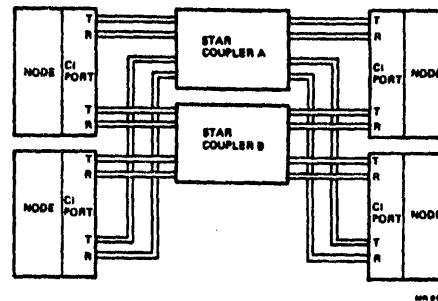


Figure 3-12. Star Coupler Dual Port Block Diagram

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A SP CI20-A-3 B

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3.4.8 Installation Of Link/FE And Packet Buffer

These modules are installed in the CI card cage looking from the front of the CPU bay. Before installing the Link/Front End module, set the node number in the switches at the edge of the board. The number may be obtained from the system manager or the customer. If this is a new installation, set the node number to zero. The switches are numbered 1 to 8 from top to bottom, and are set to the same number. Set the node number as shown in the following table. Each node on the CI must have an individual node number assigned to it.

0=OFF (CLOSED)
1=ON (OPEN)

Node	Switch Setting							
	S1	S2	S3	S4	S5	S6	S7	S8
0	0	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0	0
2	0	1	0	0	0	0	0	0
3	1	1	0	0	0	0	0	0
4	0	0	1	0	0	0	0	0
5	1	0	1	0	0	0	0	0
6	0	1	1	0	0	0	0	0
7	1	1	1	0	0	0	0	0
8	0	0	0	1	0	0	0	0
9	1	0	0	1	0	0	0	0
10	0	1	0	1	0	0	0	0
11	1	1	0	1	0	0	0	0
12	0	0	1	1	0	0	0	0
13	1	0	1	1	0	0	0	0
14	0	1	1	1	0	0	0	0
15	1	1	1	1	0	0	0	0

Install the Link/Front end module in the right hand slot. Install the Packet Buffer module in the adjacent slot to the left.

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A SP CI20-A-3 B

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3.4.9 Checkout

The physical part of the installation is complete at this point. All that remains is to verify that the system runs properly in the new configuration. Perform the following steps to verify the installation.

1. Verify that the KL10-E is no longer in the override fault state.
2. Power up the KL10
3. Readjust the 5 volt power supply to 5.0 +/- 0.25 V. This adjustment is located on H7420 number 2 in H7440 slot 5. This regulator is located nearest the H7420 power supply breaker. The voltage is monitored at the black and red wires on connector J1 of the CI card cage. J1 is located approximately in the center at the rear of the CI card cage.
4. Load and run diagnostic DFPTA for at least 5 passes in Exec mode.
5. Load and run diagnostic DFCIA for at least 5 passes in Exec mode.
6. Bring up the operating system.
7. Run diagnostics DFPTA and DFCIA in user mode for at least 5 passes.
8. Run diagnostic UETP CI20 Test in user mode for at least 4 hours.
9. Bring system down.
10. Remove all Field Service packs and tapes from the customers system, and store in a secure area.
11. Hand system over to customer.

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Figure

- 3-01 CI20 in KL10-E, Rear View 3-3
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SIZE	CODE
A	SP

NUMBER
CI20-A-3

REV
B

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY	PER	VARIATION
					VARIATION REVISION LEVEL:	00	01	
						A4	A4	
1	1	D-IA-7018178-0-0	7018178-00		WELDMENT,CARD CAGE	1	1	
2	2	D-IA-7018941-0-0	7018941-00		PLATE FAN ASSY	1	-	
3	3	D-IA-7018941-0-0	7018941-01		PLATE, FAN ASSY	-	1	
4	4	D-AD-7020492-0-0	7020492-00		DOOR, REAR ASSY	1	1	
5	5	D-IA-7018179-0-0	7018179-00		WELDMENT,COVER	1	1	
6	6	E-IA-7424873-0-0	7424873-00		PANEL,FILLER	1	1	
7	7	E-IA-7424873-0-0	7424873-01		PANEL,FILLER	1	1	
8	8	B-AD-7018181-0-0	7018181-00		STRAIN RELIEF ASSY	1	1	
9	9		5414793-00		BACKPLANE,IPAZO	1	1	
10	10	C-MD-7424875-0-0	7424875-01		GASKET	1	1	
11	11	E-MD-7424870-0-0	7424870-01		COVER,BACKPLANE	1	1	
12	12	E-IA-7424871-0-0	7424871-01		STIFFENER,BACKPLANE	1	1	
13	13		9006038-01		SCREW,MACH PAN PHIL	8-	18	18
14	14		9006660-00		WASHER,FLAT SST		16	16
15	15		9008151-00		WASHER,LOCK EXTERNAL STEEL		20	20
16	16		9006022-01		SCREW,MACH PAN PHIL	6-	18	18
17	17		9007649-00		WASHER,LOCK EXTERNAL STEEL		24	24
18	18		9006025-01		SCREW,MACH PAN PHIL	6-	2	2
19	19	C-MD-7427436-0-0	7427436-01		STRAINRELIEF,BLOCK	1	1	
20	20		1213756-16		GROUND STRAP	1	1	
21	21		3613272-00	E	LABEL,ADH BACK,MYLAR CAP	1	1	
22	22		3616606-00	A	LABEL, TOP LEVEL STATUS	2	2	
23	23	C-IA-7428223-0-0	7428223-01		PLATE, TOP	1	1	
24	24	C-MD-7428225-0-0	7428225-01		BRACKET, TOP	1	1	
25	25	C-MD-7428224-0-0	7428224-01		BRACKET, BOTTOM	1	1	
26	26	D-IA-7428221-0-0	7428221-01		PLATE, BOTTOM	1	1	
27	27		3621500-01		*** THIS ITEM IS NOT USED ***	-	-	
28	28		3621501-01		LABEL,MODULE LOCATION CI20	1	1	
29	29		9006024-01		SCREW,MACH PAN PHIL	6-	4	4

REVISION HISTORY			BASIC PART NO: 7019268			D I G I T A L		
ENG	ECO NUMBER	REV	SECTION A OF A	DRN:	W. ALLEN	DATE:	9 OCT 81	
---	INITIAL	D	SECTION VARIATION INDEX	CHK'D:	G. F.	DATE:	14-DEC-82	TITLE PARTS LIST
PC	7019268-MR001	E	[A]00,01					CARD CAGE ASSY
PC	CI20-MR01A	F	[B]	DES.ENG:	P. CAPPABIANCA	DATE:	14-APR-83	CI20
			[C]					DOCUMENT NUMBER
			[D]	RESP.ENG.:	P. CAPPABIANCA	DATE:	14-APR-83	SIZE CODE NUMBER REV
			[E]					K PL 7019268-0-DBP F
			[F]	MFG.ENG.:	S. ALMEIDA	DATE:	13-APR-83	RELEASE DATE: 28-FEB-85
				ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME: EDIT #
				E-AD-7019268-0-0				Z5878F.PLS 22

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8
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0-0-685020Z [DIA] 7020539-0-0

6

5

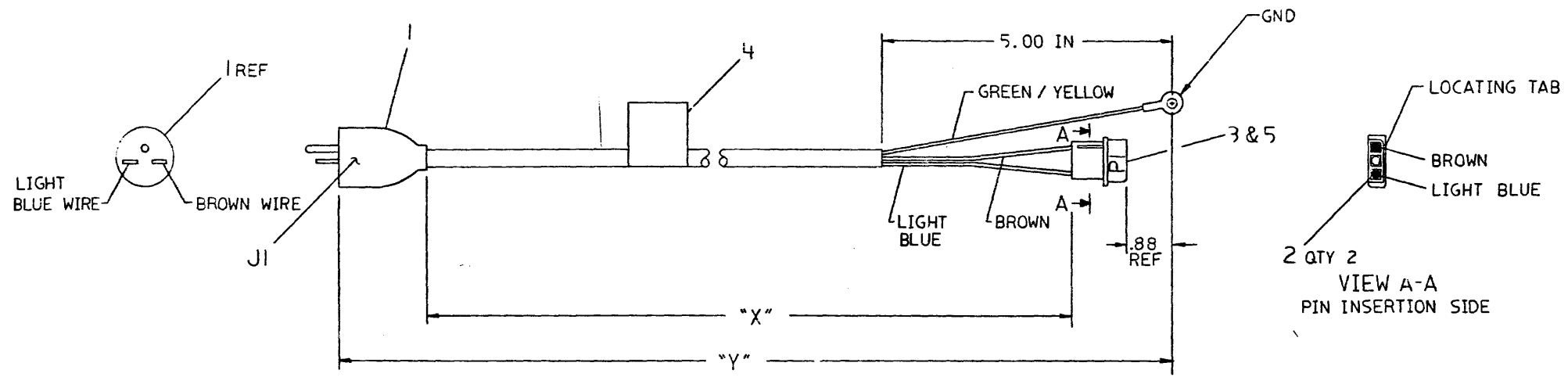
4

3

2

1

LEGEND			
PART NO.	DIM. "X" VAR.	DIM. "Y" (RECUT) REF.	REV.
7020539-06	5 FT. 9 IN ± 10 IN	6 FT. ± 5 IN.	B1



CAUTION: OFF SHEET PARTS LIST EXIST
 SEE K-PL-7020539-0-DBP

DESCRIPTION	DRAWING NO.	PART NO.	ITEM NO.				
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)							
INCHES TOLERANCES X = ± .1 XX = ± .02 XXX = ± .005	ANGLES ± 0° 30' SURFACE QUALITY ✓	APPLICABLE DIMENSION RANGE					
		DIMENSION RANGE IN INCHES					
		OVER 0 TO 0.2	OVER 0.2 TO 1.2	OVER 1.2 TO 3.0	OVER 3.0 TO 12.0	OVER 12.0 TO 40.0	OVER 40.0 TO 80.0
		± .004	± .006	± .012	± .016	± .024	± .04
QUANTITY & VARIATION	THIRD ANGLE PROJECTION	DATE	TITLE				
	DO NOT SCALE DRAWING	9-DEC-82	digital				
	REMOVE BURRS AND BREAK SHARP CORNERS	14 DEC 82	CABLE, FAN AC				
		14 APR 83	50 HZ				
		14 APR 83	DOCUMENT NUMBER				
MATERIAL	SEE PARTS LIST	9-11-83	DIA 7020539-0-0C				
FINISH		E-UA-C120-A-0	SCALE				

REVISION HISTORY
 REV. NO. 1
 DATE
 RELEASED

DATE CODE NUMBER DIA 7020539-0-0-C

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION
1	1		1700016-06		PWR CORD,TERM 3-18 SJT 230	1
2	2		1209378-00		MATE-N-LOK DIPIN 20-14AWS .08500	2
3	3		1209351-03		MATE-N-LOK DIPIN(1X03).200CC HSG	1
4	4		3616073-00		LABEL,ID W/COPY VERTICAL	1
5	5	A-DC-7409872-0-0	7409872-02		PWR HARNESS DECAL, BLACK ON CL3A	1

VARIATION REVISION LEVEL:

REVISION HISTORY		BASIC PART NO: 7020539		DPN:	P. E. DENNISON	DATE:	15-APR-83	D I G I T A L	
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D:	14 DEC 83	DATE:	14 DEC 82	TITLE	PARTS LIST
	INITIAL	IC	SECTION VARIATION INDEX		CAJ06				CABLE FAN AC 50HZ
				DES.ENG:	P. CAPPABIANCA	DATE:	14 APR 83		DOCUMENT NUMBER
								SIZE/CODE	NUMBER
				RESP.ENG.:	P. CAPPABIANCA	DATE:	14 APR 83	K PL	7020539-0-DBP
									REV
				MPG.ENG.:	J. MCCAFFERY	DATE:	11 APR 83		RELEASE DATE: 02-FEB-84
				ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	EDIT #
				ID-IA-7020539-0-0		IE-UA-CI20-A-0		Z6009C.PLS	6

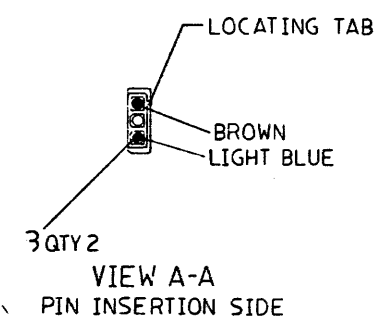
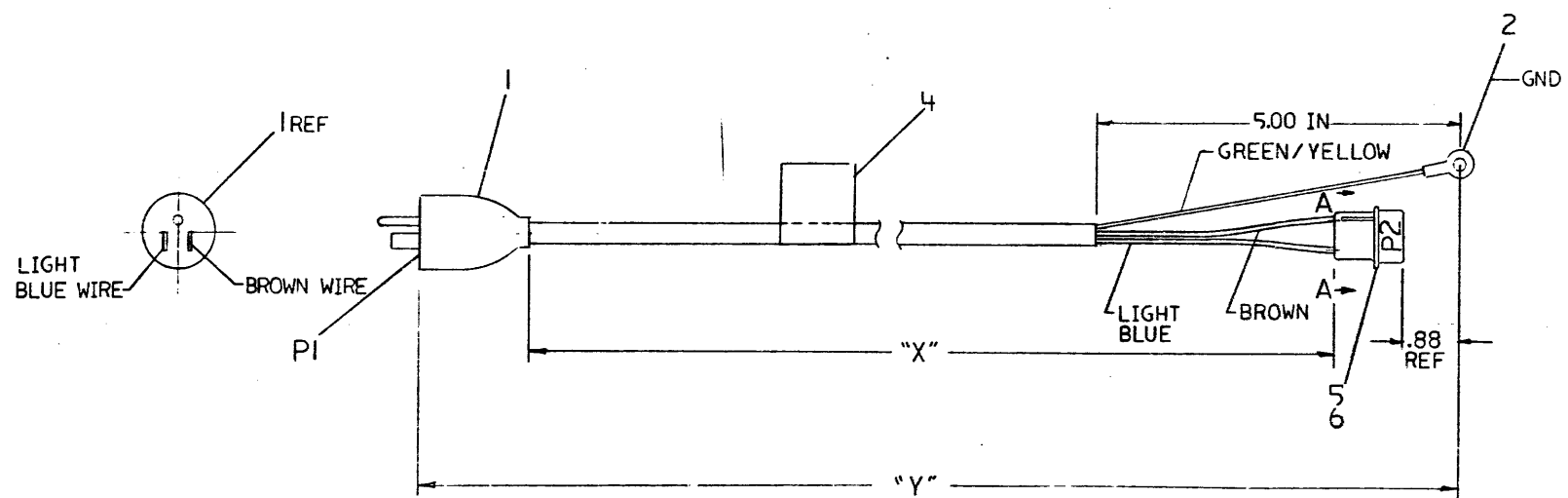
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0-0-425-010-110
 00001 0275

LEGEND			
PART NO.	DIM. "X" VAR	DIM. "Y" (PRECUT) REF	REV
7019274-06	5 FT. 9 IN. ± 1.0 IN.	6 FT. ± .5 IN.	B1

NOTES:



CAUTION: OFF SHEET SHEET PARTS LIST EXISTS.
 SEE K-PL-7019274-G-DBP.

REVISION LIBRARY
 REV. C
 DATE RELEASED

DESCRIPTION	DRAWING NO.	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)			
INCHES TOLERANCES X ± .1 XX ± .02 XXX ± .005	ANGLES ± 0° 30'	APPLICABLE DIMENSION RANGE (CHECK ONE) <input type="checkbox"/> INCHES <input type="checkbox"/> MILLIMETERS	DIMENSION RANGE IN INCHES
			OVER TO OVER TO OVER TO OVER TO OVER TO
QUANTITY & VARIATION	SURFACE QUALITY <input checked="" type="checkbox"/> MICROMETERS		
THIRD ANGLE PROJECTION	DWN W. O'Brien	DATE 19 APR 82	TITLE digital
DO NOT SCALE DRAWING	CHK'D [Signature]	DATE 14 DEC 82	CABLE, FAN AC 60 HZ
REMOVE BURRS AND BREAK SHARP CORNERS	DES. ENG. P. Cappalione	DATE 14 APR 83	
MATERIAL SEE PARTS LIST	REP. ENG. P. Cappalione	DATE 14 APR 83	DOCUMENT NUMBER
FINISH -- 11 --	MFG. ENG. [Signature]	DATE 11-11-83	SIZE CODE NUMBER REV DIA 7019274-0-01C
	FINISH	NEXT NUMBER DOC E-0A-C120-A-0	SCALE 1 MRO

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION
1	1		1700006-06		PWR CORD,TERM 3-15 SJT 115	1
2	2		9007928-00		TERM,RTNG #10STUD CRIMP 14-1	1
3	3		1209378-00		MATE-N-LOK OIPIN 20-14AWG .08500	2
4	4		3616073-00		LABEL,TD W/COPY VERTICAL	1
5	5	A-DC-7409872-0-0	7409872-02		PWR HARNESS DECAL, BLACK ON CLEA	1
6	6		1209351-03		MATE-N-LOK O3PTN(1X03).200CC HSG	1

REVISION HISTORY			BASIC PART NO: 7019274			D I G I T A L		
ENGR	ECO NUMBER	REV	SECTION A OF A	DRN:	W ALLEN	DATE:	19 APR 82	
---	INITIAL	C	SECTION VARIATION INDEX	CHK'D:	G. F.	DATE:	14 DEC 82	TITLE PARTS LIST
			EAJ06					CABLE PAN,AC 60 HZ
			EB)	DES.ENG:	P. CAPPABIANCA	DATE:	14 APR 83	DOCUMENT NUMBER
			EC)					SIZE/CODE NUMBER
			ED)	RESP.ENG.:	P. CAPPABIANCA	DATE:	14 APR 83	K PL 7019274-0-08P C
			EE)	MPG.ENG.:	J. MCCAFFERY	DATE:	11 APR 83	RELEASE DATE: 02-FEB-84
			EF)	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME: Z5903C.PLS
				ID-IA-7019274-0-0				EDIT # 8

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SYMBOLS

ITEM NO	SYMBOL	PART NO (REF)
4	□	I212170-00
5	○	9007920-00

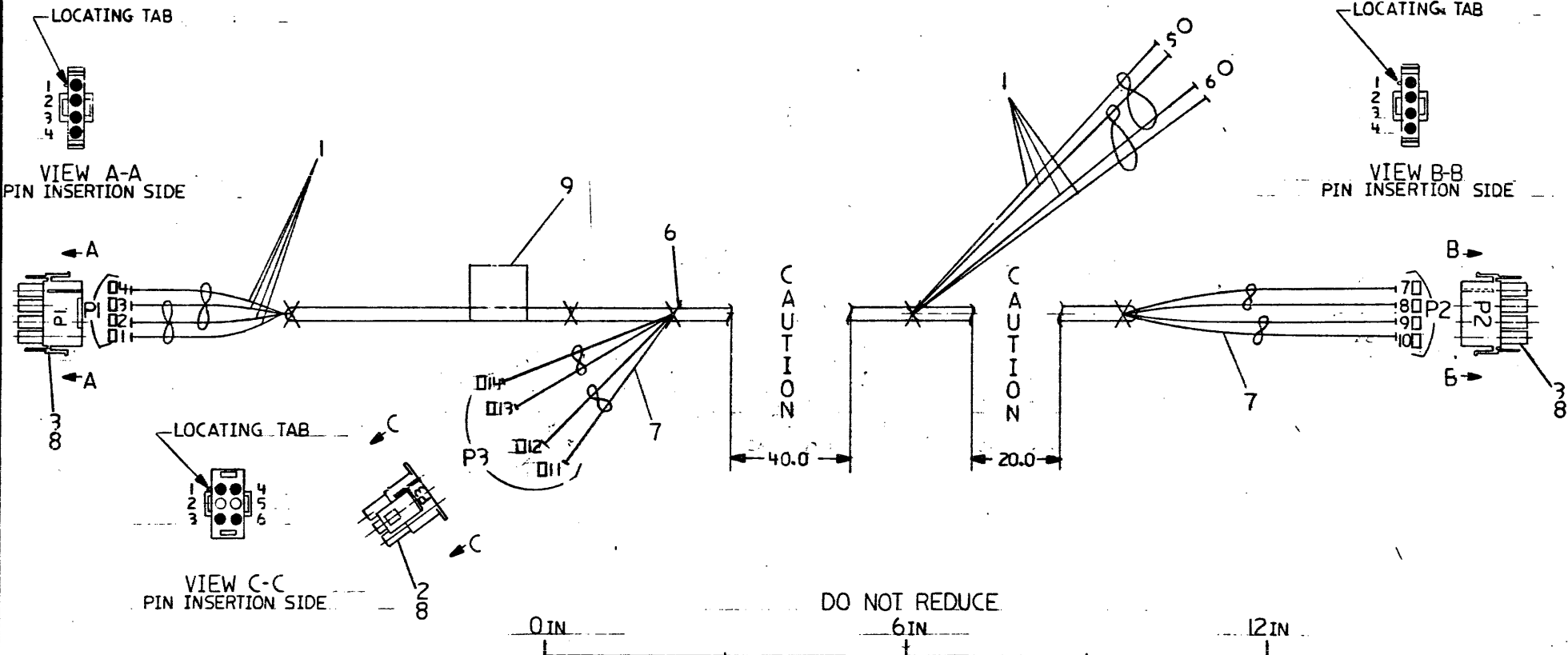
WIRE TABLE

ITEM NO	DESCRIPTION	FROM			TO			REMARKS	
		NO	AWG	COLOR POINT CONNECTION WITH	POINT CONNECTION WITH	POINT CONNECTION WITH			
1	TWP #14	BLU	1	PI-4	4	6		TWP	
		BLU	2	PI-3					
		BLK	3	PI-2					
		BLK	4	PI-1					
7	TWP #14	RED	7	P2-1		11	P3-4	4	
		BLK	8	P2-2		12	P3-1		
7	TWP #14	BLK	9	P2-3		13	P3-3		
		RED	10	P2-4	4	14	P3-6	4	

LEGEND

PART NO	VARIATION	REV
7019272-00	AS SHOWN	A2

- NOTES:**
- USE TIE WRAPS (X) ITEM 6 APPROXIMATELY EVERY 3 INCHES WHEN NECESSARY AND AT EVERY BREAKOUT POINT.
 - CONNECTOR TO BE LABELED WITH COMPONENT IDENTIFIERS.
 - UNLESS OTHERWISE NOTED ALL LENGTHS ARE IN INCHES.



CAUTION: OFF SHEET PARTS LIST EXISTS
 SEE K-PL-7019272-0-DBP.

DESCRIPTION	DRAWING NO.	PART NO.	ITEM NO.																						
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)																									
INCHES TOLERANCES X = ±.1 XX = ±.08 XXX = ±.06	ANGLES ± 1° 30'	APPLICABLE DIMENSION RANGE (CHECK ONE)	DIMENSION RANGE IN INCHES																						
			<table border="1"> <tr> <th>OVER TO</th> <th>OVER TO</th> <th>OVER TO</th> <th>OVER TO</th> <th>OVER TO</th> </tr> <tr> <td>0.2</td> <td>0.2</td> <td>1.2</td> <td>1.2</td> <td>48.0</td> </tr> <tr> <td>0.2</td> <td>0.2</td> <td>1.2</td> <td>12.0</td> <td>48.0</td> </tr> <tr> <td>0.2</td> <td>0.2</td> <td>1.2</td> <td>12.0</td> <td>48.0</td> </tr> <tr> <td>0.2</td> <td>0.2</td> <td>1.2</td> <td>12.0</td> <td>48.0</td> </tr> </table>	OVER TO	OVER TO	OVER TO	OVER TO	OVER TO	0.2	0.2	1.2	1.2	48.0	0.2	0.2	1.2	12.0	48.0	0.2	0.2	1.2	12.0	48.0	0.2	0.2
OVER TO	OVER TO	OVER TO	OVER TO	OVER TO																					
0.2	0.2	1.2	1.2	48.0																					
0.2	0.2	1.2	12.0	48.0																					
0.2	0.2	1.2	12.0	48.0																					
0.2	0.2	1.2	12.0	48.0																					
QUANTITY & VARIATION	TYPED ANGLE PROJECTION	DATE	TITLE																						
	W. ALL	13 APR 82	HARNESS DC-5.2																						
DO NOT SCALE DRAWING	DATE	19 DEC 82	SECT'N-1 DC+5																						
REMOVE BURRS AND BREAK SHARP CORNERS	DATE	14 DEC 82																							
MATERIAL	DATE	19 DEC 82																							
SEE PARTS LIST	DATE	2-11-82																							
FINISH	DATE																								
	E-UA-C120-A-1																								
DOCUMENT NUMBER		SIZE	CODE																						
D IA 7019272-0-0		1/8"	1 MRO																						

REVISION HISTORY

REV.	DATE	DESCRIPTION
B	07-28-82	RELEASED
C	07-28-82	RELEASED

M. Lee
 P. Cappabianca
 P. Cappabianca (Apr 81)

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION
					VARIATION REVISION LEVEL:	A2
1	1		9107440-06		WIRE,TWP 14AWG19/.0147IPVC 300V1	A/R
2	2		1212167-06		MATE-N-LOK 06SKT(2X03).250CC HSG	1
3	3		1212167-01		MATE-N-LOK 04SKT(1X04).250CC HSG	2
4	4		1212170-00		MATE-N-LOK 01SKT 20-14AWG .0850D	12
5	5		9007920-00		TERM QUICK .300TAB CRIMP 12-1	2
6	6		9007880-00		TIE,CABLE BUNDL DIA 0-1.14"=101	8
7	7		9107440-02		WIRE,TWP 14AWG19/.0147IPVC 300V1	A/R
8	8	A-DC-7409872-0-0	7409872-02		PWR HARNESS DECAL, BLACK ON CLEA	1
9	9		3616073-00		LABEL,ID W/COPY VERTICAL	1

REVISION HISTORY			BASIC PART NO: 7019272			D I G I T A L		
ENG	ECO NUMBER	REV	SECTION A OF A	DRN:	W. ALLEN	DATE:	13-APR-82	
---	INITIAL	B	SECTION VARIATION INDEX	CHK'D:	G. F.	DATE:	10 DEC 82	TITLE PARTS LIST
PC	7019272-MR001	C	[A]00					HARNESS DC-5.2 SECT'N-1 DC+5
			[B]	DES.ENG:	P. CAPPABIANCA	DATE:	14 APR 83	DOCUMENT NUMBER
			[C]					SIZE CODE NUMBER REV
			[D]	RESP.ENG.:	P. CAPPABIANCA	DATE:	14 APR 83	K PL 7019272-0-DBP C
			[E]	MFG.ENG.:	J. MCCAFFERY	DATE:	11 APR 83	RELEASE DATE: 30-OCT-84
			[F]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME: EDIT #
				D-IA-7019272-0-0				Z5884C.PLS 9

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SYMBOLS

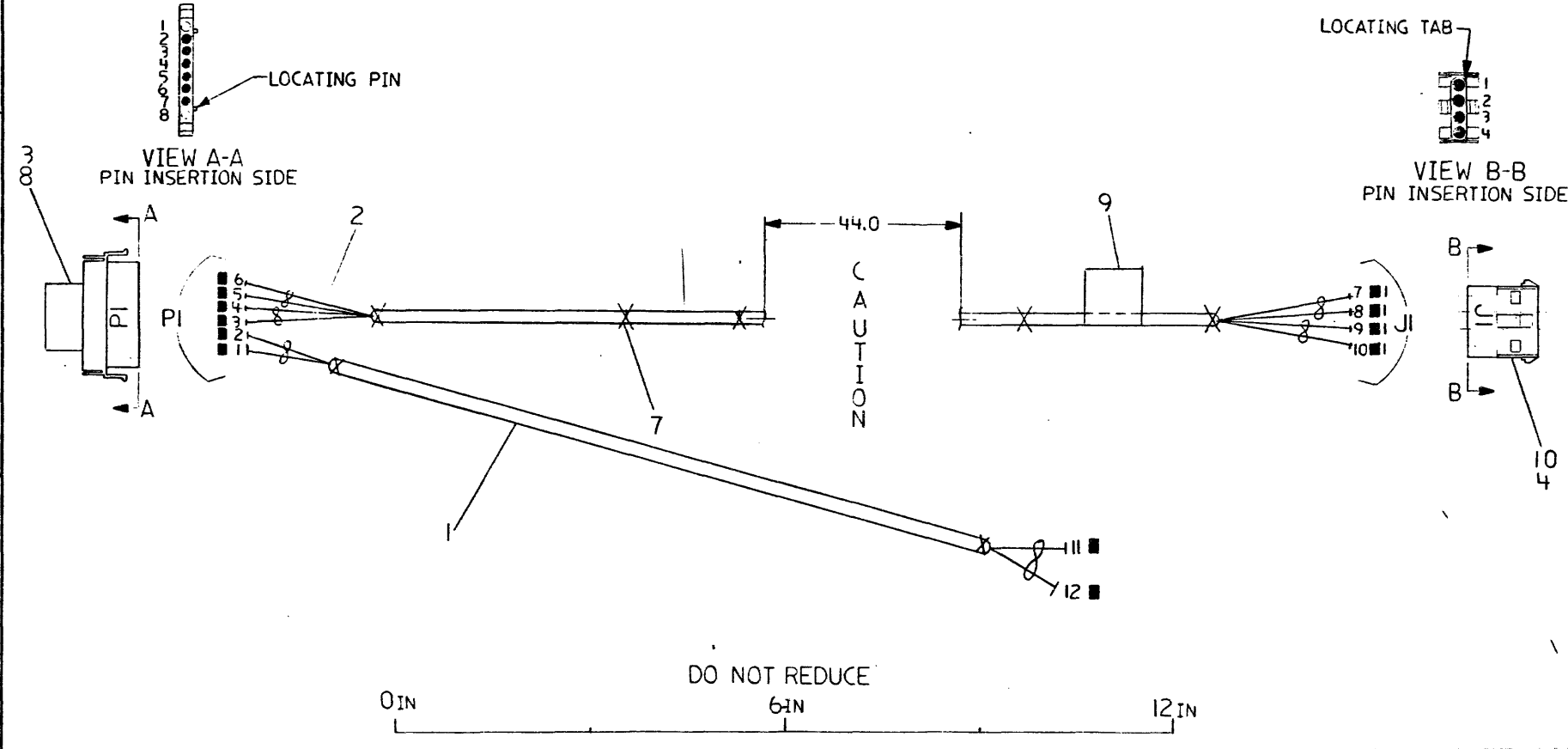
ITEM NO.	SYMBOL	PART NO. (REF)
5	■	1209378-00
6	■	1212169-00

WIRE TABLE									
ITEM NO.	DESCRIPTION	FROM			TO			REMARKS	
		AWG	COLOR	POINT	CONNECTION	WITH	POINT		CONNECTION
1	TWP #18	WHT	1	PI-7	5	12		5	
2	TWP #14	RED	2	PI-6	11			5	
		RED	3	PI-5	10	J1-4		6	
2	TWP #14	BLK	4	PI-4	9	J1-3			
		BLK	5	PI-3	8	J1-2			
		RED	6	PI-2	5	J1-1		6	

LEGEND		
PART NO	VARIATION	REV
7019273 00	AS SHOWN	BI

NOTES:

- USE TIE WRAPS (X) ITEM 7 APPROXIMATELY EVERY 3 INCHES WHEN NECESSARY AND AT EVERY BREAKOUT POINT.
- CONNECTOR TO BE LABELED WITH COMPONENT IDENTIFIERS.
- UNLESS OTHERWISE NOTED ALL LENGTHS ARE IN INCHES.



CAUTION: OFF SHEET PARTS LIST EXIST
SEE K-PL-7019273-0-DBP

REVISION HISTORY
ECC NUMBER
RELEASED

DESCRIPTION	DRAWING NO.	PART NO.	ITEM NO.					
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)								
INCHES TOLERANCES X = ± .1 XX = ± .02 XXX = ± .006	ANGLES ± 0° 30'	APPLICABLE DIMENSION RANGE (CHECK ONE) <input type="checkbox"/> INCHES <input type="checkbox"/> MILLIMETERS	DIMENSION RANGE IN INCHES					
			OVER 0 TO 0.2	OVER 0.2 TO 1.2	OVER 1.2 TO 4.0	OVER 4.0 TO 12.0	OVER 12.0 TO 40.0	OVER 40.0 TO 60.0
QUANTITY & VARIATION	THIRD ANGLE PROJECTION	DATE	TITLE					
		14 DEC 82	HARNESS DC-5.2 SECT'N-2 DC+5	digital				
DO NOT SCALE DRAWING	DATE	DATE	DATE					
REMOVE BURRS AND BREAK SHARP CORNERS	DATE	DATE	DATE					
MATERIAL	DATE	DATE	DATE					
SEE PARTS LIST	DATE	DATE	DATE					
FINISH	DATE	DATE	DATE					
DOCUMENT NUMBER				DIA 7019273-0-0 C				
SCALE				MRO 1				

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV DESCRIPTION	QTY PER VARIATION
1	1	9107430-92	WIRE,TWP 18AWG(12/30)TPVC 150V	A/R
2	2	9107440-02	WIRE,TWP 14AWG19/.0147TPVC 300V1	A/R
3	3	A-PS-1209340-0-0	1209340-01 MATE-N-LOK 08PIN(1X08) HSG	1
4	4	A-PS-1212168-0-0	1212168-02 MATE-N-LOK 04POS(1X04).250CC HSG	1
5	5	A-PS-1209378-0-0	1209378-00 MATE-N-LOK 01PIN 20-14AWG .0850D	8
6	6	A-PS-1212169-0-0	1212169-00 MATE-N-LOK 01PIN 20-14AWG .0850D	4
7	7	9007880-00	9007880-00 TIE,CABLE BUNDL DIA 0-1.14"=101	A/R
8	8	A-DC-7409872-0-0	7409872-02 PWR HARNESS DECAL, BLACK ON CLEA	1
9	9	3616073-00	3616073-00 LABEL,TD W/COPY VERTICAL	1
10	10	A-DC-7409873-0-0	7409873-02 PWR CONN DECAL BLACK ON CLEAR	1

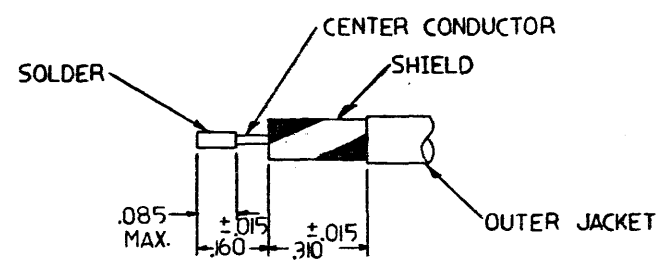
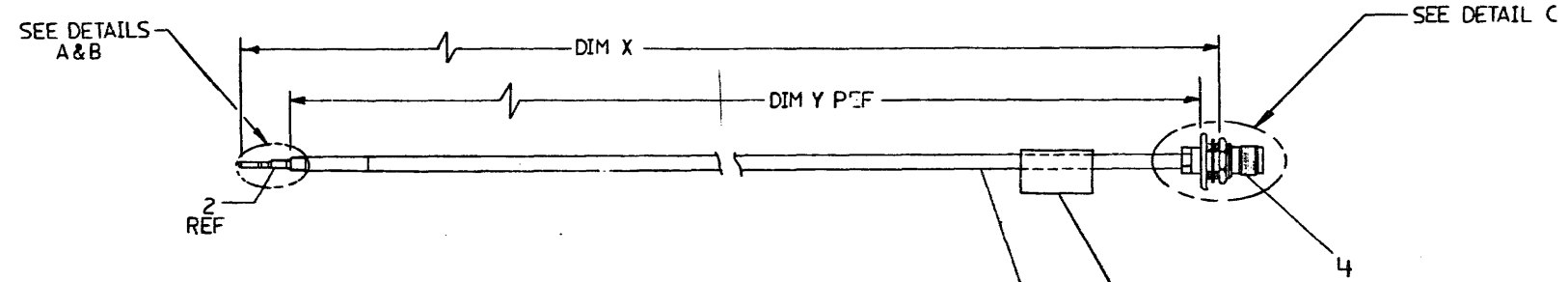
VARIATION REVISION LEVEL:

REVISION HISTORY		BASIC PART NO: 7019273		DRN: W. ALLEN		DATE: 14-APR-82		DIGITAL	
ENGR	ECU NUMBER	REV	SECTION A OF A	CHK'D:	G. F.	DATE:	14 DEC 82	FILE:	PARTS LIST
	INITIAL	C	SECTION.VARIATION INDEX						HARNESS DC-5.2
			CAJ00						SECT'N-2 DC+5
			EBJ	DES.ENG:	P. CAPPABIANCA	DATE:	14 APR 83		DOCUMENT NUMBER
			CCJ					SIZE/	CODE NUMBER
			CDJ	RESP.ENG.:	P. CAPPABIANCA	DATE:	14 APR 83	K	PL 7019273-0-DBP
			CEJ						
			CFJ	MFG.ENG.:	J. MCCAFFERY	DATE:	11 APR 83		RELEASE DATE: 02-FEB-84
				ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	EDIT #
				ID-IA-7019273-0-0		IE-UA-CI20-0-0		75883C.PLS	4

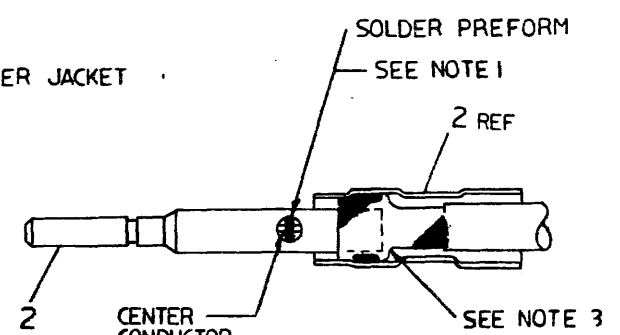
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LEGEND		
NUMBER	DIM X VARIATION	DIM Y (PRE CUT) REF
708526-1A	1 FT. 1 IN. ± 1.0 IN.	1 FT. 0 IN. ± 1.0 IN.
708526-2A	2 FT. 1 IN. ± 1.0 IN.	2 FT. 0 IN. ± 1.0 IN.
708526-3A	3 FT. 1 IN. ± 1.0 IN.	3 FT. 0 IN. ± 1.0 IN.
708526-4A	4 FT. 1 IN. ± 1.0 IN.	4 FT. 0 IN. ± 1.0 IN.
708526-5A	5 FT. 1 IN. ± 1.0 IN.	5 FT. 0 IN. ± 2.0 IN.
708526-6A	6 FT. 1 IN. ± 1.0 IN.	6 FT. 0 IN. ± 2.0 IN.
708526-7A	7 FT. 1 IN. ± 2.0 IN.	7 FT. 0 IN. ± 2.0 IN.
708526-8A	8 FT. 1 IN. ± 2.0 IN.	8 FT. 0 IN. ± 2.0 IN.

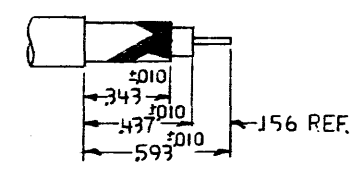
- NOTES:
1. SOLDER PREFORM IS PART OF ITEM 2.
 2. CENTER CONDUCTOR TO PASS THRU SOLDER PREFORM OF ITEM 2.
 3. SHIELD TO BE FLARED BEFORE ASSY TO ITEM 2 & SMOOTHED OVER AFTER.



DETAIL B
SCALE: NONE
SHOWN WITH ITEM 2,
CONTACT, REMOVED.



DETAIL A
SCALE: NONE



RECOMMENDED CABLE STRIPPING DIMS.

DETAIL C
SCALE: NONE
SHOWN WITH ITEM 4,
CONN, REMOVED.

CAUTION: SEE OFF SHEETS PARTS LIST
K-PL-7018526-0-DBP (Z2284B)

REV.	DATE	BY	CHKD
B	11/10/01	R. NUUEBLING	
A	8-27-01	R. NUUEBLING	

DESCRIPTION		DWG. PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			
ANGLE	CLASS OF ACCURACY	NOMINAL DIMENSION RANGE INCHES	
30° 30'		OVER 0 TO 1.2	OVER 1.2 TO 4.0
SURFACE QUALITY	(CHECK ONE)	OVER 4.0 TO 12.0	OVER 12.0 TO 40.0
IN	MEDIUM	OVER 40.0 TO 80.0	OVER 80.0 TO 160.0
QUANTITY & VARIATION	PREFERRED	2.000	2.008
		2.016	2.024
		2.032	2.040
		2.048	2.056
		2.064	2.072
		2.080	2.088
		2.100	2.112
THIRD ANGLE PROJECTION	DRW. DATE 1/25/01	FIRST USED ON	C1780 digital
CHKO J.F. Sullivan 2/1/01	ENG. Z.C. N. 7-24-01	TITLE	CABLE ASSY (C1780)
PROJ. ENG. R. C. 9/2/04	PROJ. M. 7/21/01	MATERIAL	SEE PARTS LIST
DO NOT SCALE DWG	NEXT HIGHER ASSY.	SCALE	NONE
FINISH	SHEET 1 OF 1	SIZE	D IA
		NUMBER	7018526-0-0
		REV	B
		DIST.	TW
			1

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION							
				1A	2A	3A	4A	5A	6A	7A	8A
1	1	1700278-00	CABLE,COAX 01COND 20AWG BRAIDSHL	1	1	1	1	1	1	1	1
2	2	1218505-00	CONN,COAX RG58 JACK SOLD	1	1	1	1	1	1	1	1
3	4	1217991-00	CONN,COAX TNC RG58 JACK	1	1	1	1	1	1	1	1
4	5	A-PS-3616073-0-0 3616073-00	LABEL,ID W/COPY VERTICAL	1	1	1	1	1	1	1	1

REVISION HISTORY			BASIC PART NO: 7018526			DIGITAL					
ENG	ECO NUMBER	REV	SECTION A OF A	DRN:	S. CHARTIER	DATE:	24-SEP-81	TITLE			PARTS LIST
RN	INITIAL 7018526-TW001	A B	SECTION. VARIATION INDEX [A] 1A,2A,3A,4A,5A,6A, 7A,8A [B] [C] [D] [E] [F]	CHK'D:	J. SULLIVAN	DATE:	24-SEP-81	CABLE ASSY(CI780)			
				DES.ENG.:	R. NUEBLING	DATE:	24-SEP-81	DOCUMENT NUMBER			
				RESP.ENG.:	R. CASABONA	DATE:	24-SEP-81	SIZE	CODE	NUMBER	REV
				MFG.ENG.:	M. WARSHAW	DATE:	24-SEP-81	K	PL	7018526-0-DBP	B
				ASSEMBLY NUMBER:	D-IA-7018526-0-0	TOP DOCUMENT NUMBER:	E-UA-CI780-0-0	FILE NAME:		Z2284B.PLS	EDIT #
											15

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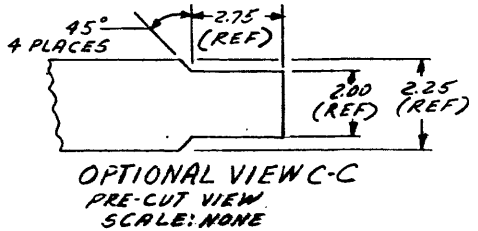
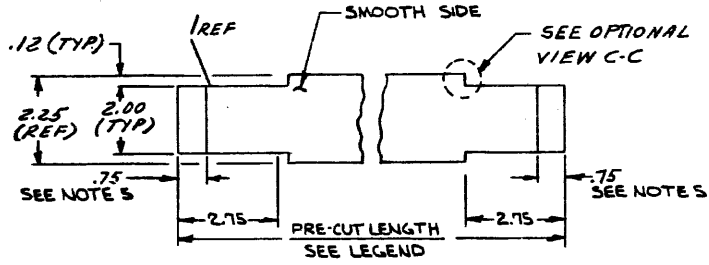
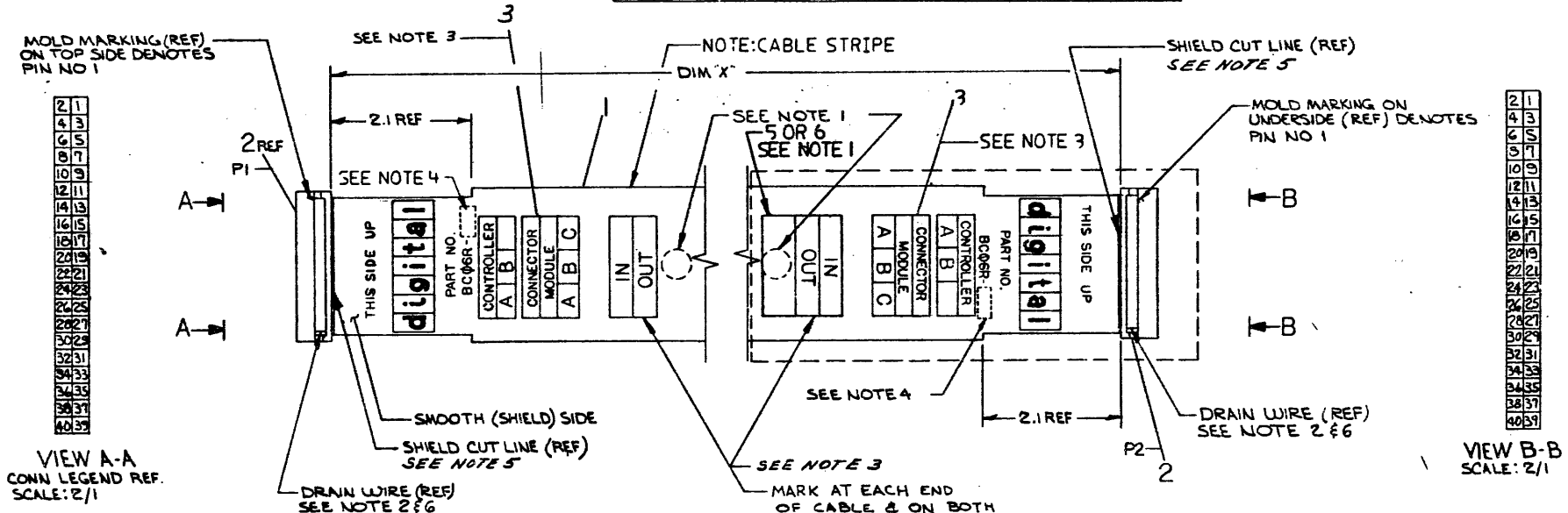
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WIRE TABLE			
FROM	TO	FROM	TO
P1-1	P2-1	P1-21	P2-21
P1-2	P2-2	P1-22	P2-22
P1-3	P2-3	P1-23	P2-23
P1-4	P2-4	P1-24	P2-24
P1-5	P2-5	P1-25	P2-25
P1-6	P2-6	P1-26	P2-26
P1-7	P2-7	P1-27	P2-27
P1-8	P2-8	P1-28	P2-28
P1-9	P2-9	P1-29	P2-29
P1-10	P2-10	P1-30	P2-30
P1-11	P2-11	P1-31	P2-31
P1-12	P2-12	P1-32	P2-32
P1-13	P2-13	P1-33	P2-33
P1-14	P2-14	P1-34	P2-34
P1-15	P2-15	P1-35	P2-35
P1-16	P2-16	P1-36	P2-36
P1-17	P2-17	P1-37	P2-37
P1-18	P2-18	P1-38	P2-38
P1-19	P2-19	P1-39	P2-39
P1-20	P2-20	P1-40	P2-40

LEGEND			
NUMBER	DIM X"	PRECUT LENGTH	REMARKS
BC06R-011	1FT	1FT 1.5IN ± 1IN	SEE PRE-CUT VIEW C-C
BC06R-02	2FT	2FT 1.5IN ± 1IN	
BC06R-03	3FT	3FT 1.5IN ± 1IN	
BC06R-04	4FT	4FT 1.5IN ± 1IN	
BC06R-05	4FT 6IN	4FT 7.5IN ± 1.5IN	SEE NOTE 7
BC06R-06	6FT	6FT 1.5IN ± 2IN	
BC06R-08	8 FT	8 FT 1.5IN ± 2IN	
BC06R-10	10 FT	10 FT 1.5IN ± 2IN	
BC06R-12	12 FT	12 FT 1.5IN ± 3IN	
BC06R-20	20 FT	20 FT 1.5IN ± 3IN	
BC06R-25	25 FT	25 FT 1.5IN ± 3IN	
BC06R-30	30 FT	30 FT 1.5IN ± 6IN	
BC06R-50	50 FT	50 FT 1.5IN ± 10 FT	
BC06R-60	60 FT	60 FT 1.5IN ± 12 FT	
BC06R-75	75 FT	75 FT 1.5IN ± 15 FT	
BC06R-A0	100 FT	100 FT 1.5IN ± 2 FT	
BC06R-07	7 FT	7 FT 1.5IN ± 2IN	
BC06R-08	8 FT 6IN	8 FT 7.5IN ± 1IN	SEE PRE-CUT VIEW C-C
BC06R-15	5 FT	5 FT 1.5IN ± 1IN	
BC06R-1C	15 IN	16.5 IN ± 1IN	SEE PRE-CUT VIEW C-C
BC06R-1K	1 FT 9IN	1 FT 10.5IN ± 1IN	SEE PRE-CUT VIEW C-C

- NOTES:
- LABEL (ITEMS 5 OR 6 CAN BE USED) TO CONTAIN:
PART NO.
REV.
DATE (OF BUILD)
MFG(STAMP) TEST(STAMP)
INSP(STAMP)
AFFIX LABEL AROUND CABLE IN APPROX CENTER.
 - DRAIN WIRE CONNECTS TO PIN NO 40.
 - RUBBER STAMP INFORMATION SHOWN USING INK (ITEM 3) & ARTWORK DEC NO A-DC-7411699-0-0.
 - STAMP APPLICABLE OPTION DASH NO. ACCORDING TO LENGTH.
 - REMOVE SHIELD .75 FROM END OF PRECUT CABLE (SEE VIEW C-C).
 - COVER EXPOSED DRAIN WIRE WITH ITEM 4 PRIOR TO ASSY (BOTH ENDS).
 - FOR RP04, RP05, RP06 USE WRAP AROUND VINYL LABEL. SEE VARIATIONS ON DRAWING NUMBER A-PS-3615389-0-0. ALSO SEE NEXT HIGHER ASSEMBLY E-IA-7009807-0-0 AND E-IA-7009808-0-0.



CAUTION: OFF SHEET PARTS LIST EXISTS. SEE: K-PL-BC06R-0-DBP (Z 6996)

REV	CHG	NO	DATE	BY	APP	DESCRIPTION
1						
2						
3						
4						
5						
6						
7						
8						

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
RP04				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		Digital EQUIPMENT CORPORATION		
DECIMALS	ANGLES	TITLE		
XXX - .001	XX - .01	BC06R I/O CABLE		
MATERIAL				
SEE PARTS LIST				
FINISH				
SHEET 1 OF 1				

DRAWING NUMBER: DUA-BC06R-0-0

DRAWING NUMBER: 1 of 1

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION
1	1	1700034-00	H	CABLE,RIBBON BOND 40COND 30AWG	A/R
2	2	1211206-00	S	CONN,IDC 40SKT(2X20).100CC GOL	2
3	3	4901150-00	A	INK,ACID BASE,WATERPROOF	A/R
4	4	3612511-00	CR	TAPE,VINYL ADH .50 WDX 7M	A/R
5	5	3616073-00	B	LABEL,ID W/COPY VERTICAL	1
6	6	3616989-00	A	LABEL,CABLE LARGE	1

REVISION HISTORY			BASIC PART NO: BC06R			DIGITAL		
ENG	ECO NUMBER	REV	SECTION A OF A	DRN:	D. DI PERRI	DATE:	12-22-73	
WH	BC06R-CX007	H	SECTION.VARIATION INDEX	CHK'D:	B. MONETTE	DATE:	3-25-74	TITLE PARTS LIST
RH	BC06R-CX008	J	[A]01					BC06R I/O CABLE
			[B]	DES.ENG:	D. POTTER	DATE:	3-29-73	DOCUMENT NUMBER
			[C]					SIZE!CODE! NUMBER ! REV
			[D]	RESP.ENG.:	D. LIGNOS	DATE:	4-17-74	K ! PL ! BC06R-0-DBP ! J
			[E]	MFG.ENG.:	A. PARLSING	DATE:	4-21-74	RELEASE DATE:
			[F]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME: ! EDIT #!
				D-UA-BC06R-0-0-0		E-IA-7009807-0-0		Z6996J.PLS ! 4 !

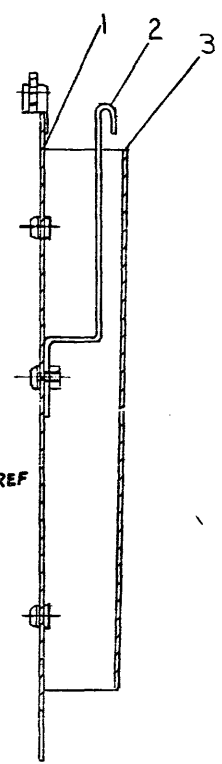
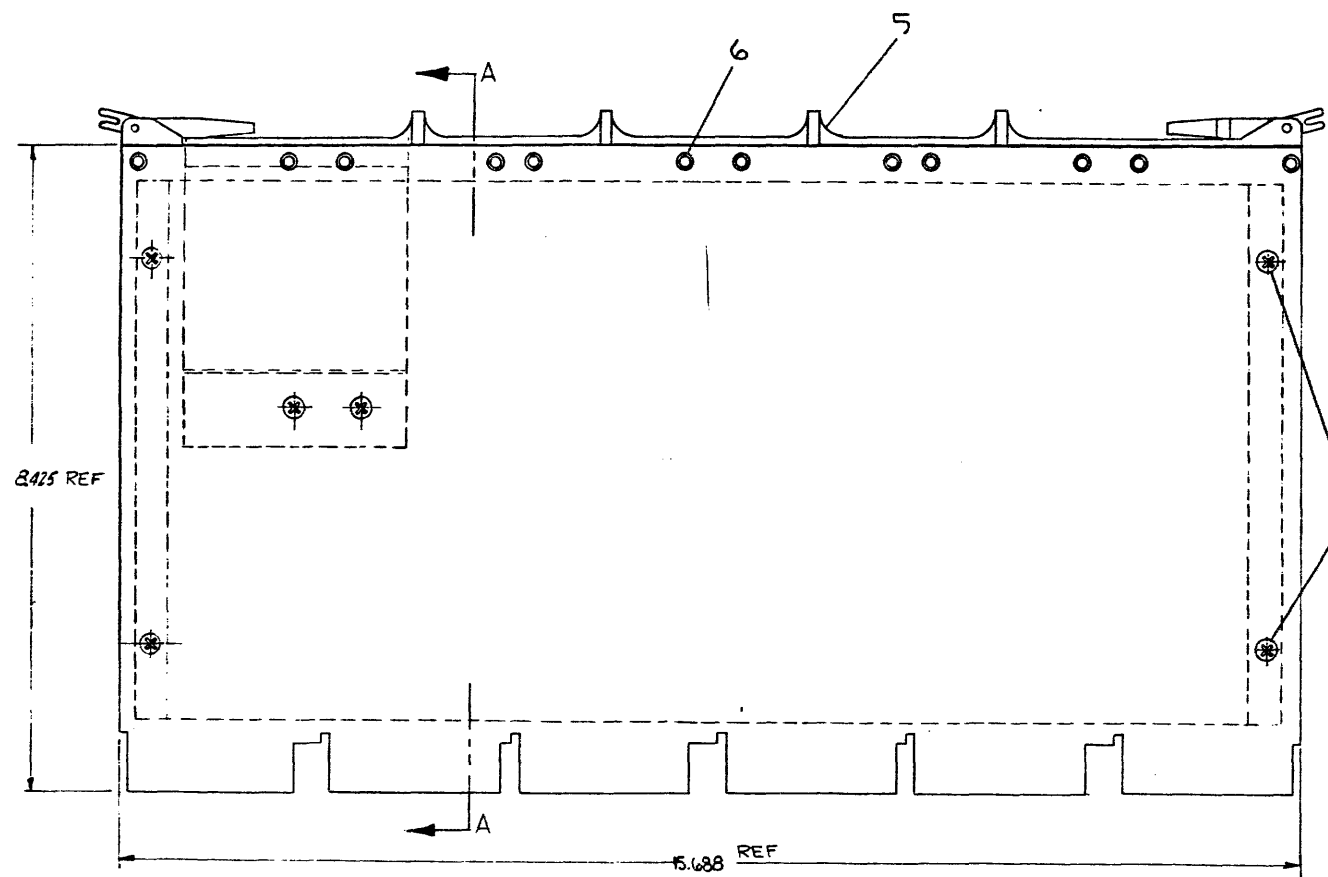
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RH
20 DEC
53

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LEGEND		
NUMBER	VARIATION	REV
7019266-00	AS SHOWN	A1

NOTES:



SECTION A-A

CAUTION: OFF SHEET PARTS LIST EXIST. SEE K-PL-7019266-0-DBP

REVISION HISTORY	REV.	DATE
RELEASED	B	

DESCRIPTION	DRAWING NO.	PART NO.	ITEM NO.		
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)					
INCHES TOLERANCES	ANGLES ±0°30'	APPLICABLE DIMENSION RANGE			
		OVER TO	OVER TO	OVER TO	OVER TO
±.1	SURFACE QUALITY	0.2 TO 1.2	1.2 TO 4.0	4.0 TO 12.0	12.0 TO 40.0
±.02	MICROINCHES	±.02	±.03	±.06	±.10
±.005		±.004	±.008	±.012	±.016
QUANTITY & VARIATION	THIRD ANGLE PROJECTION	DATE	TITLE		
		26 FEB 83	MODULE BLANK ASSY		
	DO NOT SCALE DRAWING	DATE	DOCUMENT NUMBER		
	REMOVE BURRS AND BREAK SHARP CORNERS	14 DEC 82	DAD 7019266-0-0		
	MATERIAL	DATE	REV		
	SEE PARTS LIST	13 APR 83	B		
	FINISH	DATE	SCALE		
	N/D	13 FEB 83	1 MRO		
			SHEET 1 OF 1		

DRAWING NUMBER 7019266-0-0

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION
1	D-MD-7428313-0-0	49057-00		AIR FLOW CONTROLLER, HEX (FOR CI2	1
2	C-IA-7424869-0-0	7424869-01		CLTP	1
3	D-IA-7426796-0-0	7426796-01		BAFFLE	1
4		9006022-01		SCREW, MACH PAN PHIL	5- 6

VARIATION REVISION LEVEL:

REVISION HISTORY		BASIC PART NO: 7019266		DRN: W. ALLEN		DATE: 26-FEB-82		D I G I T A L	
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D:	G. F.	DATE:	14-DEC-82	TITLE	PARTS LIST
---	INITIAL	B	SECTION.VARIATION INDEX					MODULE BLANK ASSY	
			CA300						
			[B]	DES.ENG:	P. CAPPABIANCA	DATE:	13-APR-83	DOCUMENT NUMBER	
			[C]					SIZE CODE	NUMBER
			[D]	RESP.ENG.:	P. CAPPABIANCA	DATE:	13-APR-83	K	PL
			[E]					7019266-0-DBP	8
			[F]	MFG.ENG.:	S. ALMEIDA	DATE:	13-APR-83	RELEASE DATE:	02-FEB-84
				ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	EDIT #
				D-AD-7019266-0-0				Z5881B.PLS	6

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APD

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1

SIZE CODE NUMBER
D DD M3001-0

REV. C

DRAWING NUMBER	PAGE	PART NO.	DESCRIPTION	REVISIONS
			ECO NUMBER	- M3001 MR001
		M3001-00	MODULE REVISION	A1 A1
D-LIA-M3001-0-0	2		EBUS	A A1
K-PL-M3001-0-DBP	2		PARTS LIST, M3001	A A1
D-CS-M3001-0-EBI1	1		CI20 EBUS INTFC CONTROL 1	A A
D-CS-M3001-0-EBI2	1		CI20 EBUS INTFC CONTROL 2	A A
D-CS-M3001-0-EBI3	1		CI20 EBUS INTFC EBUS-MBUS MUXS	A A
D-CS-M3001-0-EBI4	1		CI20 EBUS INTFC CSR REG	A A
D-CS-M3001-0-EBI5	1		CI20 EBUS INTFC EBUS PAR AND EBUF	A A
D-CS-M3001-0-EBI6	1		CI20 EBUS INTFC EBUS XCEIVERS	A A
D-CS-M3001-0-EBI7	1		CI20 EBUS INTFC MICROPROC ALU 1	A A
D-CS-M3001-0-EBI8	1		CI20 EBUS INTFC MICROPROC ALU 2	A A
D-CS-M3001-0-EBI9	1		CI20 EBUS INTFC UPROC CNST MUX	A A
D-CS-M3001-0-EBIA	1		CI20 EBUS INTFC PWR & GND	A A
D-CS-M3001-0-EBIB	1		CI20 EBUS INTFC UNUSED FINGER	A A
D-CS-M3001-0-BLK	1		BLOCK DIAGRAM EBUS MODULE	A A
K-PC-M3001-0-DBI	-		P.C. DESIGN DATA BASE	A REF
D-DD-5015384-0	2		DRAWING DIRECTORY, 5015384	REF REF

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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN: D. DELLORCO	DATE: 19-FEB-85	ENG: E. BLOOM	DATE: 19-FEB-85	TITLE: DRAWING DIRECTORY
	CHK'D: D. CAUNTER	DATE: 19-FEB-85	BOARD LOCATION: N/A	SHEET: 1 OF 2	M3001 (EBUS)
DSK: M3001.12PCL4.521 19-FEB-85 13:06 NEXT HIGHER ASSEMBLY:					SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL: CI20					D DD M3001-0 C

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MED

REV. C
NUMBER
M3001-0

DRAWING NUMBER	PAGE	PART NO.	DESCRIPTION	REVISIONS	
				M3001 MR001	M3001 MR002
		M3001-00	MODULE REVISION	A2	A3
D-LA-M3001-0-0	1		EBUS	B	C
K-PL-M3001-0-DBP	2		PARTS LIST, M3001	B	C
D-CS-M3001-0-EBI1	1		CI20 EBUS INTFC CONTROL 1	A	A
D-CS-M3001-0-EBI2	1		CI20 EBUS INTFC CONTROL 2	A	A
D-CS-M3001-0-EBI3	1		CI20 EBUS INTFC EBUS-MBUS MUXS	A	A
D-CS-M3001-0-EBI4	1		CI20 EBUS INTFC CSR REG	A	A
D-CS-M3001-0-EBI5	1		CI20 EBUS INTFC EBUS PAR AND EBUF	A	A
D-CS-M3001-0-EBI6	1		CI20 EBUS INTFC EBUS XCEIVERS	A	A
D-CS-M3001-0-EBI7	1		CI20 EBUS INTFC MICROPROC ALU 1	A	A
D-CS-M3001-0-EBI8	1		CI20 EBUS INTFC MICROPROC ALU 2	A	A
D-CS-M3001-0-EBI9	1		CI20 EBUS INTFC UPROC CNST MUX	A	A
D-CS-M3001-0-EBIA	1		CI20 EBUS INTFC PWR & GND	A	A
D-CS-M3001-0-EBIB	1		CI20 EBUS INTFC UNUSED FINGER	A	A
D-CS-M3001-0-BLK	1		BLOCK DIAGRAM EBUS MODULE	A	A
K-PC-M3001-0-DBI	-		P.C. DESIGN DATA BASE	B	B
D-DD-5015384-0	2		DRAWING DIRECTORY, 5015384	REF	REF

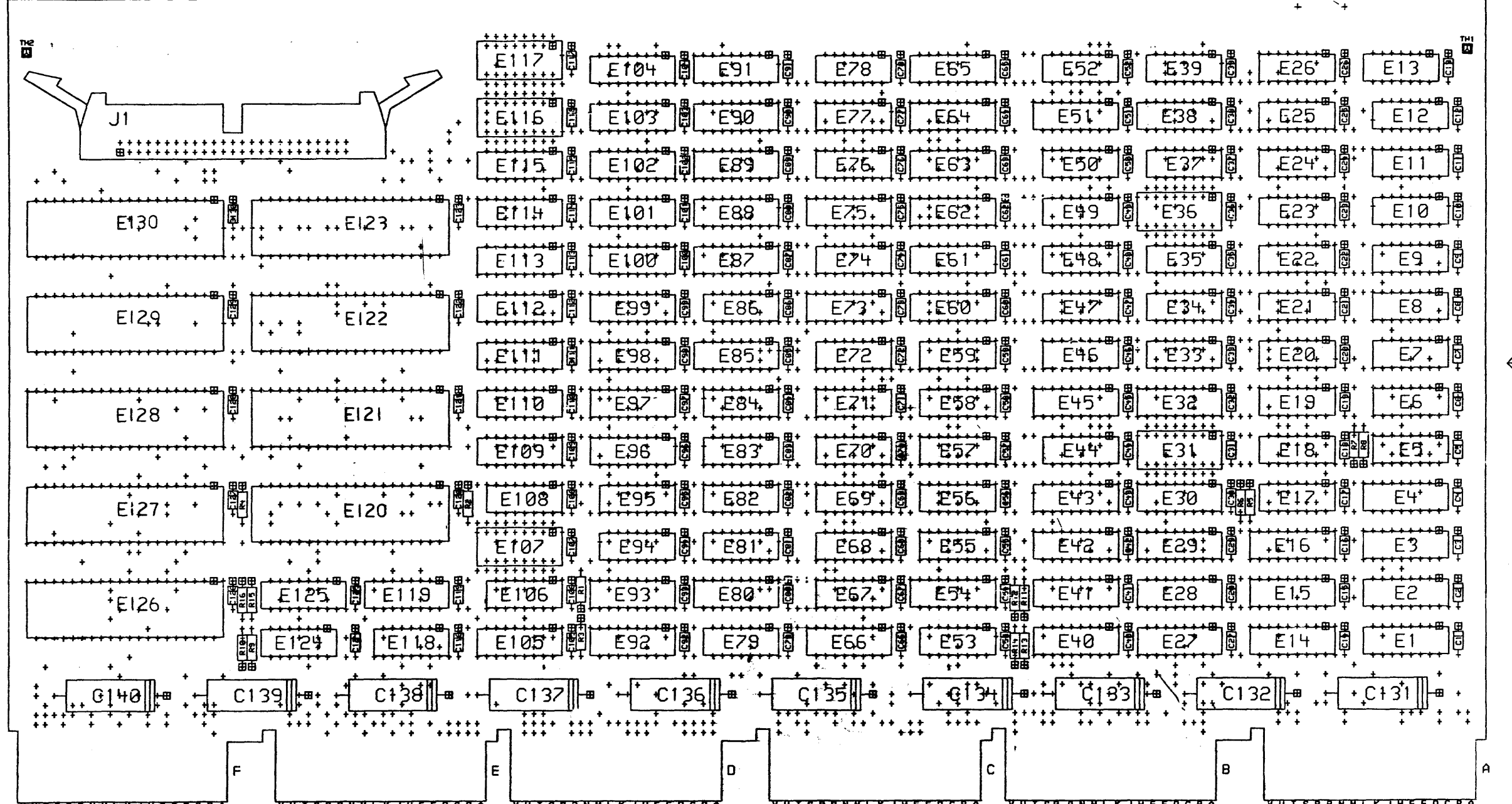
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. D. DELLORCO	DATE 19-FEB-85	ENG. E. BLOOM	DATE 19-FEB-85	TITLE: DRAWING DIRECTORY
	CHK'D. D. CAUNTER	DATE 19-FEB-85	BOARD LOCATION: N/A	SHEET 2 OF 2	M3001 (EBUS)
DSX:M30012.T2P(4,57) 19-FEB-85 13:06 NEXT HIGHER ASSEMBLY:					SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL: CI20					D DD M3001-0 C

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34 (OTY 12) COMPONENT SIDE VIEW



NOTES:
SPARE COMPONENTS: E31, E36, E107, E116, E117

STEP E → Y AXIS 0 STEP 0 TIMES
REPEAT → X AXIS 0 STEP 0 TIMES

CHANGE NO	REV
2/1	10/80 - 10/80
2/2	10/80 - 10/80
2/3	10/80 - 10/80
2/4	10/80 - 10/80
2/5	10/80 - 10/80
2/6	10/80 - 10/80
2/7	10/80 - 10/80
2/8	10/80 - 10/80
2/9	10/80 - 10/80
2/10	10/80 - 10/80

ETCH REV. C1

SIGNATURES	DATE	TITLE
DRN. <i>[Signature]</i>	7/10/84	digital
CHK'D. <i>[Signature]</i>	10/80/84	EBUS
MECH. ENG. <i>[Signature]</i>	27/06/84	
PROJ. ENG. <i>[Signature]</i>	27/06/84	
PROD. <i>[Signature]</i>	5/10/84	
SCALE 2/1		
SMT. 1 OF 1		
TOP DOC. NO: D-DD-M3001-0		

SIZE CODE NUMBER REV
0 UAM3001-0-0 C

LINE ITEM	TCP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
				VARIATION REVISION LEVEL:	00		
					A3		
1	1	D-MC-5015384-0-0	5015384-01	C DRILL & ETCH DWG FOR M3001	1		
2	2		1001610-00	.01 MFD 50V +80-20% Z5U CER	113		C1-C17,C19-C29,C31-C52,C55-C104,
3	3		1012084-03	150 MFD 15V +75-10% AL EL	10	CONT	C107-C119
4	4		1012784-00	.047 MFD 50V +80-20% CER	17		C131-C140
5	5		1216832-03	PCB,HEADER 50POS(2X25),100CC 90D	1	CONT	C18,C30,C53,C54,C105,C106,
6	6		1215278-00	CABLE,FLEX NOMEX SHEET MODULE PR	1		C120-C130
7	7		1216988-02	HANDLE,MODULE,HEX TWO EJECTORS	1		J1
8	8		1300295-00	330.0 .25 W 5.0 % CF	2		R1,R3
9	9		1300309-00	390.0 .25 W 5.0 % CF	6		R6,R8,R10,R12,R14,R16
10	11		1301322-00	180.0 .25 W 5.0 % CF	8		R2,R4,R5,R7,R9,R11,R13,R15
11	12		1910532-B0	74S00 BURNED-IN NAND GATE-	6		E12,E34,E82,E95,E106,E108
12	13		1910534-B0	74S04 BURNED-IN INVERTER G	6		E9,E10,E44,E53,E78,E79
13	14		1910536-B0	74S10 BURNED-IN NAND GATE-	3		E7,E71,E81
14	15		1910537-B0	74S11 BURNED-IN AND GATE-T	1		E124
15	15		1910544-B0	74S74 BURNED-IN FF-D DUAL,	9		E6,E8,E17,E18,E55,E67-E69,E83
16	17		1911117-00	DEC 0838 TRANSCEIVER,BUS,QUA	16		E1-E3,E14-E16,E27-E30,E40-E43,
17	18		1911573-B0	74S280 BURNED-IN PARITY GEN	10	CONT	E54,E66
18	19		1911641-B0	74S257 BURNED-IN MUX,QUAD 2	9		E46-E50,E52,E74,E76,E77,E86
19	20		1911675-B0	74S138 BURNED-IN DECODER/DE	1		E87-E90,E99-E103
20	21		1912097-00	SN 74S182 LOOK AND CARRY GEN	3		E45
21	22		1912388-B0	74S02 BURNED-IN NOR GATE-Q	8		E113,E119,E125
22	23		1912389-B0	74S08 BURNED-IN AND GATE,Q	7		E19,E20,E33,E37,E56,E58,E84,E94
23	24		1912697-00	LS174 FF-D HEX W/CLEAR	6		E21,E35,E39,E57,E59,E72,E118
24	25		1912799-B0	LS00 BURNED-IN NAND GATE-	2		E91,E98,E104,E112,E114,E115
25	26		1912805-B0	LS08 BURNED-IN AND GATE-Q	2		E22,E26
26	27		1912819-00	LS42 DECODER,BCD-DECIMAL	1		E13,E23
27	28		1910824-B0	LS74 BURNED-IN FF-D DUAL-	3		E32
							E11,E24,E25

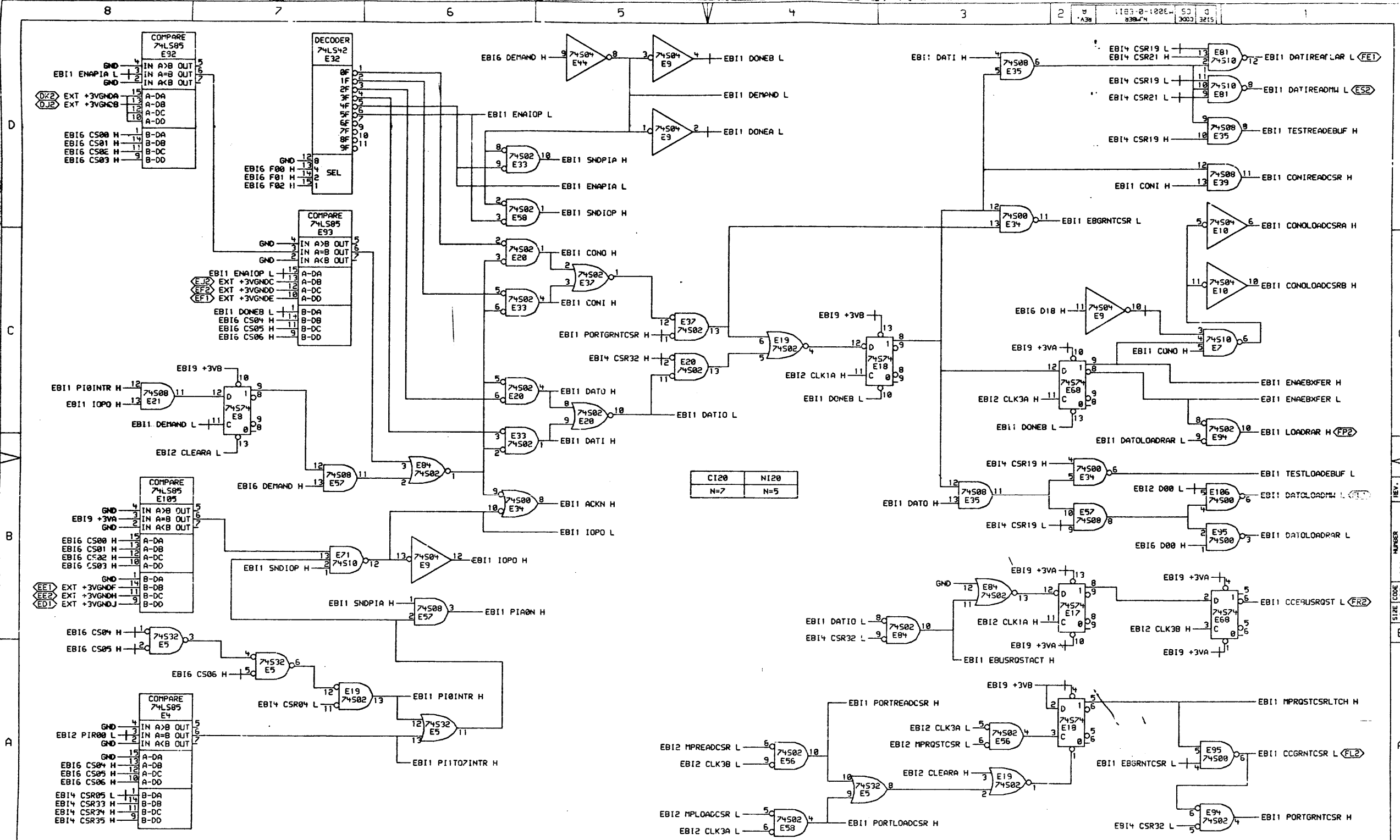
REVISION HISTORY		BASIC PART NO: M3001		DRN:	D,DELLORCO	DATE:	14-OCT-82	D	I	G	I	T	A	L
ENG:	ECC NUMBER	REV	SECTION A OF A	CHK'D:	D,DELLORCO	DATE:	3-JAN-84	M3001	PARTS LIST					
---	INITIAL	A	SECTION,VARIATION INDEX	DES.ENG:	R,CARN	DATE:	19-APR-84	EBUS	DOCUMENT NUMBER					
JT	M3001-MR001	B	(A) 00	RESP.ENG.:	R,CARN	DATE:	19-APR-84	K	PL	M3001-0-DBP	REV			
EB	M3001-MR002	C	(B)	MFG.ENG.:	R,CARN	DATE:	19-APR-84	RELEASE DATE: 18-FEB-85						
			(C)	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME: Z9533C.PLS						
			(D)	ID-UA-M3001-0-0		ID-DD-M3001-0		EDIT # 18						
			(E)											
			(F)											
			(G)											
			(H)											
			(J)											
			(K)											
			(L)											
			(M)											
			(N)											

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MRO

LINE ITEM	TCP	DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
					VARIATION REVISION LEVEL:	00 A3	
28	29		1912828-00		LS85 COMPARATOR,4BIT MAGN	4	E4,E92,E93,E105
29	30		1912847-00		LS157 MUX 1 OF 2(QUAD)	14	E38,E51,E60,E61,E63-E65,E73,E85, CONT E96,E97,E109-E111
30	31		1912853-B0		LS175 BURNED-IN FF-D,QUAD	3	E62,E75,E80
31	32		1913245-00		2901 BIPOLAR MICROPROCESSO	9	E120-E123,E126-E130
32	33		1913340-B0		74S32 BURNED-IN OR GATE,QU	2	E5,E70
33	34		9000024-01		EYELET,ROLLED 0.1210DX0.192	12	

! D I G I T A L !	! TITLE !	M3001	! SECTION A OF A !	! SIZE !	! CODE !	! DOCUMENT NUMBER !	! REV !
	EBUS				K PL	M3001-Q-DBP	C



CI20	NI20
N=7	N=5

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REVISIONS		
CHK	CHANGE NO.	REV

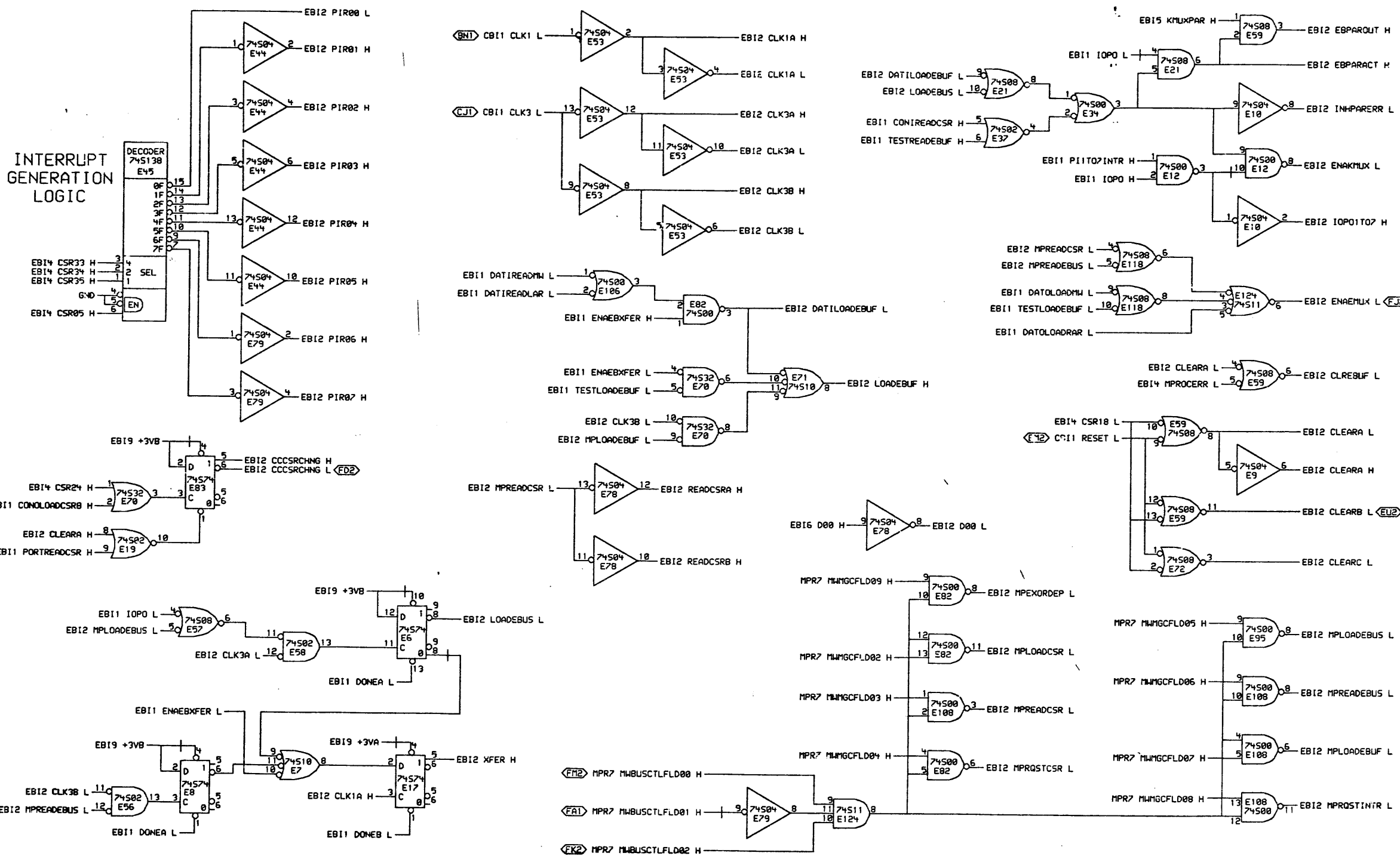
digital *DRW. J. Family* DATE 05-APR-84 ENG R. Lam DATE 05-APR-84 TITLE: CI20 EBUS INTFC CONTROL 1

DATE 05-APR-84 BOARD LOCATION: DATE 05-APR-84 SHEET 1 OF 1

SUBCONTRACTOR: (BOWEN.ECO) EB1183.DRM 129-MAR-84 08:43 NEXT HIGHER ASSEMBLY: CI20 D-DD-M3001-0

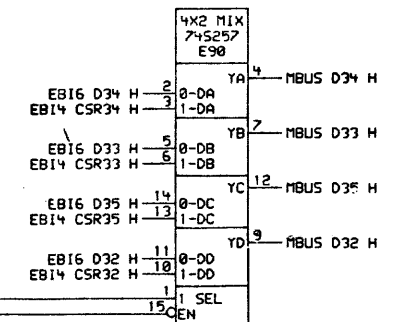
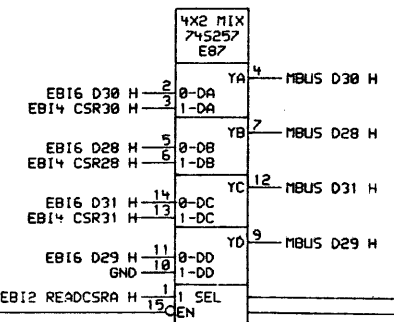
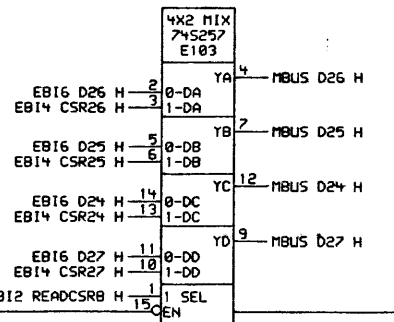
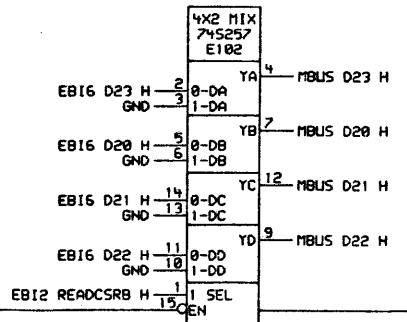
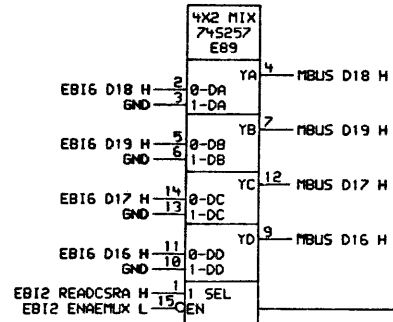
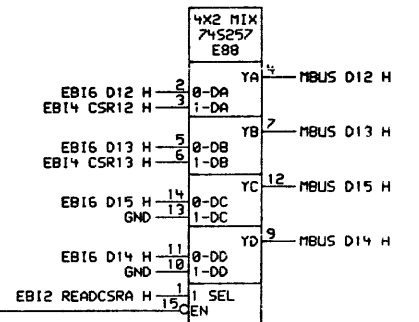
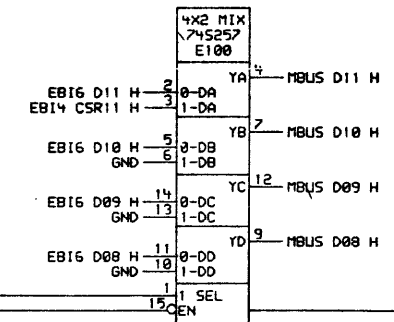
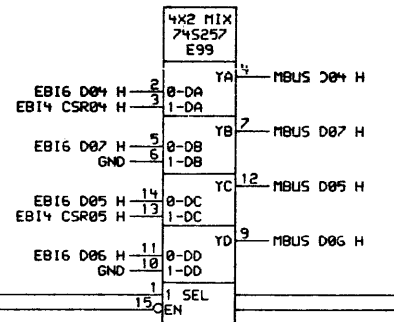
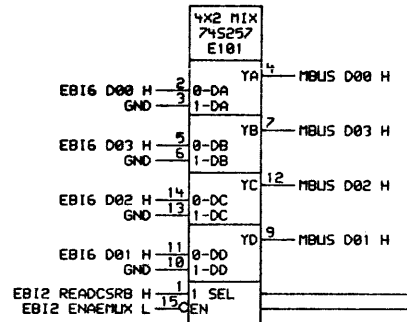
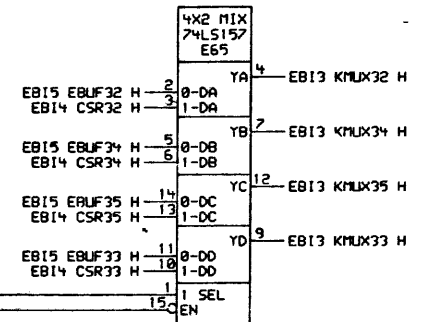
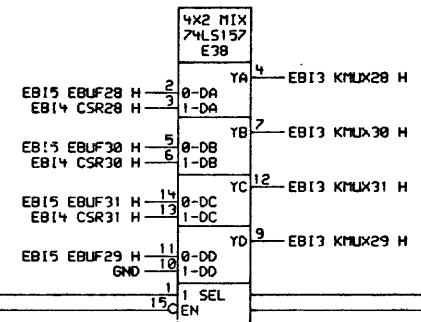
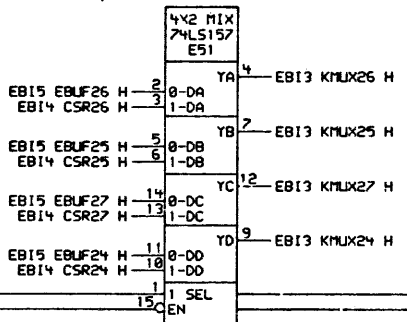
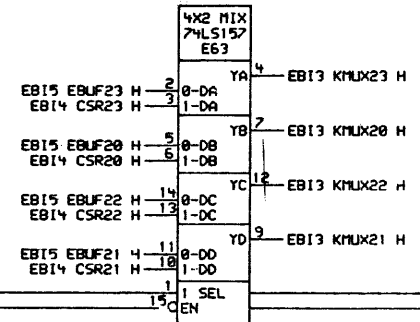
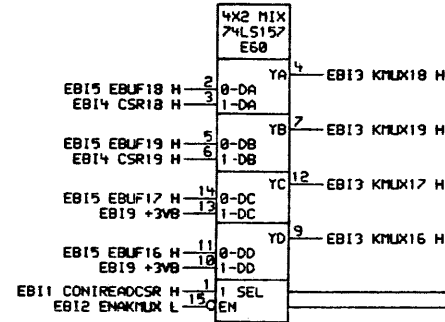
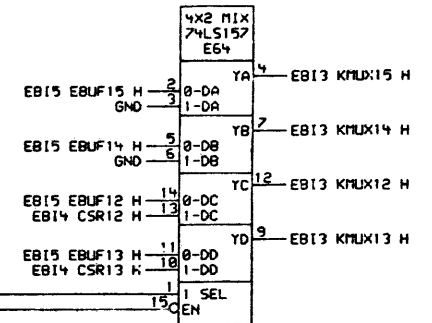
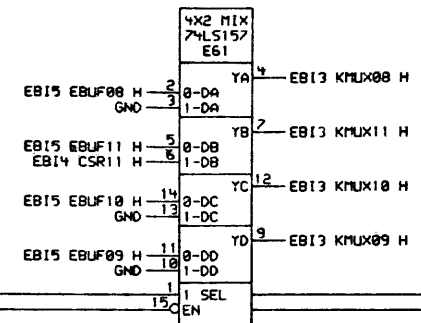
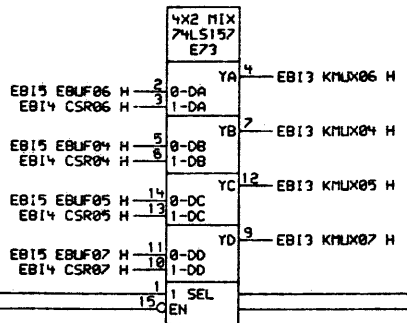
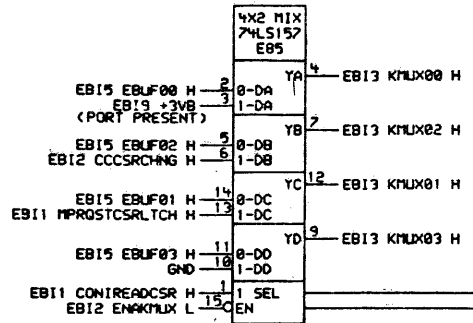
FIRST USED ON OPTION/MODEL: CI20 D-DD-M3001-0

SIZE	CODE	NUMBER	REV.
D	CS	M3001-0-EB11	A



KMUX

EMUX



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REVISIONS	CHK	CHANGE NO.	REV

digit@l

DATE 05-APR-84 ENG. R. Lee

DATE 05-APR-84 BOARD LOCATION:

DATE 07-APR-84 SHEET 1 OF 1

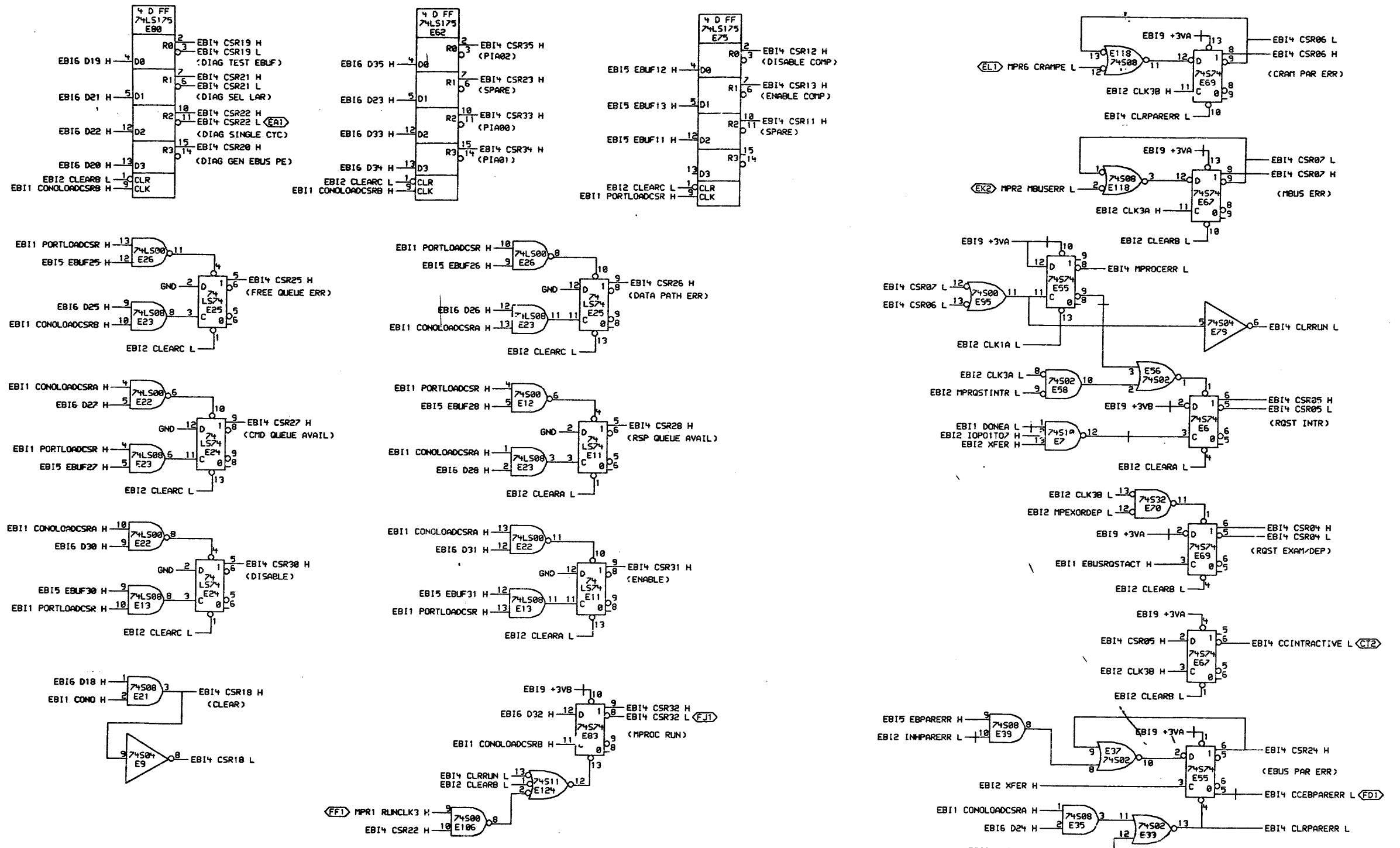
SUDCOM: (BOWEN.ECD)EB1200.DRW 29-MAR-84 08:45 NEXT HIGHER ASSEMBLY:

FIRST USED ON OPTION MODEL: CI20 0-DD-M3001-0

TITLE: CI20 EBUS INTFC
 EBUS-MBUS MUXS

NUMBER M3001-0-EB13

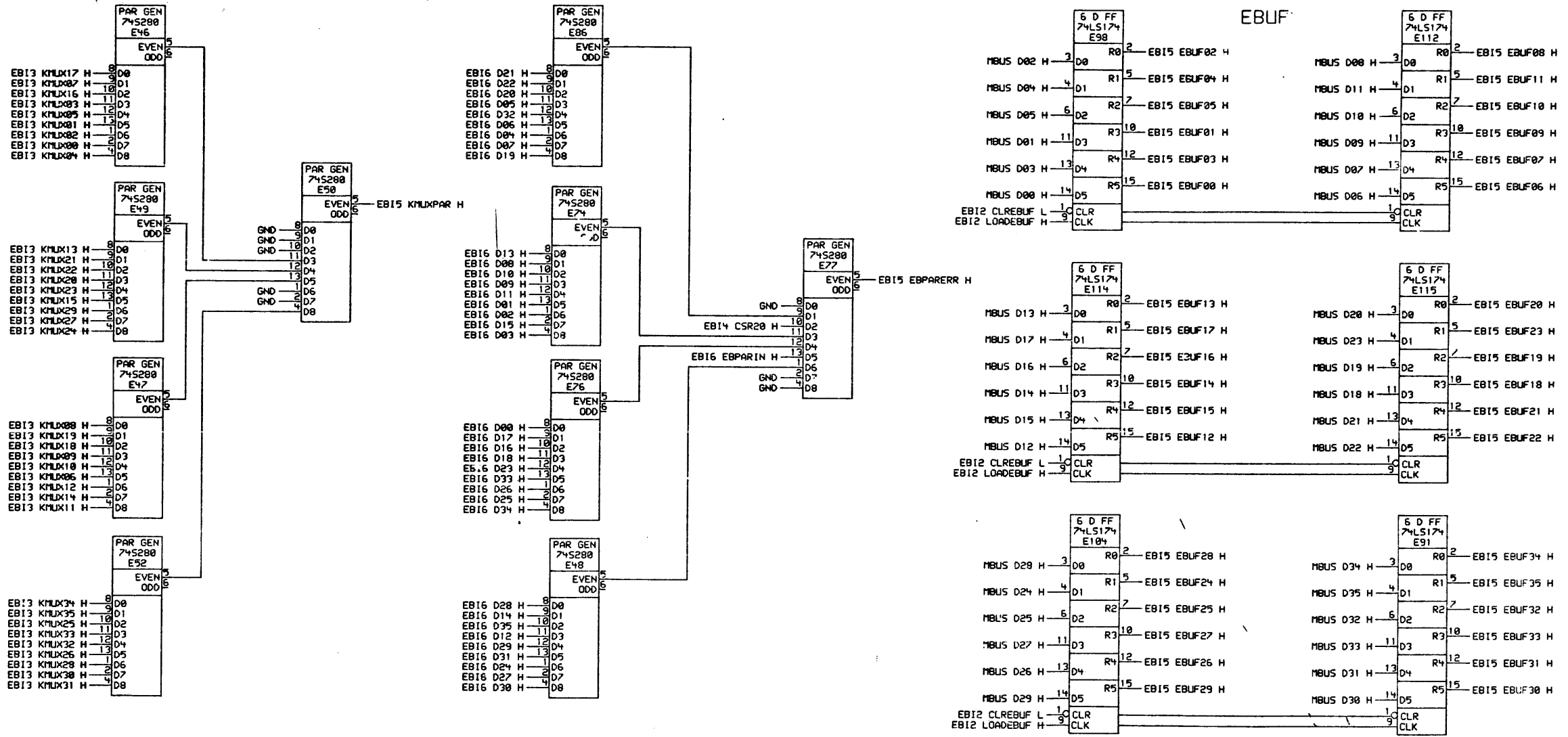
REV. A



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REVISIONS	
CHK	CHANGE NO. REV

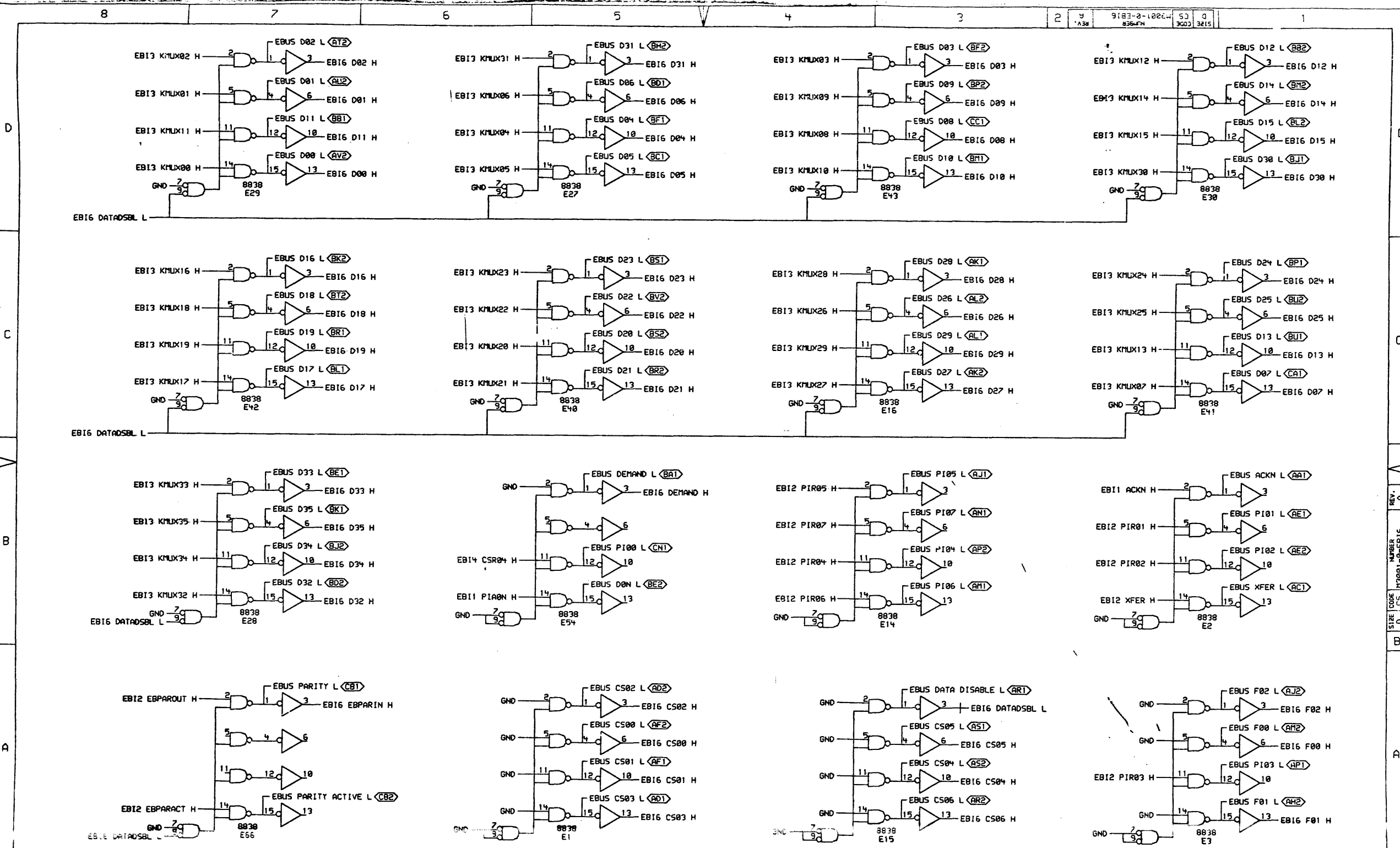
	DRN: <i>J. J. J.</i>	DATE: 05-APR-81	ENG: <i>R. Lee</i>	DATE: 05-APR-81	TITLE: CI20 EBUS INTFC CSR REG
	CHK'D: <i>J. J. J.</i>	DATE: 13-APR-81	BOARD LOCATION: 13	SHEET: 1 OF 1	SIZE CODE: D CS M3001-0-EB14
SUBCOM: (BOWEN_ECO)EB1403.DRW 129-MAR-84 08:45			NEXT HIGHER ASSEMBLY: D-DD-M3001-0		NUMBER: 1
FIRST USED ON OPTION/MODEL: CI20			D-DD-M3001-0		REV: A



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REVISIONS	
CHK	CHANGE NO. REV

	DRN: <i>J. Family</i> CHK'D: <i>B. Jones</i>	DATE: 03-APR-84 DATE: 19-APR-84	ENG: <i>R. Cow</i> BOARD LOCATION:	DATE: 03-APR-84 SHEET: 1 OF 1	TITLE: CI20 EBUS INTFC EBUS PAR & EBUF
	SUBCONTRACTOR: EB1500.DRN 23-MAR-84 08:46 NEXT HIGHER ASSEMBLY: FIRST USED ON OPTION/MODEL: CI20 D-DD-M3001-0				SIZE: D CODE: CS



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REVISIONS		
CHK	CHANGE NO.	REV

	DATE: 05-29-84	ENG. R. Linn	DATE: 05-29-84	TITLE: C120 EBUS INTFC EBUS XCEIVERS
	CHK'D: D. Squires	DATE: 19 APR 84	BOARD LOCATION: 1 OF 1	SIZE CODE: D C5
SUDCOM: <BOHEN.ECD>EB1602.DRW 129-MAR-84 08:47		NEXT HIGHER ASSEMBLY: 10-DD-M3001-0		NUMBER: M3001-0-EB16
FIRST USED ON OPTION MODEL: C120				REV. A

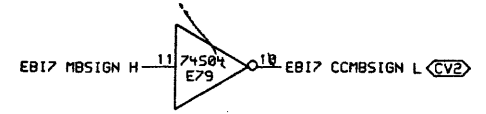
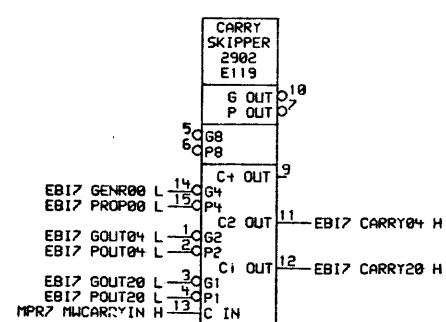
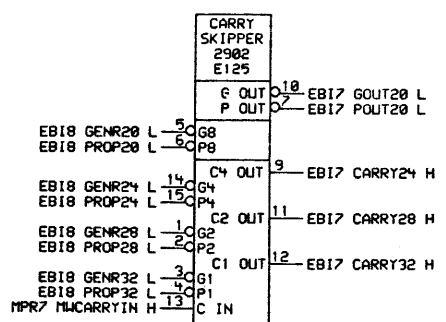
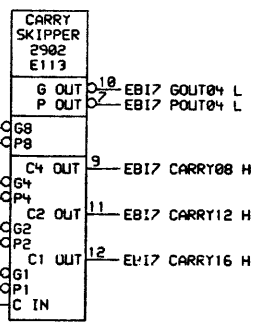
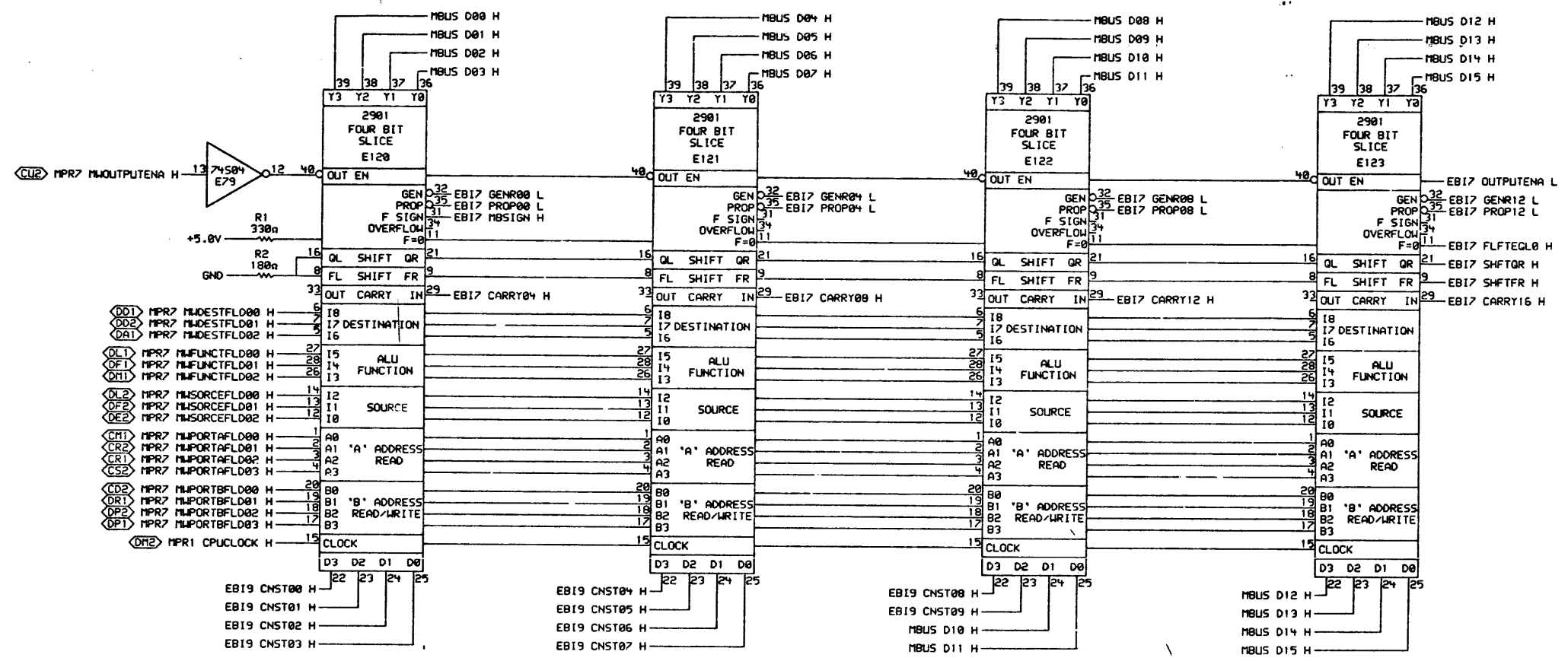
REV. A
 NUMBER M3001-0-EB16
 SITE CODE D C5
 BOARD B

D

C

B

A



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CHK	CHANGE NO.	REV

REVISIONS

digital

DATE: 05-APR-84
 ENG: R. Law
 DATE: 17-APR-84
 BOARD LOCATION: 17-APR-84
 SHEET: 1 OF 1

DATE: 05-APR-84
 DATE: 17-APR-84
 BOARD LOCATION: 17-APR-84
 SHEET: 1 OF 1

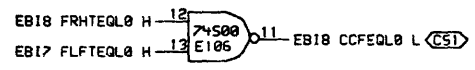
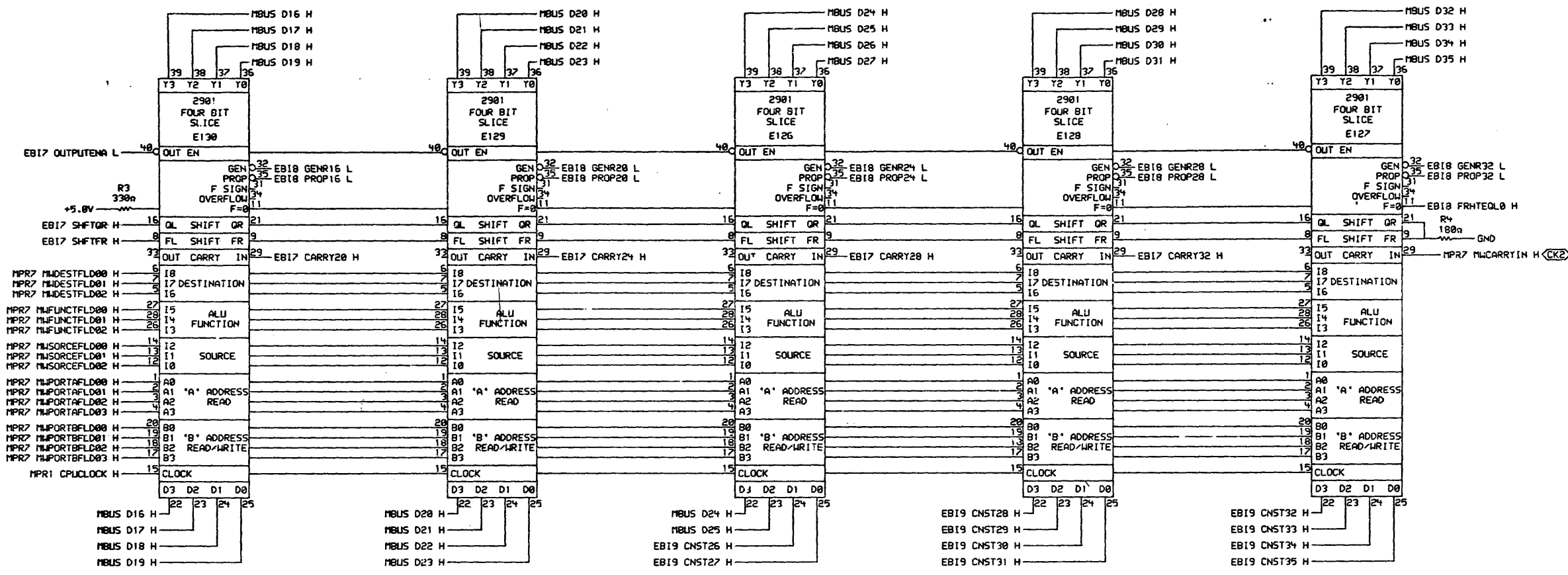
TITLE: CI20 EBUS INTFC MICROPROC ALU 1

SUBCOM: <BOWEN.ECD> EB1701.DRW 123-MAR-84-00147 NEXT HIGHER ASSEMBLY: FIRST USED ON OPTION/MODEL: CI20 D-DD-M3001-0

SIZE CODE: D CS M3001-0-EB17
 NUMBER: 0
 REV: A

A A
 B B
 C C
 D D
 SIZE CODE CS M3001-0-EB17
 NUMBER 0
 REV A

D
C
B
A

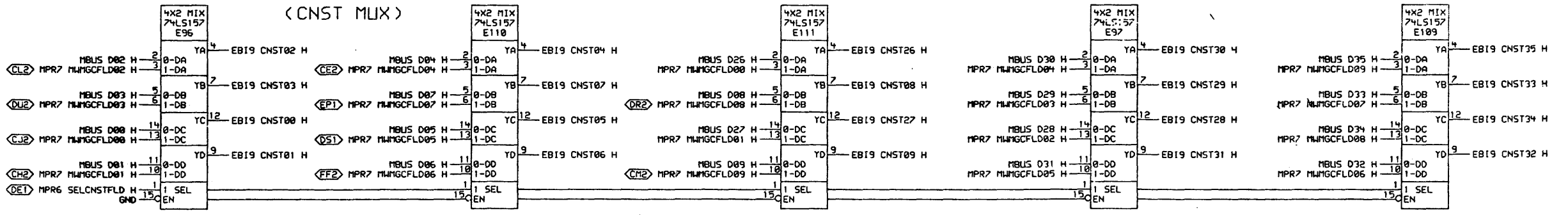
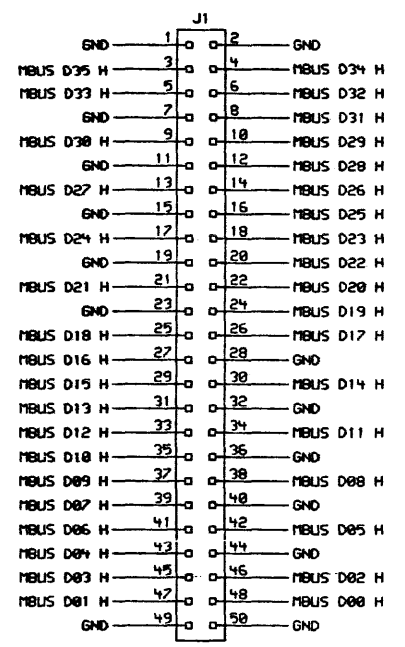
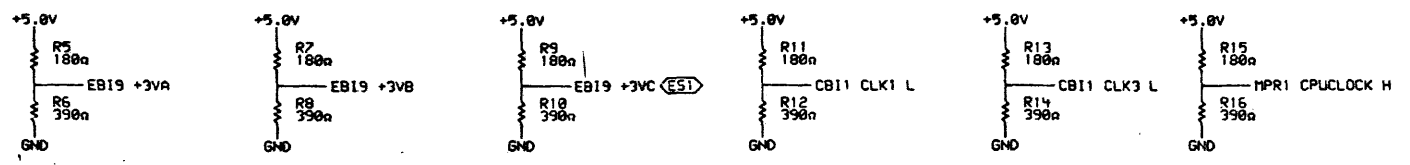


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REV.	DESCRIPTION
1	CHK CHANGE NO. REV.

	DATE: 03-APR-84	ENG: R. Law	DATE: 03-APR-84	TITLE: C120 EBUS INTFC MICROPROC ALU 2
	CHK'D: [Signature]	DATE: 19-APR-84	SHEET: 1 OF 1	REV. A
SUCCH: (BOMEN.ECO)EB1801.DRW 29-MAR-84 08:48		NEXT HIGHER ASSEMBLY: D-DD-M3001-0		SIZE CODE: D CS
FIRST USE ON OPTION/MODEL: C120		NUMBER: M3001-0-EB18		REV. A

REV. A
NUMBER M3001-0-EB18
SIZE CODE D CS

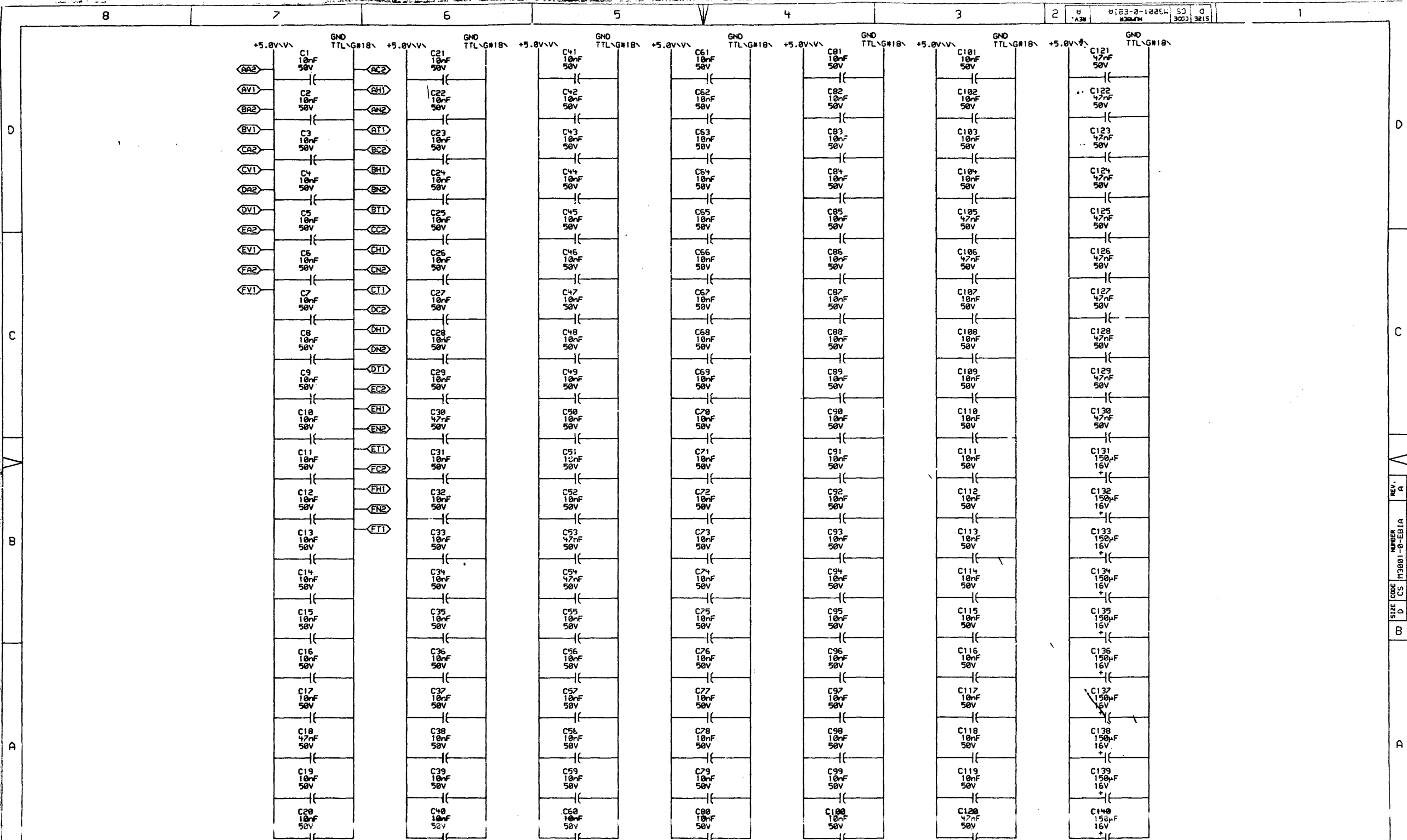


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REVISIONS		
CHK	CHANGE NO.	REV.

digital	DATE: 05-07-84	ENG: R. Lam	DATE: 05-07-84	TITLE: C120 EBUS INTFC
	CHK'D: [Signature]	DATE: 19 APR 84	BOARD LOCATION: 1 OF 1	NUMBER: M3001-0-EB19
SUDCOM: (BOWEN.ECO)EB1900.DRW 29-MAR-84 08:48			NEXT HIGHER ASSEMBLY: D-DD-M3001-0	REV: A
FIRST USED ON OPTION/MODEL: C120				

SIZE: D	CODE: CS	NUMBER: M3001-0-EB19	REV: A
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REVISIONS	
CHK	CHANGE NO. REV

	DRN. <i>J. J. J.</i>	DATE <i>02-APR-84</i>	ENG. <i>R. L. L.</i>	DATE <i>02-APR-84</i>	TITLE: CI20 EBUS INTFC PWR & GND
	CHK'D. <i>M. M.</i>	DATE <i>11-APR-84</i>	DATE <i>11-APR-84</i>	DATE <i>11-APR-84</i>	BOARD LOCATION: D CS M3001-0-EBIA
SUDCOM: (BOHLEN.ECO)EBIA08.DRW		NEXT HIGHER ASSEMBLY: D-DD-M3001-0		SIZE CODE D CS	NUMBER M3001-0-EBIA
FIRST USED ON OPTION/MODEL: CI20		D-DD-M3001-0		REV. A	

- AB1 - N/C SEE NOTE 3
- AB2 - N/C SEE NOTE 3
- AU1 - N/C SEE NOTE 3
- CD1 - N/C SEE NOTE 1
- CE1 - N/C SEE NOTE 1
- CF1 - N/C SEE NOTE 1
- CF2 - CB11 CLK2 L
- CK1 - N/C SEE NOTE 1
- CL1 - CB11 CLK4 L
- CP1 - N/C SEE NOTE 2
- CP2 - N/C SEE NOTE 2
- CU1 - N/C SEE NOTE 3
- DB1 - N/C SEE NOTE 3
- DB2 - N/C SEE NOTE 3
- DC1 - N/C NOTE:EXTRA GND
- DH2 - N/C NOTE:EXTRA GND
- DJ1 - N/C SEE NOTE 2
- DK1 - N/C SEE NOTE 2
- DN1 - N/C NOTE:EXTRA GND
- DS2 - N/C SEE NOTE 1
- DT2 - N/C NOTE:EXTRA GND
- DUI - N/C SEE NOTE 3
- DV2 - N/C SEE NOTE 2
- EB1 - N/C SEE NOTE 3
- EB2 - N/C SEE NOTE 3
- EC1 - N/C NOTE:EXTRA GND
- ED2 - N/C SEE NOTE 1
- EH2 - N/C NOTE:EXTRA GND
- EJ1 - N/C NOTE:EXTRA GND
- EK1 - N/C NOTE:EXTRA GND
- EL2 - N/C SEE NOTE 1
- EN1 - N/C SEE NOTE 2
- EN1 - N/C NOTE:EXTRA GND
- EP2 - N/C NOTE:EXTRA GND
- ER1 - N/C SEE NOTE 2
- ER2 - N/C SEE NOTE 2
- ET2 - N/C NOTE:EXTRA GND
- EU1 - N/C SEE NOTE 3
- EV2 - N/C SEE NOTE 1
- FB1 - N/C SEE NOTE 3
- FR2 - N/C SEE NOTE 3
- FCT - N/C NOTE:EXTRA GND
- FE2 - N/C SEE NOTE 2
- FH2 - N/C NOTE:EXTRA GND
- FK1 - N/C SEE NOTE 1
- FR1 - N/C SEE NOTE 1
- FNI - N/C NOTE:EXTRA GND
- FP1 - MPR7 MARKBIT H
- FR1 - N/C SEE NOTE 1
- FS1 - N/C SEE NOTE 2
- FS2 - N/C SEE NOTE 1
- FT2 - N/C SEE NOTE 2
- FU1 - N/C SEE NOTE 3
- FU2 - N/C NOTE:EXTRA GND
- FV2 - N/C SEE NOTE 2

NOTE 1: THESE PINS ARE CONNECTED AS SHOWN BELOW

FROM	TO
C13D1	A14A1
C13E1	A14U2
F13R1	B14H2
F13M1	B14K1
D13S2	B14K2
E13L2	B14R1
E13D2	C14J1
C13F1	C14R2
C13K1	D14H2
F13K1	D14J1
E13V2	A15U2
F13S2	A15J1

NOTE 2: THESE PINS DO NOT CONNECT TO EITHER THE MPROC OR CBUS

NOTE 3: RSVD FOR -5.2V

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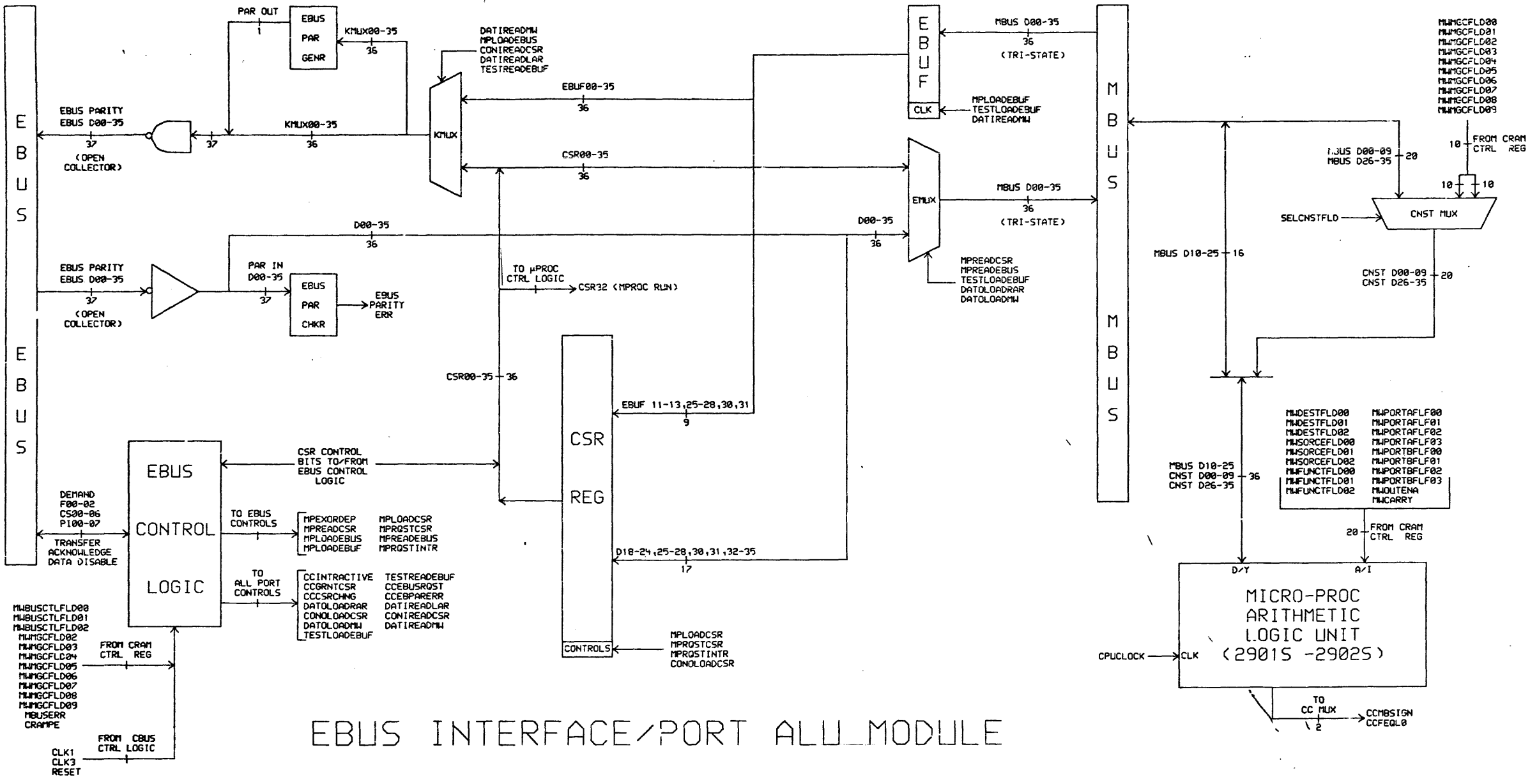
REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG. R. Law	DATE	TITLE: C120 EBUS INTFC UNUSED FINGER
	CHK'D. W. J. Lewis	DATE 03-APR-84	BOARD LOCATION:	SIZE CODE D CS
SUBCOM: <BOLHEM.ECD>EB1800.DRW 129-MAR-84 08:49		19-APR-84	SHEET 1 OF 1	NUMBER M3001-0-EB1B
FIRST USED ON OPTION/MODEL: C120		10-DD-M3001-0	REV. A	

REV. A
NUMBER M3001-0-EB1B
CS
D

D
C
B
A

D
C
B
A



EBUS INTERFACE/PORT ALU MODULE

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REVISIONS		
CHK	CHANGE NO.	REV

digital *DRN: J. L. Lundy*
 DATE: 05-09-84 ENG. R. L. Lundy
 CHK'D: W. J. Lundy
 DATE: 19 APR 84 SHEET 1 OF 1
 SUBCOM: (BOLLEN.ECO)EBUS.DRW [29-MAR-84 08:49] NEXT HIGHER ASSEMBLY:
 FIRST USED ON OPTION MODEL: KLIPA D-DD-M3001-0

TITLE: BLOCK DIAGRAM
EBUS MODULE
 SIZE CODE NUMBER REV.
D BD M3001-0-BLK A

DRAWING NUMBER	PAGE	PART NO.	DESCRIPTION	REVISIONS
			ECO NUMBER	- M3002 MR001
		M3002-00	MODULE REVISION	A1 A1
D-LIA-M3002-0-0	1		MICRO PROCESSOR	A A1
K-PL-M3002-0-DBP	2		PARTS LIST, M3002	A A1
D-CS-M3002-0-MPR1	1		CI20 MPROC CONT CTRL LOGIC 1	A A
D-CS-M3002-0-MPR2	1		CI20 MPROC CONT CTRL LOGIC 2	A A
D-CS-M3002-0-MPR3	1		CI20 MPROC CONT SEGR AND RAM ADDR	A A
D-CS-M3002-0-MPR4	1		CI20 MPROC CONT CRAM RAM 1	A A
D-CS-M3002-0-MPR5	1		CI20 MPROC CONT CRAM RAM 2	A A
D-CS-M3002-0-MPR6	1		CI20 MPROC CONT CRAM PARITY	A A
D-CS-M3002-0-MPR7	1		CI20 MPROC CONT CRAM REG	A A
D-CS-M3002-0-MPR8	1		CI20 MPROC CONT LOCAL STORAGE	A A
D-CS-M3002-0-MPR9	1		CI20 MPROC CONT CRAM LOAD 1	A A
D-CS-M3002-0-MPRA	1		CI20 MPROC CONT CRAM LOAD 2	A A
D-CS-M3002-0-MPRB	1		CI20 MPROC CONT PWR AND GND	A A
D-CS-M3002-0-MPRC	1		CI20 MPROC CONT UNUSED FINGERS	A A
D-BD-M3002-0-BLK	1		BLOCK DIAGRAM MICROPROCESSOR MOD	A A
K-PC-M3002-0-DBI	-		P.C. DESIGN DATA BASE	A A
D-DD-5015385-0	2		DRAWING DIRECTORY, 5015385	REF REF

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CHK	CHANGE NO.	REV

REV.	DATE	BY	DESCRIPTION

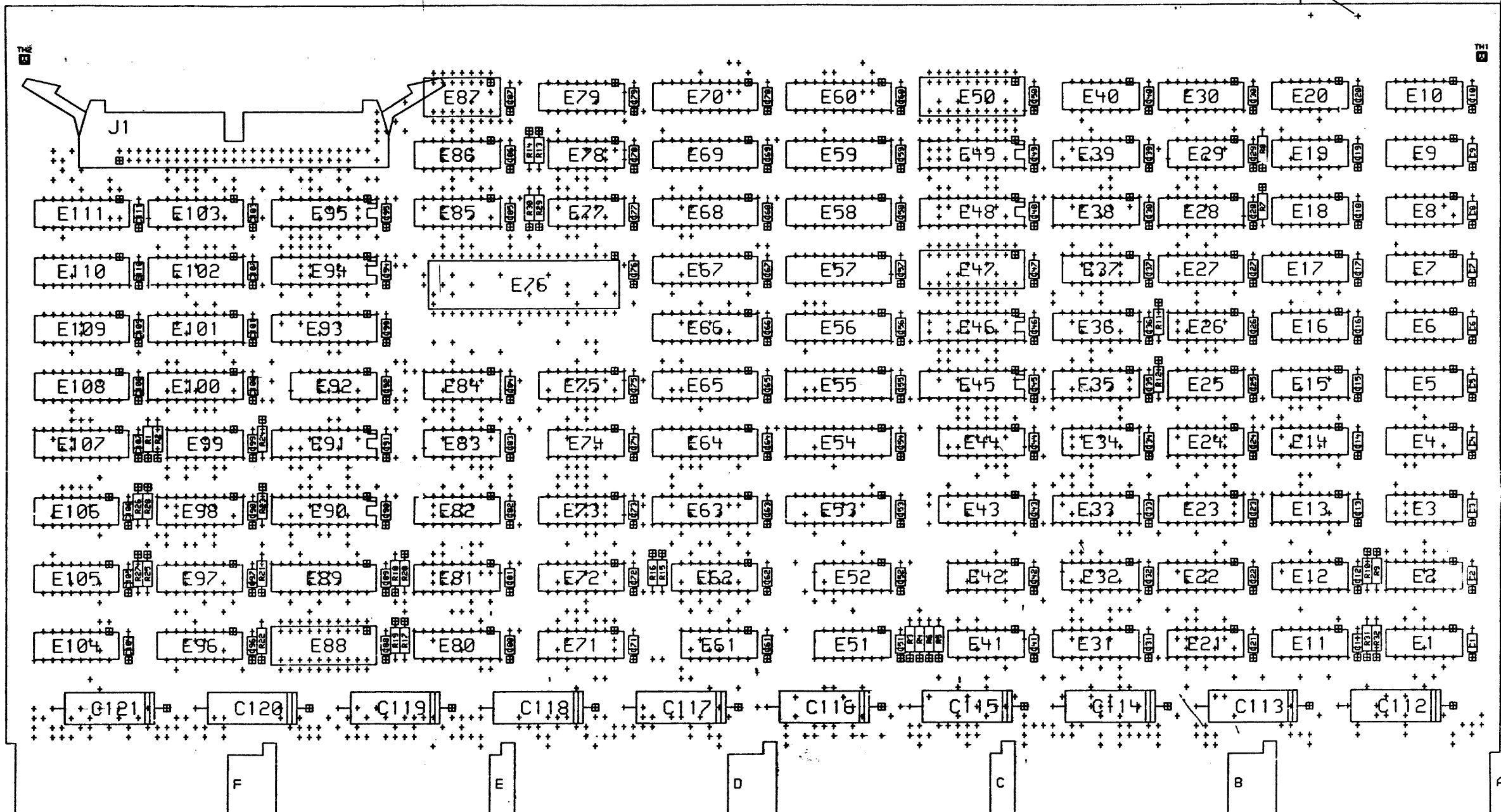
DRN. D. DELLORCO	DATE 19-FEB-89	ENG. E BLOOM	DATE 19-FEB-89
CHK'D. D. CALUNTER	DATE 19-FEB-89	BOARD LOCATION: N/A	
DSK: M30021.T2P(4,57)		NEXT HIGHER ASSEMBLY:	
FIRST USED ON OPTION/MODEL: CI20			

TITLE: DRAWING DIRECTORY	
M3002	
SIZE CODE D	DD
NUMBER M3002-0	REV. C

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0-0-M3002-0-0 2 1

36(QTY.12) COMPONENT SIDE VIEW 35



NOTES: SPARE COMPONENT LOCATIONS:
 E47, E50, E87, E88

THIS BOARD MUST MEET SAFETY
 REQ. FOR "HPWR"

STEP E + Y AXIS 0 STEP 0 TIMES
 REPEAT + X AXIS 0 STEP 0 TIMES

CHG	NO	REV	DATE	BY	CHK
1	1	1	2/28/84
2	1	1	2/28/84
3	1	1	2/28/84
4	1	1	2/28/84
5	1	1	2/28/84
6	1	1	2/28/84
7	1	1	2/28/84
8	1	1	2/28/84

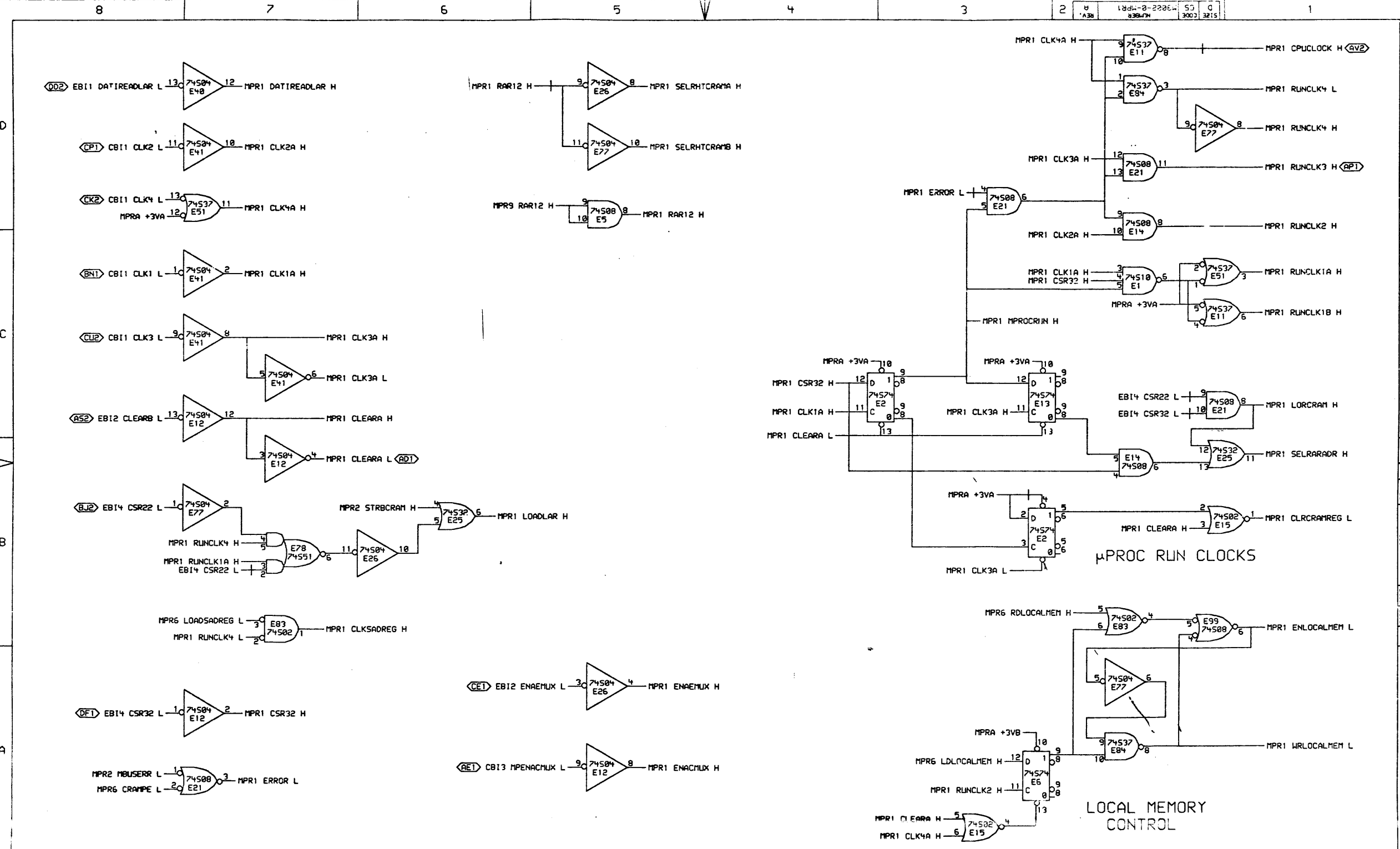
SIGNATURES	DATE
DRN. <i>[Signature]</i>	2/28/84
CHK'D. <i>[Signature]</i>	2/28/84
MECH. ENG. <i>[Signature]</i>	2/28/84
PROJ. ENG. <i>[Signature]</i>	2/28/84
PROD. <i>[Signature]</i>	2/28/84
SCALE 2/1	
SHT. 1 OF 1	
ETCH REV. C1	

SIGNATURES		DATE	digital
DRN. <i>[Signature]</i>		2/28/84	
CHK'D. <i>[Signature]</i>		2/28/84	TITLE MICRO PROCESSOR
MECH. ENG. <i>[Signature]</i>		2/28/84	
PROJ. ENG. <i>[Signature]</i>		2/28/84	SIZE CODE NUMBER 0 UA M3002-0-0 C
PROD. <i>[Signature]</i>		2/28/84	
SCALE 2/1			REV C
SHT. 1 OF 1			
TOP DOC NO: 0-00-M3002-0			

LINE ITEM	TCP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
1	D-MC-5015385-0-0	5015385-01	C	DRILL & ETCH DWG FOR M3002	1		
2		1001610-00		.01 MFD 50V +80-20% Z5U CER	76		C1-C10,C12-C50,C52,C61,C62,C71,C73-C75,C77-C84,C87,C90-C99,C104
3		1012784-00		.047 MFD 50V +80-20% CER	35	CONT	C11,C51,C53-C60,C63-C70,C72,C78,C85,C86,C88,C89,C100-C103,C105-C111
4		1012084-03		150 MFD 15V +75-10% AL EL	10	CONT	C112-C121
5		1910536-B0		74S10 BURNED-IN NAND GATE-	1		E1
6		1910544-B0		74S74 BURNED-IN FF-D DUAL,	3		E2,E6,E13
7		1910532-B0		74S00 BURNED-IN NAND GATE-	1		E3
8		1910534-B0		74S04 BURNED-IN INVERTER G	8		E4,E7,E12,E20,E26,E40,E41,E77
9		1912389-B0		74S08 BURNED-IN AND GATE,Q	4		E5,E14,E21,E99
10		1910542-B0		74S64 BURNED-IN A-O-I GATE	4		E8,E9,E18,E19
11		1910539-B0		74S20 BURNED-IN NAND GATE-	1		E10
12		1912746-B0		74S37 BURNED-IN NAND GATE-	3		E11,E51,E84
13		1912388-B0		74S02 BURNED-IN NCR GATE-Q	3		E15,E52,E83
14		1910537-B0		74S11 BURNED-IN AND GATE-T	1		E16
15		1911675-B0		74S138 BURNED-IN DECODER/DE	1		E17
16		1910956-00		74S151 MUX 1 OF 8	2		E22,E31
17		1912697-00		LS174 FF-D HEX W/CLEAR	3		E23,E98,E106
18		1911573-B0		74S280 BURNED-IN PARITY GEN	8		E24,E29,E32,E34,E37,E42,E61,E74
19		1913340-B0		74S32 BURNED-IN OR GATE,QU	1		E25
20		1910550-B0		74S174 BURNED-IN FF-D HEX	11	CONT	E27,E28,E33,E35,E38,E43,E62,E72,E73,E75,E95
21		1911641-B0		74S257 BURNED-IN MUX,QUAD 2	8		E30,E36,E39,E44,E81,E82,E85,E86
22		1913493-B0		74S241 BURNED-IN OCTAL BUFF	8		E45,E46,E48,E49,E90,E91,E94,E95
23		2119250-00		4KX4 STATIC RAM 55NS	16		E53-E60,E63-E70
24		1910548-B0		74S157 BURNED-IN MUX 1 OF 2	5		E71,E80,E97,E104,E105
25		1916358-00		CONTROLLER,MICROPROG	1		E76
26		1911712-B0		74S51 BURNED-IN AND/OR GAT	1		E78

REVISION HISTORY		BASIC PART NO: M3002		DRN: D,DELLORCO		DATE: 5-APR-83		D I G I T A L	
ENG!	ECC NUMBER	REV	SECTION A OF A	CHK'D:	R.W.CAUNTER	DATE:	7-APR-83	TITLE	PARTS LIST
	INITIAL	A	SECTION.VARIATION INDEX					M3002	
JT	M3002-MR001	B	[A] 00					MICRO PROCESSOR	
EB	M3002-MR002	C	[B]						
			[C]	DES.ENG:	R.CARN	DATE:	19-APR-84	DOCUMENT NUMBER	
			[D]					SIZE!CODE! NUMBER	REV
			[E]						
			[F]	RESP.ENG.:	R.CARN	DATE:	19-APR-84	K ! PL ! M3002-0-DBP	C
			[H]						
			[J]						
			[K]	MFG.ENG.:	R.CARN	DATE:	19-APR-84	RELEASE DATE:	18-FEB-85
			[L]						
			[M]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	EDIT #
			[N]	D-UA-M3002-0-0		!8D-DD-M3002-0		! Z8532C.PLS	9

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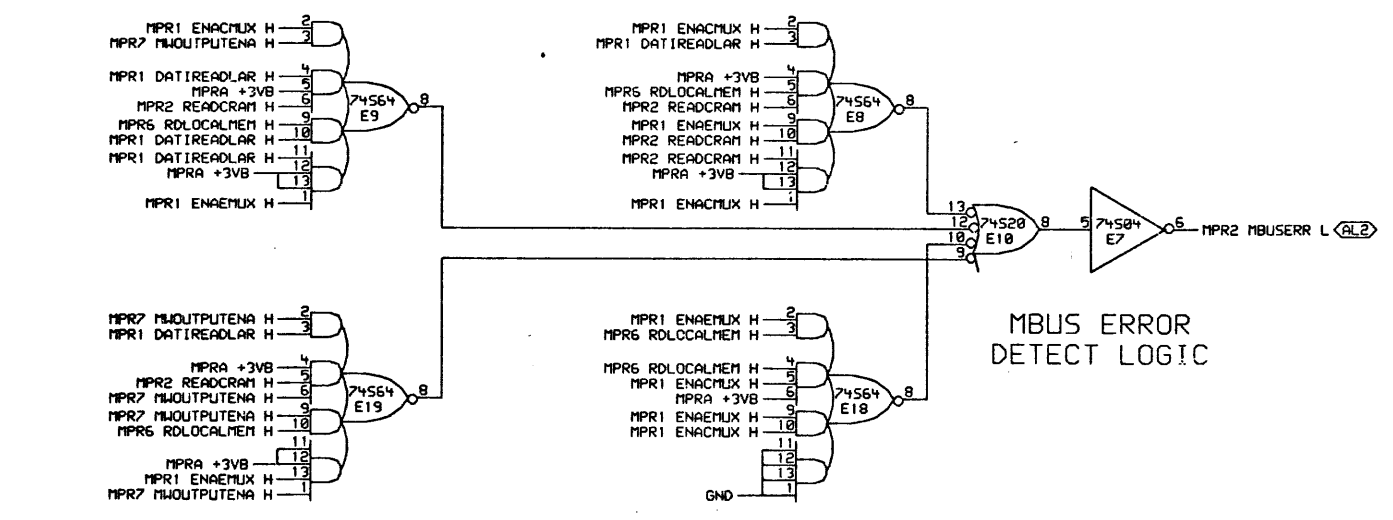
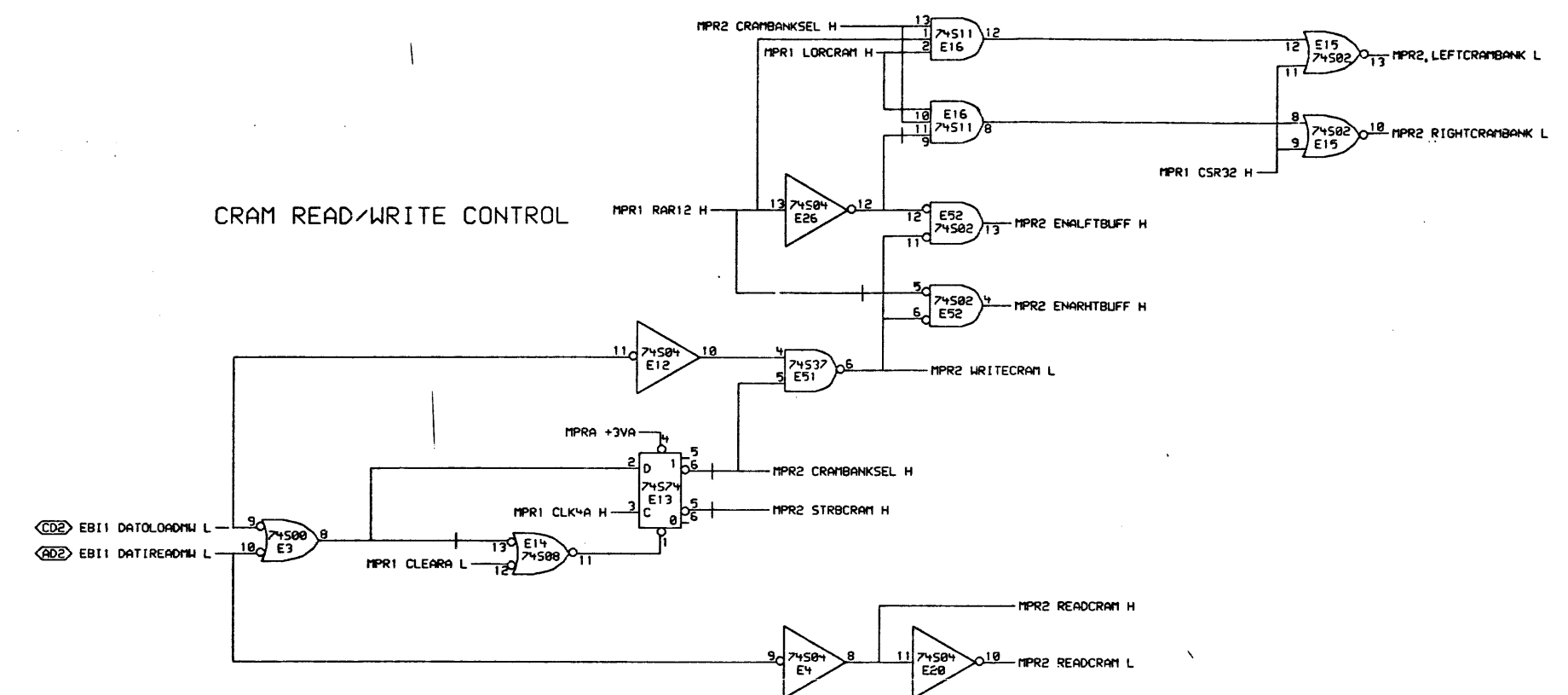


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REVISIONS
CHK CHANGE NO. REV.

DRW: <i>J. J. J.</i>	DATE: 04-APR-84	ENG: <i>Plan</i>	DATE: 04-APR-84	TITLE: CI20 MPROC CONT CTRL LOGIC 1
CHK'D: <i>N. Williams</i>	DATE: <i>04-APR-84</i>	BOARD LOCATION: <i>1</i>	SHEET: <i>1</i>	
SUBCOM: BOWEN, ECO, MPR100, DRW 29-MAR-84 08:51 NEXT HIGHER ASSEMBLY:				SIZE CODE: <i>D CS</i>
FIRST USED ON OPTION/MODEL: <i>CI20</i>				NUMBER: <i>M3002-0-MPR1</i>

REV. A	CS	D	53	0
M/3002-0-MPR1				



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REVISIONS		
CHK	CHANGE NO.	REV

DRW. <i>J. J. J.</i>	DATE 01-APR-84	ENG. <i>R. P.</i>	DATE 01-APR-84	TITLE: CI20 MPROC CONT CTRL LOGIC 2
CHK'D. <i>D. Nelson</i>	DATE 10-APR-84	BOARD LOCATION: SHEET 1 OF 1	NEXT HIGHER ASSEMBLY: D-DD-M3002-0	SIZE CODE NUMBER REV. D CS M3002-0-MPR2 A
SUBCOM: <BCHEN.ECO>MPR200.DRW 29-MAR-84 08:51				FIRST USED ON OPTION/MODEL: CI20

8	7	6	5	4	3	2	1	MA
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EV. 4
 BOARD LOCATION: SHEET 1 OF 1
 SIZE CODE NUMBER REV. D CS M3002-0-MPR2 A

D

C

B

A

D

C

REV. A

NUMBER

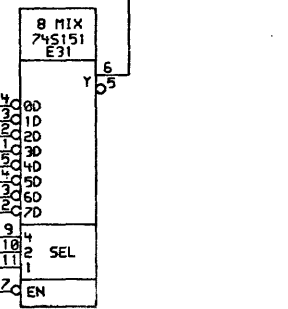
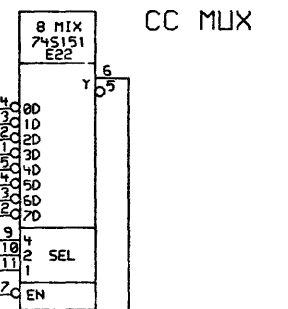
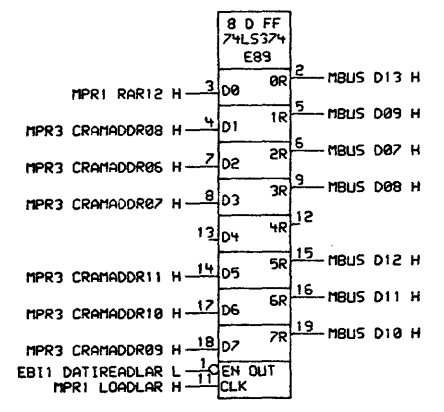
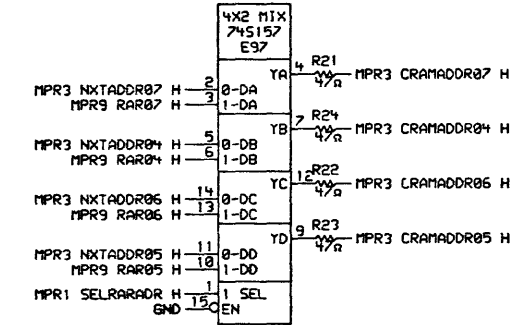
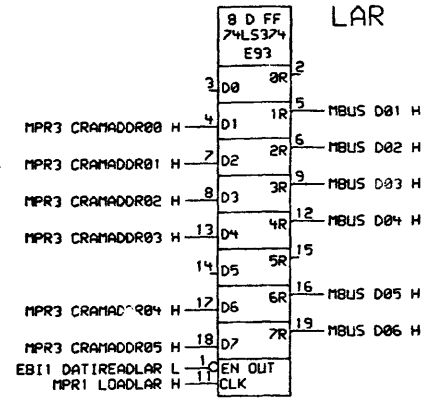
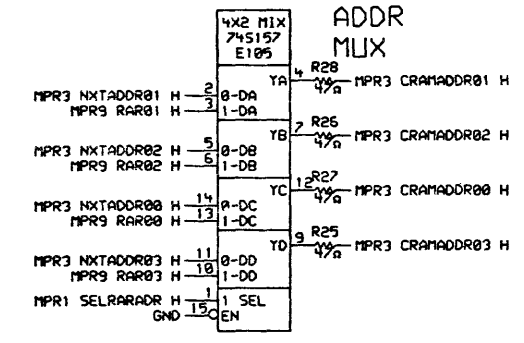
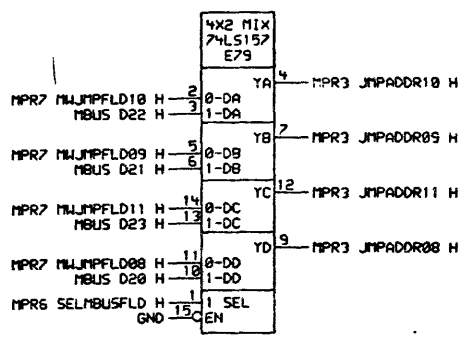
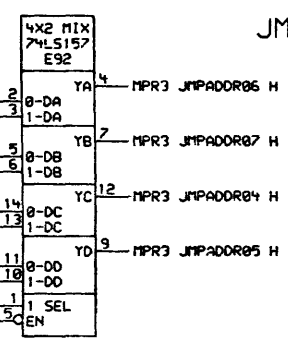
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SIZE

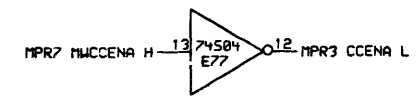
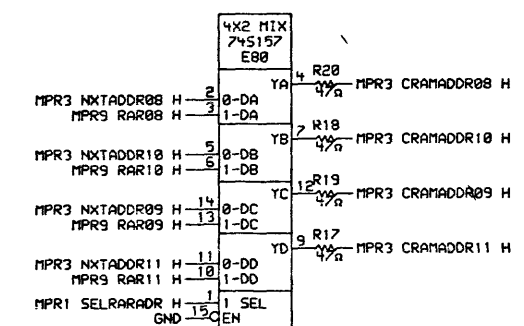
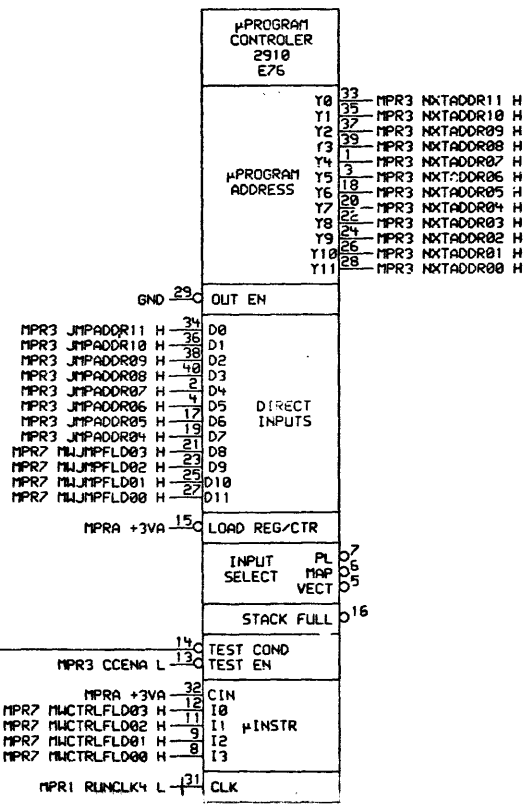
D C5

B

A



MICROSEQR



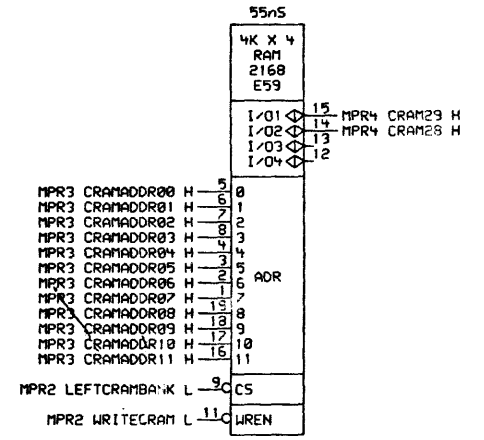
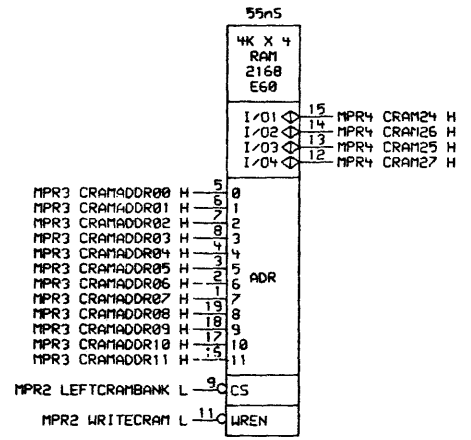
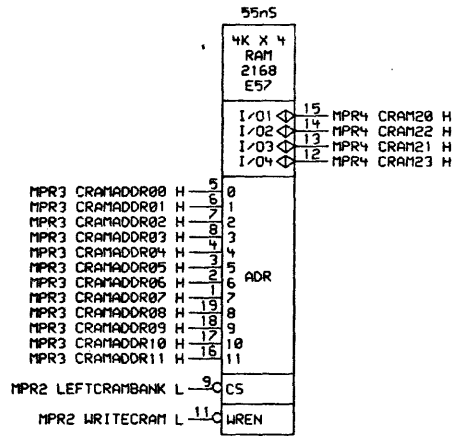
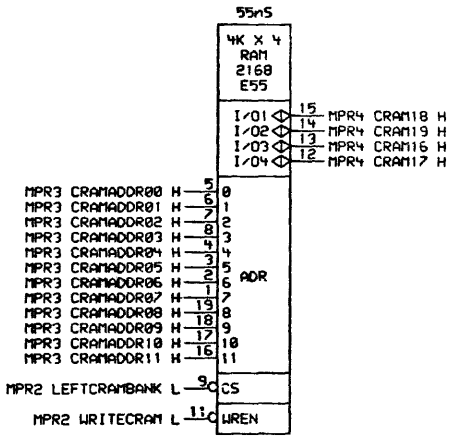
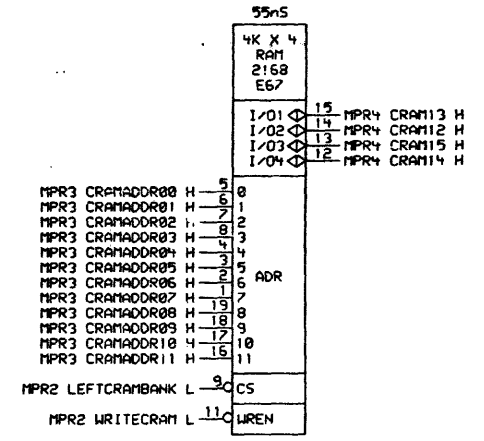
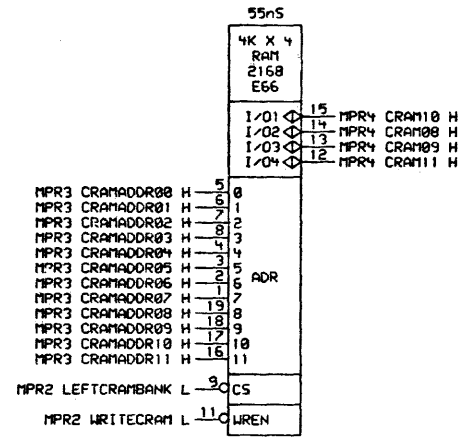
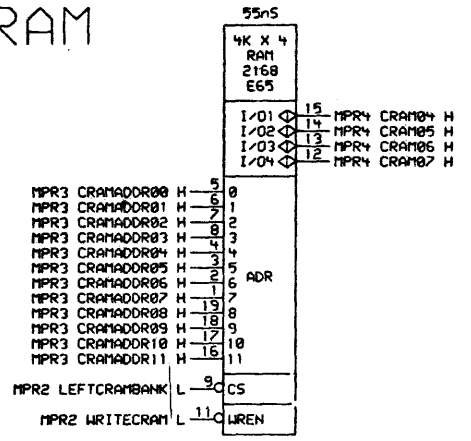
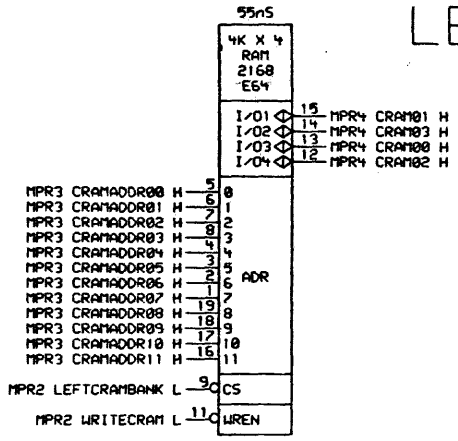
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REVISIONS	
CHK	CHANGE NO. REV

	DRN: <i>J. L. L...</i> CHK'D: <i>J. L. L...</i>	DATE: 04-APR-84 DATE: 04-APR-84	ENG: <i>R. L...</i> DATE: 04-APR-84	TITLE: C120 MPROC CONT SEQR & RAM ADDR	
	SUBCON: CBUEN.ECO:MPR300.DRW:129-MAR-84 08:52 NEXT HIGHER ASSEMBLY:	SHEET: 1 OF 1	SIZE: D	CODE: C5	NUMBER: M3002-0-MPR3

FIRST USED ON OPTION/MODEL: C120 D-DD-M3002-0

LEFT CRAM



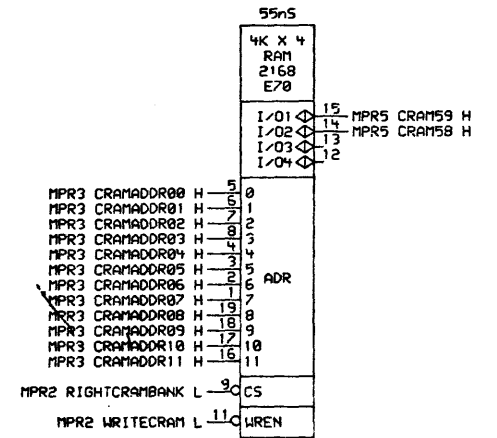
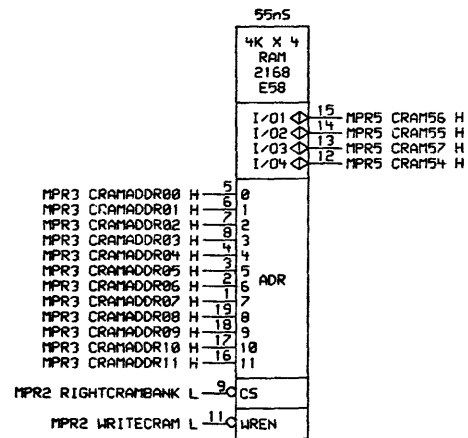
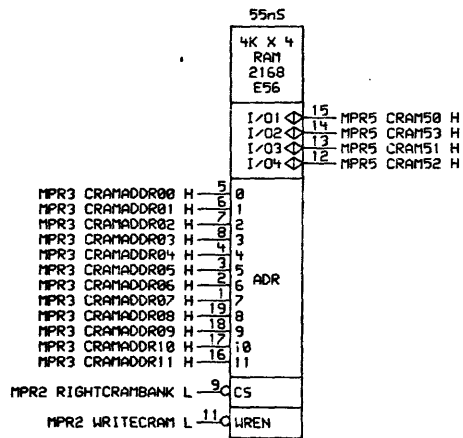
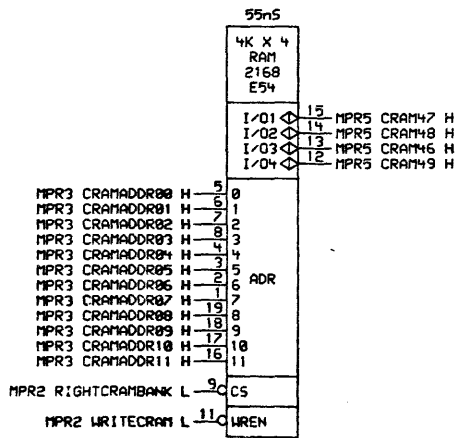
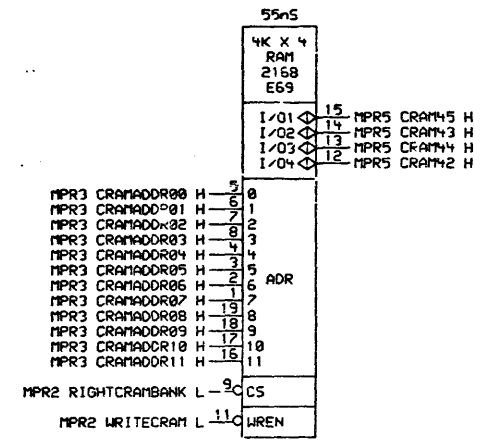
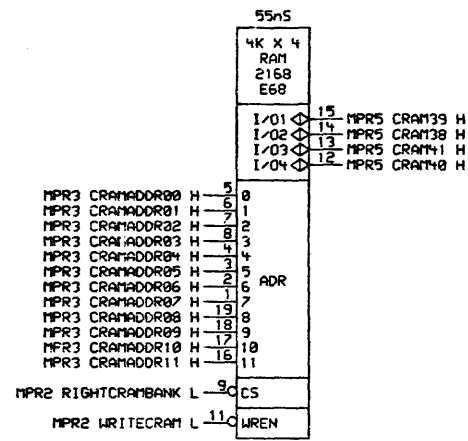
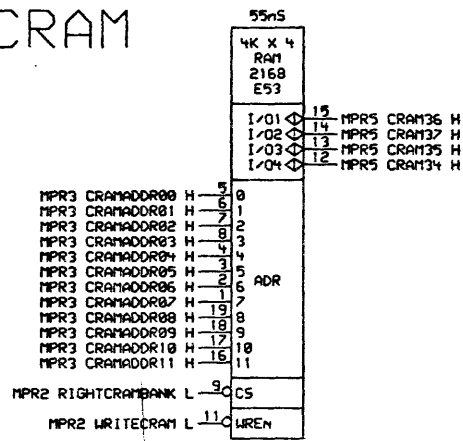
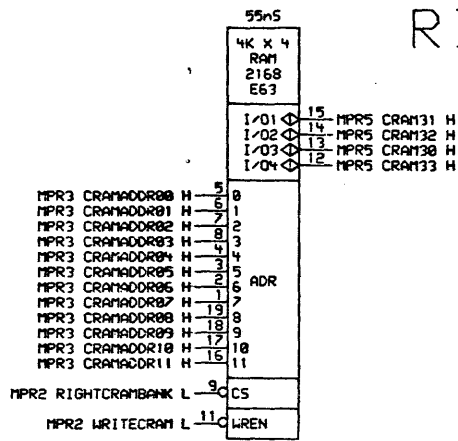
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REVISIONS		
CHK	CHANGE NO.	REV

digit@l *St. Jansky*
 DATE: 04-APR-84 ENG: R. Bann
 DATE: 04-APR-84 BOARD LOCATION:
 A-APR84 SHEET 1 OF 1
 SUDCOM: BOHEN ECO MPR-00 ORJ 29-MAR-84 08:52 NEXT HIGHER ASSEMBLY:
 FIRST USED ON OPTION MODEL: CI20 D-DD-M3002-0

TITLE: CI20 MPROC CONT		REV. A	
CRAM RAM 1			
SIZE CODE	NUMBER	REV.	
D CS	M3002-0-MPR4	A	

RIGHT CRAM

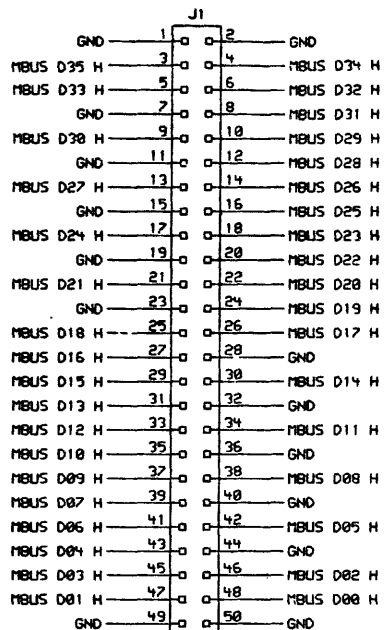
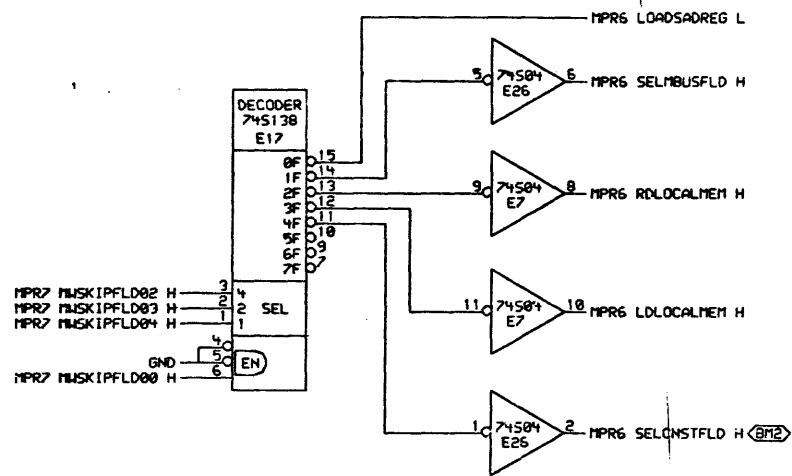


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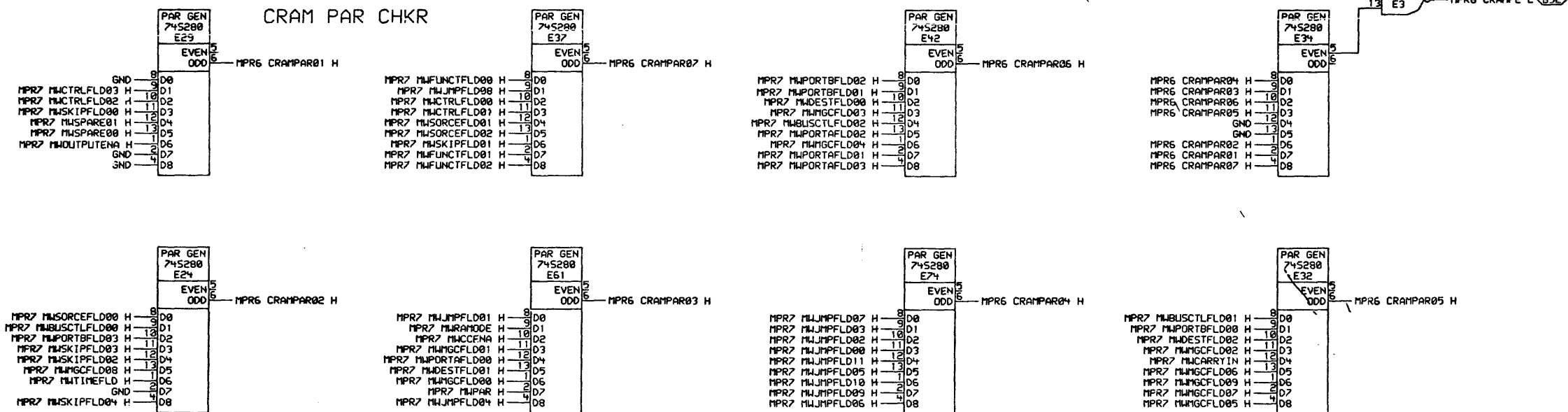
REVISIONS	
CHK	CHANGE NO. REV

	DRN: <i>J. J. J.</i>	DATE: 01-APR-84	ENG: <i>P. Bow</i>	DATE: 01-APR-84	TITLE: C120 MPROC CONT
	CHK'D: <i>W. J. J.</i>	DATE: 12-APR-84	BOARD LOCATION: 1	SHEET: 1	OF: 1
	SUBCOM: BOWEN.ECD:MPR500.ORG:129-MAR-84 08:52	NEXT HIGHER ASSEMBLY: D-DD-M3002-0		SIZE CODE: D CS	NUMBER: M3002-0-MPR5

COND/SKIP DECODER



CRAM PAR CHKR



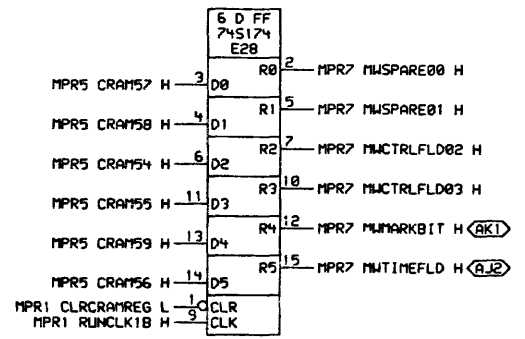
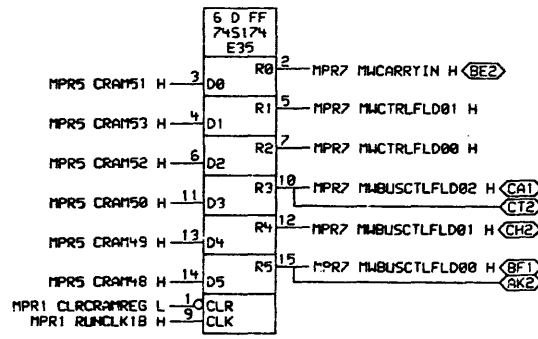
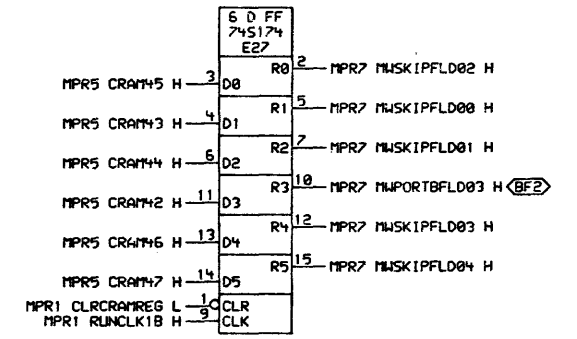
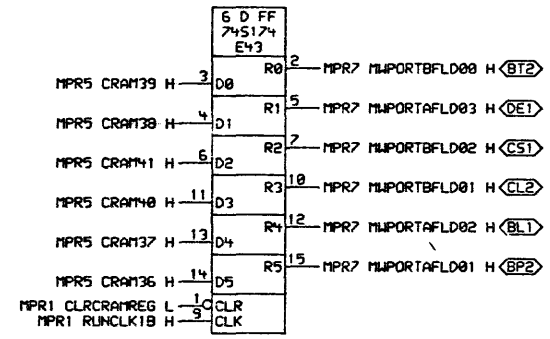
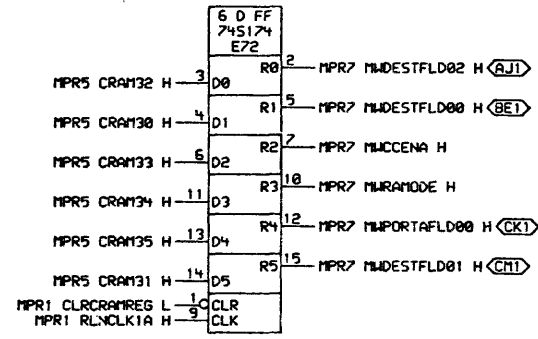
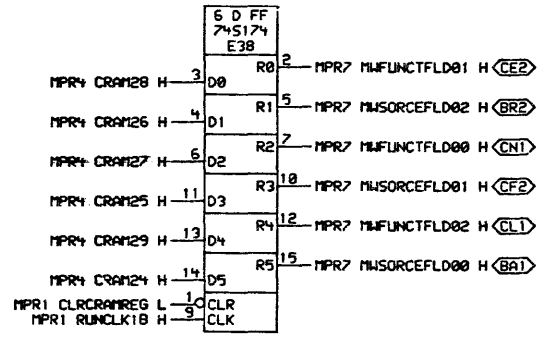
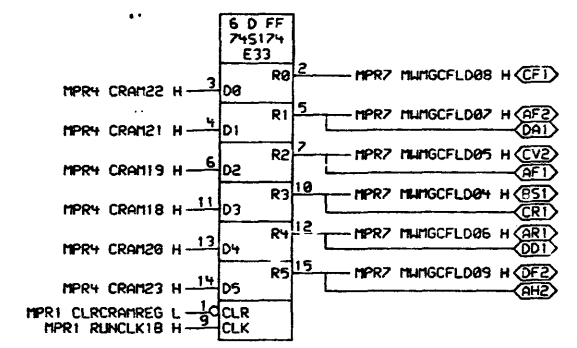
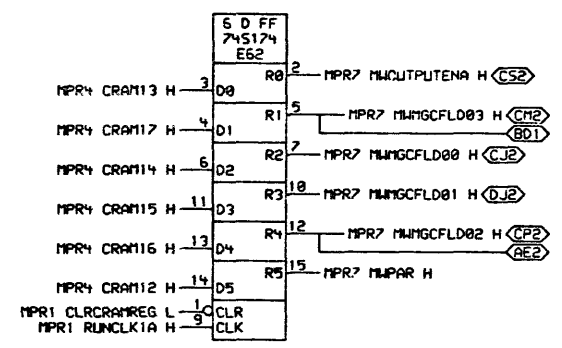
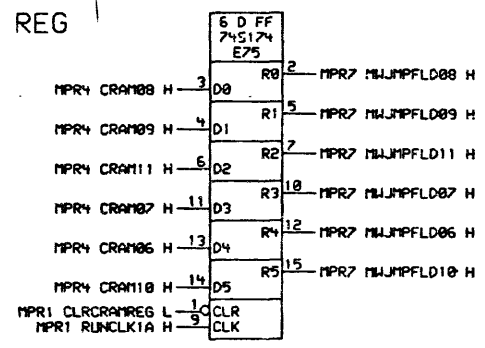
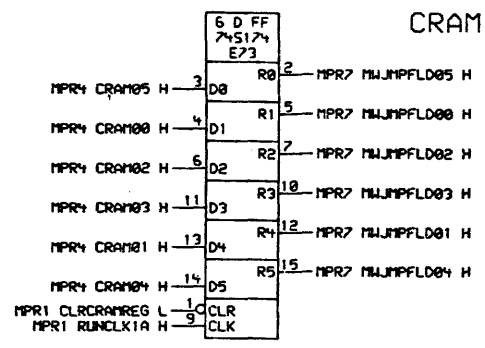
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. J. J. J.	DATE 01-APR-84	ENG. E. B.	DATE 01-APR-84
	CHK'D. U. S.	DATE 14 APR 84	BOARD LOCATION:	OF 1
SUDCOM: (BOMEN, ECO) MPR600.DRW 29-MAR-84 08:53 NEXT HIGHER ASSEMBLY:				
FIRST USED ON OPTION/MODEL: CI20 D-DD-M3002-0				

TITLE: CI20 MPROC CONT CRAM PARITY			
SIZE	CODE	NUMBER	REV.
D	CS	M3002-0-MPR6	A

CRAM CTRL REG



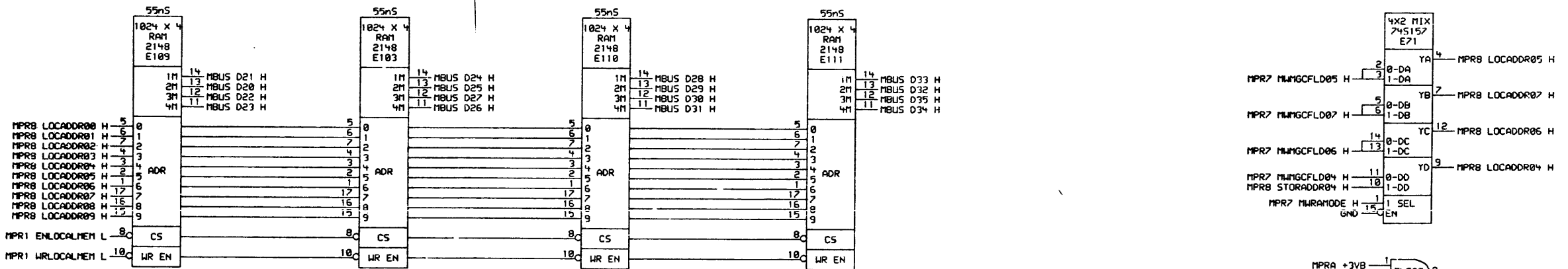
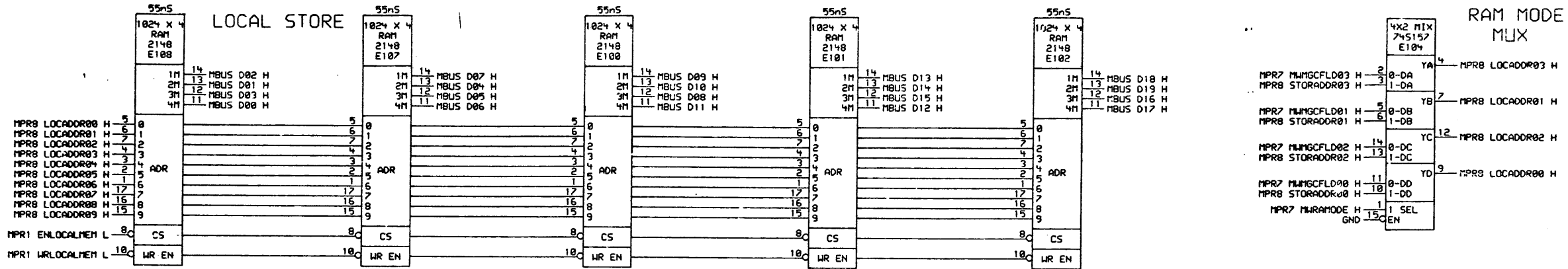
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REVISIONS	CHK	CHANGE NO.	REV.

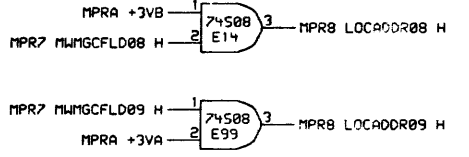
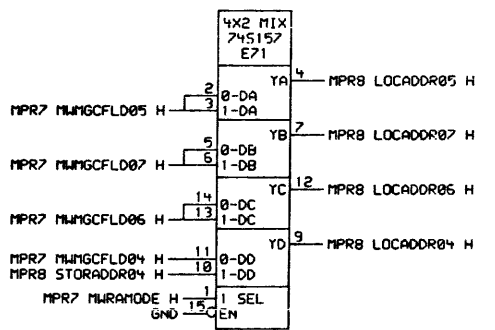
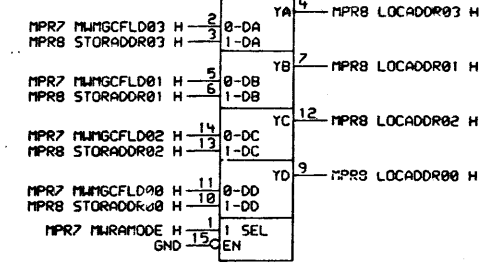
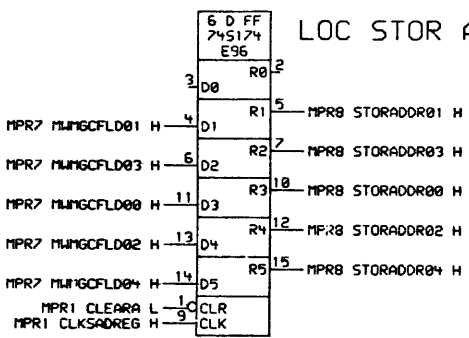
digital
DATE 01-APR-84
ENG R.P.W.
DATE 01-APR-84
TITLE: C120 MPROC CONT
CRAM REG
SHEET 1 OF 1
SIZE CODE D CS
NUMBER M3002-0-MPR7
REV. A

LOCAL STORE

RAM MODE MUX



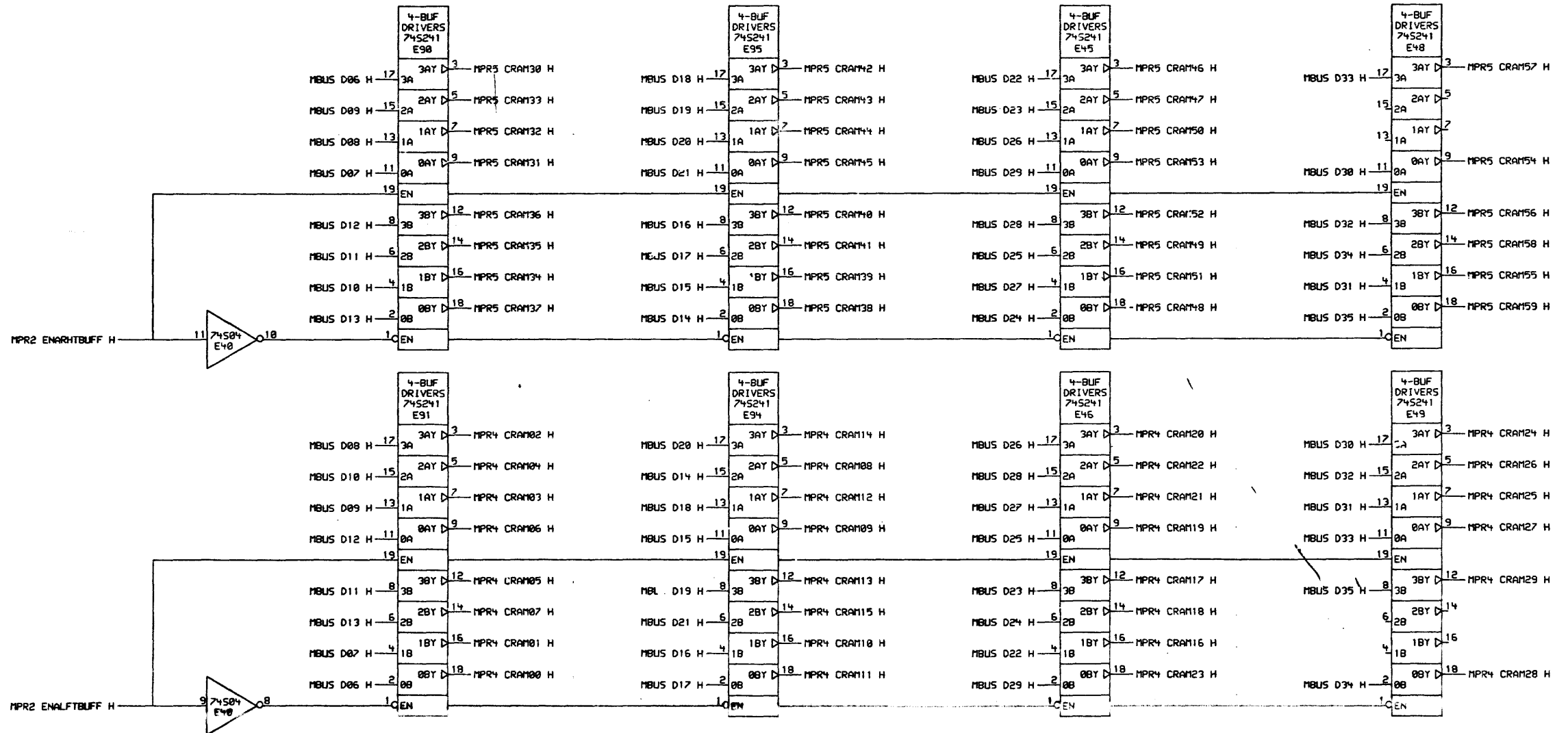
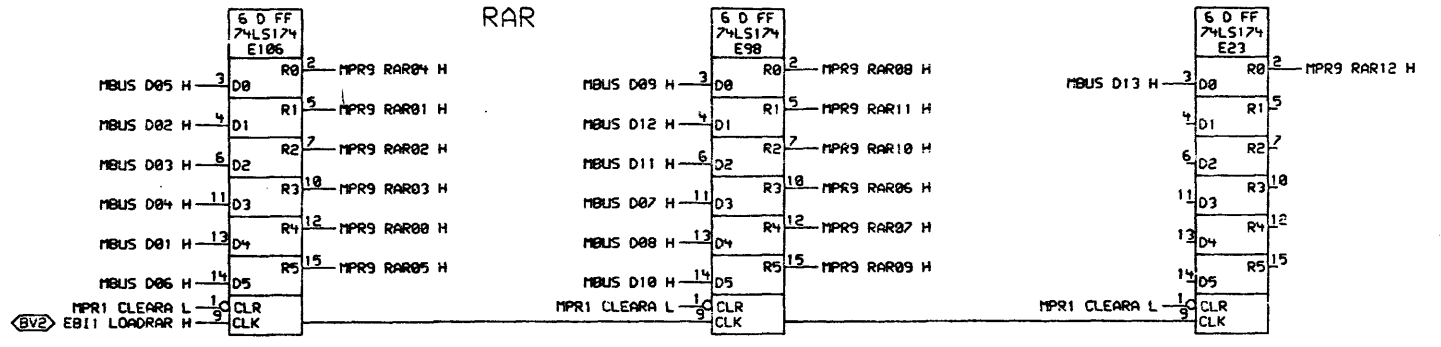
LOC STOR ADDR REG



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CHK	CHANGE NO.	REV

digital *Johny* DATE 04-APR-84 ENG. R. Low DATE 04-APR-84 TITLE: C120 MPROC CONT LOCAL STORAGE
 DATE 12-APR-84 BOARD LOCATION: SHEET 1 OF 1
 SUDCON180-EN-ECO: MPR800.DRW 129-MAR-84 09:53 NEXT HIGHER ASSEMBLY: D-DD-M3002-0
 FIRST USED ON OPTION/MODEL: C120 SIZE CODE NUMBER REV. D CS M3002-0-MPR8 A



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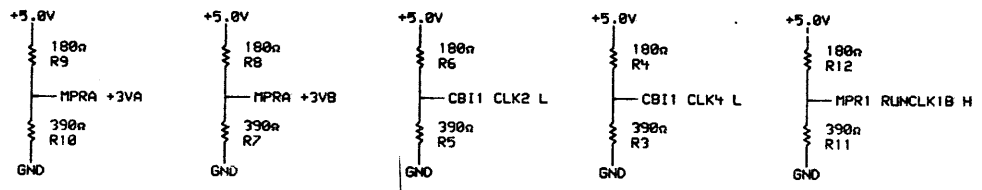
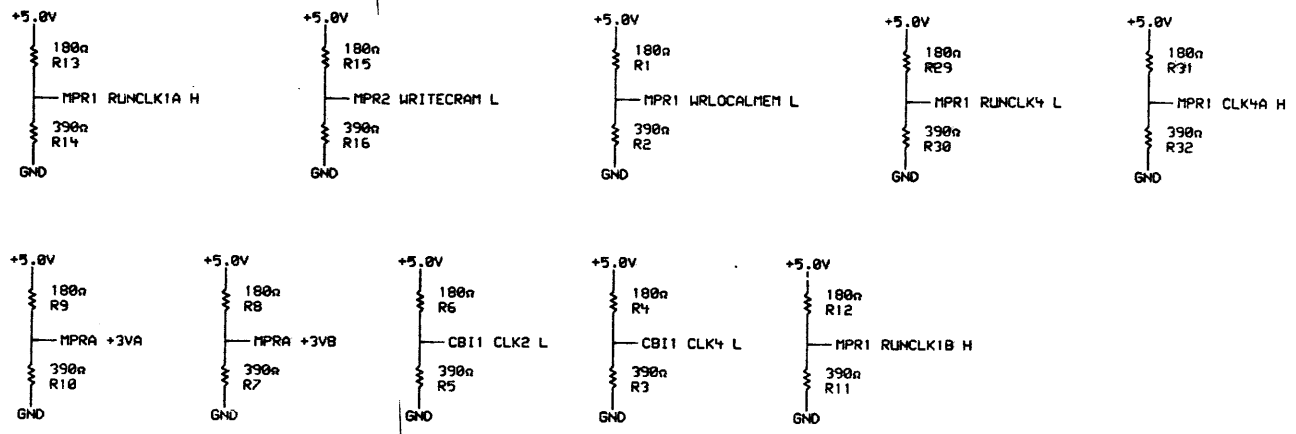
REVISIONS	
CHK	CHANGE NO. / REV

digital *John* *Re*

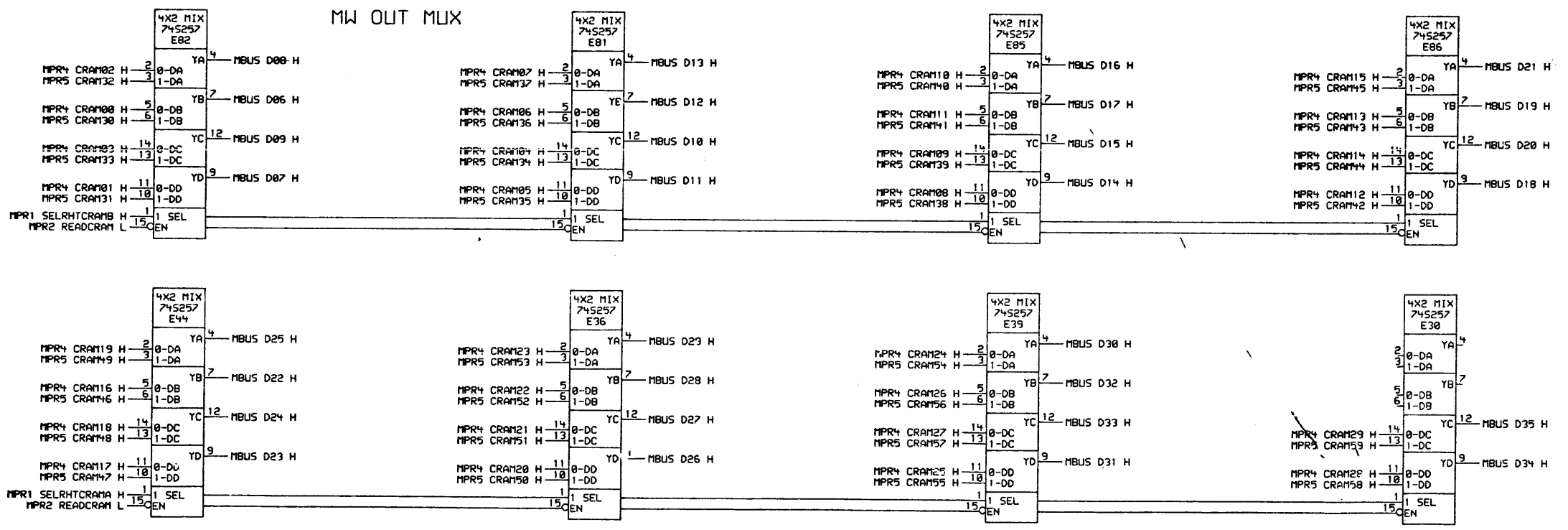
DATE: 04-APR-84
 DATE: 17-APR-84
 BOARD LOCATION: 1 OF 1
 SHEET: 1 OF 1

SUDDON: C:\BOWEN.ECD\M3002.DWG [29-MAR-84 08:53] NEXT HIGHER ASSEMBLY: 1
 FIRST USED ON OPTION/MODEL: CI20 D-DD-M3002-0

TITLE: CI20 MPROC CONT CRAM LOAD 1	SIZE: D	CODE: C5	NUMBER: M3002-0-MPR9	REV: A
------------------------------------	---------	----------	----------------------	--------



MW OUT MUX



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REVISIONS	
CHK	CHANGE NO. REV.

digital DRN: J. Jambly DATE: 01-APR-84 ENG: R. P. DATE: 01-APR-84 TITLE: CI20 MPROC CONT CRAM LOAD 2

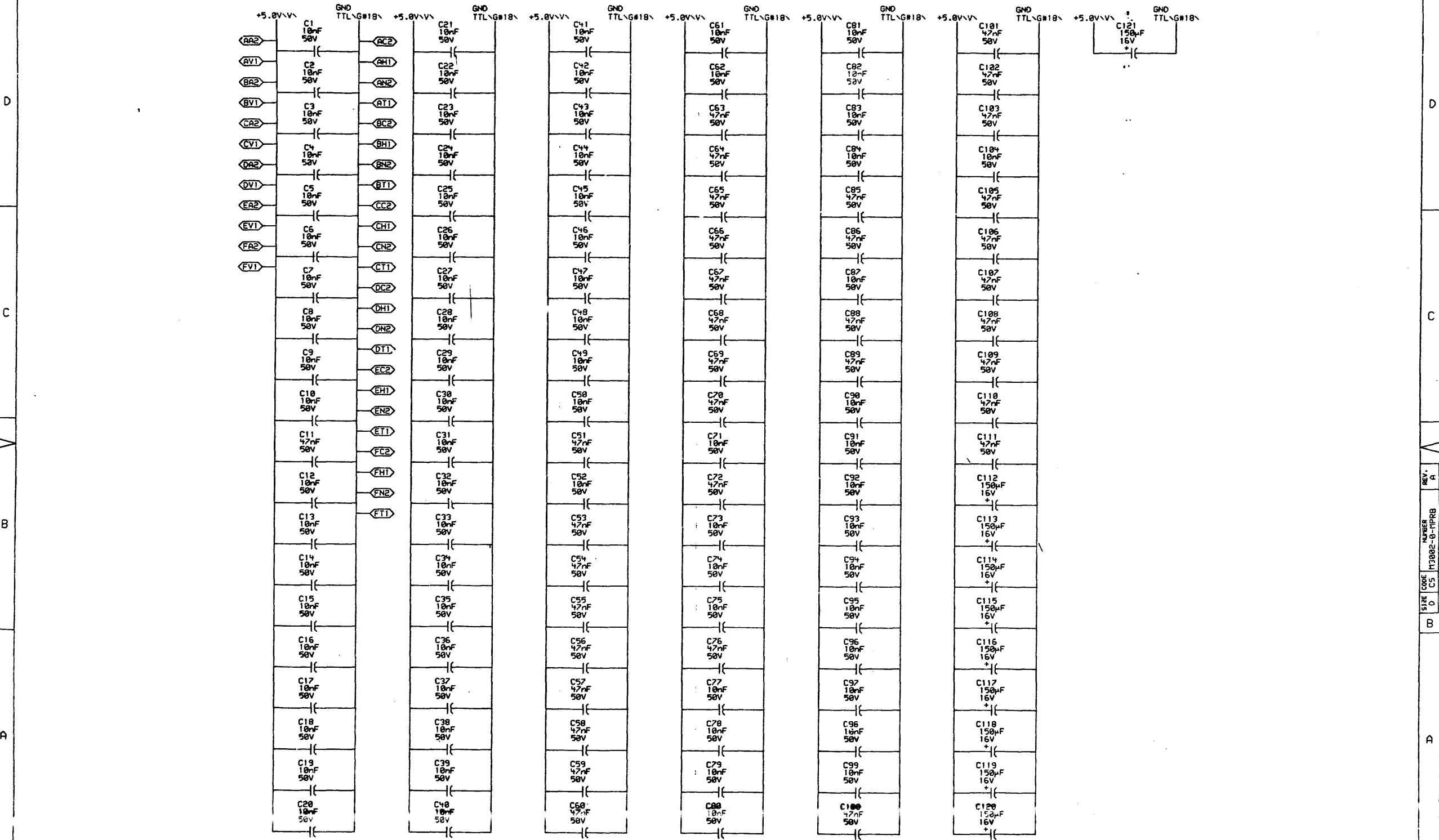
CHK'D: AS DATE: 17-APR-84 BOARD LOCATION: SHEET OF

SUCCOR: C:\BOLLEN.ECD\MPRAB0.DRW [29-MAR-84] 08:53 NEXT HIGHER ASSEMBLY: D-DD-M3002-0

FIRST USED ON OPTION/MODEL: CI20

SIZE	CODE	NUMBER	REV.
D	CS	M3002-0-MPRA	A

REV. 13
NUMBER M3002-0-MPRA
CODE CS
SIZE D
B



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REVISIONS	
CHK	CHANGE NO. REV

DATE: 04-APR-84	ENG: R. P. [Signature]	DATE: 01-APR-84	TITLE: C120 MPROC CONT PWR & GND
DATE: 19-APR-84	BOARD LOCATION: 10	SHEET: 1	OF: 1
SUCOML(BOWEN.ECO)MPC800.DRW 29-MAR-84 08:53		NEXT HIGHER ASSEMBLY: D-DD-M3002-0	
FIRST USED ON OPTION/MODEL: C120		SIZE CODE: D CS	NUMBER: M3002-0-MPRB

REV. A	REV. A
--------	--------

AA1 N/C SEE NOTE 1
 AB1 N/C SEE NOTE 3
 AB2 N/C -5.2V
 AC1 N/C EXTRA GND
 AL1 N/C -5.2V
 AL2 N/C SEE NOTE 1
 BB1 N/C SEE NOTE 3
 BB2 N/C SEE NOTE 3
 BC1 N/C EXTRA GND
 BH2 N/C SEE NOTE 1
 BK1 N/C SEE NOTE 1
 BK2 N/C SEE NOTE 1
 BR1 N/C SEE NOTE 1
 BL1 N/C SEE NOTE 3
 CB1 N/C SEE NOTE 3
 CB2 N/C SEE NOTE 3
 CC1 N/C EXTRA GND
 CJ1 N/C SEE NOTE 1
 CR2 N/C SEE NOTE 1
 CL1 N/C SEE NOTE 3
 DB1 N/C SEE NOTE 3
 DB2 N/C SEE NOTE 3
 DC1 N/C EXTRA GND
 DH2 N/C SEE NOTE 1
 DJ1 N/C SEE NOTE 1
 DK1 N/C SEE NOTE 2

DK2 N/C SEE NOTE 2
 DL1 N/C SEE NOTE 2
 DL2 N/C SEE NOTE 2
 DM1 N/C SEE NOTE 2
 DM2 N/C SEE NOTE 2
 DN1 N/C SEE NOTE 2
 DP1 N/C SEE NOTE 2
 DP2 N/C SEE NOTE 2
 DR1 N/C SEE NOTE 2
 DR2 N/C SEE NOTE 2
 DS1 N/C SEE NOTE 2
 DS2 N/C SEE NOTE 2
 DT2 N/C SEE NOTE 2
 DU1 N/C SEE NOTE 3
 DU2 N/C SEE NOTE 2
 DV2 N/C SEE NOTE 2
 EA1 N/C SEE NOTE 2
 EB1 N/C SEE NOTE 3
 EB2 N/C SEE NOTE 3
 EC1 N/C SEE NOTE 2
 ED1 N/C SEE NOTE 2
 ED2 N/C SEE NOTE 2
 EE1 N/C SEE NOTE 2
 EE2 N/C SEE NOTE 2
 EF1 N/C SEE NOTE 2
 EF2 N/C SEE NOTE 2
 EH2 N/C SEE NOTE 2

EJ1 N/C SEE NOTE 2
 EJ2 N/C SEE NOTE 2
 EK1 N/C SEE NOTE 2
 EK2 N/C SEE NOTE 2
 EL1 N/C SEE NOTE 2
 EL2 N/C SEE NOTE 2
 EM1 N/C SEE NOTE 2
 EM2 N/C SEE NOTE 2
 EN1 N/C SEE NOTE 2
 EP1 N/C SEE NOTE 2
 EP2 N/C SEE NOTE 2
 ER1 N/C SEE NOTE 2
 ER2 N/C SEE NOTE 2
 ES1 N/C SEE NOTE 2
 ES2 N/C SEE NOTE 2
 ET2 N/C SEE NOTE 2
 EU1 N/C SEE NOTE 3
 EU2 N/C SEE NOTE 2
 EV2 N/C SEE NOTE 2
 FA1 N/C SEE NOTE 2
 FB1 N/C SEE NOTE 3
 FB2 N/C -5.2V
 FC1 N/C SEE NOTE 2
 FD1 N/C SEE NOTE 2
 FD2 N/C SEE NOTE 2
 FE1 N/C SEE NOTE 2
 FE2 N/C SEE NOTE 2

FF1 N/C SEE NOTE 2
 FF2 N/C SEE NOTE 2
 FH2 N/C SEE NOTE 2
 FJ1 N/C SEE NOTE 2
 FJ2 N/C SEE NOTE 2
 FK1 N/C SEE NOTE 2
 FK2 N/C SEE NOTE 2
 FL1 N/C SEE NOTE 2
 FL2 N/C SEE NOTE 2
 FM1 N/C SEE NOTE 2
 FM2 N/C SEE NOTE 2
 FN1 N/C SEE NOTE 2
 FP1 N/C SEE NOTE 2
 FP2 N/C SEE NOTE 2
 FR1 N/C SEE NOTE 2
 FR2 N/C SEE NOTE 2
 FS1 N/C SEE NOTE 2
 FS2 N/C SEE NOTE 2
 FT2 N/C SEE NOTE 2
 FU1 N/C -5.2V
 FL2 N/C SEE NOTE 2
 FV2 N/C SEE NOTE 2

NOTE 1: THESE PINS ARE CONNECTED AS SHOWN BELOW

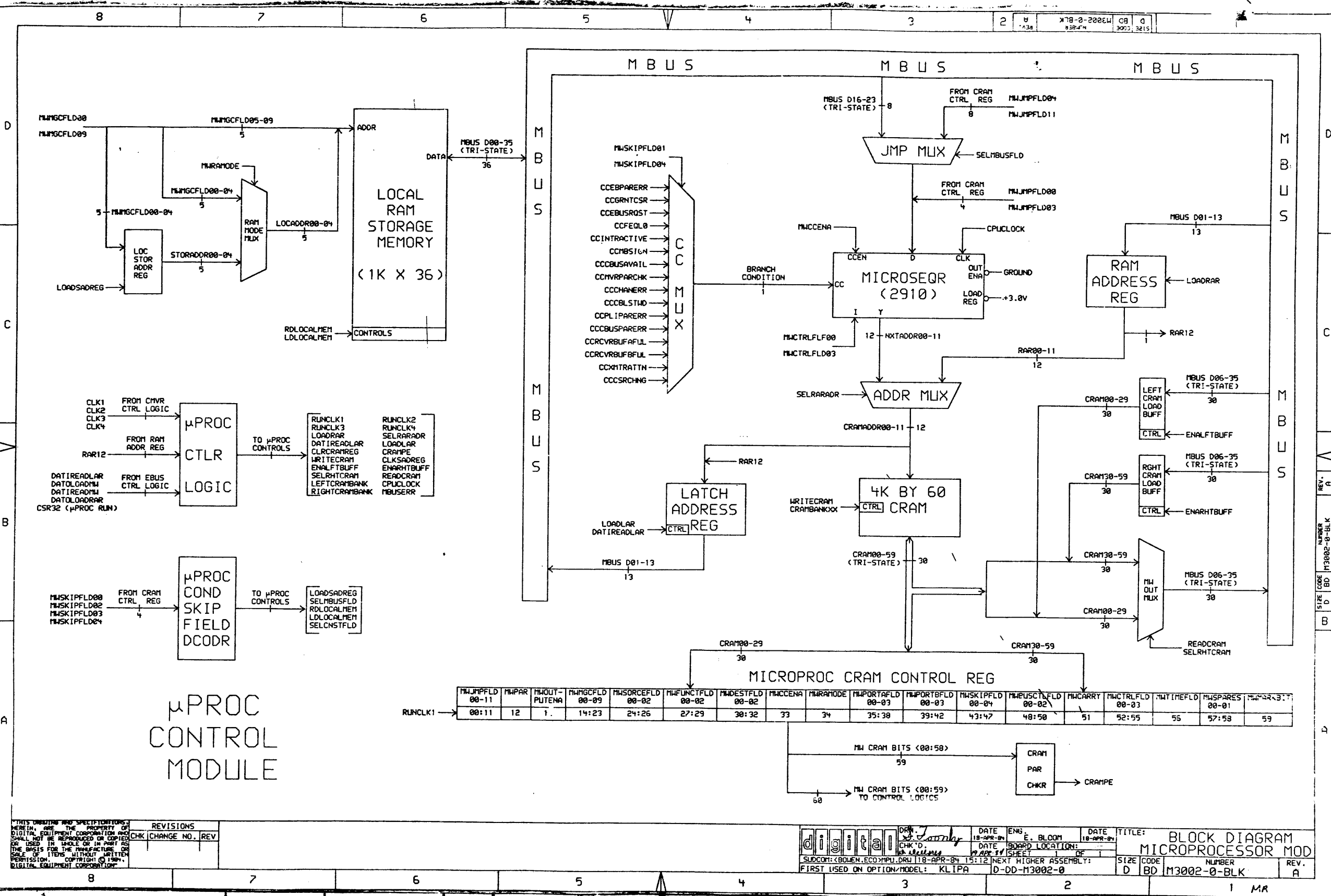
FROM	TO
C13D1	A14A1
C13E1	A14U2
F13R1	B14H2
F13M1	B14K1
D13S2	B14K2
E13L2	B14R1
E13D2	C14J1
C13F1	C14R2
C13K1	D14H2
F13K1	D14J1

NOTE 2: RSVD FOR MASS BUS
 NOTE 3: RSVD FOR -5.2V

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REVISIONS	
CHK	CHANGE NO. REV

digit@l	DATE	ENG. R. Low	DATE	TITLE: C120 MPROC CONT UNUSED FINGERS
	CHK'D. NO. 11/11/84	DATE	BOARD LOCATION:	
SUBCOM: C:\BOLLEN.ECO\MPROC00.DRW	DATE	19 APR 84	SHEET	OF 1
FIRST USED ON OPTION/MODEL: C120	DATE	29-MAR-84 08:54	NEXT HIGHER ASSEMBLY:	D-DD-M3002-0
SIZE	CODE	NUMBER	REV.	
D	CS	M3002-0-MPRC	A	



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REVISIONS	
CHK	CHANGE NO. REV

REV. A
 NUMBER 113002-0-BLK
 SIZE D
 CODE BD

DRAWING NUMBER	PAGE	PART NO.	DESCRIPTION	REVISIONS
			ECO NUMBER	- M3003 MR001
		M3003-00	MODULE REVISION	A1 A1
D-UA-M3003-0-0	2		CBUS	A A1
K-PL-M3003-0-DBP	2		PARTS LIST, M3003	A A1
D-CS-M3003-0-CB11	1		CI20 CBUS INTFC CBUS CONTROL 1	A A
D-CS-M3003-0-CB12	1		CI20 CBUS INTFC CBUS CONTROL 2	A A
D-CS-M3003-0-CB13	1		CI20 CBUS INTFC MVR CONTROL 1	A A
D-CS-M3003-0-CB14	1		CI20 CBUS INTFC MVR CONTROL 2	A A
D-CS-M3003-0-CB15	1		CI20 CBUS INTFC CBUS INTERFACE	A A
D-CS-M3003-0-CB16	1		CI20 CBUS INTFC CBUS PARITY	A A
D-CS-M3003-0-CB17	1		CI20 CBUS INTFC FMTR INPUT MUXS	A A
D-CS-M3003-0-CB18	1		CI20 CBUS INTFC MVR/FMTR	A A
D-CS-M3003-0-CB19	1		CI20 CBUS INTFC PLI INTERFACE	A A
D-CS-M3003-0-CB1A	1		CI20 CBUS INTFC MBUS INTERFACE	A A
D-CS-M3003-0-CB1B	1		CI20 CBUS INTFC PWR & GND	A A
D-CS-M3003-0-CB1C	1		CI20 CBUS INTFC UNUSED FINGERS	A A
D-CS-M3003-0-BLK	1		BLOCK DIAGRAM CBUS MODULE	A A
K-PC-M3003-0-DBI	-		P.C. DESIGN DATA BASE	A A
D-DD-5015386-0	2		DRAWING DIRECTORY, 5015386	REF REF

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. D. DELLORCO	DATE 19-FEB-85	ENG. E. BLOOM	DATE 19-FEB-85	TITLE: DRAWING DIRECTORY
	CHK'D. D. CAUNTER	DATE 19-FEB-85	BOARD LOCATION: N/A	SHEET 1 OF 2	M3003 (CBUS)
DSK:M30031.T2P(4,52)		19-FEB-85 13:07	NEXT HIGHER ASSEMBLY:		SIZE CODE D DD
FIRST USED ON OPTION/MODEL: CI20				NUMBER M3003-0	REV. C

DRAWING NUMBER	PAGE	PART NO.	DESCRIPTION	REVISIONS	
			ECO NUMBER	M3003	M3003
				MR001	MR002
		M3003-00	MODULE REVISION	A2	A3
D-UA-M3003-0-0	1		CBUS	B	C
K-PL-M3003-0-DBP	2		PARTS LIST, M3003	B	C
D-CS-M3003-0-CB11	1		CI20 CBUS INTFC CBUS CONTROL 1	A	A
D-CS-M3003-0-CB12	1		CI20 CBUS INTFC CBUS CONTROL 2	A	A
D-CS-M3003-0-CB13	1		CI20 CBUS INTFC MVR CONTROL 1	A	A
D-CS-M3003-0-CB14	1		CI20 CBUS INTFC MVR CONTROL 2	A	A
D-CS-M3003-0-CB15	1		CI20 CBUS INTFC CBUS INTERFACE	A	A
D-CS-M3003-0-CB16	1		CI20 CBUS INTFC CBUS PARITY	A	A
D-CS-M3003-0-CB17	1		CI20 CBUS INTFC FMTR INPUT MUXS	A	A
D-CS-M3003-0-CB18	1		CI20 CBUS INTFC MVR/FMTR	A	A
D-CS-M3003-0-CB19	1		CI20 CBUS INTFC PLI INTERFACE	A	A
D-CS-M3003-0-CB1A	1		CI20 CBUS INTFC MBUS INTERFACE	A	A
D-CS-M3003-0-CB1B	1		CI20 CBUS INTFC PWR & GND	A	A
D-CS-M3003-0-CB1C	1		CI20 CBUS INTFC UNUSED FINGERS	A	A
D-CS-M3003-0-BLK	1		BLOCK DIAGRAM CBUS MODULE	A	A
K-PC-M3003-0-DBI	-		P.C. DESIGN DATA BASE	B	B
D-DD-5015386-0	2		DRAWING DIRECTORY, 5015386	REF	REF

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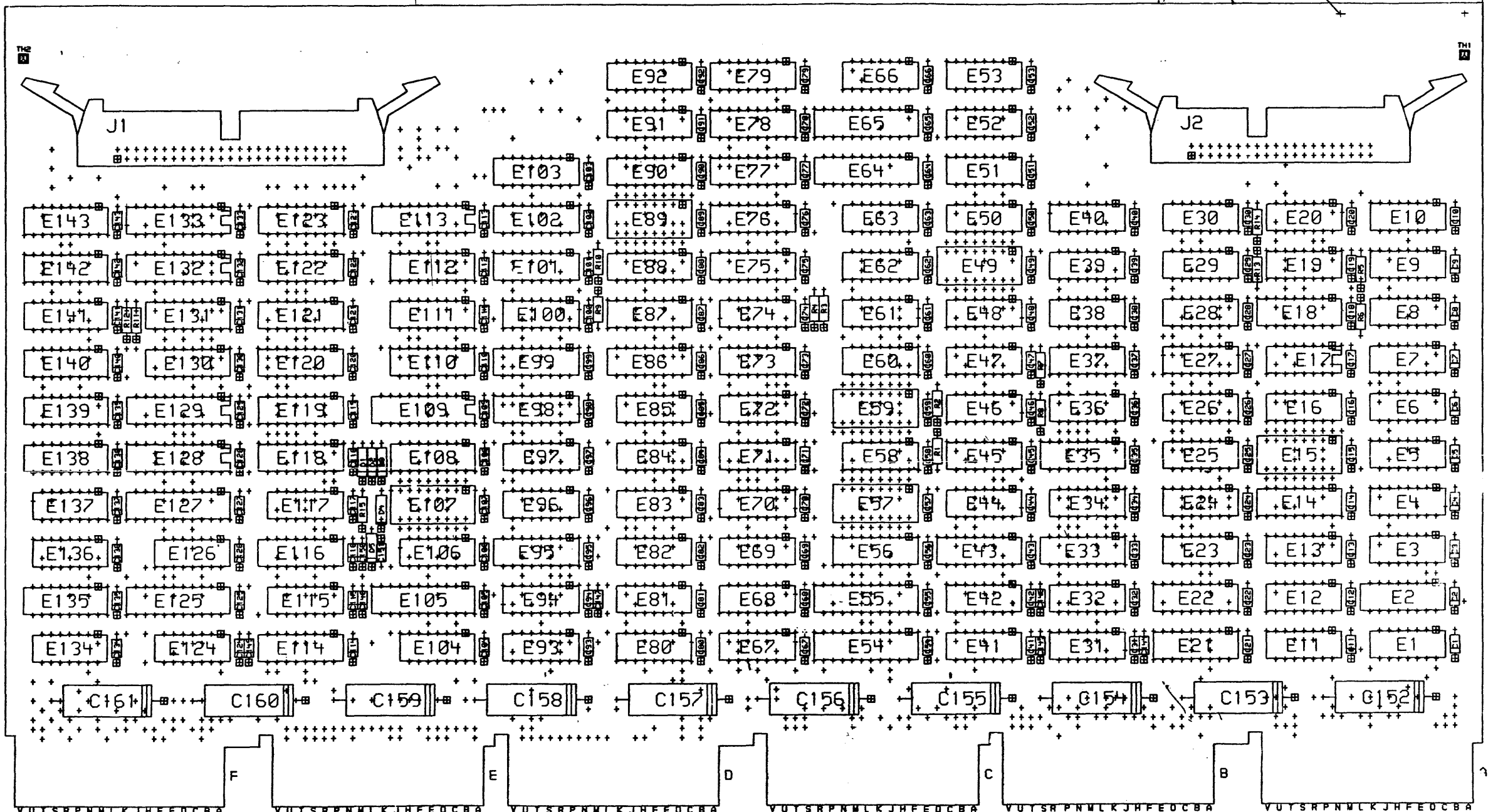
REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. D. DELLORCO	DATE 19-FEB-89	ENG. E BLOOM	DATE 19-FEB-89	TITLE: DRAWING DIRECTORY M3003 (CBUS)
	CHK'D D. CAUNTER	DATE 19-FEB-89	BOARD LOCATION: N/A	SHEET 2 OF 2	
DSK: M30032.T2P(4,52)		119-FEB-89 13:07		NEXT HIGHER ASSEMBLY:	
FIRST USED ON OPTION/MODEL: CI20		SIZE	CODE	NUMBER	REV.
		D	DD	M3003-0	C

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2 DUA M 3003-0-0

41 QTY 12 COMPONENT SIDE VIEW



NOTES: 1. SPARE COMPONENT LOCATIONS ARE: E15, E49, E57, E59, E89, E107.
2. THIS BOARD MUST MEET SAFETY REQ. FOR HPWR.

STEP E	+ Y AXIS	0	STEP 0	TIMES
REPEAT	+ X AXIS	0	STEP 0	TIMES

CHANGE NO	REV	DATE	BY
1	1	12/11/84	W. J. BLOOM
2	1	12/11/84	W. J. BLOOM
3	1	12/11/84	W. J. BLOOM
4	1	12/11/84	W. J. BLOOM
5	1	12/11/84	W. J. BLOOM
6	1	12/11/84	W. J. BLOOM
7	1	12/11/84	W. J. BLOOM
8	1	12/11/84	W. J. BLOOM

ETCH REV.	C1
-----------	----

SIGNATURES		DATE	digital
DRN. W. J. Bloom		12/11/84	
CHK. D. S. Lawrence		12/11/84	TITLE
MECH. ENG. J. Lawrence		12/11/84	
PROJ. ENG. J. Lawrence		12/11/84	CBUS
PROD. J. Lawrence		12/11/84	
SCALE 2/1	SIZE CODE	NUMBER	REV
SHT. 1 OF 1	D UA	M3003-0-0	C
TOP DOC NO: D-DD-M3003-0			

LINE ITEM	TCP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
					00		
					A3		
1	D-MC-5015386-0-0	5015386-01	C	DRILL & ETCH DWG FOR M3003	1		
2		1001610-00		.01 MFD 50V +80-20% Z5U CER	132		C1-C17,C19-C28,C30,C32-C40, CONT C43-C45,C47-C58,C60-C73,C75-C93, CONT C95-C99,C101-C114,C116-C123, CONT C125-C140,C142,C143,C150,C151 C18,C29,C46,C59,C74,C100,C141 C31,C41,C42,C94,C115,C124, CONT C144-C149 C152-C161 D1-D5 J1 J2
3		1012784-00		.047 MFD 50V +80-20% CER	7		
4		1010274-00		.22 MFD 50V +80-20% Z5U CER	12		
5		1012084-03		150 MFD 15V +75-10% AL EL	10		
6		1100113-00		D 662 OS 600PCB(STABISTOR)	5		
7		1216832-03		PCB,HEADER 50POS(2X25),100CC 90D	1		
8		1216832-02		PCB,HEADER 40POS(2X20),100CC 90D	1		
9		1215278-00		CABLE,FLEX NOMEX SHEET MODULE PR	1		
10		1216988-02		HANDLE,MODULE,HEX TWO EJECTORS	1		
11		1301322-00		180.0 .25 W 5.0 % CF	7		R1,R3,R5,R7,R9,R11,R13
12		1300309-00		390.0 .25 W 5.0 % CF	7		R2,R4,R6,R8,R10,R12,R14
13		1301477-00		82.0 .25 W 5.0 % CF	1		R15
14		1912746-B0		74S37 BURNED-IN NAND GATE-	5		E1,E8,E11,E30,E104
15		1911117-00		DEC 8838 TRANSCEIVER,BUS,QUA	1		E2
16		1910539-B0		74S20 BURNED-IN NAND GATE-	2		E3,E29
17		1910544-B0		74S74 BURNED-IN FF-D DUAL,	11		E4,E13,E16,E20,E23,E34,E38,E45, CONT E52,E61,E62
18		1912388-B0		74S02 BURNED-IN MCR GATE-Q	10		E5,E10,E19,E24,E25,E47,E48,E71, CONT E72,E81
19		1910534-B0		74S04 BURNED-IN INVERTER G	8		E6,E12,E39,E46,E53,E85,E93,E100
20		1910536-B0		74S10 BURNED-IN NAND GATE-	1		E7
21		1910532-B0		74S00 BURNED-IN NAND GATE-	4		E9,E28,E63,E80
22		1910957-B0		74S175 BURNED-IN FF-D QUAD	3		E14,E18,E35
23		1616653-00		DELAY= 50NS,10TAPS WITH TTLBU	1		E17
24		1911675-B0		74S138 BURNED-IN DECODER/DE	1		E21

REVISION HISTORY		BASIC PART NO: M3003		DRN:	D,DELLORCO	DATE:	18-NOV-82	D I G I T A L			
ENG	ECC NUMBER	REV	SECTION A OF A	CHK'D:	D,CAUNTER	DATE:	18-NOV-82	TITLE PARTS LIST			
INITIAL	A	SECTION VARIATION INDEX	CHK'D:	D,CAUNTER	DATE:	18-NOV-82	M3003 C BUS				
JT	M3003-MR001	B	[B]	DES.ENG:	R,CARN	DATE:	19-APR-84	DOCUMENT NUMBER			
EB	M3003-MR002	C	[C]					SIZE	CODE	NUMBER	REV
			[D]								
			[E]								
			[F]	RESP.ENG.:	R,CARN	DATE:	19-APR-84	K	PL	M3003-0-DBP	C
			[H]								
			[J]								
			[K]	IMFG.ENG.:	R,CARN	DATE:	19-APR-84	RELEASE DATE: 18-FEB-85			
			[L]								
			[M]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #:	
			[N]	ID-UA-M3003-0-0		ID-DD-M3003-0		Z8534C.PLS		18	

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D

C

B

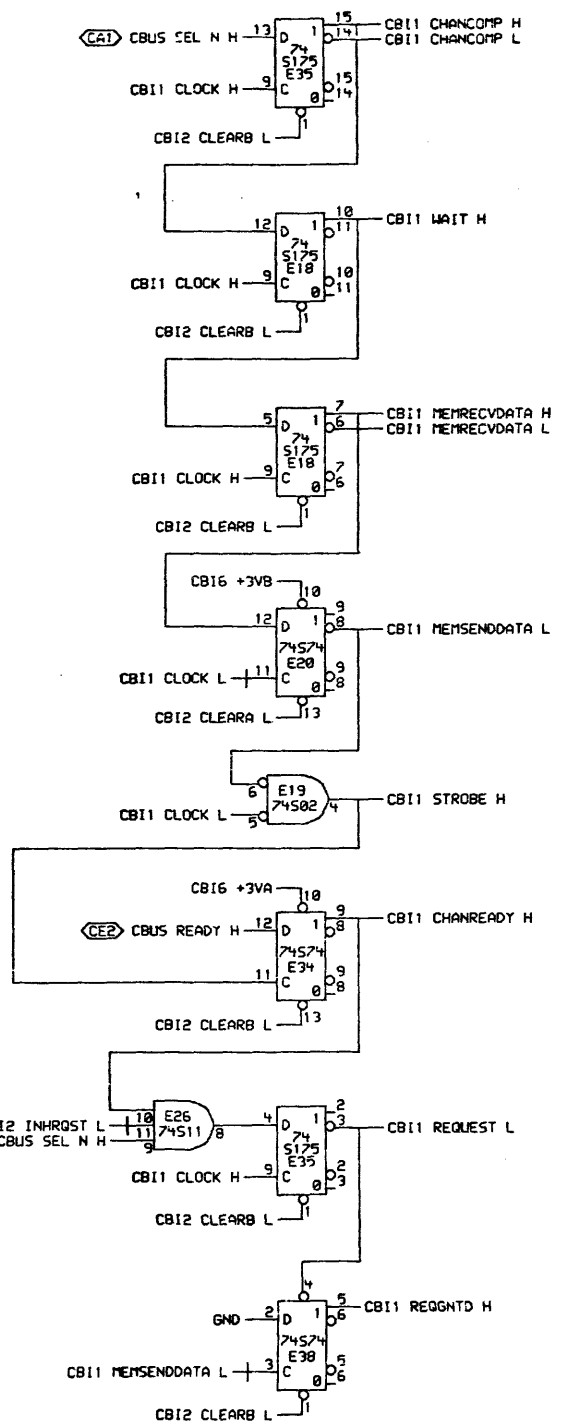
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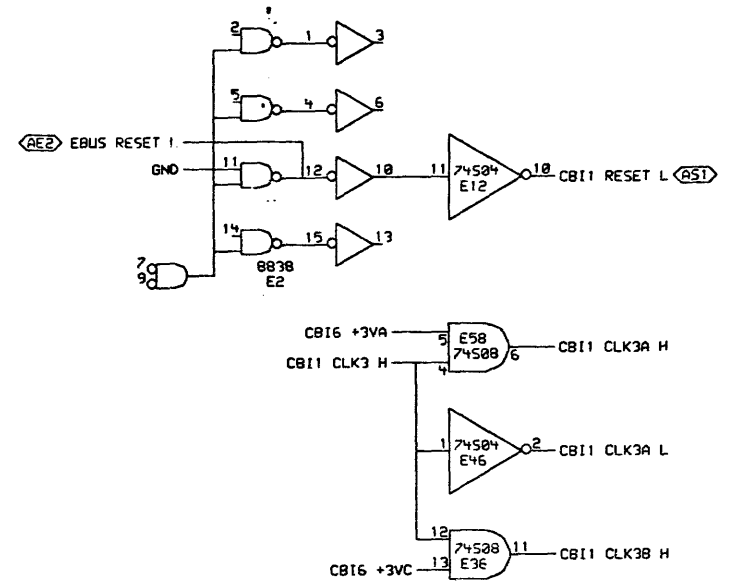
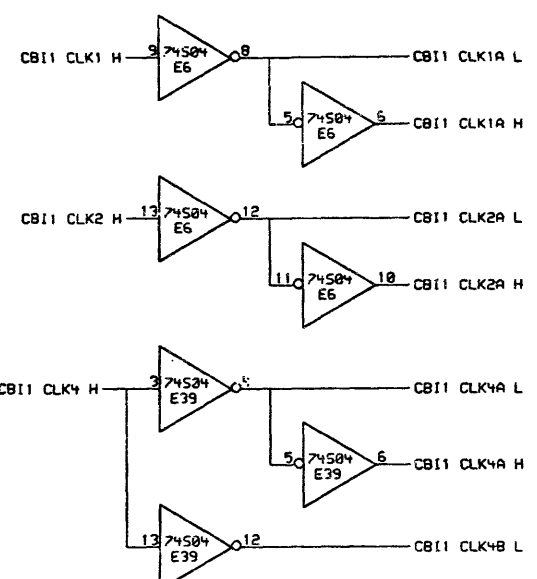
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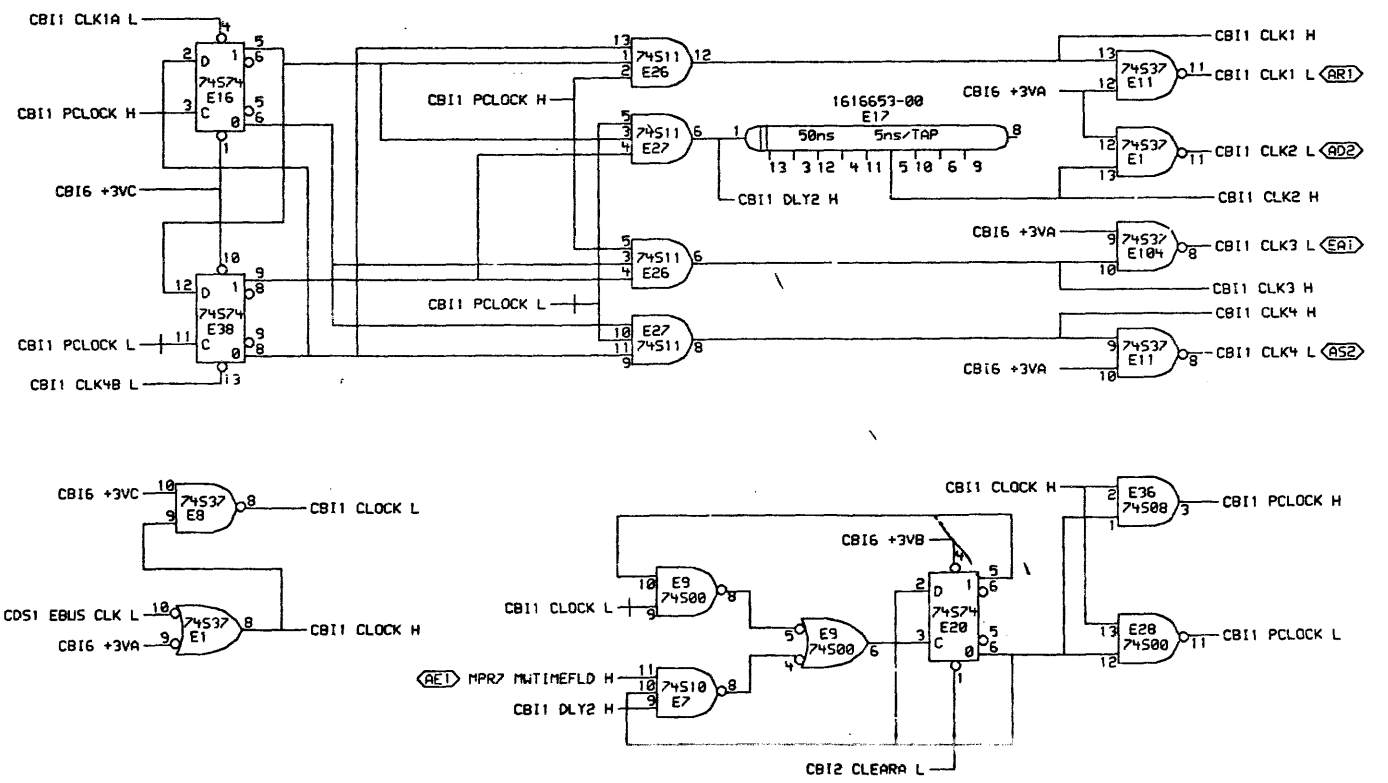
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C120	N120
N=7	N=5



CLOCK GENERATION LOGIC



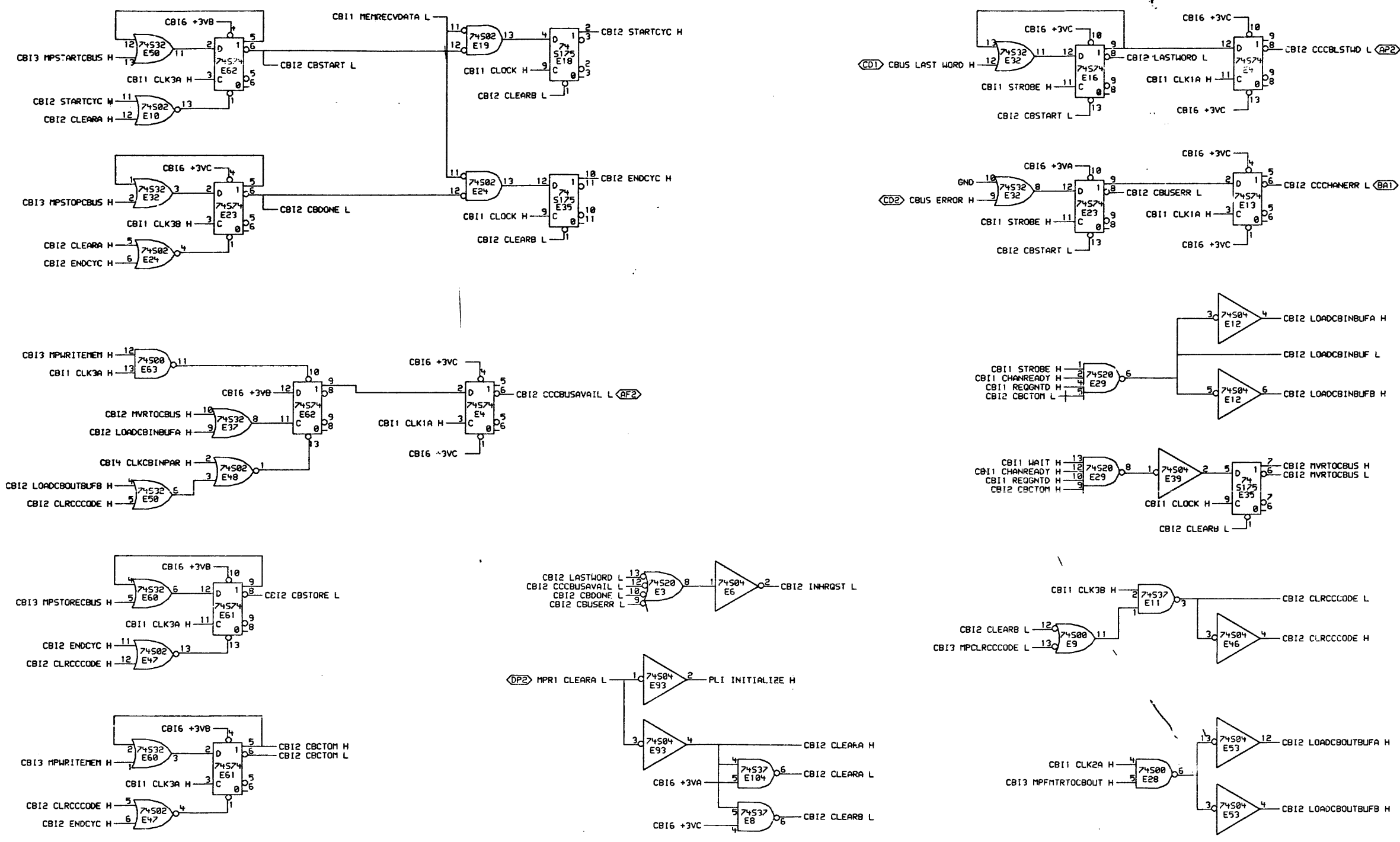
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG. <i>R. CARL</i>	DATE	TITLE:
	03-APR-84		03-APR-84	C120 CBUS INTFC
	DATE	BOARD LOCATION:		
	12 APR 84	OF 1		
SUBCON: BOHEN, ECO: CB1101, DRW: 123-MAR-84 09:00		NEXT HIGHER ASSEMBLY:		
FIRST USED ON OPTION/MODEL: C120		D-DD-M3003-0		

SIZE	CODE	NUMBER	REV.
D	CS	M3003-0-CB11	A

REV. C
 NUMBER M3003-0-CB11
 SIZE CODE CS



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REVISIONS		
CHK	CHANGE NO.	REV

	DRN: <i>J. Lamb</i> CHK'D: <i>w. Williams</i>	DATE: 03-APR-84 DATE: 19 APR 84 SHEET: OF	ENG: <i>R. Carr</i> DATE: 03-APR-84 BOARD LOCATION:	TITLE: C120 CBUS INTFC CBUS CONTROL 2
	SUDCOM: <BOWEN.ECO>C120.DRW 129-MAR-84 09:01 FIRST USED ON OPTION/MODEL: C120		NEXT HIGHER ASSEMBLY: D-DD-M3003-0	
SIZE	CODE	CS	NUMBER	REV.
D	CS	M3003-0-CB12	A	

D

C

B

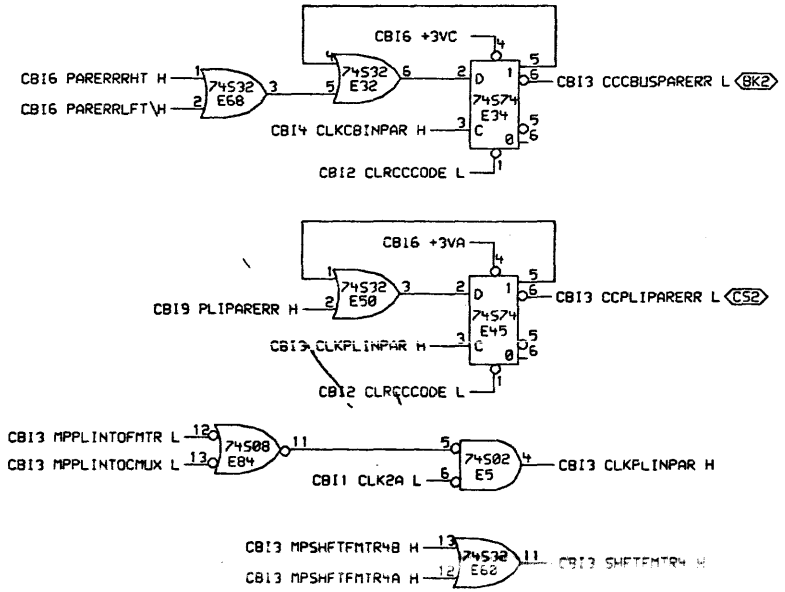
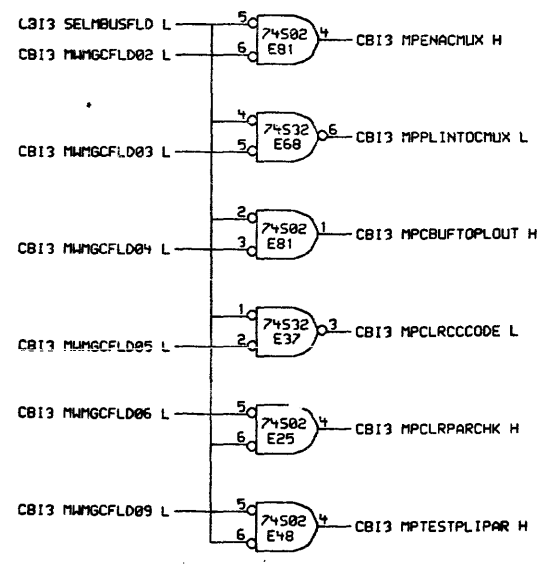
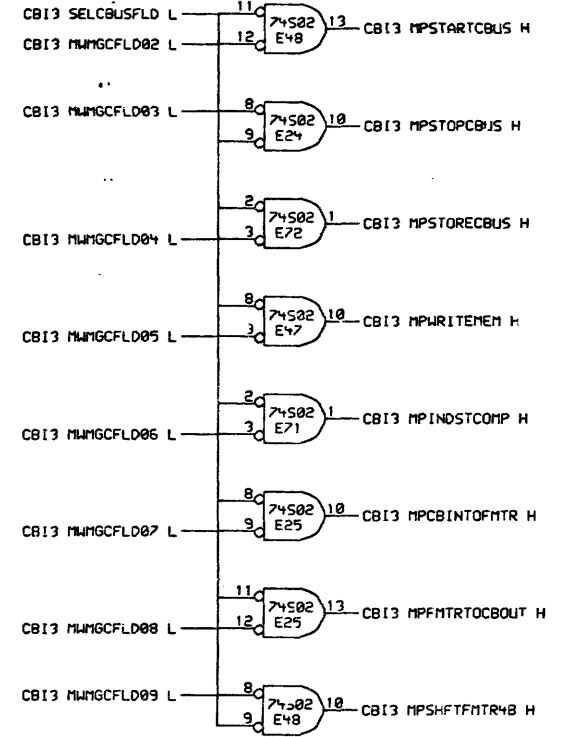
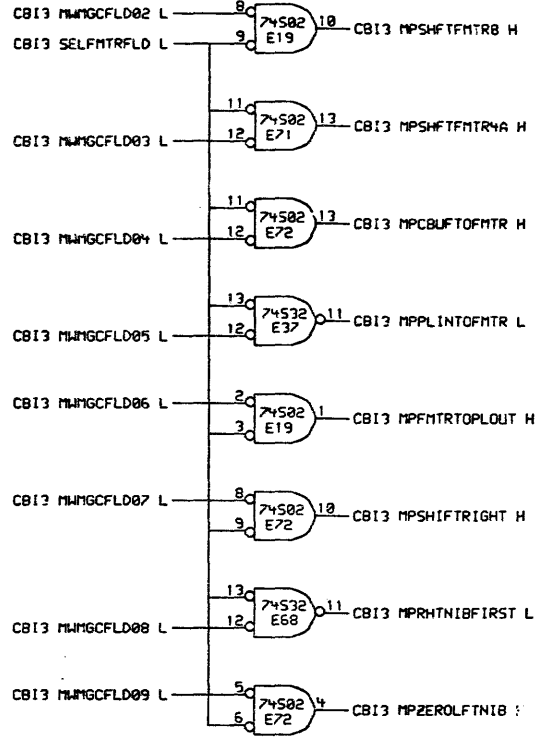
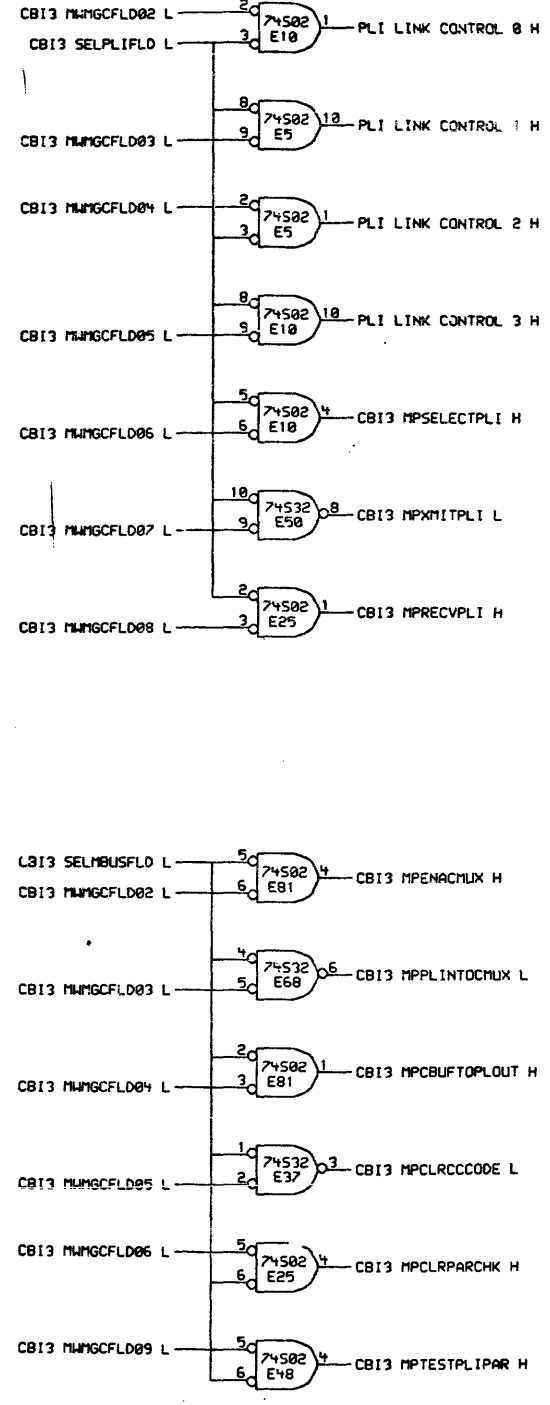
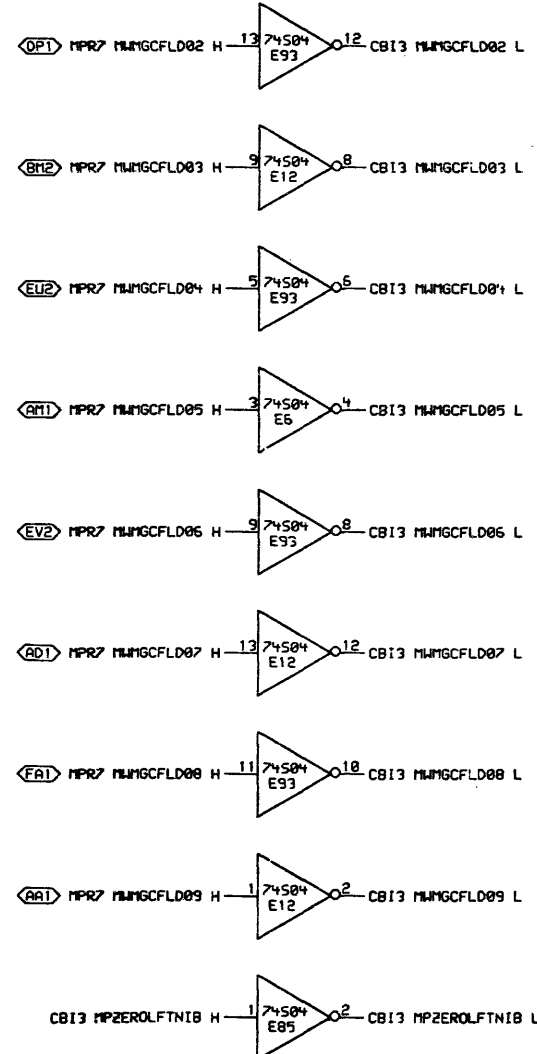
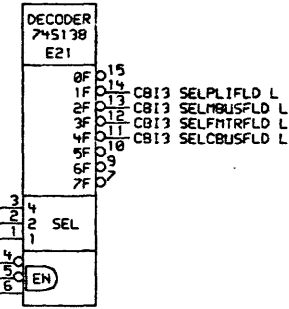
A

D

C

B

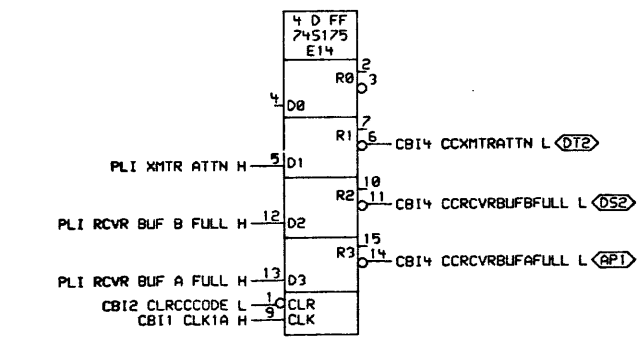
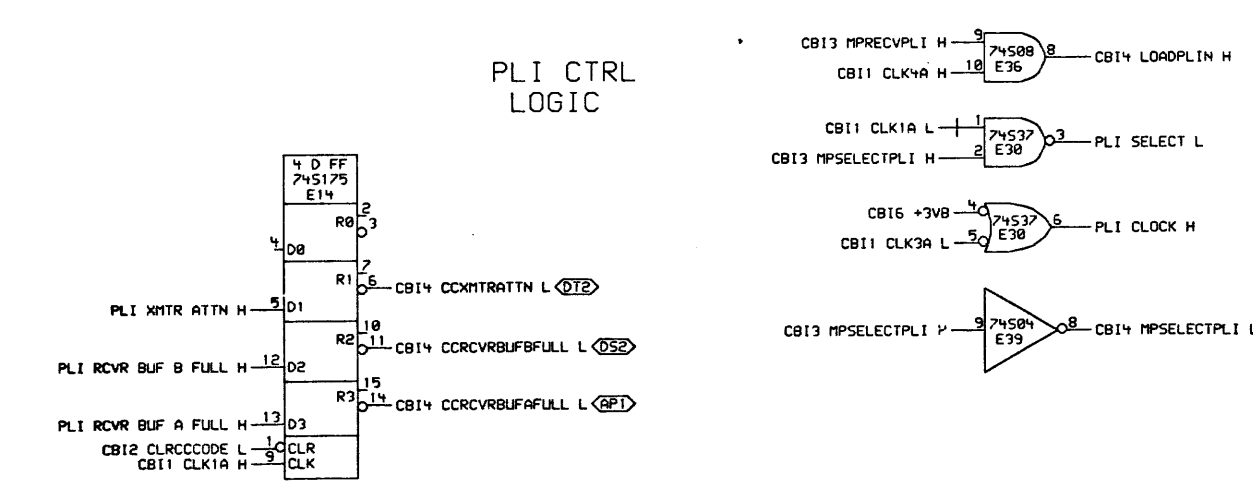
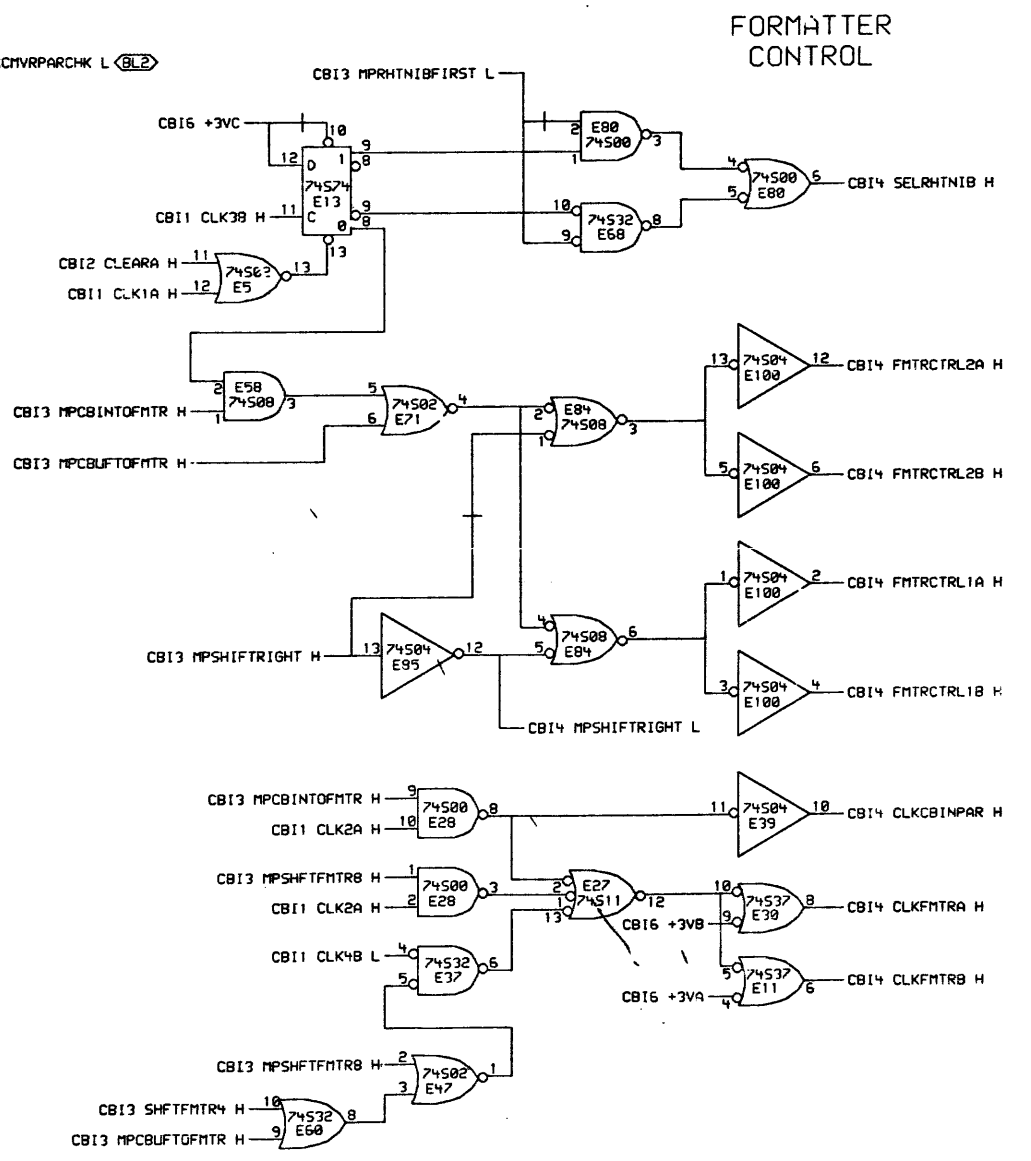
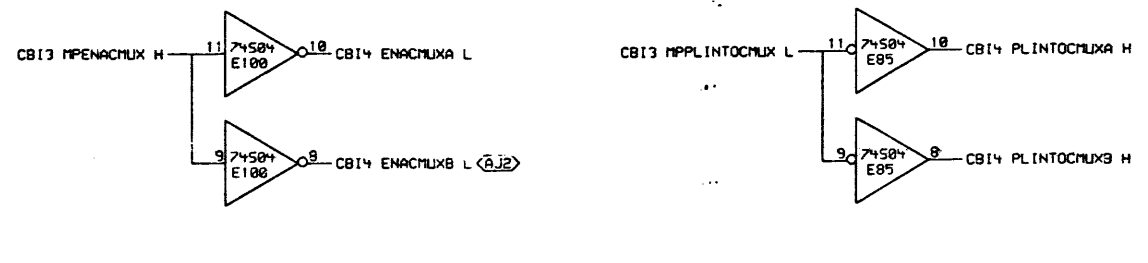
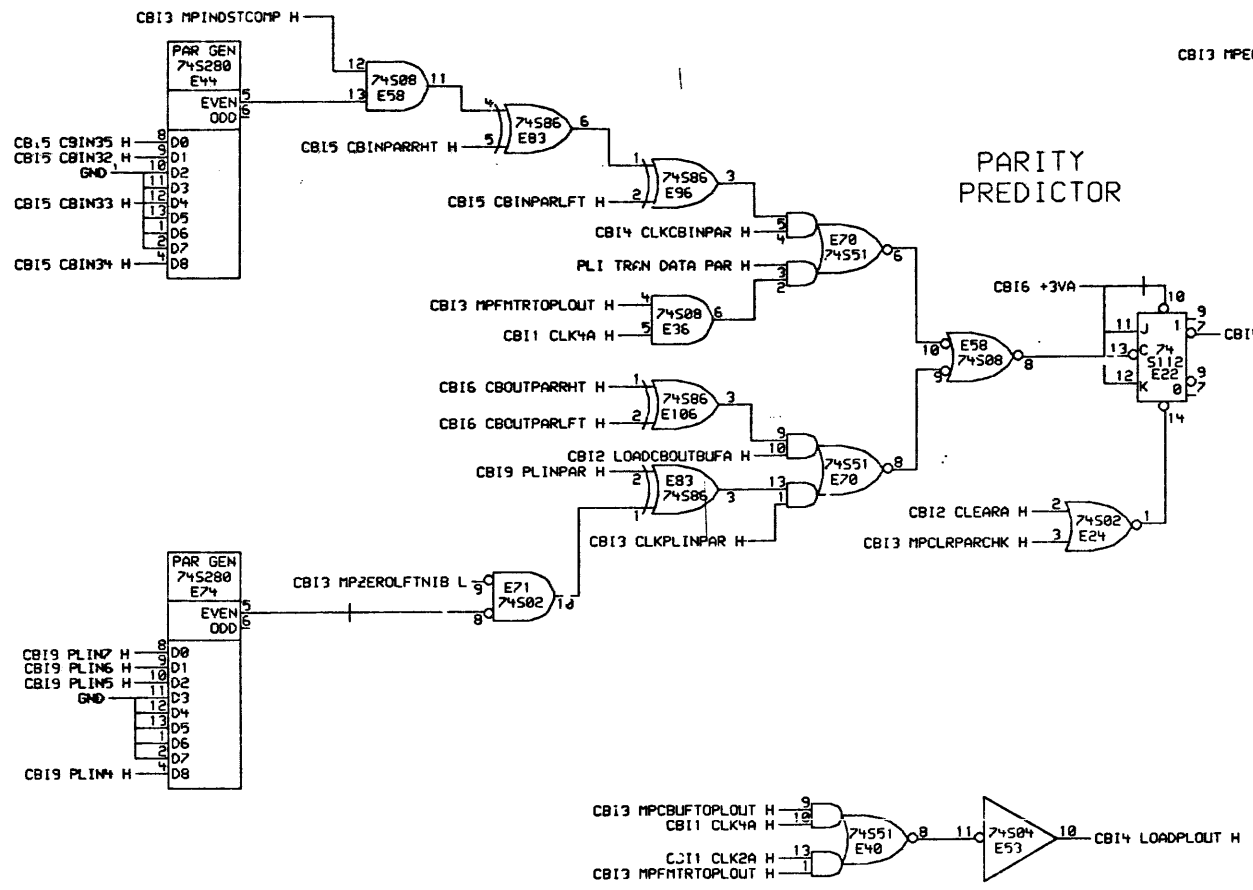
A



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REVISIONS	
CHK	CHANGE NO. REV

	DRN: J. L. L...	DATE: 03-APR-84	ENG: R. Ben	DATE: 03-APR-84	TITLE: CI20 CBUS INTFC
	CHK'D: N. J. L...	DATE: 03-APR-84	BOARD LOCATION: 4-400 31 SHEET	OF: 1	MVR CONTROL 1
SUCCESSOR: BOWEN.EC0X:CB1302.DRW 129-MAR-84 09:01 NEXT HIGHER ASSEMBLY: 0-00-M3003-0					SIZE CODE: D C5
FIRST USED ON OPTION/MODEL: CI20					NUMBER: M3003-0-CB13
					REV: A

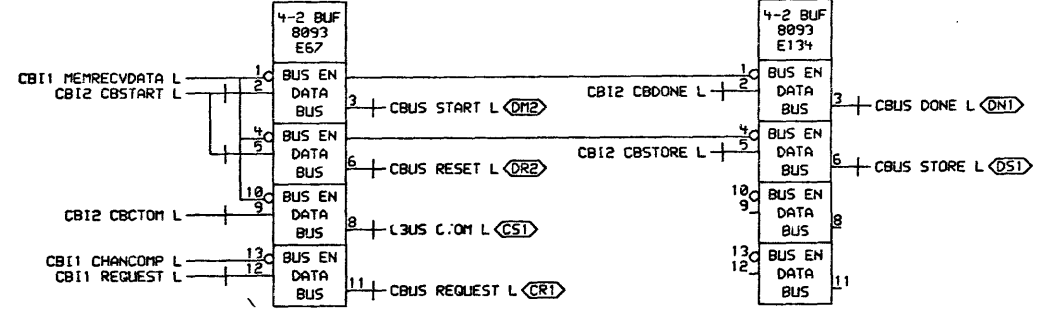
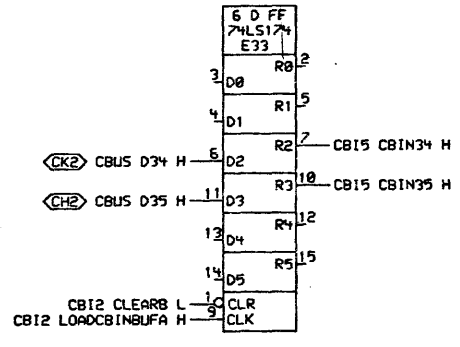
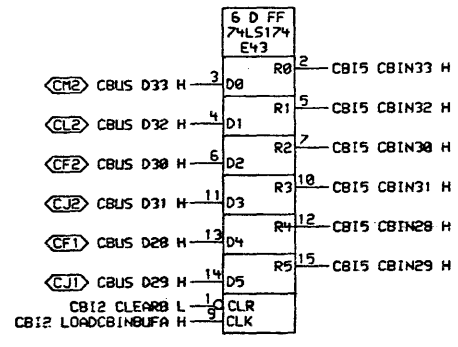
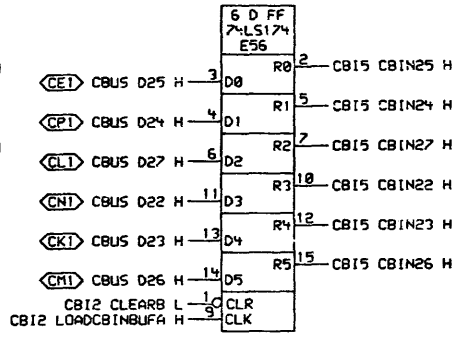
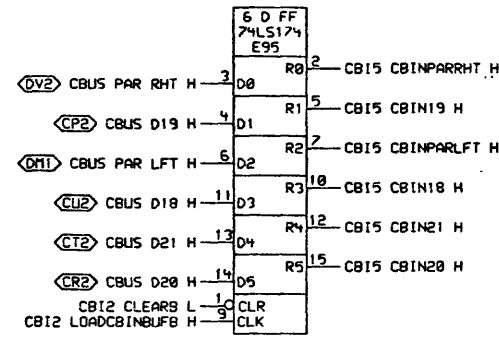
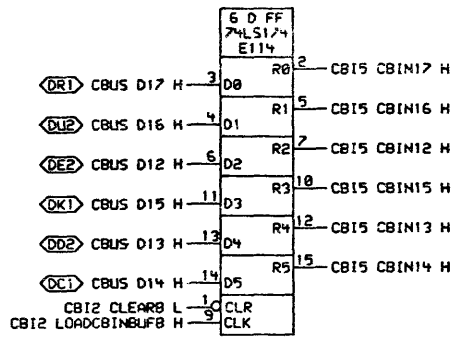
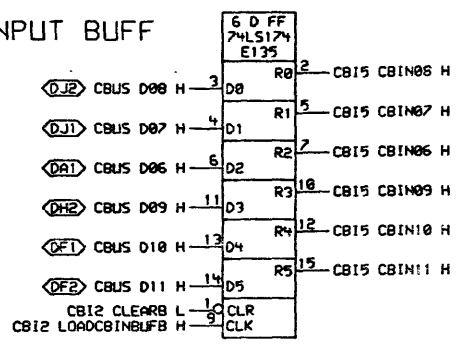
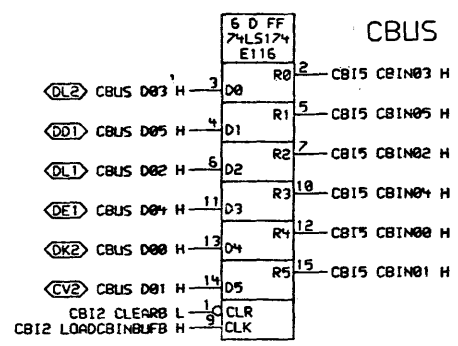


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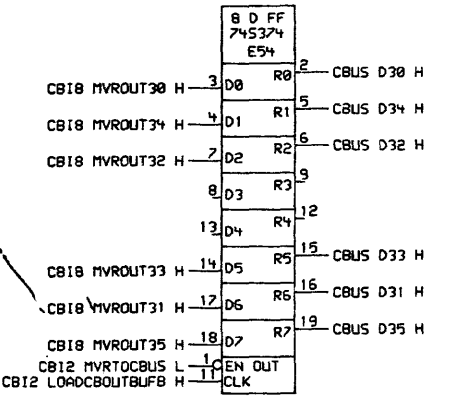
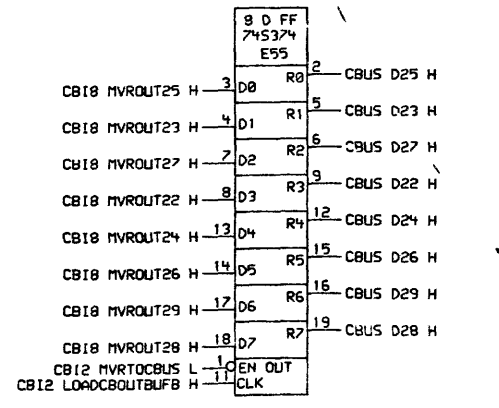
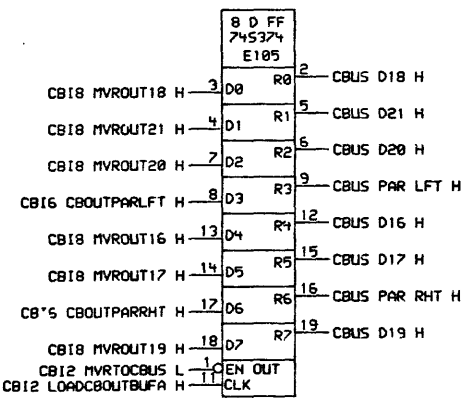
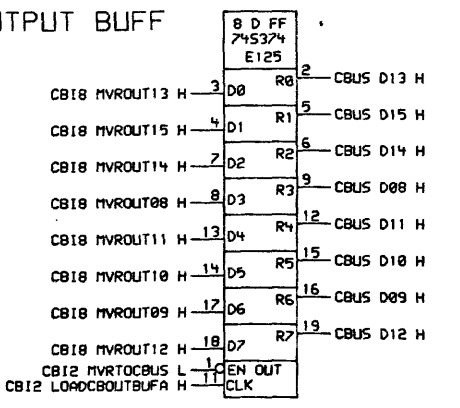
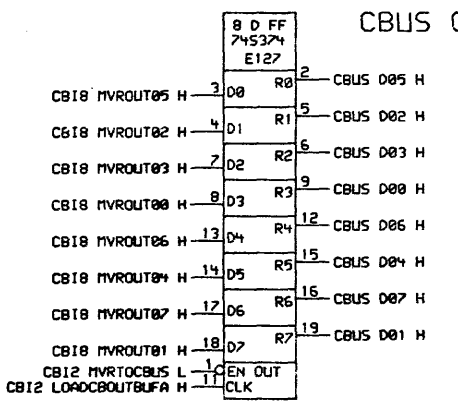
REVISIONS	
CHK	CHANGE NO. REV

digit	DRW: R. Carr	DATE: 03-APR-84	ENG: R. Carr	DATE: 03-APR-84
	CHK'D: B. Suresh	DATE: 12-APR-84	BOARD LOCATION: 13	SHEET 1 OF 1
SUBCOMP: BOWEN.ECO:CB1401.DRU		NEXT HIGHER ASSEMBLY: D-DD-M3003-0		TITLE: C120 CBUS INTFC MVR CONTROL 2
FIRST USED ON OPTION/MODEL: C120		SIZE CODE: D CS	NUMBER: M3003-0-CB14	REV: A

CBUS INPUT BUFF



CBUS OUTPUT BUFF

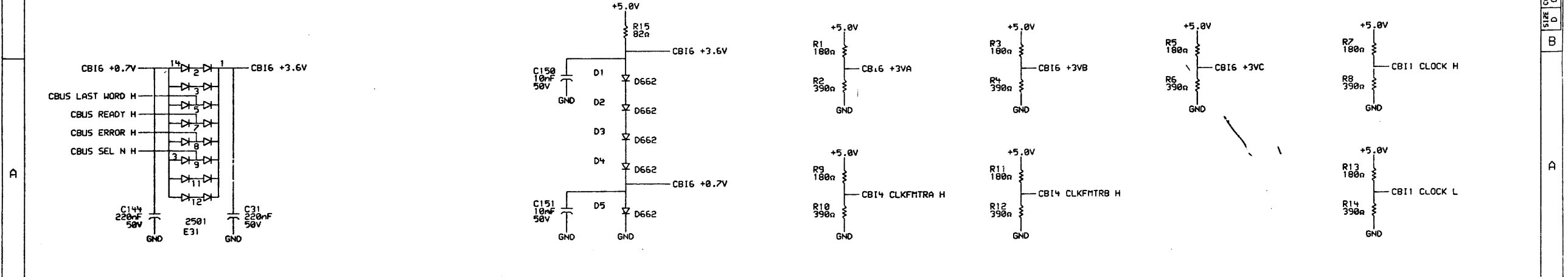
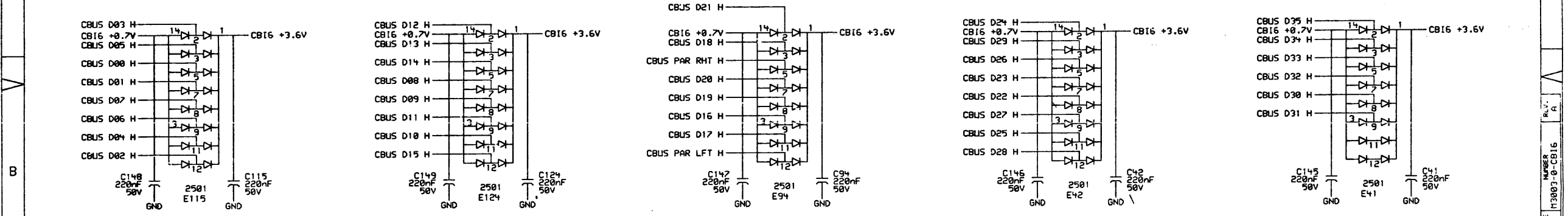
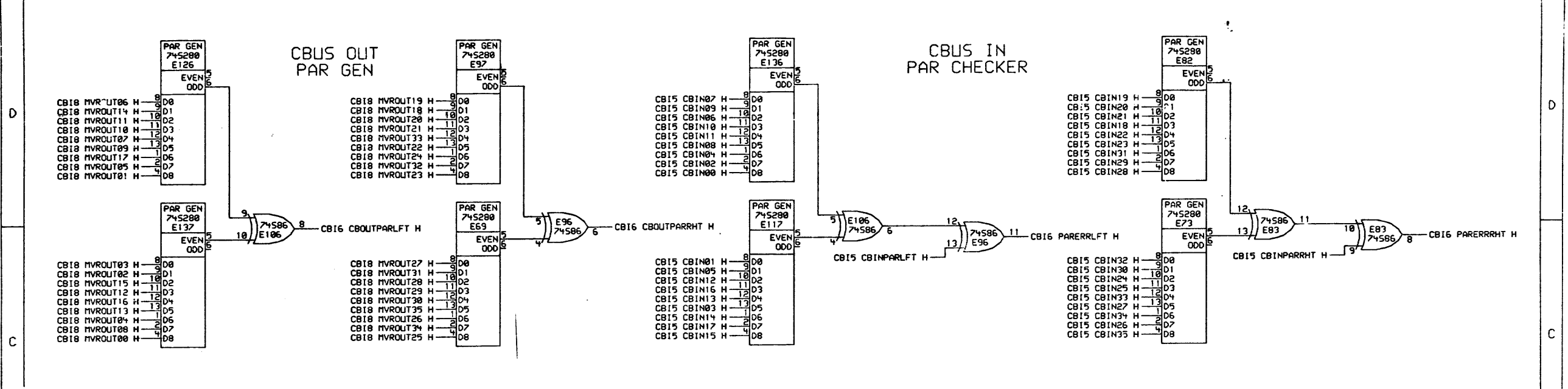


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REVISIONS		
CHK	CHANGE NO.	REV.

digital *John* *R. Law*
 DATE: 03-APR-84
 DATE: 03-APR-84
 BOARD LOCATION: 19-APR-84 SHEET OF 1
 SUBCIRCUIT: BOUHEM.ECO:CB1500.DRW 129-MAR-84 09:02 NEXT HIGHER ASSEMBLY:
 FIRST USED ON OPTION/MODEL: CI20 D-DD-M3003-0

TITLE:	CI20 CBUS INTFC		
	CBUS INTERFACE		
SIZE CODE	D	CS	M3003-0-CB15
NUMBER			
REV.			A



D

C

B

A

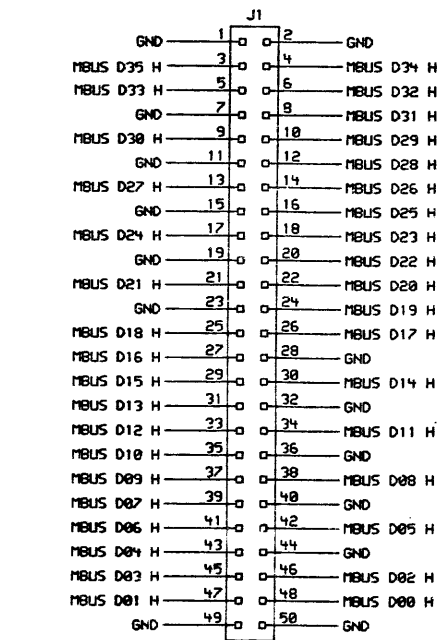
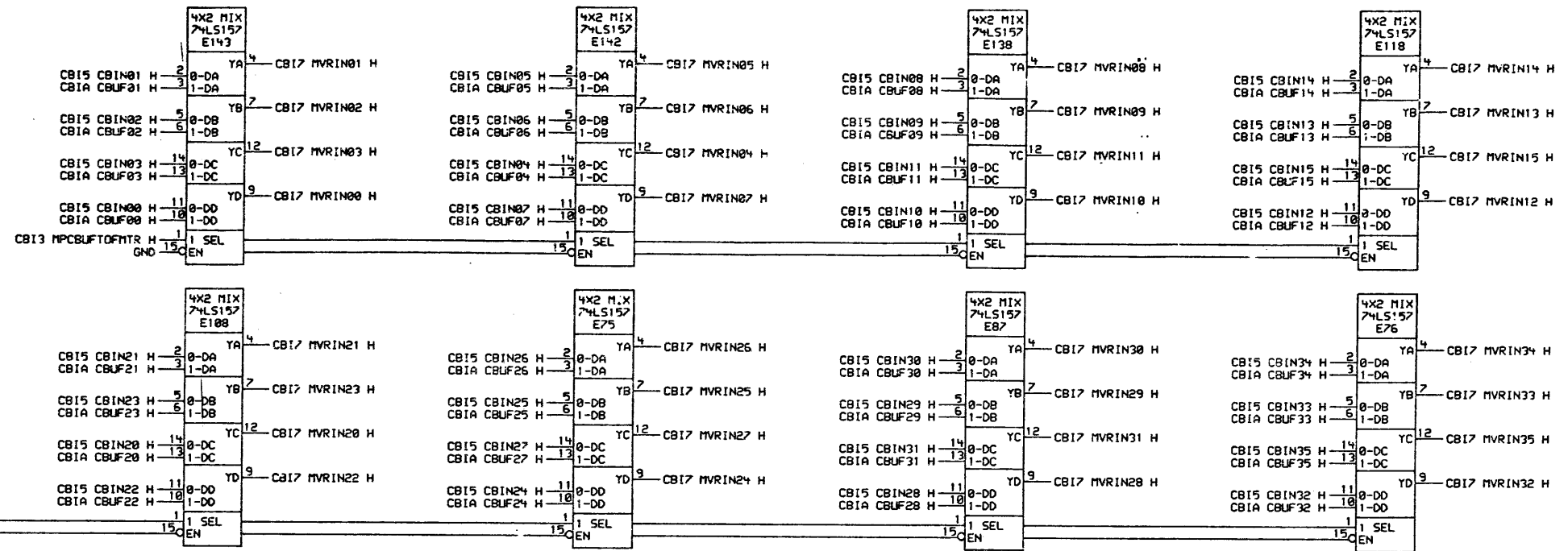
D

C

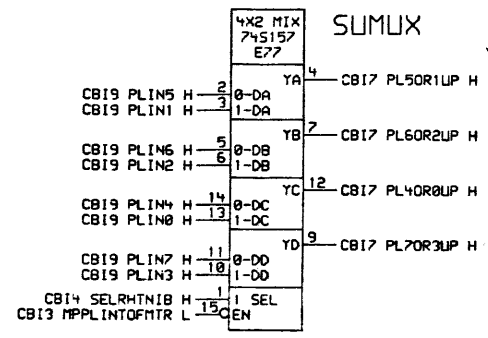
B

A

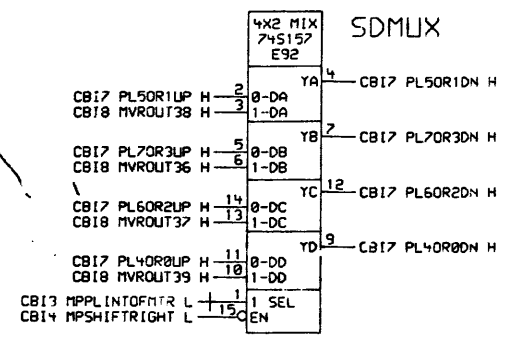
DMUX



SUMUX



SDMUX



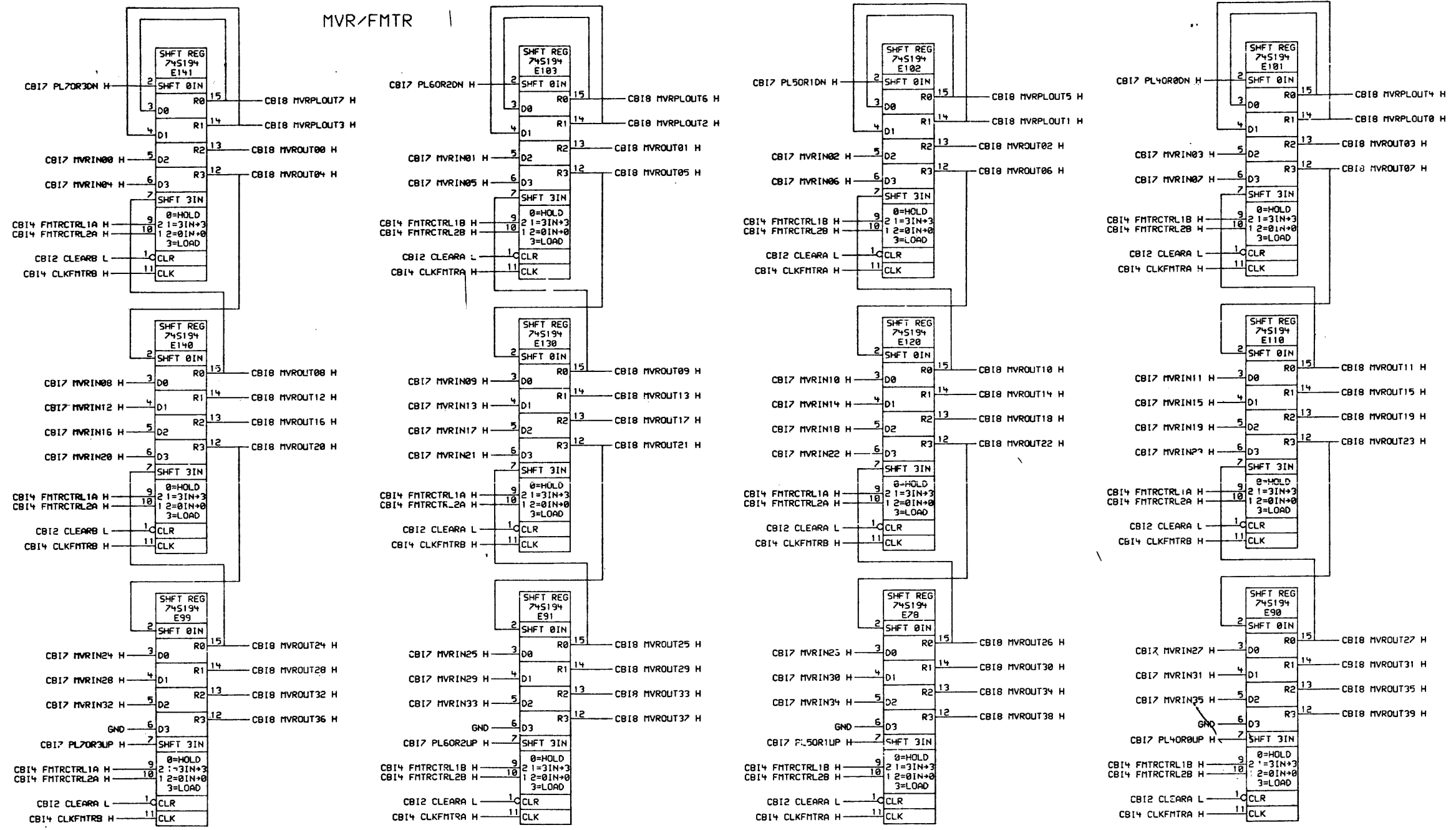
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REVISIONS		
CHK	CHANGE NO.	REV

	DATE: 03-APR-81 ENG: R. Carr DATE: 03-APR-81	DATE: 03-APR-81 BOARD LOCATION:	TITLE: C120 CBUS INTFC FMTX INPUT MUXS
	DATE: 03-APR-81 CHECK'D: J. Williams	DATE: 03-APR-81 SHEET: 1 OF 1	SIZE: D CODE: CS

SUCOM1:KBOLEN.EC02CB1200.DRW 29-MAR-84 09:10* NEXT HIGHER ASSEMBLY: 0-DD-M3003-0
 FIRST USED ON OPTION MODEL: C120

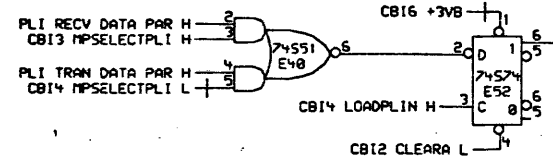
MVR/FMTR



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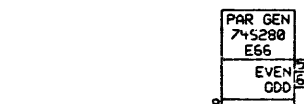
REVISIONS	
CHK	CHANGE NO. / REV

	DATE: 03-APR-84 ENG: R. Low DATE: 03-APR-84	TITLE: C120 CBUS INTFC MVR/FMTR
	BOARD LOCATION: 1 OF 1 SHEET: 1 OF 1	SIZE: D CS CODE: M3003-0-CB18
SUBDIR: C:\BEN.ECD\CB1800.DRW [29-MAR-84 09:05] NEXT HIGHER ASSEMBLY: D-DD-M3003-0	FIRST USED ON OPTION MODEL: C120	REV: A



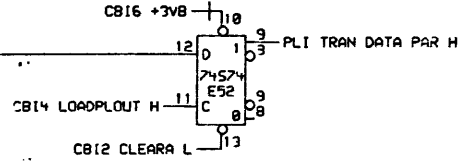
PLI PAR INPUT CHECKER

PAR GEN 745288 E51	1	00
EVEN	2	01
ODD	3	02
	4	03
	5	04
	6	05
	7	06
	8	07
	9	08



PLI PAR OUTPUT GENERATOR

PAR GEN 745288 E66	1	00
EVEN	2	01
ODD	3	02
	4	03
	5	04
	6	05
	7	06
	8	07
	9	08



PLI OUTPUT BUFFER

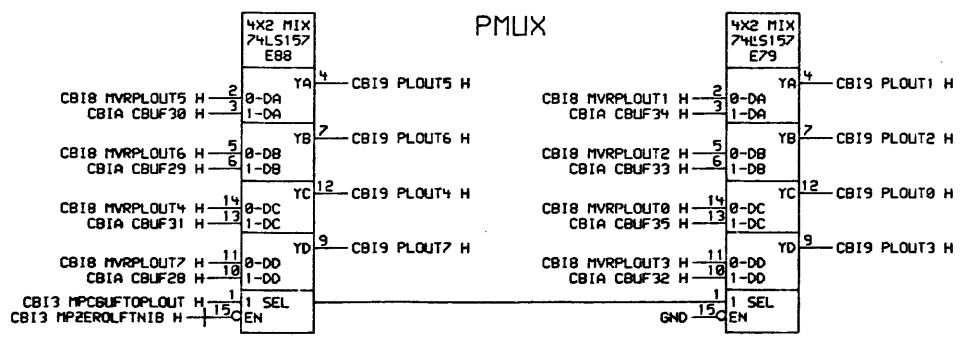
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	2	01	5	PLI DATA 1 H
	3	02	6	PLI DATA 3 H
	4	03	9	PLI DATA 2 H
	5	04	12	PLI DATA 7 H
	6	05	15	PLI DATA 4 H
	7	06	16	PLI DATA 6 H
	8	07	19	PLI DATA 5 H
	9	08		
	10	09		
	11	10		
	12	11		
	13	12		
	14	13		
	15	14		
	16	15		
	17	16		
	18	17		
	19	18		
	20	19		

8 D FF 745374 E64	1	00	2	CB19 PLIN5 H
	2	01	5	CB19 PLIN1 H
	3	02	6	CB19 PLIN6 H
	4	03	9	CB19 PLIN2 H
	5	04	12	CB19 PLIN7 H
	6	05	15	CB19 PLIN4 H
	7	06	16	CB19 PLIN3 H
	8	07	19	CB19 PLIN0 H
	9	08		
	10	09		
	11	10		
	12	11		
	13	12		
	14	13		
	15	14		
	16	15		
	17	16		
	18	17		
	19	18		
	20	19		

PLI INPUT BUFFER

8 D FF 745374 E64	1	00	2	CB19 PLIN5 H
	2	01	5	CB19 PLIN1 H
	3	02	6	CB19 PLIN6 H
	4	03	9	CB19 PLIN2 H
	5	04	12	CB19 PLIN7 H
	6	05	15	CB19 PLIN4 H
	7	06	16	CB19 PLIN3 H
	8	07	19	CB19 PLIN0 H
	9	08		
	10	09		
	11	10		
	12	11		
	13	12		
	14	13		
	15	14		
	16	15		
	17	16		
	18	17		
	19	18		
	20	19		

PMUX



1	0	2	GND
2	0	4	GND
3	0	6	GND
4	0	8	GND
5	0	10	GND
6	0	12	GND
7	0	14	GND
8	0	16	GND
9	0	18	GND
10	0	20	GND
11	0	22	GND
12	0	24	GND
13	0	26	GND
14	0	28	GND
15	0	30	GND
16	0	32	GND
17	0	34	GND
18	0	36	GND
19	0	38	GND
20	0	40	GND

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REVISIONS		
CHK	CHANGE NO.	REV

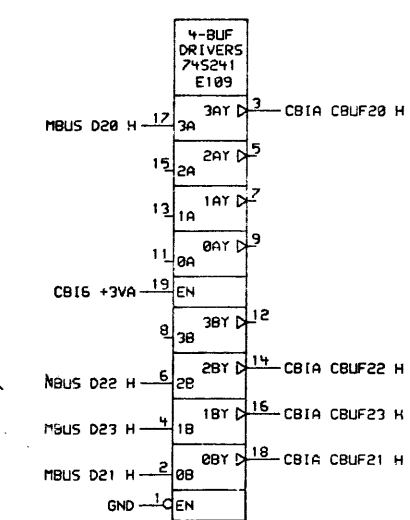
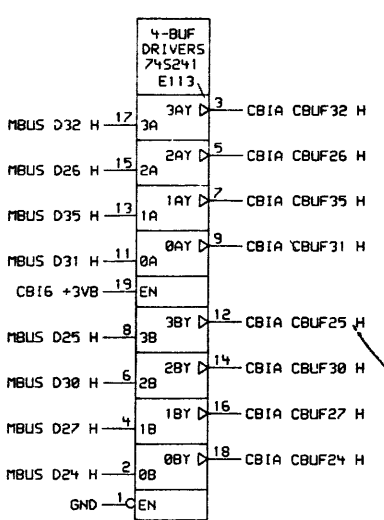
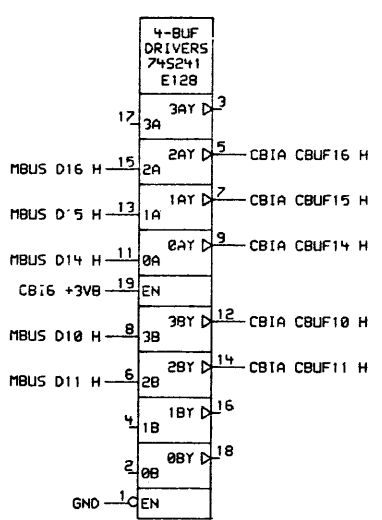
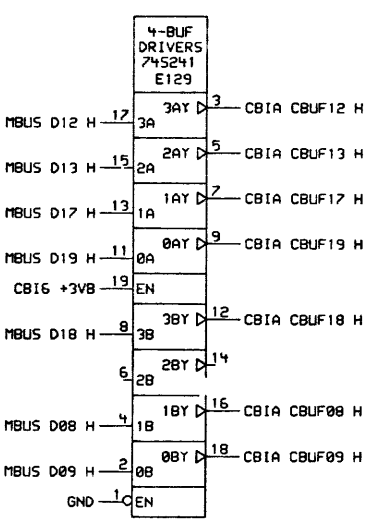
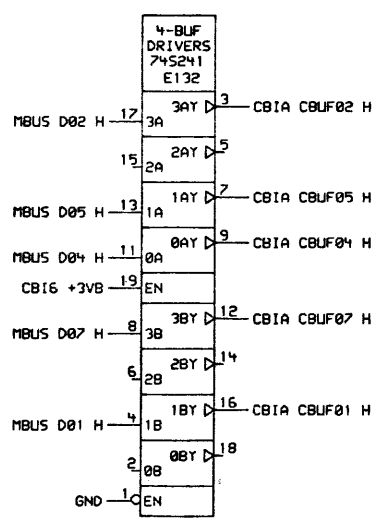
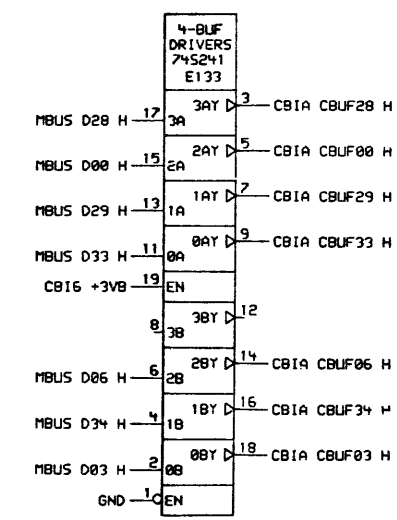
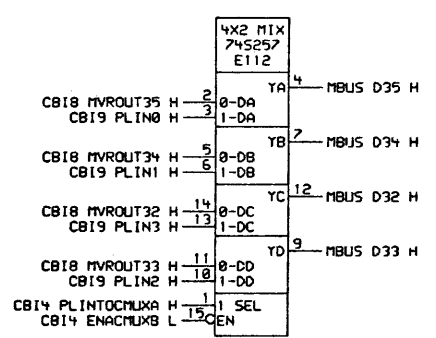
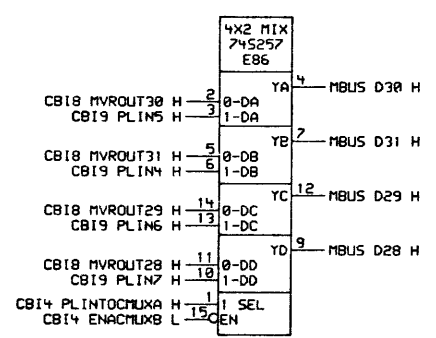
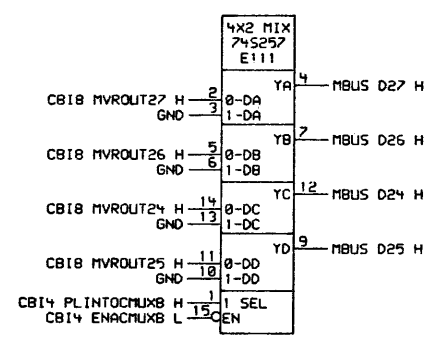
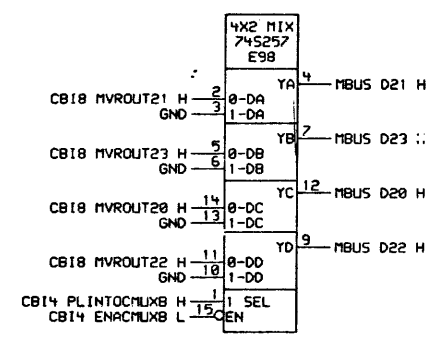
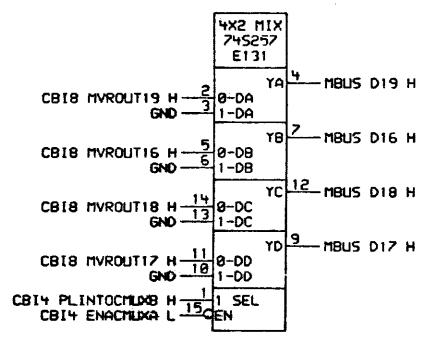
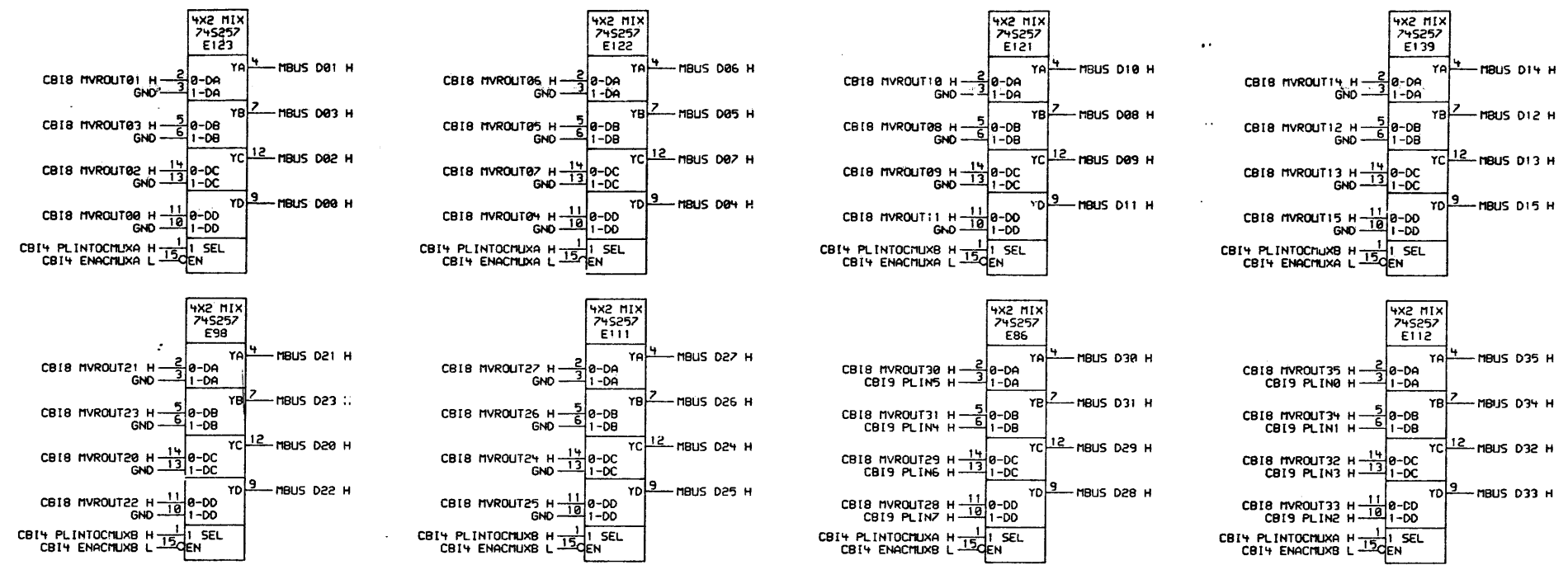
digital DRN: *St. J. J. J.* DATE: 03-APR-84 ENG: *R. L. L.* DATE: 03-APR-84 TITLE: C120 CBUS INTEFC PLI INTERFACE

CHK'D: *St. J. J. J.* DATE: 02-APR-84 BOARD LOCATION: DE

SUDCOM\BOWEN.ECO\CB198A.DRW [29-MAR-84] 09:05 NEXT HIGHER ASSEMBLY: SIZE CODE NUMBER REV. D CS M3003-0-CB19 A

FIRST USED ON OPTION/MODEL: C120 D-DD-M3003-0

CMUX



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REVISIONS	
CHK	CHANGE NO. REV.

8	7	6	5	4	3	2	1
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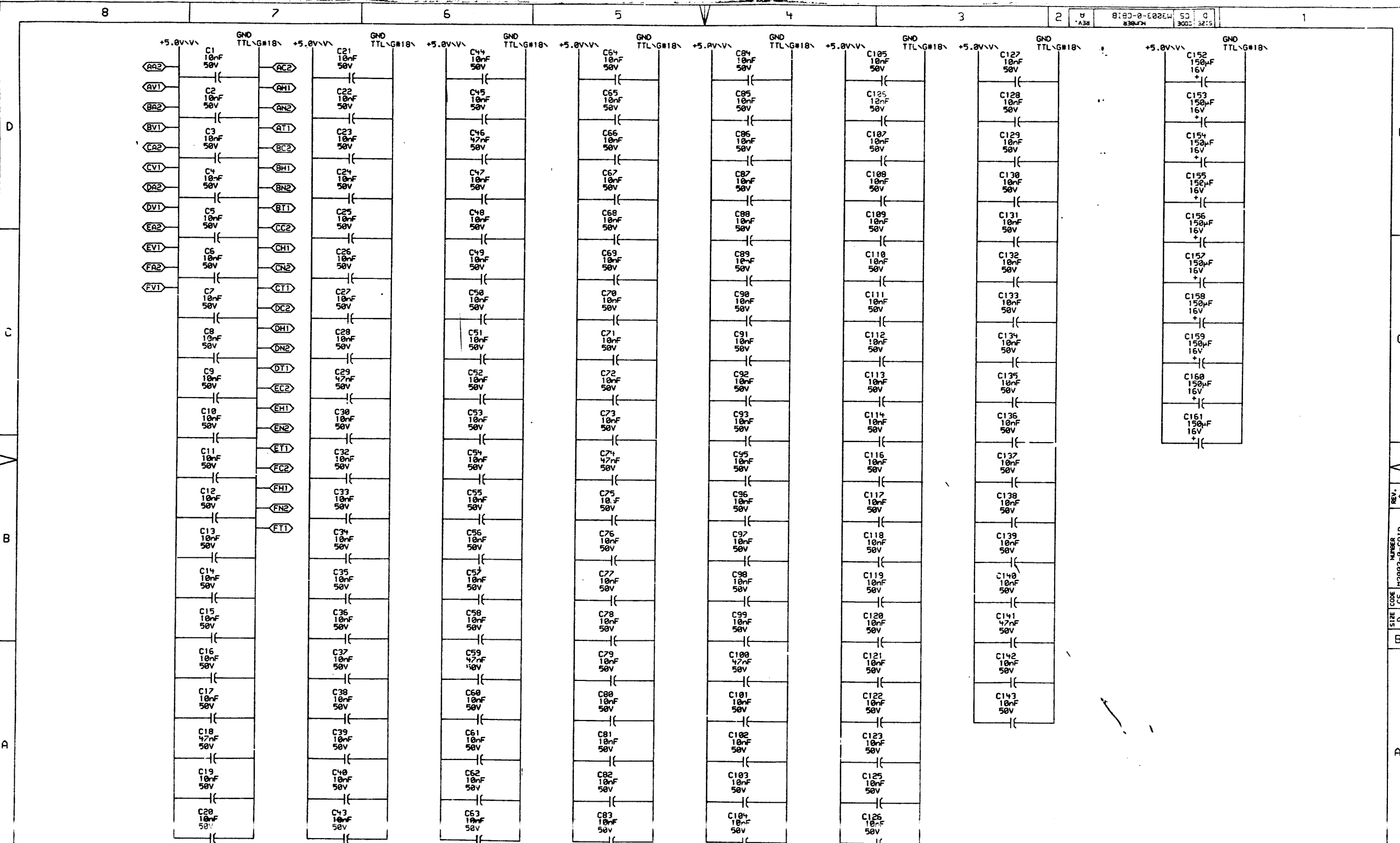
digit@l

DATE 03-APR-84 ENG. R. Lam DATE 03-APR-84 TITLE: CI20 CBUS INTFC MBUS INTERFACE

CHK'D. DATE BOARD LOCATION: 19-APA SV SHEET 1 OF 1

SUBCOM: (BOWEN_ECO)CBIA00.DRU 129-MAR-84 09:06 NEXT HIGHER ASSEMBLY: SIZE CODE NUMBER REV. D C5 M3003-0-CBIA A

FIRST USED ON OPTION MODEL: CI20 D-DD-M3003-0



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REVISIONS		
CHK	CHANGE NO.	REV

DATE: 83-APR-84	ENG: R. Law	DATE: 83-APR-84	TITLE: C120 CBUS INTFC PWR & GND
CHK'D: M. A. Science	DATE: 83-APR-84	BOARD LOCATION: 1 OF 1	SIZE CODE: D CS
SUBCOM: <BOWEN.ECD>C81800.DR4 [29-MAR-84 09:06 NEXT HIGHER ASSEMBLY: D-DD-M3003-0		NUMBER: M3003-0-CB1B	
FIRST USED ON OPTION MODEL: C120		REV: A	

DRN: J. Lashby
 DATE: 83-APR-84
 ENG: R. Law
 DATE: 83-APR-84
 TITLE: C120 CBUS INTFC PWR & GND
 BOARD LOCATION: 1 OF 1
 SIZE CODE: D CS
 NUMBER: M3003-0-CB1B
 REV: A

- | | | | | |
|----------------------|----------------------|----------------------|----------------------|----------------------|
| AB2 — N/C SEE NOTE 3 | BF1 — N/C EXTRA GND | DB2 — N/C SEE NOTE 3 | EP2 — N/C SEE NOTE 2 | FL2 — N/C SEE NOTE 2 |
| AB1 — N/C SEE NOTE 3 | BH2 — EBUS D31 L | DB1 — N/C SEE NOTE 3 | EPI — N/C SEE NOTE 2 | FL1 — N/C SEE NOTE 2 |
| AC1 — N/C EXTRA GND | BJ2 — EBUS D34 L | DUI — N/C SEE NOTE 3 | ER2 — N/C SEE NOTE 2 | FM2 — N/C SEE NOTE 2 |
| AH2 — N/C EXTRA GND | BJ1 — EBUS D30 L | EB2 — N/C SEE NOTE 3 | ERI — N/C SEE NOTE 2 | FMI — N/C SEE NOTE 2 |
| AJ1 — N/C SEE NOTE 1 | BK1 — EBUS D35 L | EB1 — N/C SEE NOTE 3 | ES2 — N/C SEE NOTE 2 | FNI — N/C EXTRA GND |
| AK2 — EBUS D27 L | BL1 — EBUS D17 L | EC1 — N/C EXTRA GND | ES1 — N/C SEE NOTE 2 | FP2 — N/C SEE NOTE 2 |
| AK1 — EBUS D28 L | BM1 — N/C EXTRA GND | ED2 — N/C SEE NOTE 2 | ET2 — N/C EXTRA GND | FPI — N/C SEE NOTE 2 |
| AL2 — EBUS D26 L | BN1 — N/C EXTRA GND | ED1 — N/C SEE NOTE 2 | EUI — N/C SEE NOTE 3 | FR2 — N/C SEE NOTE 2 |
| AL1 — EBUS D29 L | BP2 — N/C EXTRA GND | EE2 — N/C SEE NOTE 2 | FB2 — N/C SEE NOTE 3 | FRI — N/C SEE NOTE 2 |
| AN1 — N/C EXTRA GND | BP1 — EBUS D24 L | EE1 — N/C SEE NOTE 2 | FBI — N/C SEE NOTE 3 | FS2 — N/C SEE NOTE 2 |
| AT2 — N/C EXTRA GND | BR2 — EBUS D21 L | EF2 — N/C SEE NOTE 2 | FC1 — N/C EXTRA GND | FS1 — N/C SEE NOTE 2 |
| AU2 — N/C SEE NOTE 1 | BR1 — EBUS D19 L | EF1 — N/C SEE NOTE 2 | FD2 — N/C SEE NOTE 2 | FT2 — N/C EXTRA GND |
| AU1 — N/C SEE NOTE 3 | BS2 — EBUS D20 L | EH2 — N/C EXTRA GND | FD1 — N/C SEE NOTE 2 | FU2 — N/C EXTRA GND |
| BB2 — N/C SEE NOTE 3 | BS1 — EBUS D23 L | EJ2 — N/C SEE NOTE 2 | FE2 — N/C SEE NOTE 2 | FUI — N/C SEE NOTE 3 |
| BB1 — N/C SEE NOTE 3 | BT2 — EBUS D18 L | EJ1 — N/C SEE NOTE 2 | FE1 — N/C SEE NOTE 2 | FV2 — N/C SEE NOTE 4 |
| BC1 — N/C EXTRA GND | BU2 — EBUS D25 L | EK2 — N/C SEE NOTE 2 | FF2 — N/C SEE NOTE 2 | |
| BD2 — EBUS D32 L | BU1 — N/C SEE NOTE 3 | EK1 — N/C SEE NOTE 2 | FF1 — N/C SEE NOTE 2 | |
| BD1 — N/C EXTRA GND | BV2 — EBUS D22 L | EL2 — N/C SEE NOTE 2 | FH2 — N/C EXTRA GND | |
| BE2 — N/C EXTRA GND | CB2 — N/C SEE NOTE 3 | EL1 — N/C SEE NOTE 2 | FJ2 — N/C SEE NOTE 2 | |
| BE1 — EBUS D33 L | CB1 — N/C SEE NOTE 3 | EM2 — N/C SEE NOTE 2 | FJ1 — N/C SEE NOTE 2 | |
| BF2 — N/C EXTRA GND | CC1 — N/C EXTRA GND | EN1 — N/C SEE NOTE 2 | FK2 — N/C SEE NOTE 2 | |
| | CU1 — N/C SEE NOTE 3 | EN1 — N/C EXTRA GND | FK1 — N/C SEE NOTE 2 | |

NOTE 1: THESE PINS ARE CONNECTED AS SHOWN BELOW

FROM	TO
F1352	A15J1
E13V2	A15U2

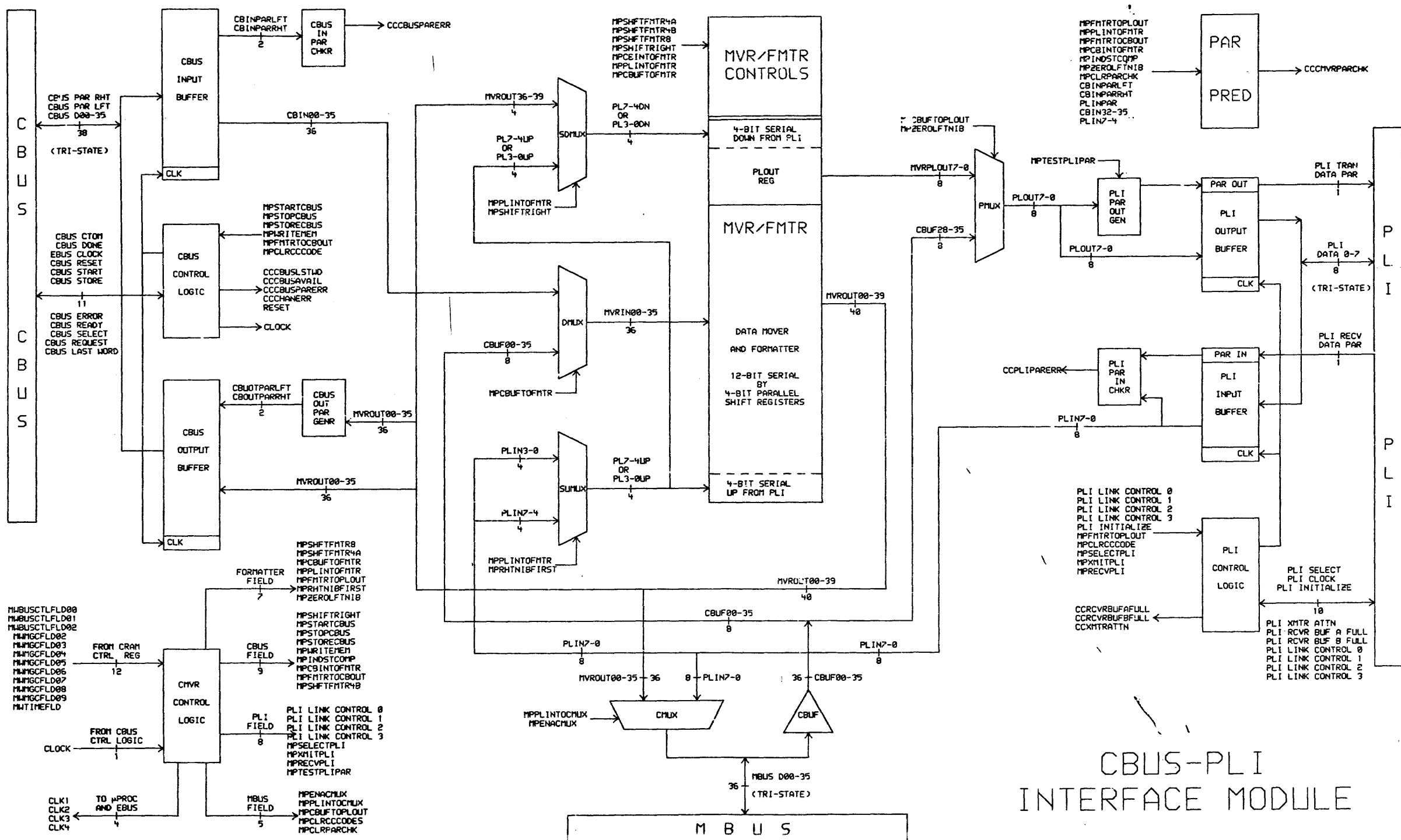
NOTE 2: RSVD FOR MASS BUS
 NOTE 3: RSVD FOR -5.2V
 NOTE 4: THESE PINS DO NOT CONNECT TO EITHER THE EBUS OR MPRO

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>R. Low</i>	DATE: 03-APR-84	ENG. <i>R. Low</i>	DATE: 03-APR-84	TITLE: CI20 CBUS INTFC UNUSED FINGERS
	CHK'D <i>R. Low</i>	DATE: 03-APR-84	BOARD LOCATION: 1 OF 1	SHEET: 1 OF 1	SIZE CODE: D CS
SUBCOM: <BOWEN.ECO>CBIC00.DRW		09-MAR-84 09:06	NEXT HIGHER ASSEMBLY: 10-DD-M3003-0		NUMBER: M3003-0-CB1C
FIRST USED ON OPTION/MODEL: CI20		10-DD-M3003-0		REV: A	MR

D
C
B
A



CBUS-PLI INTERFACE MODULE

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	87-APR-84	ENG	DATE	87-APR-84
	CHK'D	19-APR-84	BOARD LOCATION	SHEET	OF
SUPPORT: <BOWEN,ECO>CBUS.DRI		129-MAR-84	09:06	NEXT HIGHER ASSEMBLY:	
FIRST USED ON OPTION/MODEL:		KLIPA	D-DD-M3003-0		

TITLE:		BLOCK DIAGRAM CBUS MODULE		
SIZE	CODE	NUMBER	REV.	
D	BD	M3003-0-BLK	A	

DRAWING NO.	NO. OF SHOTS.	PART NO.	DESCRIPTION	REVISIONS															
				A	B	C	D												
		LO100-01	PART REVISION																
BDD-LO100-0	1		ILI	A	B	C	D												
EUA-LO100-0-0	3		ILI	A	B	C	D												
DMD-5014430-0-0	5		DRILL & ETCH DRAWING	A	A	B	C												
E-EG-5014430-0-0	3		ETCH CUT DRAWING	A	B	C	D												
		5014430	ETCHED BOARD	B	B	C	C												
K-PG-LO100-0-DBI			PC DATA BASE	B	B	C	C												
K-CS-LO100-0-DBS			CS DATA BASE	A	A	A	B												
K-PL-LO100-0-DBF	4		PL DATA BASE	A	B	B	C												
K-BS-LO100-0-DBS			BS DATA BASE	A	A	A	A												
K-TD-LO100-0-DBS			TD DATA BASE	A	A	A	A												
D-TD-LO100-0-2	8		ILI	A	A	A	A												
D-BS-LO100-0-3	1		ILI	A	A	A	A												
K-CS-LO100-0-ILIA	1			-	-	C	C												
K-CS-LO100-0-ILIB	1			-	-	C	D												
K-CS-LO100-0-ILIC	1			-	-	C	C												
K-CS-LO100-0-ILID	1			-	-	C	C												
K-CS-LO100-0-ILIE	1			-	-	C	C												
K-CS-LO100-0-ILIF	1			-	-	C	C												
K-CS-LO100-0-ILIH	1			-	-	C	C												
K-CS-LO100-0-ILIJ	1			-	-	C	D												
K-CS-LO100-0-ILIK	1			-	-	C	D												
K-CS-LO100-0-ILIL	1			-	-	C	D												
K-CS-LO100-0-ILIM	1			-	-	C	D												
K-CS-LO100-0-ILIN	1			-	-	C	C												

NOTES:

REVISIONS	CHG NO.	REV.	DATE	REVISIONS															
				B	C	D													
	6-82	TW001	B																
	9-82	TW002	C																
	10-82	TW003	D																

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USED ON OPTION/MODEL	DRN.	CHK'D	ENG.	PROD.
CI780	Casey			9-28-81
CI750	Casey			9-28-81
HSC50				9-28-81
CI20				9-28-81

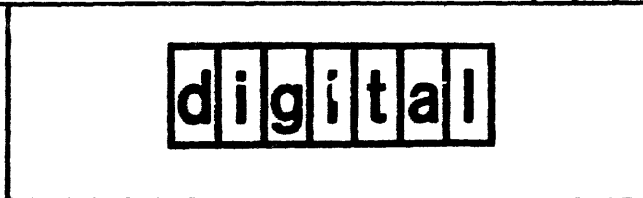
TITLE INTERPROCESSOR LINK INTERFACE
 SIZE B CODE DD NUMBER LO100-0 REV. D
 SHEET 1 OF 3
 TW

DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS													
K-CS-LO100-0-ILIP				-	-	C	D										
K-CS-LO100-0-ILIR				-	-	C	C										
K-CS-LO100-0-ILIS				-	-	C	C										
K-CS-LO100-0-ILIT				-	-	C	C										
K-CS-LO100-0-ILIU				-	-	C	C										
K-CS-LO100-0-ILIV				-	-	C	C										
K-CS-LO100-0-ILIW				-	-	C	C										
K-CS-LO100-0-ILIX				-	-	C	D										

NOTES:

REVISIONS		DATE	CHG NO.	REV.
		9 82	TW002	C
		10 83	TW003	D

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USED ON OPTION/MODEL
CI780
CI780
HSC50
CI20

DRN	<i>Thomas F. Welch</i>	9-22-82
CHK'D	<i>Thomas F. Welch</i>	9-22-82
ENG.		
PROD.		

TITLE INTERPRESSOR LINK INTERFACE			
SIZE	CODE	NUMBER	REV.
B	DD	LO100-0	D
SHEET 2 OF 3			

REV. D
 NUMBER 0-00107
 CODE DD
 SIZE B

DRAWING NO.	NO. OF SHES.	PART NO.	DESCRIPTION	REVISIONS																			
EUA-LO100-0-0	3		ILI	BI	B2																		
EEC 5014430-0-0	3		ILI	BI	B2																		

NOTES:

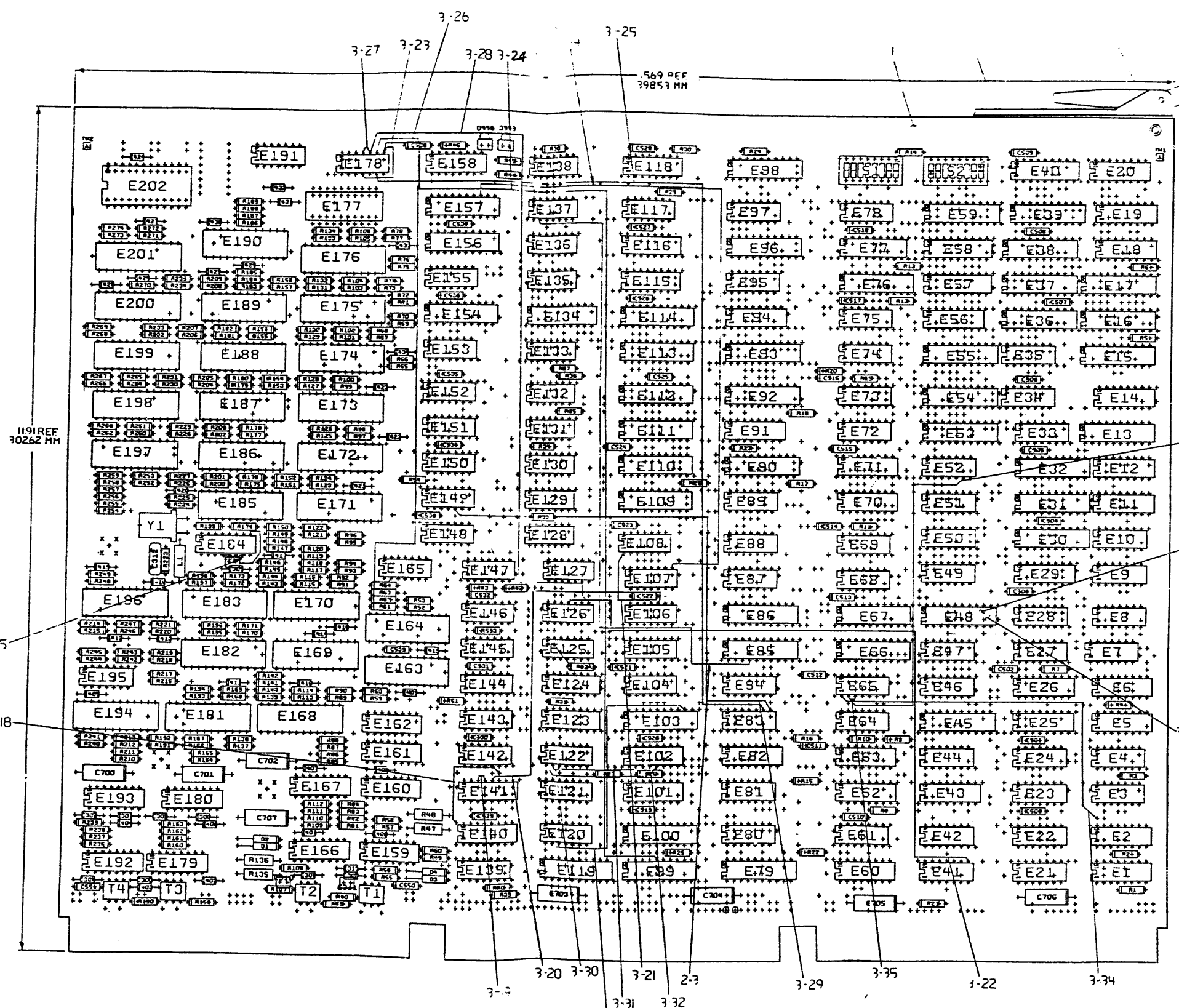
REVISIONS	
DATE	CHG NO. REV.
9-82	TW002 C
1083	TW003 D

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USED ON OPTION/MODEL	DBN	CHK'D	ENG.	PROD.
CI780	ThomasFWalsh			
CI750		ThomasFWalsh		
HSC50				
CI20				

TITLE INTERPRESSOR LINK INTERFACE
 SIZE CODE NUMBER REV.
B DD LO100-0 D
 SHEET 3 OF 3



DATE	1977 03 22
DRN	3-30
CHK'D BY	J. J. KELLY
DESIGNED BY	J. J. KELLY
PROJ. ENGR.	...
SCALE	1:1
SHEET NO.	1 OF 1
REV.	...
TITLE	digital
LINK INTERFACOR	...
LINK INTERFACE	...
STANDARD NUMBER	...
EUA	10000-3-0

CHANGE NO.	...
DATE	...
BY	...
DESCRIPTION	...

NOTES:
1. ALL IC LOCATIONS ARE 401 & 402

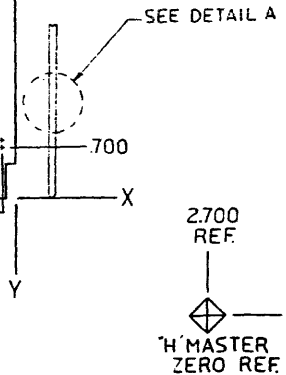
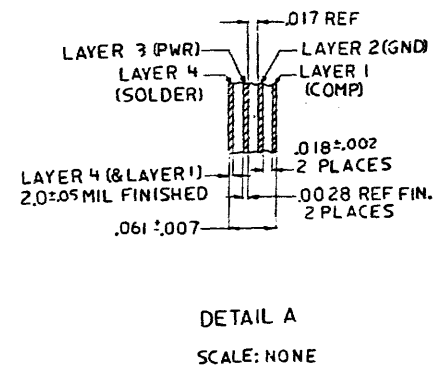
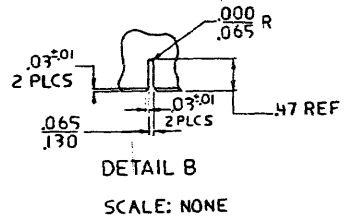
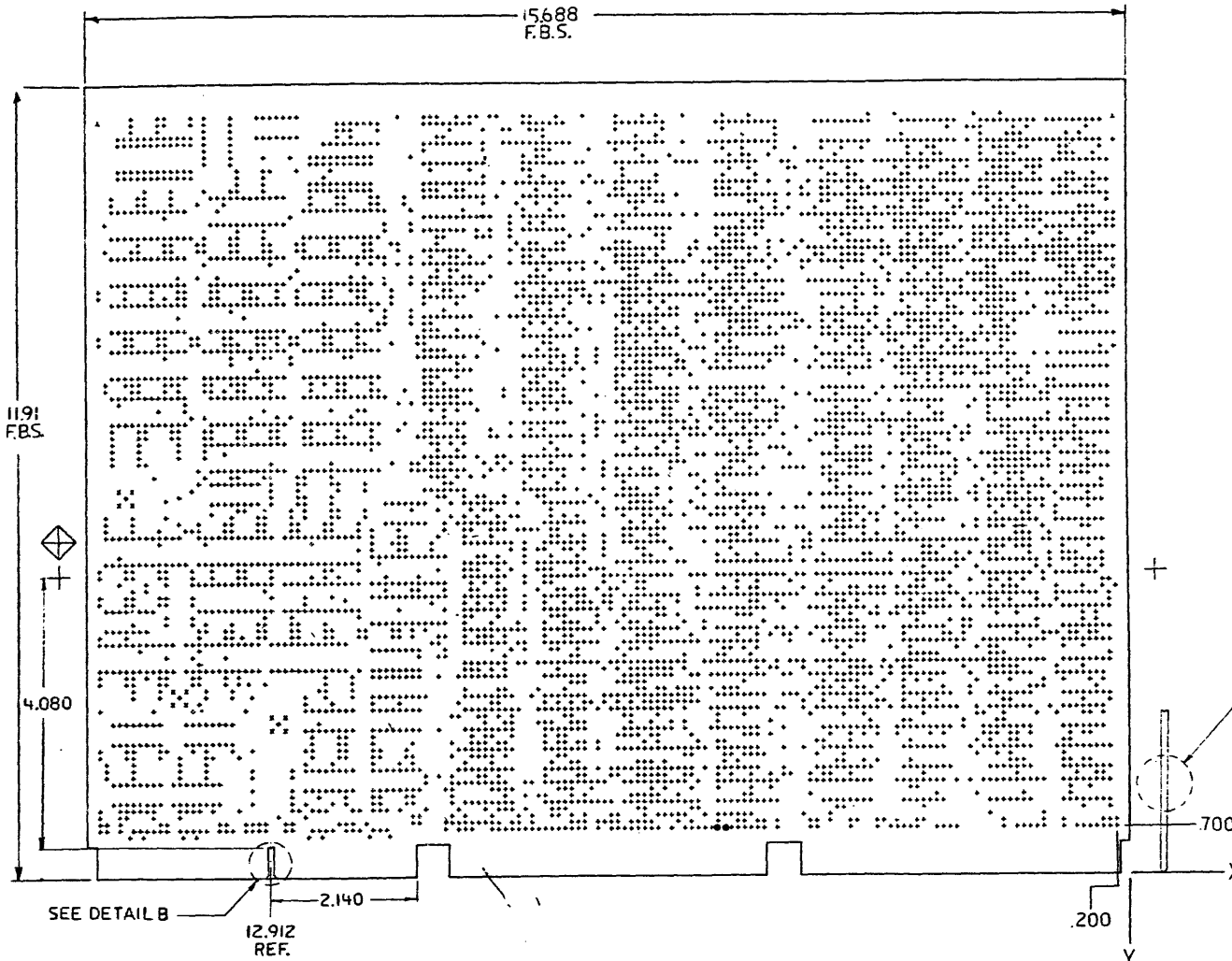
STEP	DATE	BY	TIME
1
2

Grid letters: A, B, C, D, E, F, G, H, J, K, L, M, N, P, Q, R, S, T, U, V, W, X, Y, Z

Grid numbers: 1, 2, 3, 4, 5, 6, 7, 8, 9, 10

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VIEWED FROM SIDE 1
COMPONENT SIDE



FOR HOLE TOLERANCES USE DEC STD 176

SYMBOL	+	A	X	+	Z	M	A	0	*
FIN HOLE SIZE	.008	.157	.066	.042					
PLATED									
NON PLATED									
QTY.	5881	2	12	3					
OFF GRID HOLE	2	0	0	0					
DRILL SIZE									

NOTE: ALL HOLE LOCATIONS ARE DESIGNED ON .025 GRID INCREMENTS FROM ORIGIN UNLESS SYMBOL IS CIRCLED.

DESIGN INFORMATION

CIRCUIT SIZE: X 15.688 Y 11.910 INCHES

CIRCUIT TYPE: PFC(1) PTH(1) ML(M)

CIRCUIT: H015X15(1)

TECHNOLOGY: S(0) H(12X13) H(20) OTHER(1)

FINGER CONNECTOR DD# PER D-MD-11EN603-1-3 (X AS SHOWN)

LAYER CONSTRUCTION PER (X AS SHOWN)

ARTWORK LAYOUT: MANUAL(1) CRO(M)

ENG SPECIAL FEATURES: SIDE 1 109

SPECIAL NOTES:
1. THIS BOARD USES 8/8 & 15/15 TECH.
2. USE ONLY DRY FILM SOLDER MASK.
3. FIN LINE WIDTH: 15.22 (IN 15 MIL SEC. ONLY)
4. IMPED. MUST BE: 75 OHM

FABRICATION INFORMATION

FABRICATE BOARD PER D-MD-11ME658-0-0 (X AS SHOWN)

SOLDER MASK, SIDE 1 (M) SIDE 2 (M) NONE(1)

SPECIFICATIONS AND STANDARDS:
MATERIALS AND WORKMANSHIP FOR ALL FABRICATED PRINTED WIRING BOARDS MUST MEET OR EXCEED THE REQUIREMENTS OF DEC STD 176.

SPECIAL NOTES:

TOLERANCES

INCHES UNLESS SPECIFIED

.XXX ± .005	ANGLES
.XX ± .010	±8 DEG 30 MIN
.X ± .020	

SIGNATURES

DATE

DRN: *Ron Love* 7-17-92

CHK'D: *E. T. GERE* 7-22-92

MECH ENG: *P. C. MUEBLING* 7-27-92

PROD ENG: *P. C. MUEBLING* 7-27-92

HFG ENG: *James L. Coates* 7-30-92

SCALE: 1:1

SHEET 1 OF 5

NEXT HIGHER ASSY: E-UA-L0100-0-0

digital

TITLE: CIRCUIT DRILL AND ETCH

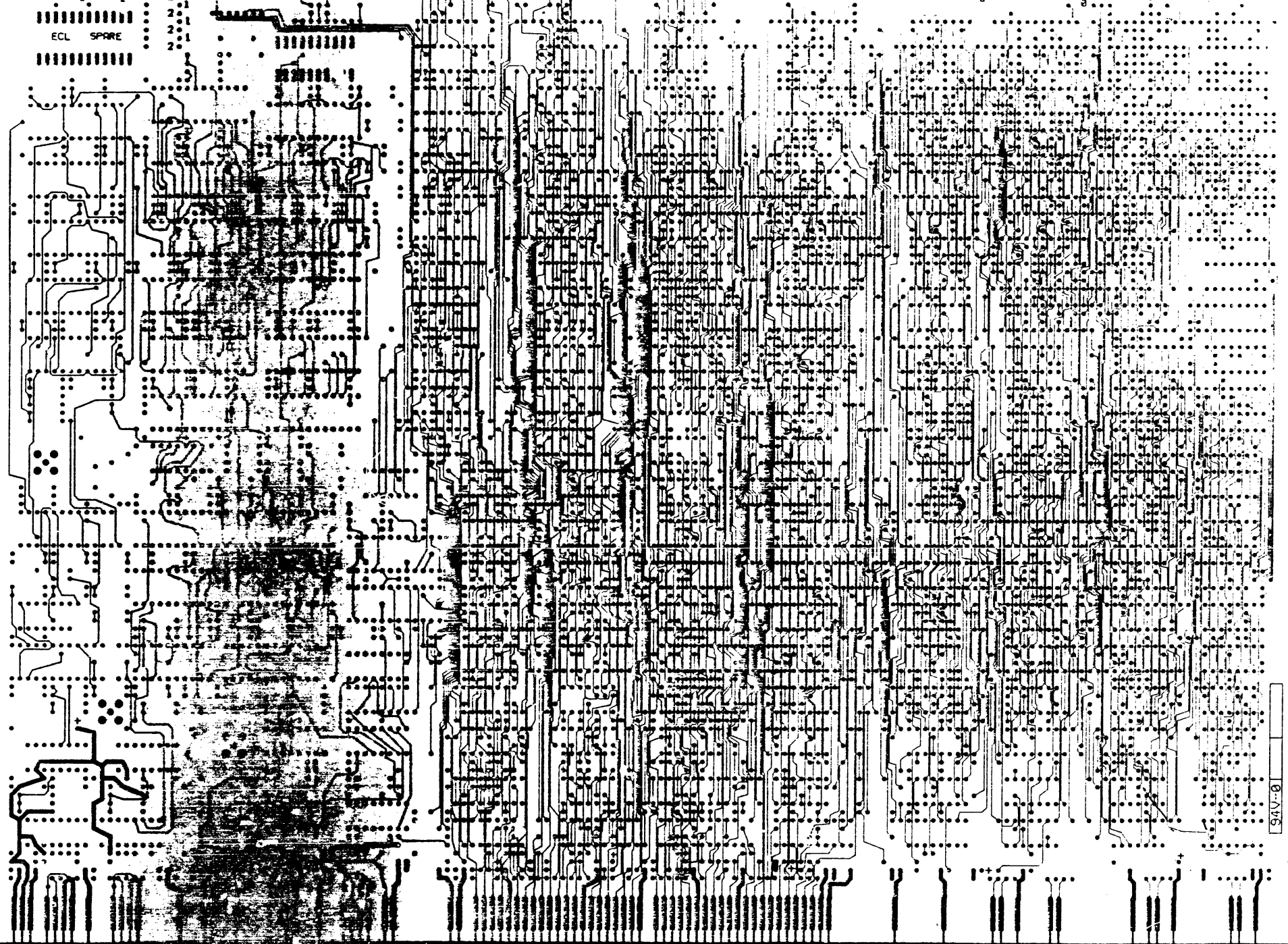
SIZE CODE NUMBER: 0 MD5014430-0-0 B

ETCH REV: CPI

1. CHANGE WIRE
2. LOUD-1W2 B
3. WIRE W/ 2Z
4. WIRE W/ 2Z
5. WIRE W/ 2Z

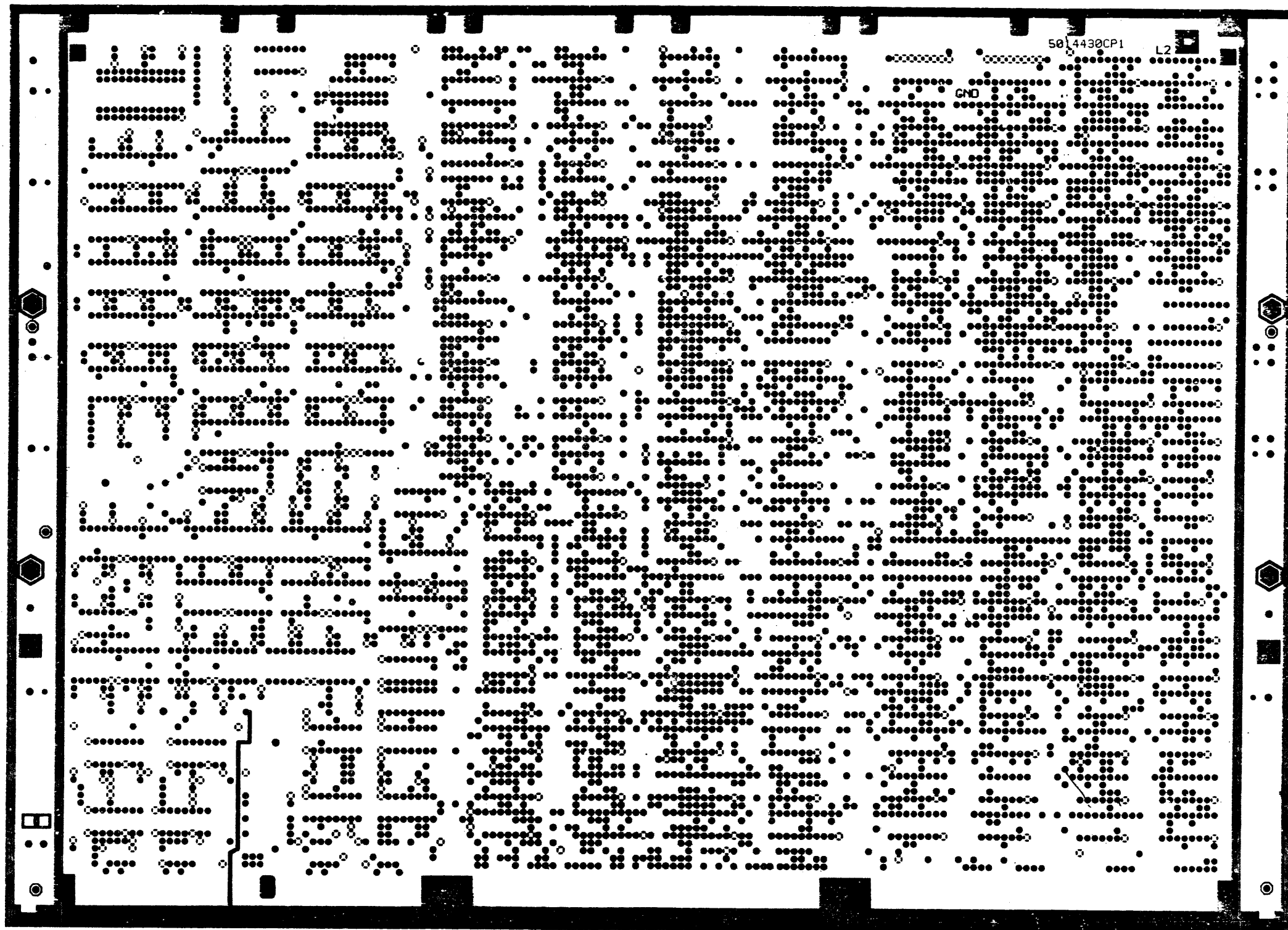
DIM 5014430-0-0

051AB00EFHUKL44RS
E202 2-5.2V
1-5.2V
2-5.2V
ECL SPARE
E191
E165
E138 0998 0998
LOCAL C. ACTIVITY
INT LOOP
E138
E118
E99 L0100
CNODE ADDR > S1E28
TNODE ADDR > S2 E59
G-9411
S31443007
F40



94V-0

LAYER C

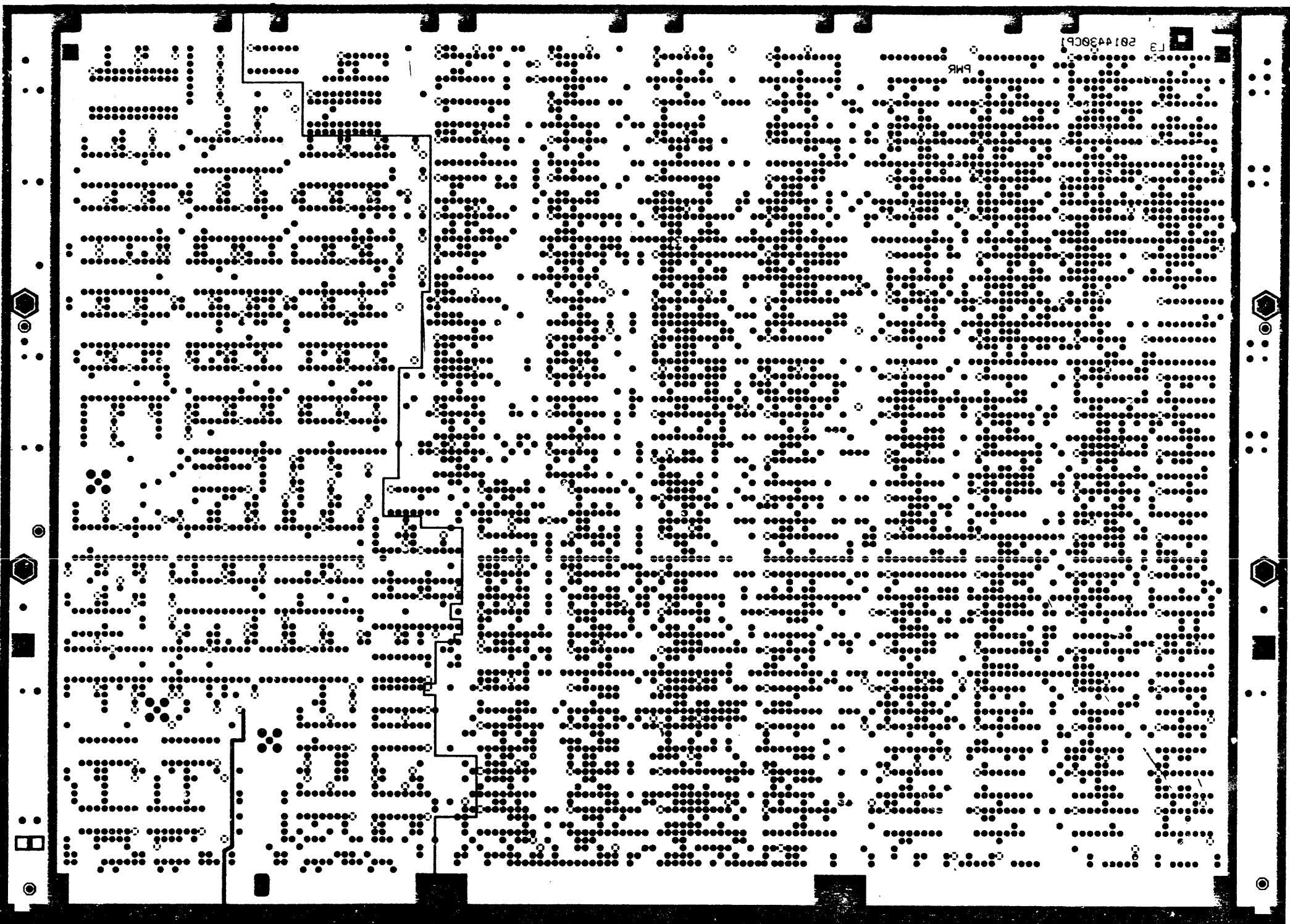


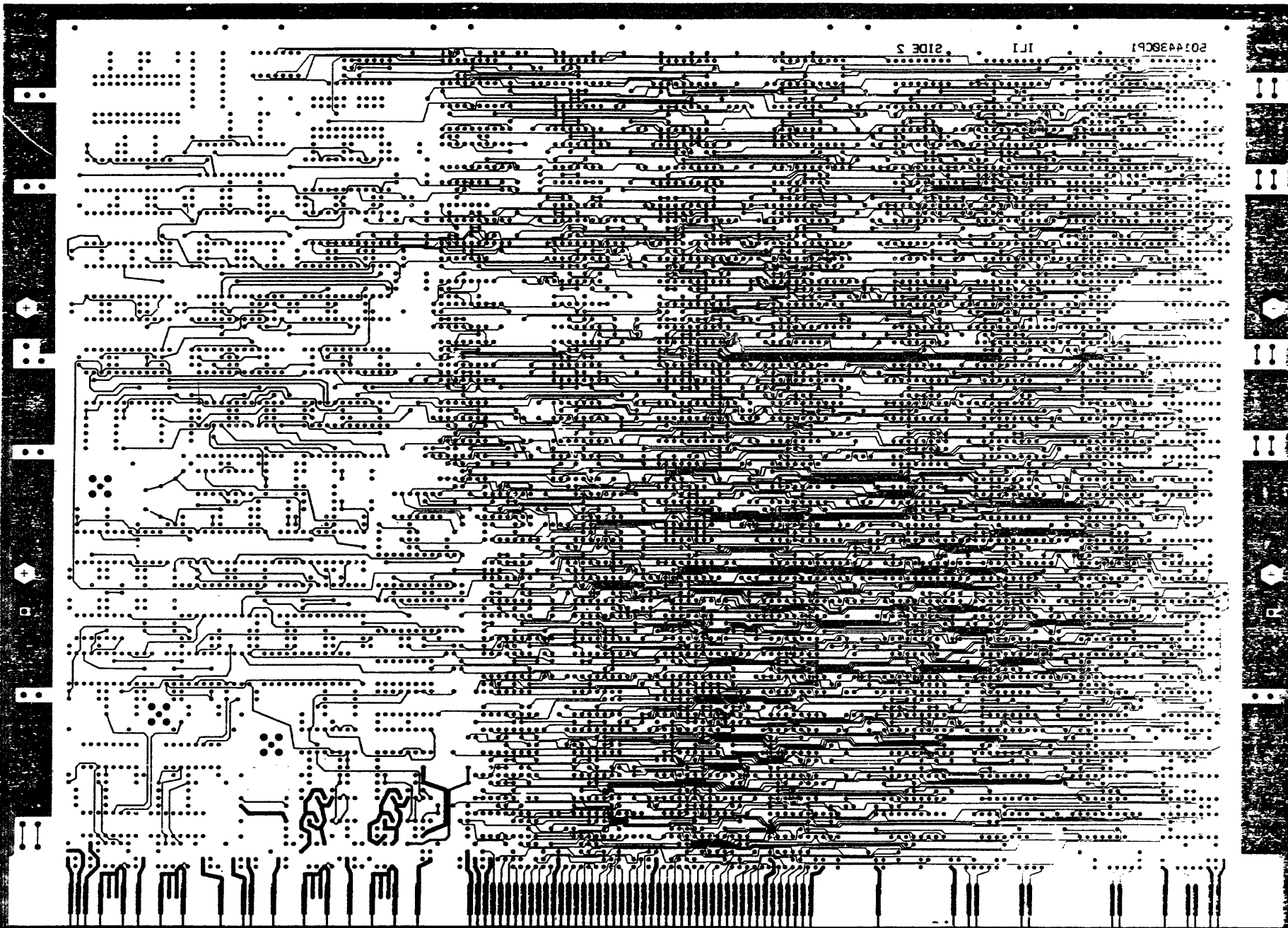
504430CP1 L2

GND

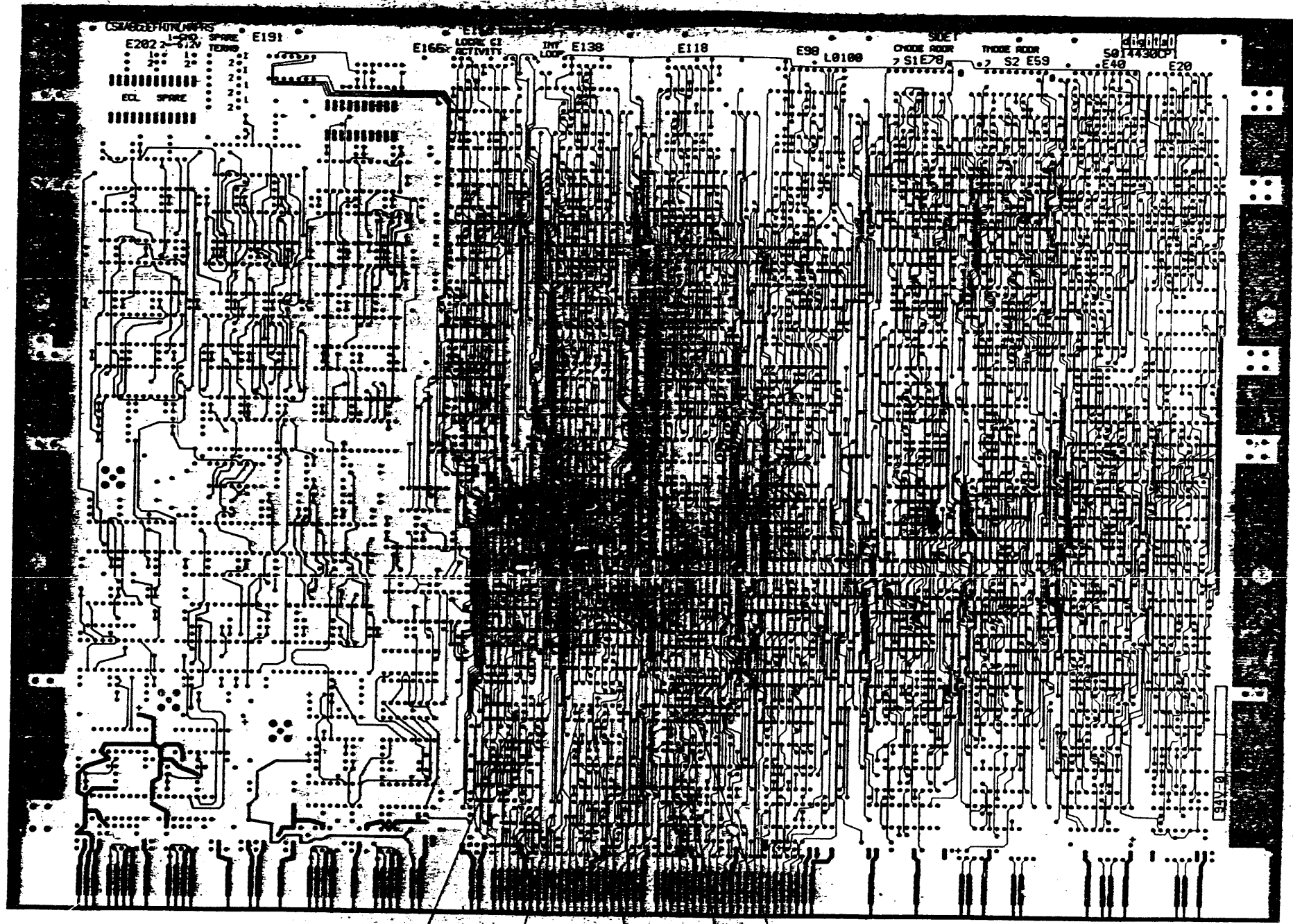
2814430CP

PHR





LAYER 1



3-1 3-3 3-4 3-2 3-5

NO.	DESCRIPTION	REV.
1	ORIGINAL	1
2	MODIFIED	2
3	REVISED	3
4	REWORKED	4
5	REDESIGNED	5
6	REMANUFACTURED	6
7	REPLACED	7
8	REMOVED	8

REV.	DATE	DESCRIPTION

DESIGNER	S. MANSON	DATE	2-16-73
CHECKED	T.F. WALSH	DATE	2-16-73
DRAWN	T.C. ANDREWS	DATE	2-16-73
COPY	T.C. ANDREWS	DATE	2-16-73

TITLE: ETCH CUT DRAWING

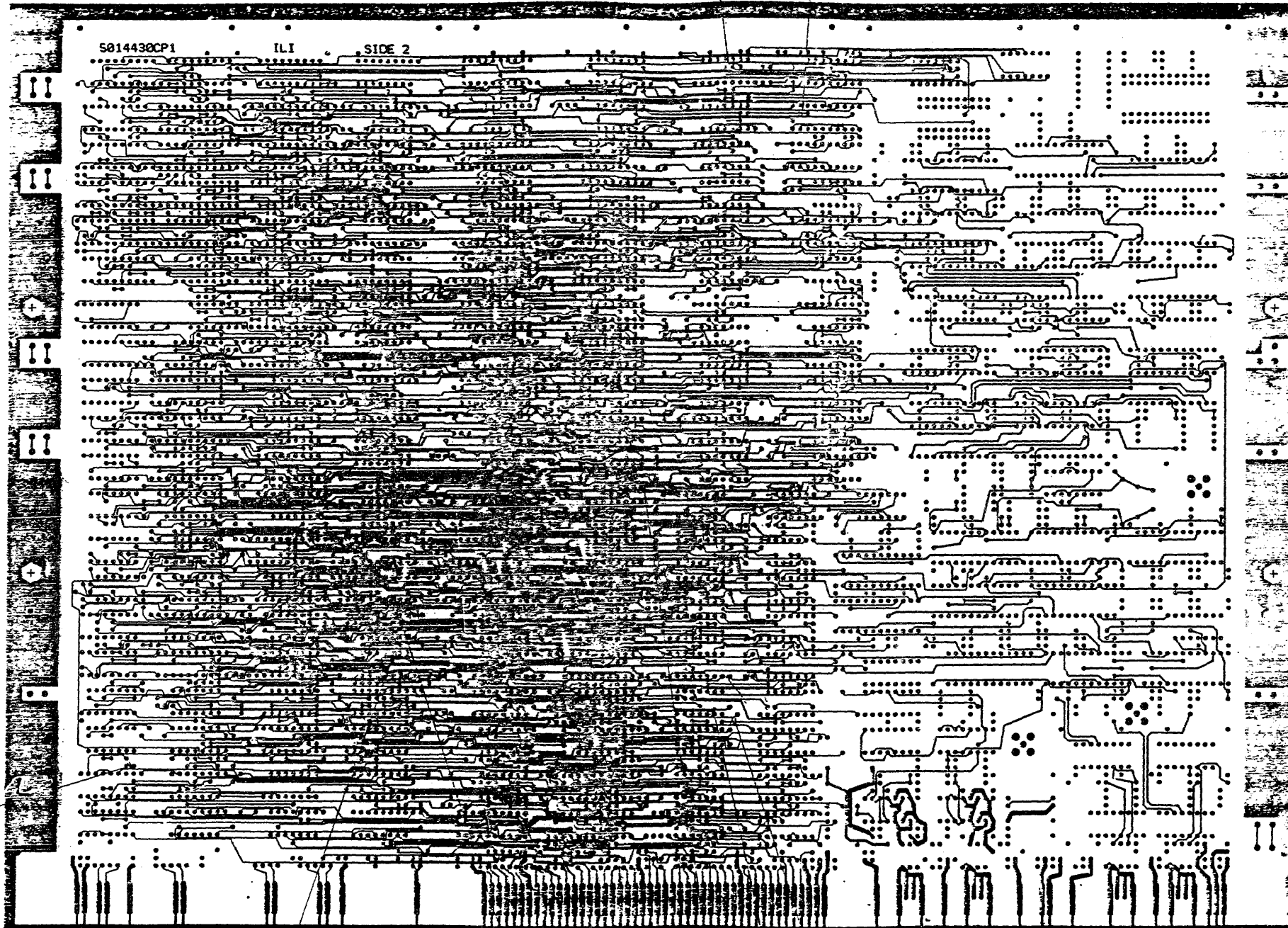
MATERIAL: 100-1000-0-0

E C 501430-C-0-C

S014430CP1 ILI SIDE 2

REV 834AJ

21 22



3-8

3-10

3-11

3-7

3-9

3-6

DATE	BY	CHKD

TITLE	DOCUMENT NUMBER
ETCH CUT DRAWING	E E C 304430-0-0 C
SCALE 2:1	SHEET 2 OF 3

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REWORK INSTRUCTIONS

ETCH CUTS SIDE 2

2-1 CUT ETCH AT E137-2

2-2 CUT ETCH AT PTH BELOW E157-13

ECO# 3

ETCH CUTS SIDE 1

- 3-1 EMI-12
- 3-2 E99-16
- 3-3 E122-2
- 3-4 E122-3
- 3-5 E102-5

ETCH CUTS SIDE 2

- 3-6 PTH BELOW E122-1
- 3-7 E108-1
- 3-8 PTH ABOVE E2-16
- 3-9 E122-2
- 3-10 PTH BELOW E65-2
- 3-11 PTH BELOW E65-19

J
H
F
E
D
C
B
A

J
H
F
E
D
C
B
A

2
0-0-0244105-1
1
1

REVISION HISTORY		
DATE	ECO NUMBER	REV

DOCUMENT NUMBER		
REV	ECO	REV

TITLE
ETCH CUT DRAWINGS

SCALE 2" = 1" SHEET 2 OF 2

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REWORK INSTRUCTIONS

WIRE ADDS SIDE I LO100-TW002

- 2-3 ADD WIRE FROM E35-4 TO E137-2
- 2-4 ADD WIRE FROM E107-8 TO E157-13
- 2-5 ADD WIRE FROM E184-6 TO E184-9

ECO#3

WIRE ADDS SIDE I

- 3-18 FROM PTH TO THE LEFT OF E40-16 TO PTH ABOVE E41-11
- 3-19 FROM E41-13 TO E41-14
- 3-20 FROM E106-10 TO E41-12
- 3-21 FROM E106-11 TO E108-11
- 3-22 FROM E41-8 TO E125-11
- 3-23 FROM E178-4 TO E178-8
- 3-24 FROM E178-8 TO E99-16
- 3-25 FROM E178-6 TO E83-9
- 3-26 FROM E178-9 TO E165-2
- 3-27 FROM E178-10 TO E178-11
- 3-28 FROM E178-10 TO E126-8
- 3-29 FROM E83-8 TO E149-5
- 3-30 FROM E122-2 TO E122-3
- 3-31 FROM E122-3 TO E102-2
- 3-32 FROM E122-1 TO E102-5
- 3-33 FROM E120-2 TO E103-12
- 3-34 PTH ABOVE E2-16 TO E65-1
- 3-35 FROM E65-2 TO E65-3
- 3-36 FROM E52-6 TO E65-2

COMPONENT DELETE SIDE I

3-37 E48 (23-058K3-00)

COMPONENT ADD SIDE I

3-38 E48 (23-091K3-00)

REVISION HISTORY	
DATE	ECO NUMBER

INTERPROCESSOR
LINK INTERFACE

DOCUMENT NUMBER	
ORGANIZATION	NUMBER
EUA	LO100-0-0
SCALE	2/1
SHEET	2 OF 2

EUA LO100-0-0

TW

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	VARIATION REVISION LEVEL:	QTY PER VARIATION	REFERENCE DESIGNATOR
1	1	5014430-00		L0100 ETCH BOARD		1	
2	78	1001610-00		.01 MFD 50V +80-20% Z5U CER		45	C300-C308,C313,C400-C434
3	2	1001739-00		27.0 MMF 100V 5%200PPM MICA		1	C312
4	3	1011740-01		3300.0 MMF 50V 10% CER		3	C309-C311
5	91	1012784-00		.047 MFD 50V +80-20% CER		42	C500-C538,C550,C551,C539
6	90	1017472-00		10 MFD 35V +75-10% AL EL		8	C700-C707
7	4	1109517-00		PIV= 75 IO=150 MA - 4NS 1N914B		4	D1-D4
8	98	1117373-00		LED ASSY GREEN		1	D998
9	99	1117373-01		LED ASSY RED		1	D999
10	5	1217310-05		SW,DIP 8POS/1PST 5VDC100MA S		2	S1,S2
11	6	1216988-02		HANDLE,MODULE,HEX TWO EJECTORS		1	
12	7	1300005-01		R NETWORK 13-1K 5.0 % 14PIN		1	E78
13	8	1300005-07		R NETWORK 15-4.7K 5.0 % 16PIN		2	E19,E56
14	9	1300250-00		150.0 .25 W 5.0 % CF		7	R17,R18,R28,R55,R56,R107,R108
15	10	1300295-00		330.0 .25 W 5.0 % CF		2	R44,R45
16	11	1300365-00		1.0 K .25 W 5.0 % CF		40	R1-R5,R7-R11,R14-R16,R19-R26, CONT R29,R30,R32-R36,R42,R43,R46,R51, CONT R54,R593,R27,R31,R37,R38-R40
17	18	1300447-00		4.70 K .25 W 5.0 % CF		2	R12,R13
18	12	1301425-00		300.0 .25 W 5.0 % CF		4	R79,R80,R159,R190
19	13	1301781-00		82.0 .50 W 5.0 % CF		4	R47,R48,R135,R136
20	14	1302602-00		55.0 .25 W 5.0 % CF		1	R6
21	15	1302956-00		196.0 .25 W 1.0 % RN55D-F10		103	R50,R53,R58,R60,R61,R63,R66,R67, CONT R69,R72,R74,R75,R77,R81,R83,R86, CONT R88,R90,R92,R93,R96,R97,R100, CONT R102,R104,R106,R109,R111,R113, CONT R115,R118,R120,R121,R124,R126, CONT R128,R129,R132,R133,R137,R139, CONT R142,R146,R148,R150,R152,R154, CONT R155,R158,R164,R166,R169,R170.

REVISION HISTORY			BASIC PART NO: L0100								
ENG	ECO NUMBER	REV	SECTION A OF A	DRN:	J.CASEY	DATE:	24-JUN-81	D I G I T A L			
---	INITIAL	A	SECTION VARIATION INDEX	CHK'D:	E.T.GERRY	DATE:	24-JUN-81	TITLE PARTS LIST			
JB	L0100-TW001	B	[A] 00					INTERPROCESSOR			
FD	L0100-TW003	C	[B]					LINK INTERFACE			
			[C]	DES.ENG:	J.BUZYNSKI	DATE:	28-SEP-81	DOCUMENT NUMBER			
			[D]					SIZE	CODE	NUMBER	REV
			[E]	RESP.ENG.:	J.BUZYNSKI	DATE:	28-SEP-81	K	PL	L0100-0-DBP	C
			[F]					RELEASE DATE: 26-MAR-84			
			[H]	MFG.ENG.:	M.WARSHAW	DATE:	28-SEP-81				
			[J]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	EDIT #		
			[K]	E-UA-L0100-0-0				Z1292C.PLS	23		
			[L]								
			[M]								
			[N]								

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LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	VARIATION REVISION LEVEL:	QTY PER VARIATION	REFERENCE DESIGNATOR
							00	
22	16		1302957-00	121.0	.25 W 1.0 % RN55D-F10		99	CONT R173-R175,R178,R180,R181, CONT R184-R186,R188,R191,R194,R195, CONT R197,R201,R203,R205,R206,R208, CONT R210,R212,R215,R217,R218,R220, CONT R224,R226,R228,R231,R232,R234, CONT R241,R242,R245,R247,R249,R252, CONT R254,R256,R258,R261,R262,R264, CONT R266,R268-R271,R273,R143 R49,R52,R57,R59,R62,R64,R65,R68, CONT R70,R71,R73,R76,R78,R82,R84,R85, CONT R87,R89,R91,R94,R95,R98,R99, CONT R101,R103,R105,R110,R112,R114, CONT R116,R117,R119,R122,R123,R125, CONT R127,R130,R131,R134,R138,R140, CONT R141,R144,R145,R147,R149,R151, CONT R153,R156,R157,R165,R167,R168, CONT R172,R176,R177,R179,R182,R183, CONT R187,R189,R192,R193,R196, CONT R198-R200,R202,R204,R207,R209, CONT R211,R213,R214,R216,R219,R221, CONT R225,R227,R229,R230,R233,R235, CONT R240,R243,R244,R246,R248,R253, CONT R255,R257,R259,R260,R265,R267, CONT R272,R274,R171,R263 R163,R239 R223 R160,R161,R237,R238 R162,R236 L1 T1,T2 E195 T3,T4 Y1 E33,E52,E89,E107,E128,E139,E145, CONT E149,E151 E61,E69,E126,E129 E34,E35,E87,E148 E104 E108,E127 E125,E131,E152 E3,E4,E121,E133,E153,E10,E80, CONT E81,E136,E138,E191 E26,E29,E63,E101,E118 E6 E115,E116 E160,E162 E167,E184 E161
23	17		1303111-00	205.0	.25 W 1.0 % RN55D-F10		2	
24	19		1312930-00	5.10 K	.25 W 5.0 % CF		1	
25	20		1313008-00	5.11	.25 W 1.0 % RN55D-F10		4	
26	21		1313155-00	604.0	.25 W 1.0 % RN55D-F10		2	
27	22		1603377-00	.22UH	10% 2.7A #WEE.22		1	
28	23		1617970-00		PULSE XFMR 250MW 6PIN DIP		2	
29	24		1617985-00		DELAY= 10.2NS 14PIN DIP		1	
30	103		1618515-00		PULSE XFMR RATIO 2:1,250 MW		2	
31	25		1812051-18		XTAL 70.000 MHZ		1	
32	26		1910532-00		74S00 NAND GATE-QUAD 2IN		9	
33	27		1910534-00		74S04 INVERTER GATE-HEX 1I		4	
34	28		1910536-00		74S10 NAND GATE-TRIPLE 3IN		4	
35	29		1910537-00		74S11 AND GATE-TRIPLE 3INP		1	
36	30		1910539-00		74S20 NAND GATE-DUAL 4INPU		2	
37	31		1910544-00		74S74 FF-D DUAL,EDGE TRIGG		3	
38	32		1910544-01		74S74-60GG-D DUAL,EDGE TRIG		11	
39	33		1910545-00		74S112 FF-JK DUAL,EDGE TRIG		5	
40	34		1910547-00		74S153 MUX 1 OF 4 (DUAL)		1	
41	35		1910548-00		74S157 MUX 1 OF 2 (QUAD)		2	
42	36		1911404-00		10107 XOR/NOR GATE,3-2IN		2	
43	37		1911409-00		10115 QUAD DIF.LINE RCVR		2	
44	38		1911414-00		10124 TTL TO ECL TRNSLTR		1	

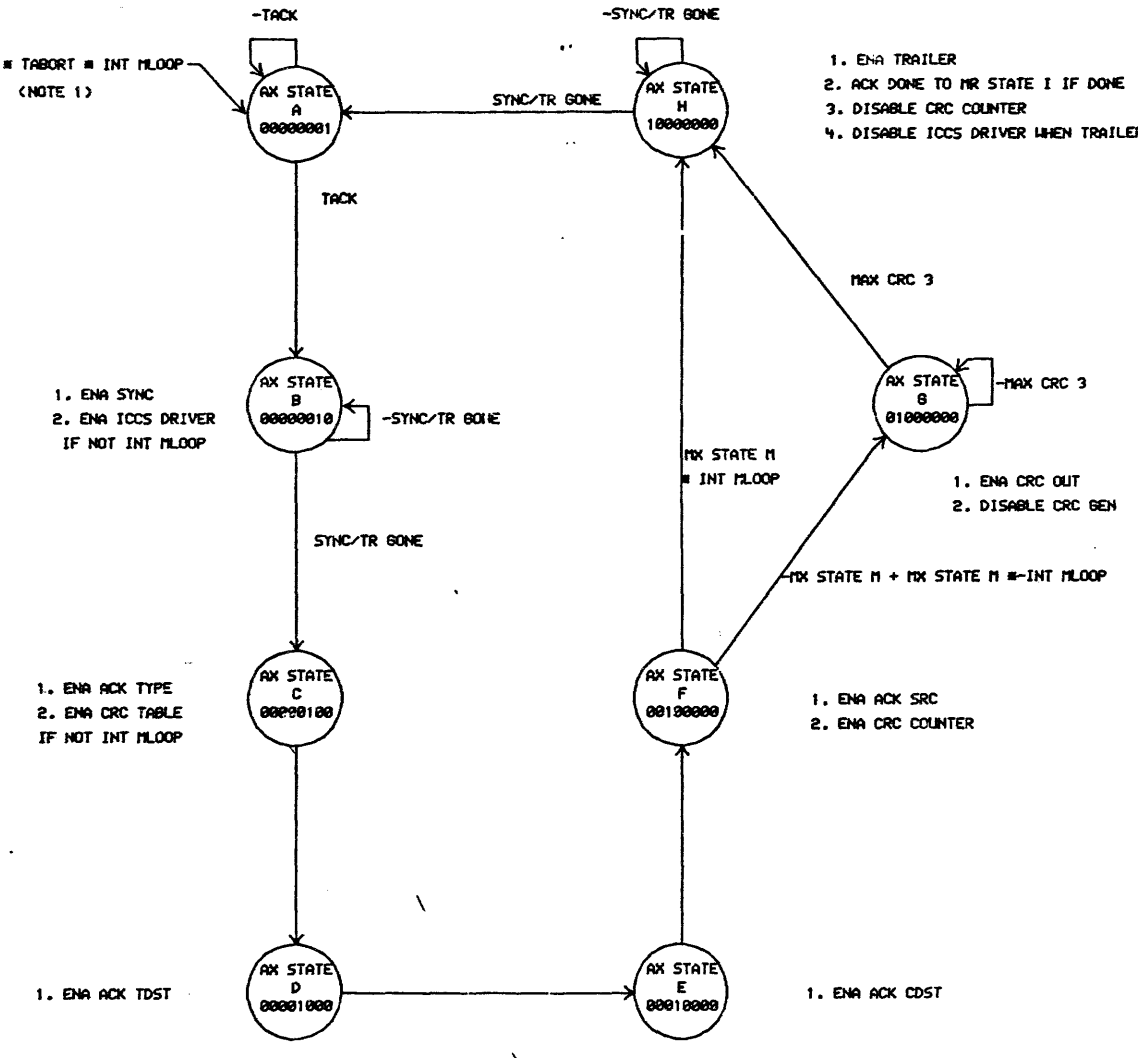
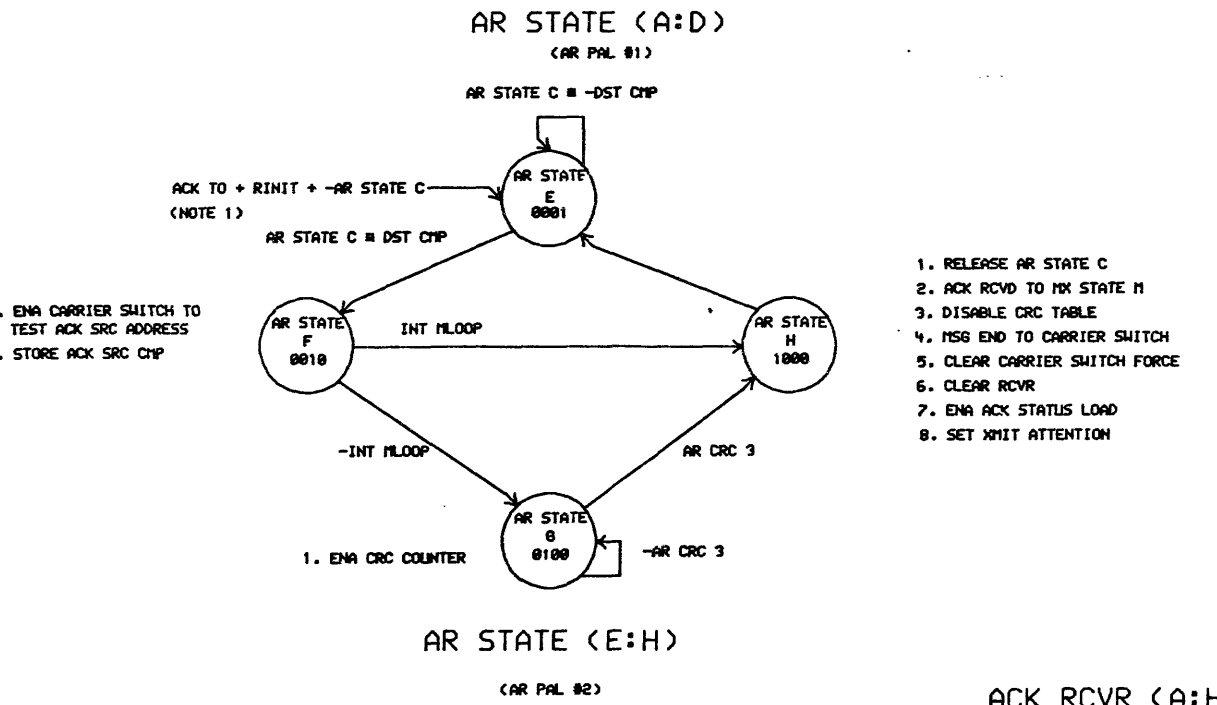
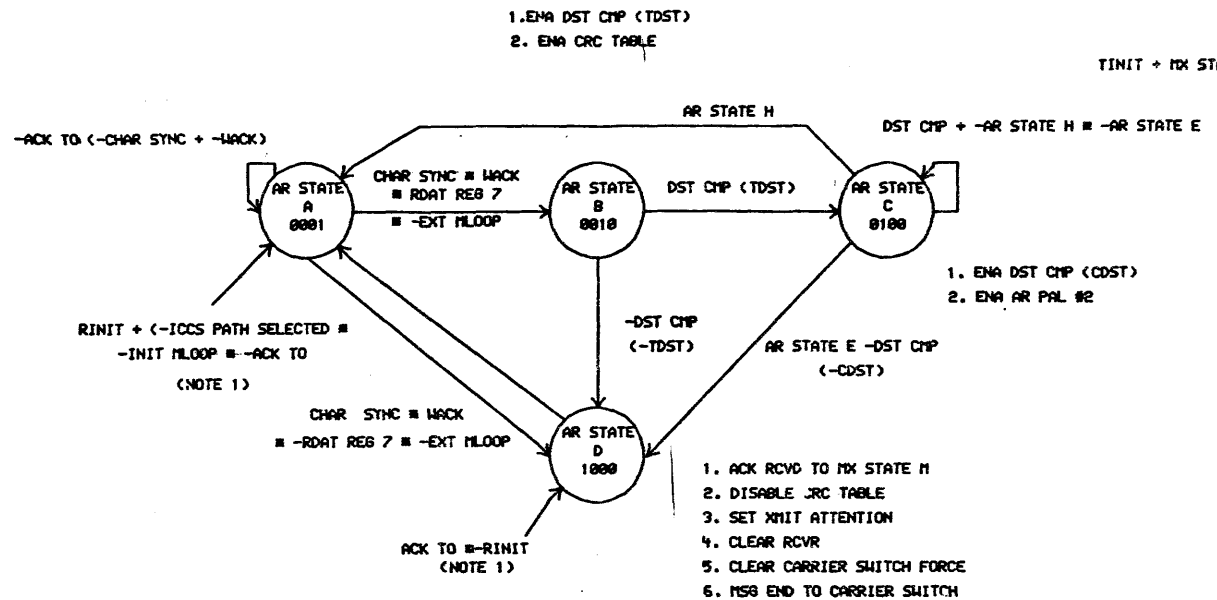
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							INTERPROCESSOR LINK INTERFACE			K	PL	L0100-0-DBP	C

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
						00		
						VARIATION REVISION LEVEL:		
45	39		1911573-00		74S280 PARITY GEN/CHKR,9BIT	2		E150,E155
46	40		1911675-00		74S138 DECODER/DEMUX 3-8 LI	1		E60
47	41		1911712-00		74S51 AND-OR GATE-INVERT D	5		E8,E64,E130,E135,E146
48	42		1912089-00		74S85 CMPRTR-4BIT MAGNITUD	1		E70
49	43		1912096-00	DEC	74S86 XOR GATE,QUAD 2IN	6		E72-E75,E95,E97
50	44		1912388-00		74S02 NOR GATE-QUAD 2IN,PO	5		E24,E27,E65,E105,E120
51	45		1912389-00		74S08 AND GATE-QUAD 2IN,PO	2		E41,E49
52	46		1912746-01		74S37-1BUFFER-GATE	3		E117,E132,E147
53	47		1912799-00		LS00 NAND-GATE-QUAD 2IN,P	4		E9,E46,E68,E84
54	48		1912801-00		LS02 NOR-GATE-QUAD 2IN	5		E21,E23,E28,E122,E137
55	49		1912803-00		LS04 INVERTER GATE,HEX	5		E50,E83,E88,E106,E165
56	50		1912805-00		LS08 AND GATE-QUAD 2IN,PO	2		E22,E51
57	51		1912810-00		LS20 NAND GATE-DUAL 4IN	2		E42,E142
58	52		1912820-00		LS51 A-O-I GATE 2-WIDE 2I	2		E143,E144
59	53		1912824-00		LS74 FF-D DUAL,EDGE TRIGG	1		E44
60	54		1912833-00		LS109 FF-JK DUAL,POS EDGE	11		E2,E5,E11,E62,E82,E102,E123, CONT E124,E140,E141,E158
61	55		1912851-00		LS169 COUNTER,SYNCH. UP/DO	3		E25,E71,E91
62	56		1913462-00		74S240 OCTAL BUFFER,INVERTI	3		E100,E112,E114
63	57		1913670-00		74S373 LATCH,8BIT TRANS TRI	1		E119
64	100		1913671-00		74S374 FF-D,OCTAL,TR1 STATE	11		E16,E38,E54,E55,E93,E96,E109, CONT E111,E113,E134,E156
65	58		1913671-50		74S374 FF-D,OCTAL,TRI STATE	3		E103,E154,E157
66	59		1913777-00		LS240 DRIVER,LINE,OCTAL,T	4		E15,E36,E37,E53
67	60		1913888-00	DC	102A EQUALS CHECKER 8BIT	7		E17,E39,E59,E90,E92,E98,E110
68	61		1914082-00		74S163 COUNTER,SYNCH UP/DOW	4		E1,E12,E14,E43
69	62		1914084-00		74S299 SHIFT REG.,8BIT RIGH	1		E13
70	63		1914085-00		74S260 NOR GATE-DUAL,POS	1		E178
71	64		1914086-00		74S30 NAND GATE-POS 8IN	1		E47
72	65		1917036-00		VOLTAGE COMPARATOR	4		E179,E180,E192,E193
73	66		1917275-00		100141 SHIFT REG.8-BIT	5		E185,E186,E188,E190,E199
74	67		1917277-00		100131 FF-D TRIPLE	5		E170,E173,E182,E183,E197
75	68		1917280-00		100125 TRANSLATOR,ECL TO TT	3		E164,E175,E176
76	69		1917281-00		100124 TRANSLATOR,TTL TO EC	3		E163,E171,E172
77	70		1917288-00		100107 OR/NOR,EXCLUSIVE,QUI	4		E169,E189,E194,E201
78	71		1917289-00		100102 OR/NOR GATE,QUINT,2	4		E168,E174,E198,E200
79	72		1917290-00		100101 OR/NOR GATE,TRIPLE,	2		E181,E187
80	73		1917442-00		100113 DRIVER,QUAD	1		E196
81	74		1917839-00		10192 LINE DRIVER,QUAD DIF	2		E159,E166
82	88		23009K5-00	K5-01		1		E86
83	85		23010K5-00	K5-01		1		E67
84	84		23011K5-00	K5-01		1		E66
85	87		23021K4-00	K4-01		1		E85
86	82		23022K4-00	K4-01		1		E45
87	89		23023K4-00	K4-01		1		E99
88	80		23042K3-00	K3-01	PAL,REG CONT	1		E31
89	81		23043K3-00	K3-01	PAL,REG CONT	1		E32
90	79		23063K3-00	K3-01	PAL,REG, CONT	1		E30

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							INTERPROCESSOR LINK INTERFACE		K	PL	L0100-0-DBP	C

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	VARIATION REVISION LEVEL:	QTY PER VARIATION 00	REFERENCE DESIGNATOR
91	86		23055K3-00		K3-01	PAL,REG CONT	1	E79
92	83		23091K3-00		K3-01	PAL,REG	1	E48
93	77		23071B1-00		B1-01		1	E76
94	74		23309A1-00		A1-07		1	E94
95	75		23601A2-00		A2-05		1	E20
96	92		23817F1-00		F1-01		1	E58
97	93		23818F1-00		F1-01		1	E40
98	94		23819F1-00		F1-01		1	E77
99	95		23820F1-00		F1-01		1	E57
100	76		23964A9-00		A9-01		1	E18
101	96		9000024-01		EYELET,ROLLED	0.1210DX0.192	12	
102	101		9105740-55		WIRE(WRAP)	30AWG KYNAR UL14	A/R	
103	102		9107688-00		WIRE(WRAP)	24AWG KYNAR UL13	A/R	
104	104		1912815-00		LS30 NAND GATE-SINGLE	8IN	1	E7

D I G I T A L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
	INTERPROCESSOR LINK INTERFACE		K	PL	L0100-0-DBP	C

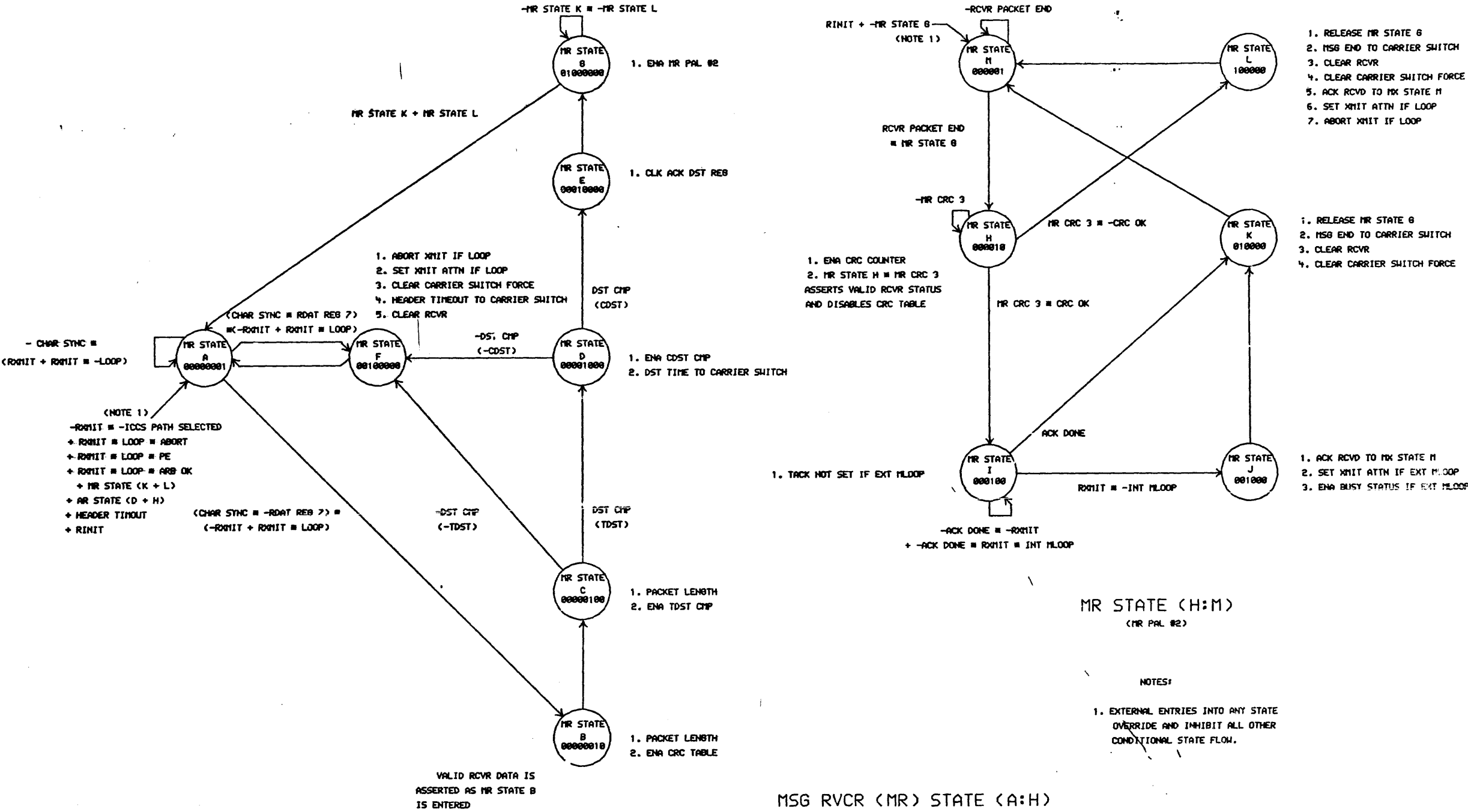


NOTES:
1. EXTERNAL ENTRIES INTO ANY STATE OVERRIDE AND INHIBIT ALL OTHER CONDITIONAL STATE FLOW

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REV. NO.	CHANGE NO.	REV.

digit@l	DRN. 00000001	DATE 28-SEP-81	DATE 28-SEP-81	TITLE: INTERPROCESSOR LINK INTERFACE
352,5000	CHART 1 DRU	30-SEP-81 10:49	NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1	SIZE CODE C TD
FIRST USED ON OPTION/MODEL: C1780				NUMBER L0100-0-2
				REV. A



1. RELEASE MR STATE G
2. MSG END TO CARRIER SWITCH
3. CLEAR RCVR
4. CLEAR CARRIER SWITCH FORCE
5. ACK RCVD TO MR STATE M
6. SET XMIT ATTN IF LOOP
7. ABORT XMIT IF LOOP

1. RELEASE MR STATE G
2. MSG END TO CARRIER SWITCH
3. CLEAR RCVR
4. CLEAR CARRIER SWITCH FORCE

1. ACK RCVD TO MR STATE M
2. SET XMIT ATTN IF EXT MLOOP
3. ENA BUSY STATUS IF EXT MLOOP

MR STATE (H:M)
(MR PAL #2)

NOTES:

1. EXTERNAL ENTRIES INTO ANY STATE OVERRIDE AND INHIBIT ALL OTHER CONDITIONAL STATE FLOW.

MSG RCVR (MR) STATE (A:H)

MR STATE (A:G)
(MR PAL #1)

VALID RCVR DATA IS
ASSERTED AS MR STATE B
IS ENTERED

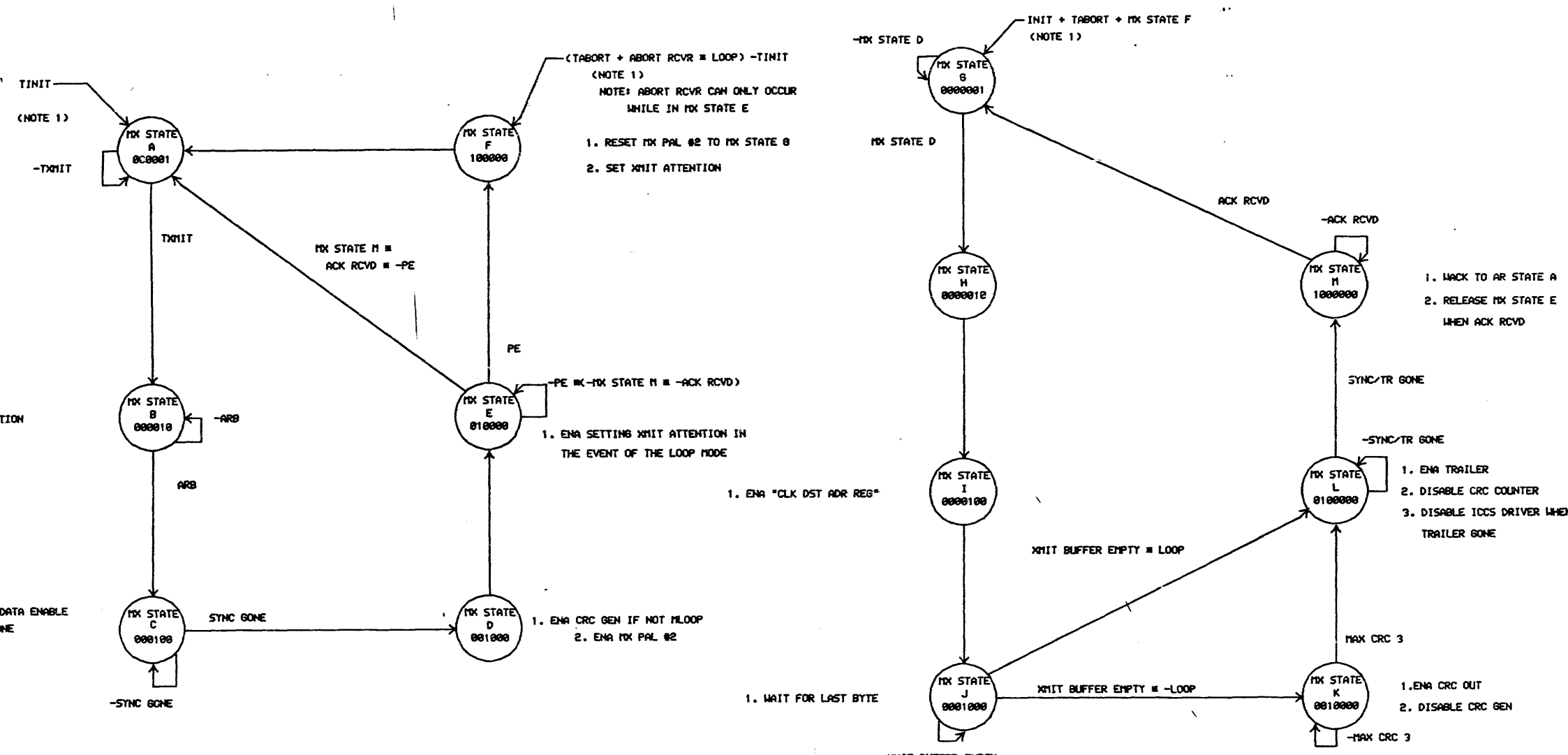
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REV.	DATE	CHG	NO.	REV.

CHK	CHANGE NO.	REV.

DRN.	DATE	ENG.	DATE	TITLE:
001000	28-SEP-81	DLK/KA	28-SEP-81	INTERPROCESSOR LINK INTERFACE
CIR-10	DATE	BOARD LOCATION:	SHEET	DF
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:		

SIZE:	CODE	NUMBER	REV.
D	TD	L0100-0-2	A



MX STATE (A:F)
 (MX PAL #1)

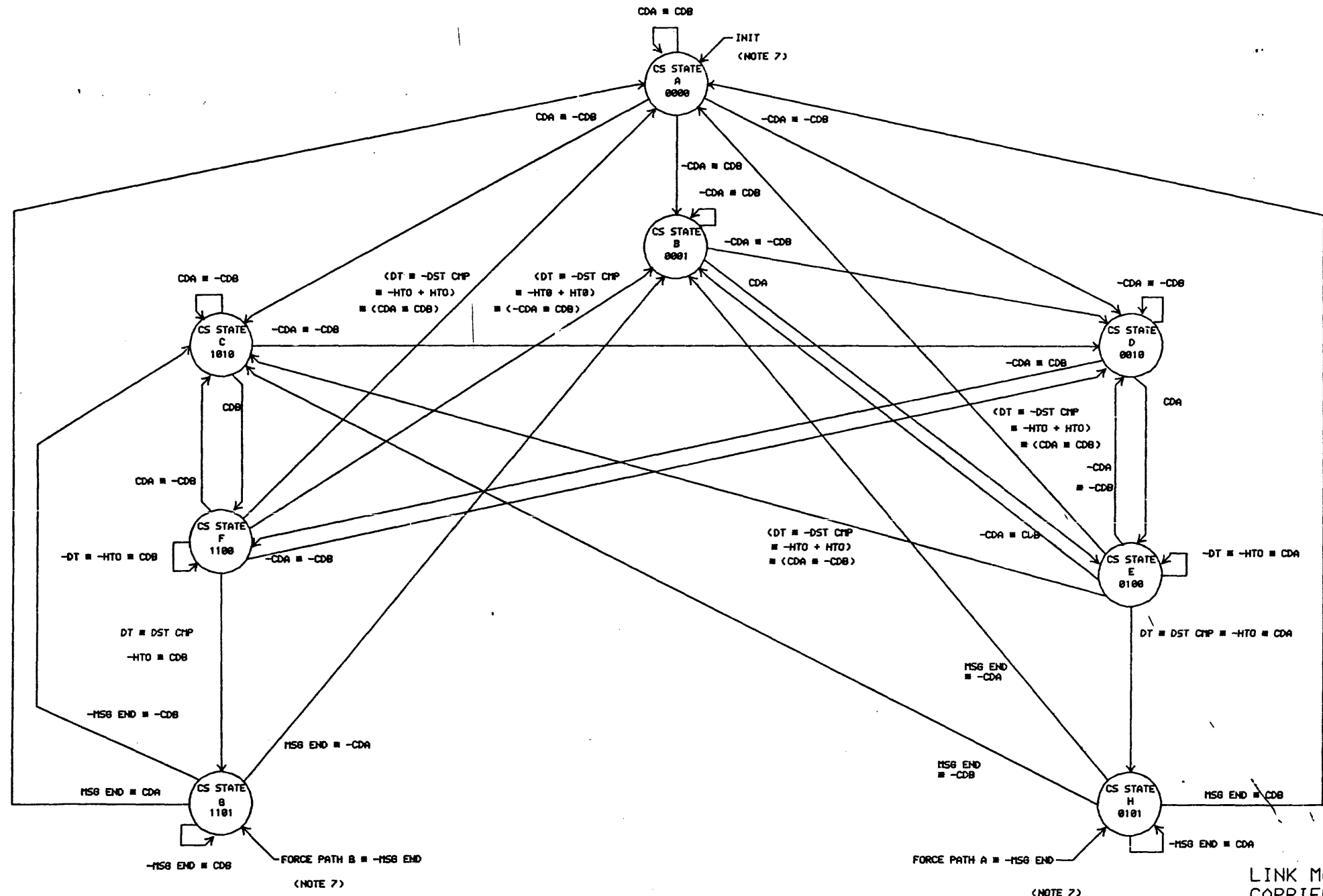
MX STATE (G:M)
 (MX PAL #2)

MESSAGE XMIT STATE (A:H)

NOTES:
 1. EXTERNAL ENTRIES INTO ANY STATE OVERRIDE AND INHIBIT ALL OTHER CONDITIONAL STATE FLOW.

NOTES:

1. CDA : CARRIER DETECT FOR PATH A
2. CDB : CARRIER DETECT FOR PATH B
3. DT : DESTINATION DECODE TIME
 A. MR STATE D
 B. AR STATE F
4. DST CMP : DESTINATION COMPARE
 A. CDST
 B. ACK SRC ADR
5. HTO : HEADER TIME OUT
6. STATE BIT ASSIGNMENT
 11XX = CARRIER SELECTED
 11XX = CARRIER B SELECTED
 01XX = CARRIER A SELECTED
7. EXTERNAL ENTRIES INTO ANY STATE OVERRIDE AND INHIBIT ALL CONDITIONAL STATE FLOW.



LINK MODULE
 CARRIER SW (CS)
 STATE (A:H)

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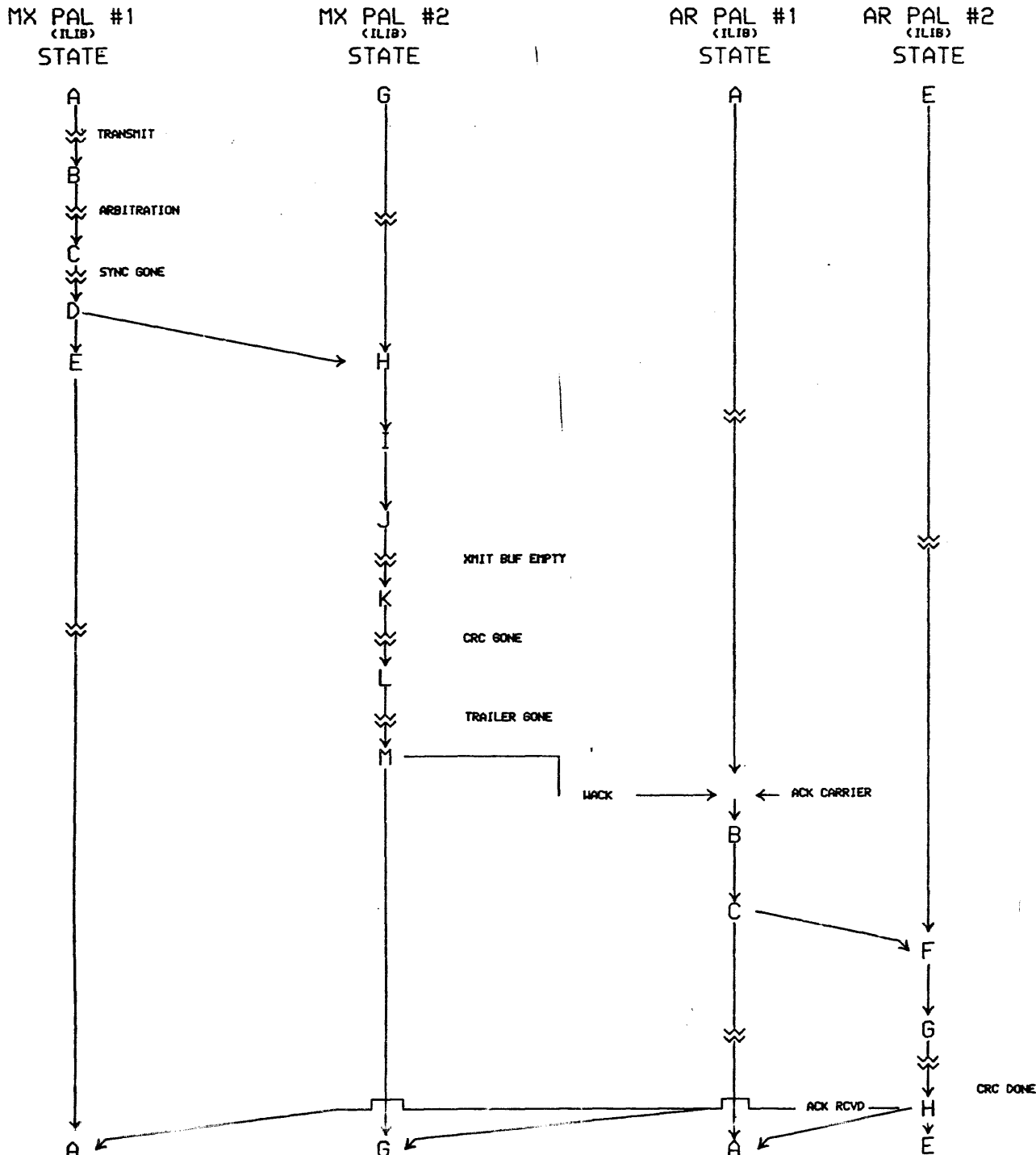
REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN.	DATE	ENG.	DATE	TITLE
	CHK'D	30-SEP-81	DC/KA	30-SEP-81	INTERPROCESSOR LINK INTERFACE
FIRST USED ON OPTION MODEL: C1700		130-SEP-81	10153	NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1	

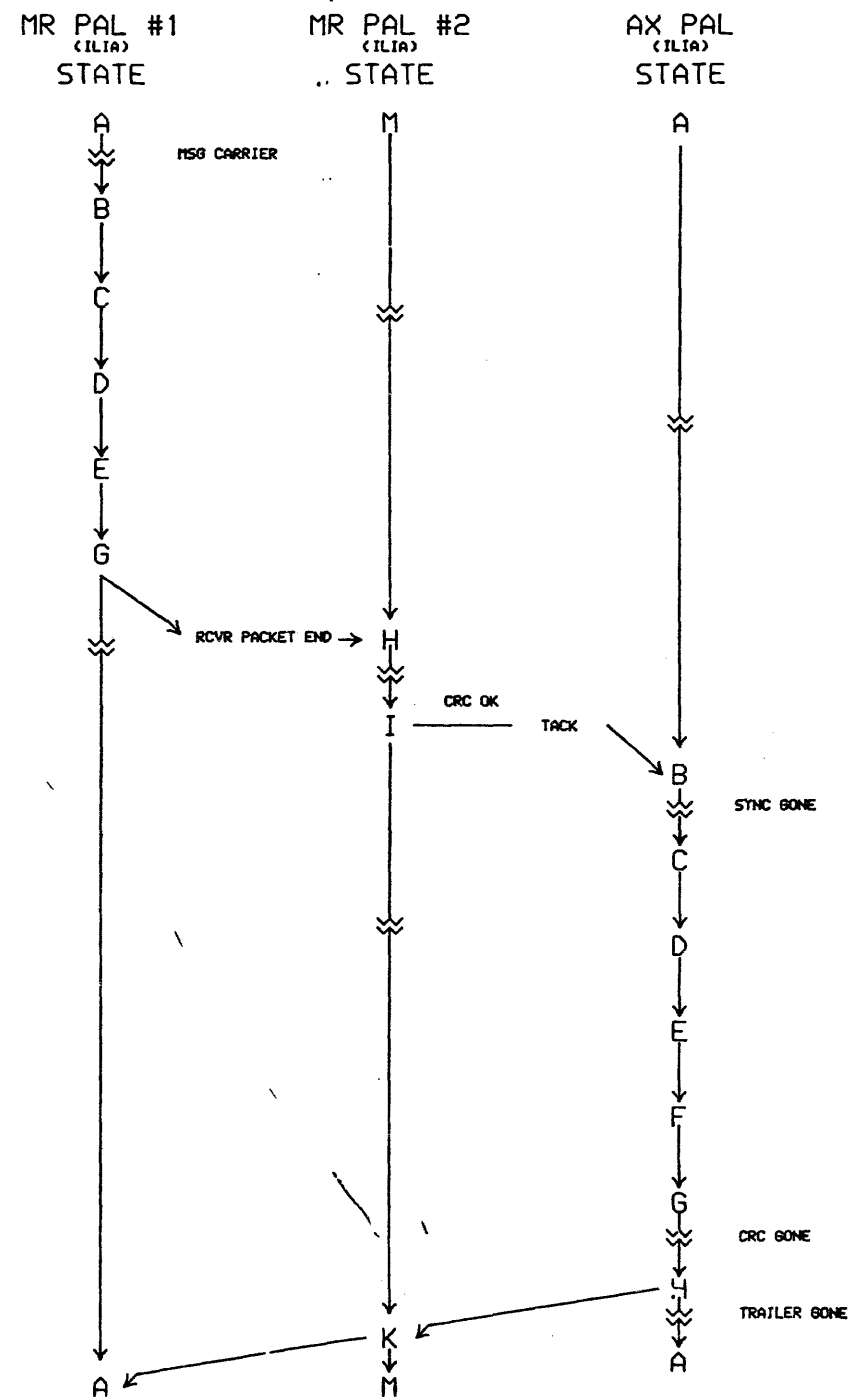
SIZE CODE	NUMBER	REV.
D TD	L0100-0-2	A

TW

NORMAL MESSAGE TRANSMISSION STATE FLOW



NORMAL MESSAGE RECEPTION STATE FLOW

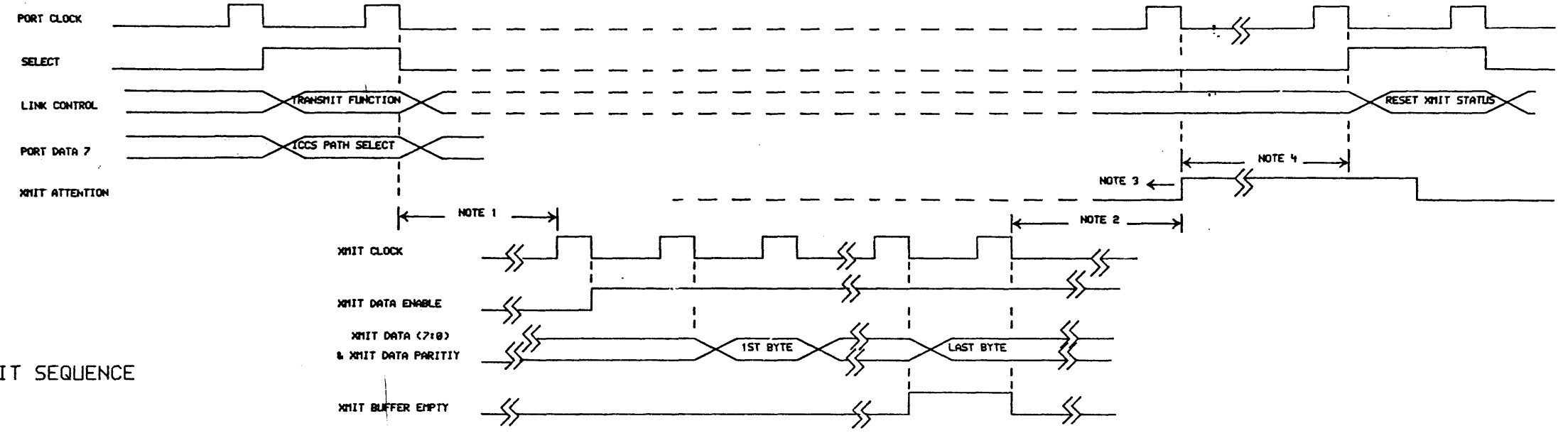


XMIT/RCVR MSG STATE FLOWS

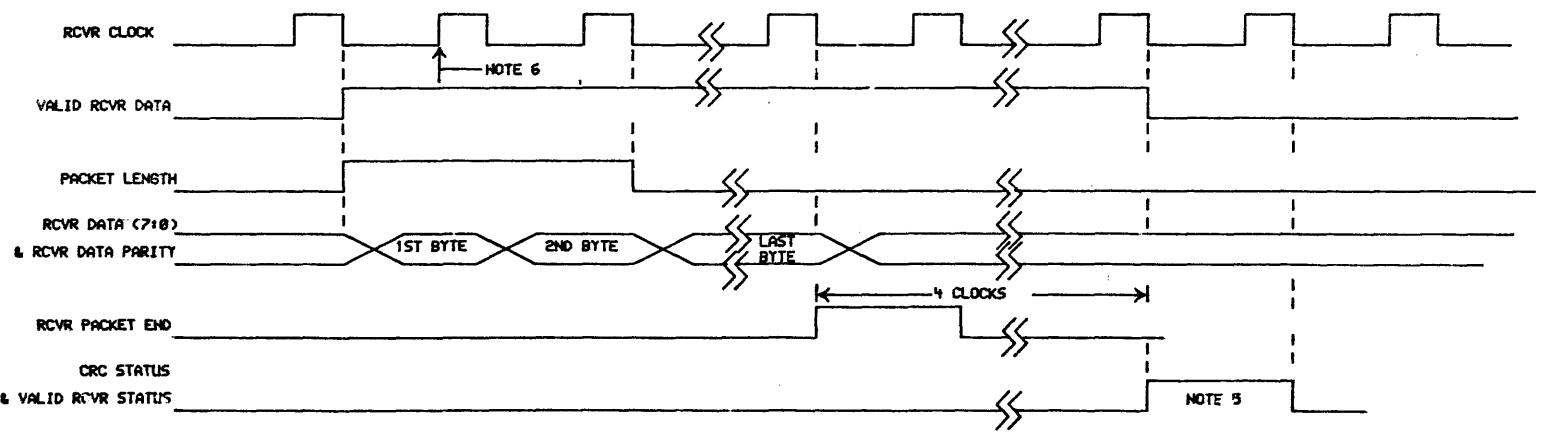
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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. NIKA	DATE 28-SEP-81	ENG.	DATE	TITLE: INTERPROCESSOR LINK INTERFACE
	CHK'D	DATE 30-SEP-81	BOARD LOCATION: 5 OF 8	SHEET	
FIRST USED ON OPTION/MODEL: C1780		DATE 10-SEP-81	NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1	SIZE CODE: D TD	NUMBER: L0100-0-2



TRANSMIT SEQUENCE



RECEIVE SEQUENCE

NOTES:

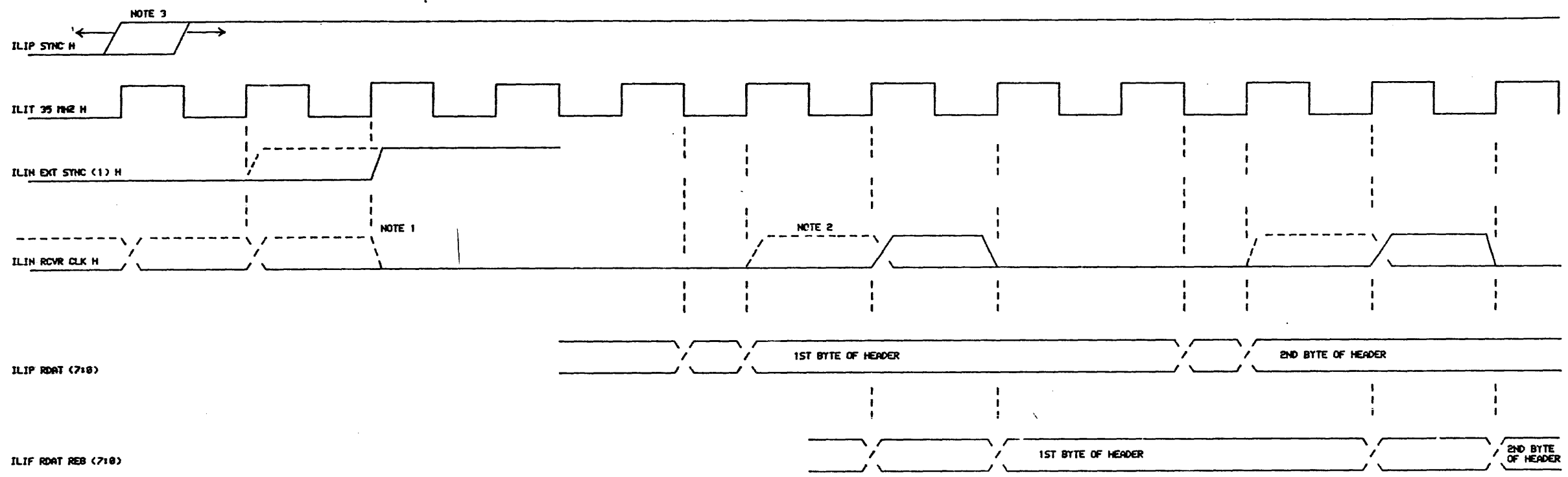
1. ARBITRATION OCCURS DURING THIS TIME.
2. LINK WAITS FOR A RESPONSE FROM THE DESTINATION NODE AND SETS UP XMIT STATUS DURING THIS TIME.
3. XMIT ATTENTION MAY OCCUR EARLIER AS A RESULT OF A XMIT DATA PARITY ERROR OR ABORT TRANSMISSION FCN.
4. THE PORT READS XMIT STATUS DURING THIS TIME.
5. CRC STATUS & RCVR PATH INDICATOR ARE VALID DURING THIS TIME.
6. RCVR BUFFERS FULL SIGNAL IS STROBED AT THIS TIME.

LINK/PORT PROCESSOR/TIMING

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REVISIONS	
CHK	CHANGE NO., REV

	ORGN. ALI/KA	DATE 20-SEP-81	ENG.	DATE	TITLE
	CHK-20	DATE	BOARD LOCATION:	SHEET 5 OF 8	INTERPROCESSOR LINK INTERFACE
FIRST USED ON OPTION/MODEL: CI780		130-SEP-81 10:56	NEW HIGHER ASSEMBLY:	D-LA-L0100-0-1	SIZE CODE D TD
				NUMBER L0100-0-2	REV. A



INTERNAL RCVR CLOCK SYNCHRONIZATION TIMING DIAGRAM

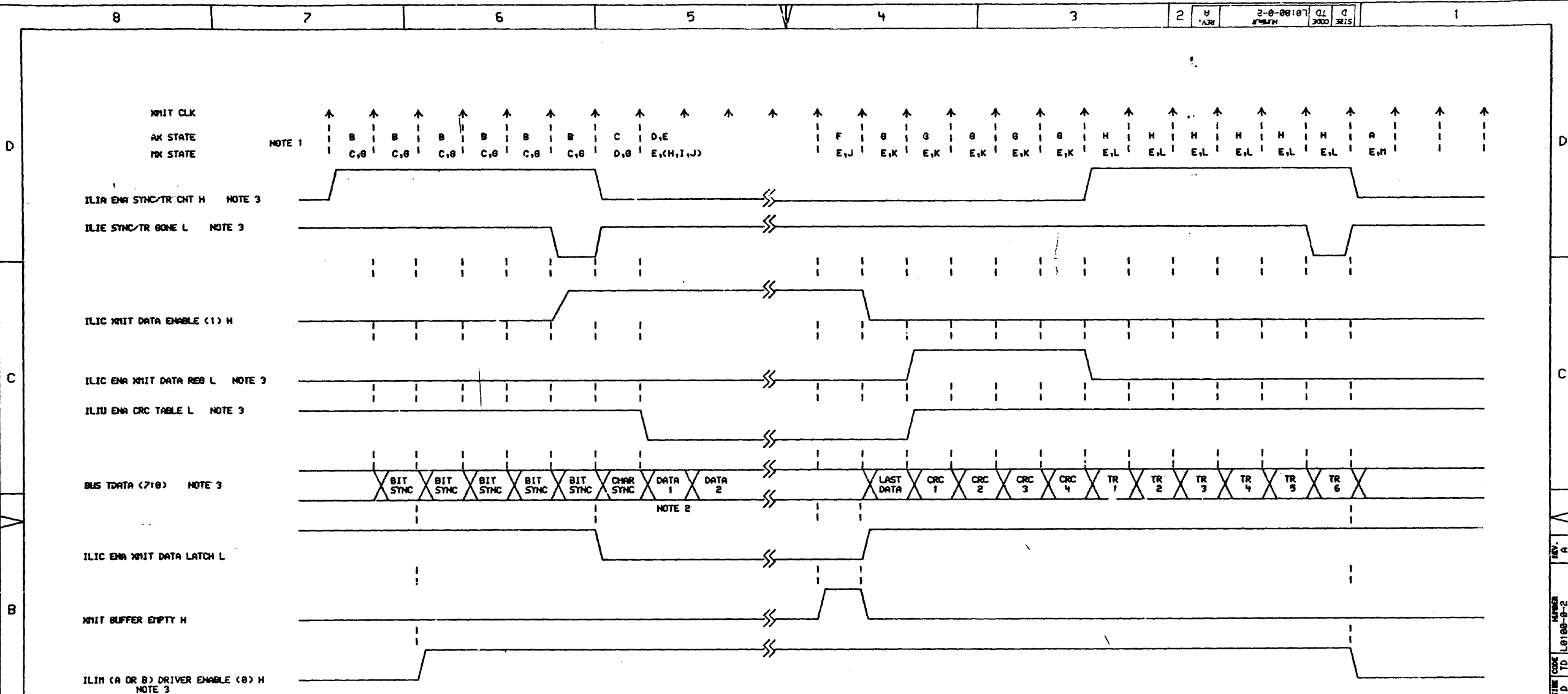
- NOTES:**
- THE RCVR CLK CIRCUIT IS SYNCHRONIZED TO THE INCOMING MESSAGE OR ACK AT THIS POINT.
 - DASHED SIGNALS MAY BE TRUE, INSTEAD OF SOLID SIGNALS, DEPENDING ON WHEN THE RCVR CLK GETS SYNCHRONIZED.
 - SYNC H IS ASSERTED WHEN CHARACTER SYNC IS DETECTED IN THE INCOMING MESSAGE OR ACK.

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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. DICKA	DATE 28-SEP-81	ENG.	DATE	TITLE: INTERPROCESSOR LINK INTERFACE
	CHK. D	DATE	BOARD LOCATION: 7	SHEET OF 8	SIZE CODE NUMBER REV. D TD L0100-0-2 A
FIRST USED ON OPTION/MODEL: C1780		NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1		7u	

REV. A
 NUMBER L0100-0-2
 SITE CODE TD
 D
 B
 A



TRANSMIT DATA TIMING DIAGRAM

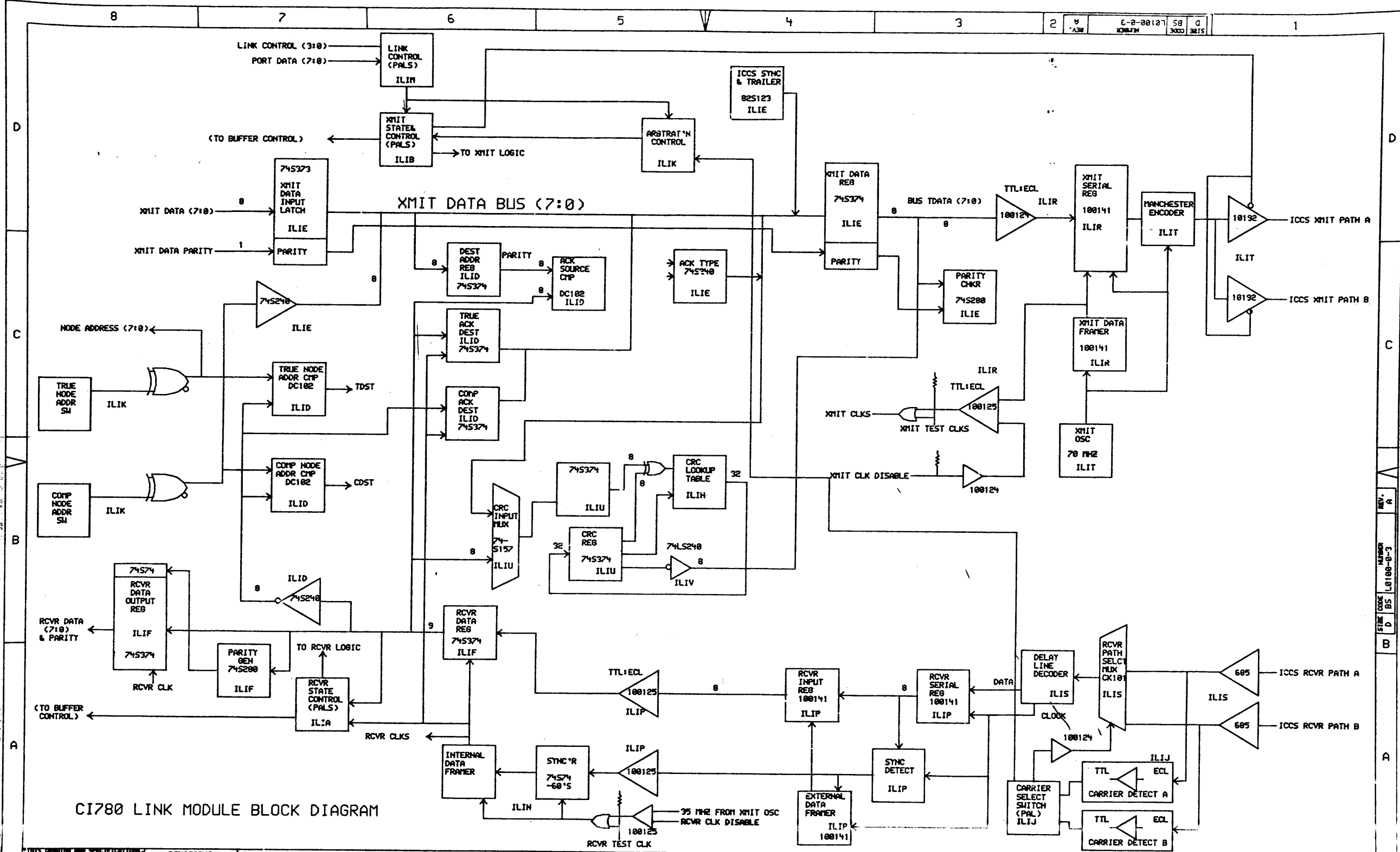
NOTES:

1. THE PK STATES INDICATE BOTH PK STATE PALS AS FOLLOWS: PK PAL A, PK PAL B.
2. FOR ACK/NAK PACKETS, DATA PORTION CONSISTS ONLY OF PACKET TYPE, TRUE DESTINATION, COMPLEMENT DESTINATION AND SOURCE.
3. ONLY THESE SIGNALS ARE RELEVANT FOR ACK/NAK PACKETS.

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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. P. KA	DATE 28-SEP-91	ENGR. P. KA	DATE 28-SEP-91	DATE 28-SEP-91	TITLE: INTERPROCESSOR LINK INTERFACE
	CHK. D.	DATE 130-SEP-91 10:58	BOARD LOCATION: SHEET 8 OF 8	SIZE CODE D	TD	NUMBER L0100-0-2
FIRST USED ON OPTION/MODEL: C1780		NEXT HIGHER ASSEMBLY: D-LA-L0100-0-1		REV. A		



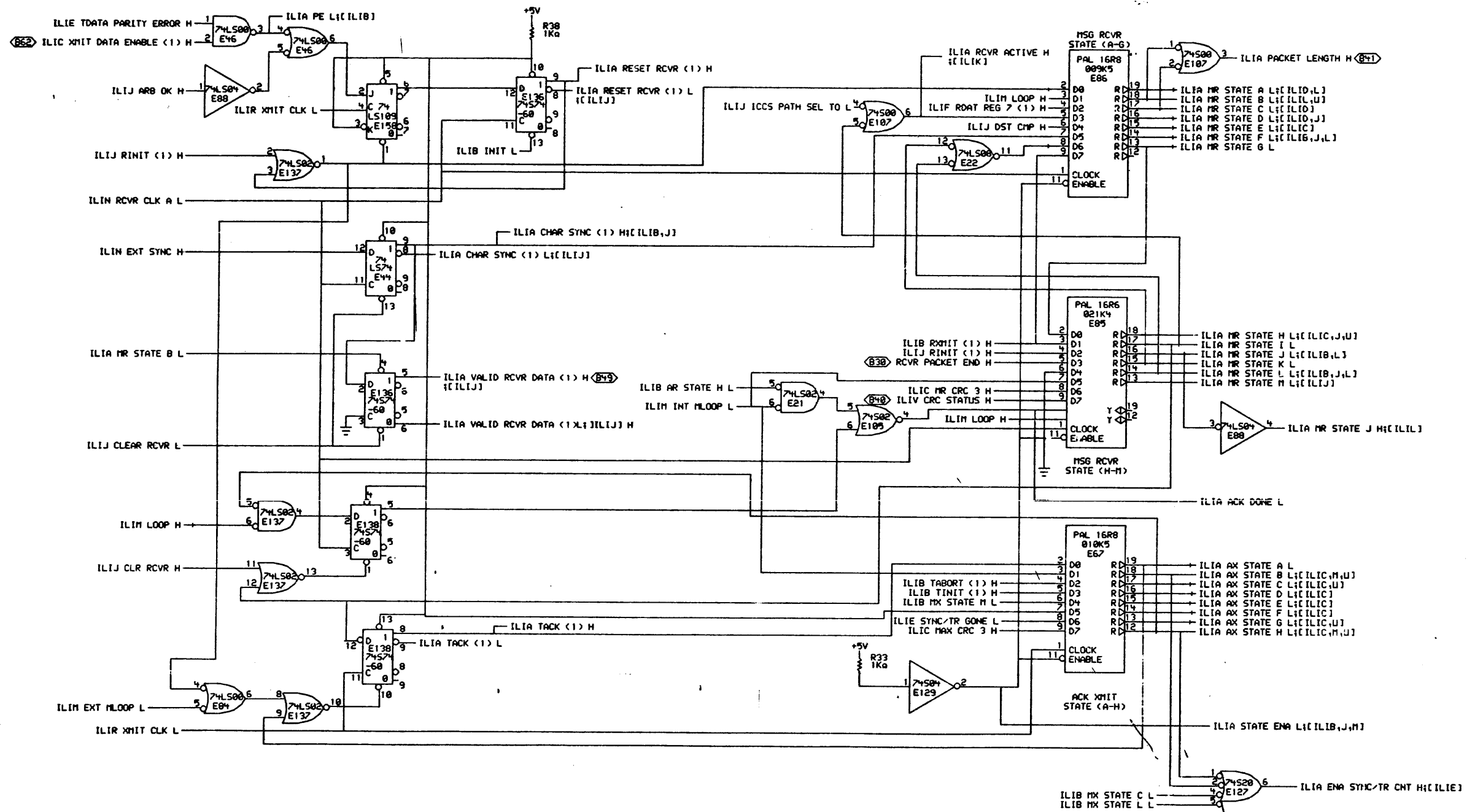
CI780 LINK MODULE BLOCK DIAGRAM

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REV.	CHG.	CHANGE NO.	REV.

	DRN. 01780	DATE ENG 30-SEP-81	DATE 30-SEP-81	TITLE: INTERPROCESSOR LINK INTERFACE
	CHK'D: [Signature]	DATE BOARD EDUCATION 30-SEP-81	SHEET 1 OF 1	SIZE CODE D BS L0100-0-3
FIRST USED ON OPTION MODEL: CI780		NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1		NUMBER REV. A

REV. A
 NUMBER L0100-0-3
 SIZE CODE D BS



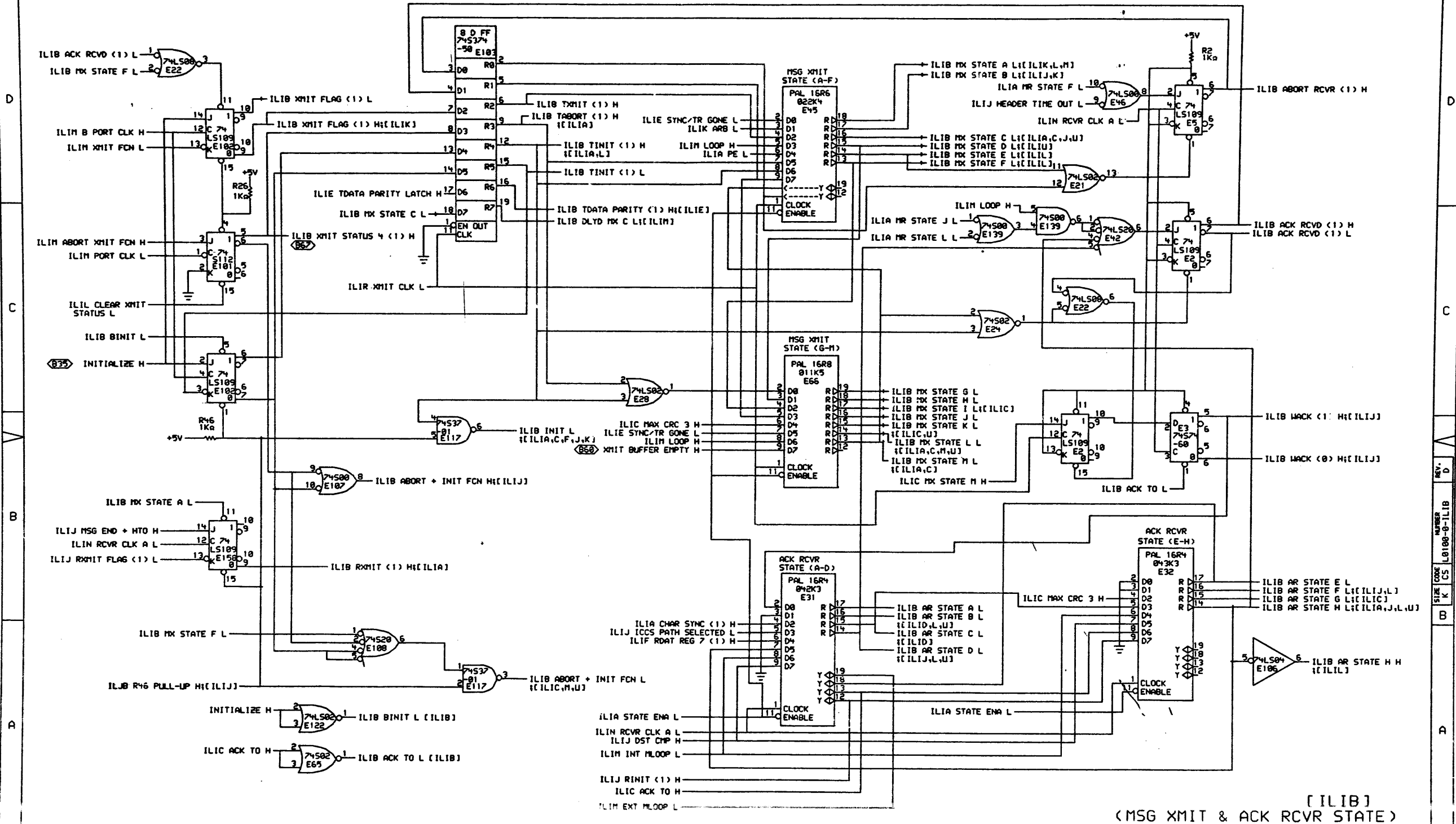
[ILIA]
 (MSG RCVR & ACK XMIT STATE)

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REVISIONS	
CHK	CHANGE NO. REV

digital	DRW. <i>Janey</i>	DATE 22-JUL-83	ENG. <i>C. Collins</i>	DATE 8-4-83	TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
	CHK'D <i>Marvin</i>	DATE 4-AUG-83	BOARD LOCATION: 1 OF 1	SIZE K	CODE CS
FIRST USED ON OPTION/MODEL: C1780		NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1		NUMBER K	REV. C

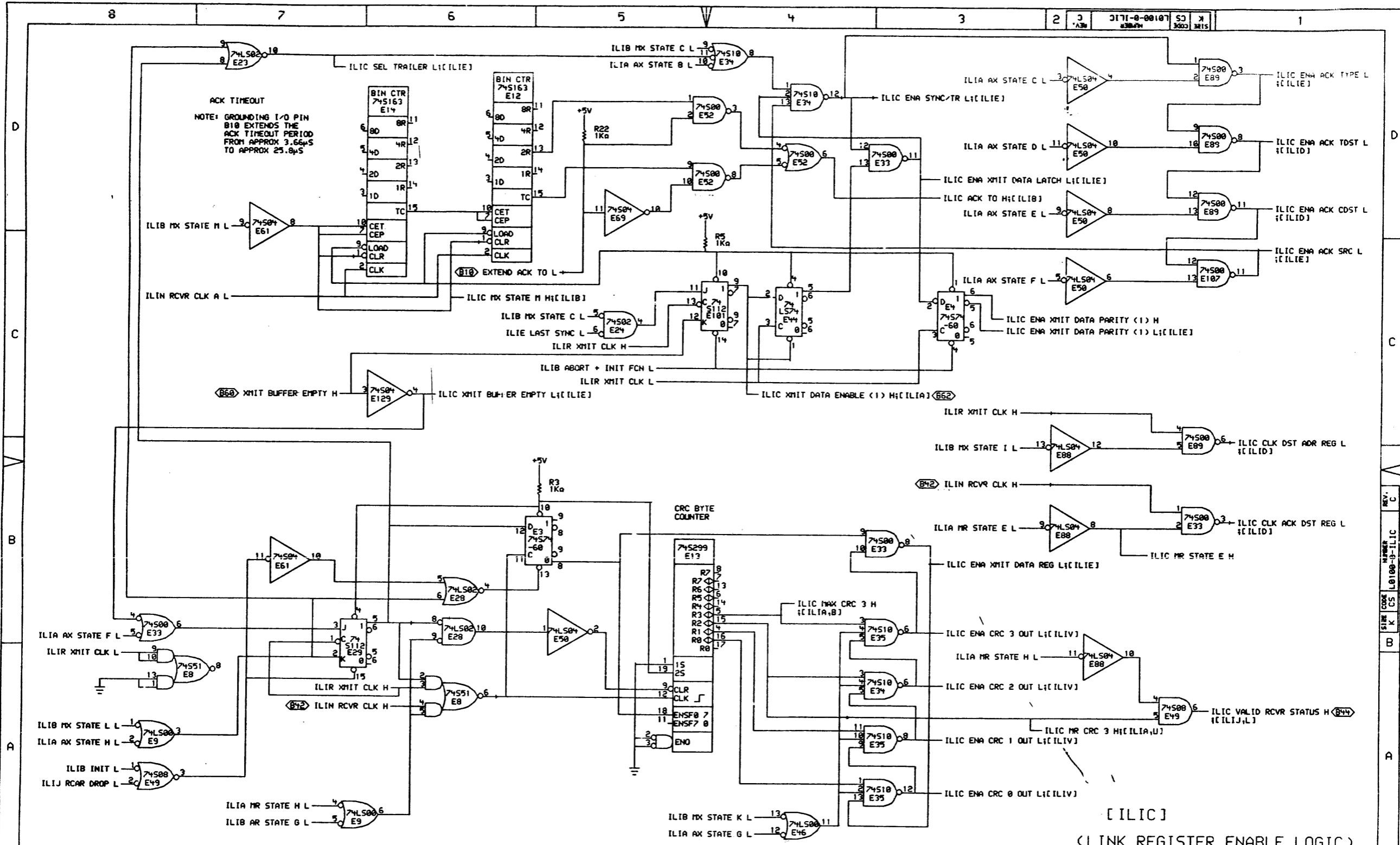
REV. C
 NUMBER L0100-0-ILIA
 CS
 K



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REV.	CHG	CHANGE NO.	REV

digital <small>DIGITAL EQUIPMENT CORPORATION 300 MASSACHUSETTS AVENUE CAMBRIDGE, MASSACHUSETTS 02142</small>	ORN: J. J. J. ONLY CHK'D:	DATE: 02-MAR-84 DATE:	ENG.	DATE:	TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
	DSKD: PL QURDE 2AP1L2.DRW 02-MAR-84 11:54 NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1 FIRST USED ON OPTION MODEL: C1780	BOARD LOCATION:	SHEET: 1	NUMBER: K CS L0100-0-ILIB	REV.: D



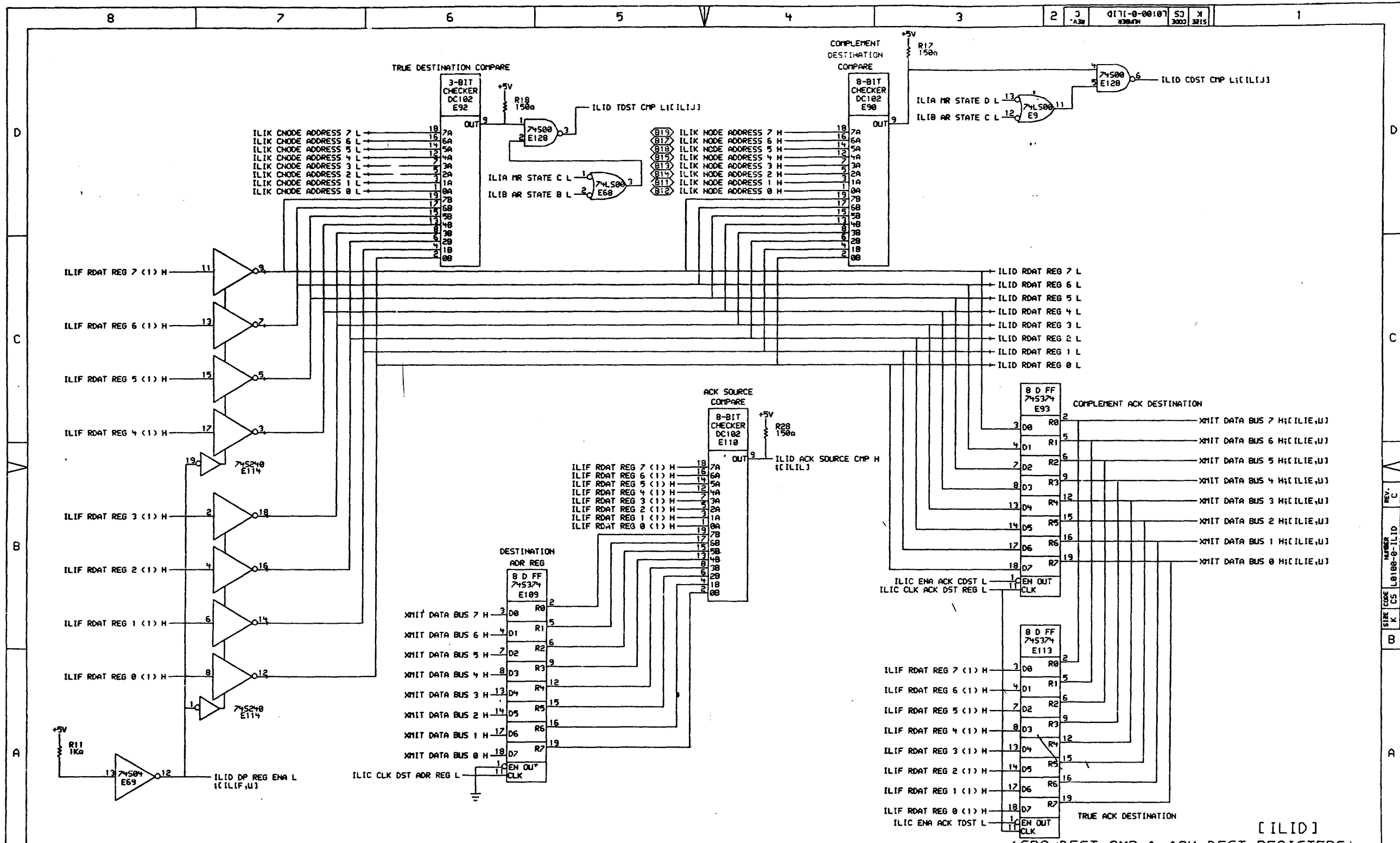
[ILIC]
(LINK REGISTER ENABLE LOGIC)

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REV.	CHG.	DESCRIPTION
1		INITIAL RELEASE

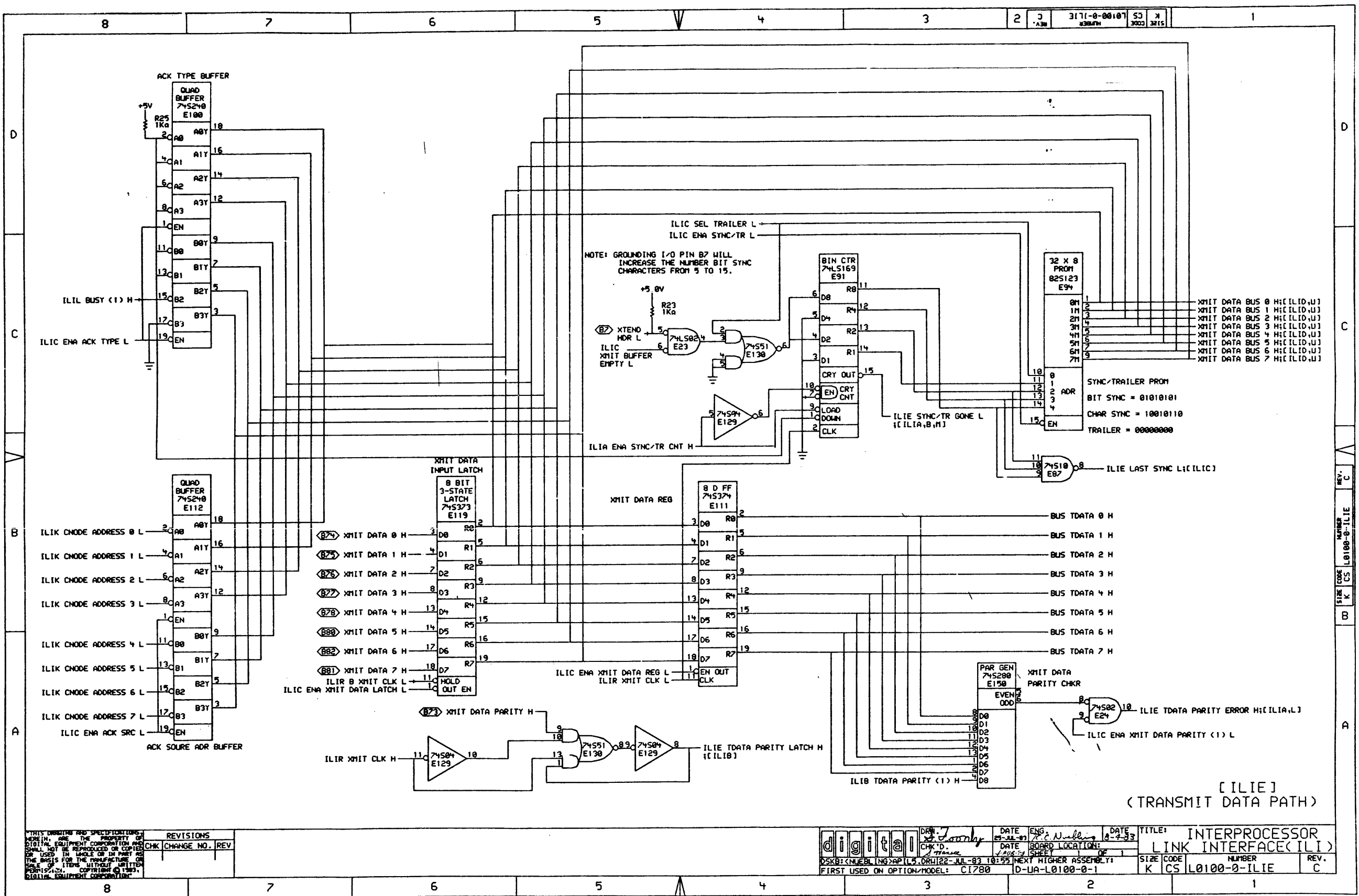
NO.	DATE	BY	CHK'D	REV.
1	23-JUL-83	J. J. Mulvaney		1

digital		DATE: 23-JUL-83 BOARD LOCATION: 3-4-93	TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
DSKB: (NUEBLING)APIL3.DRM [22-JUL-83 10:55] NEXT HIGHER ASSEMBLY:		SIZE CODE: K CS	NUMBER: L0100-0-ILIC
FIRST USED ON OPTION/MODEL: C1780		DATE: 0-UA-L0100-0-1	REV.: C



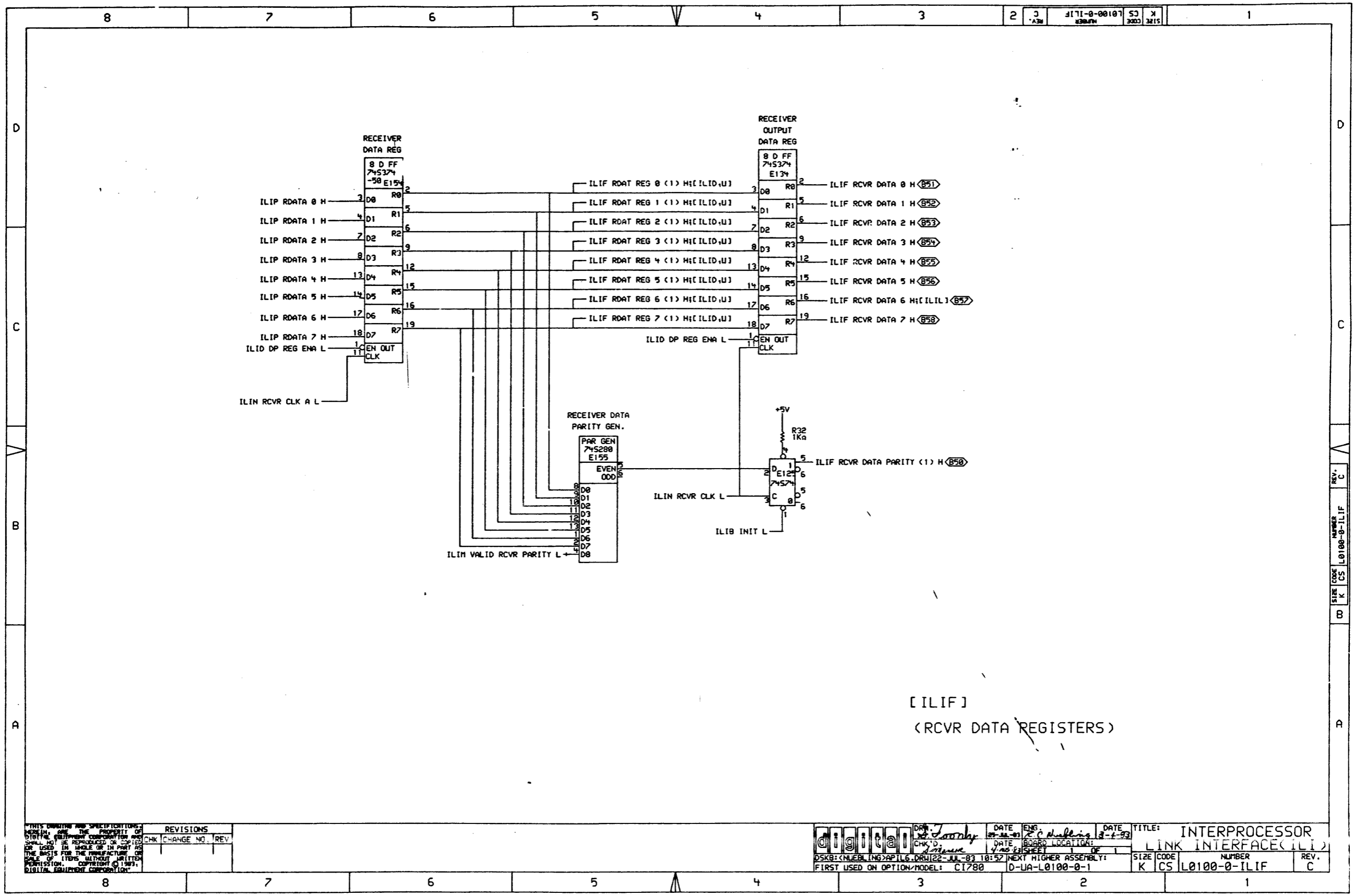
[ILID]
 (SRC / DEST CMP & ACK DEST REGISTERS)
 TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
 DATE: 22-JUL-83
 BOARD LOCATION: 4-A-11 SHEET 1 OF 1
 DSKB: (NUEBLING)APIL4.DRW 22-JUL-83 10:55 NEXT HIGHER ASSEMBLY: 1
 FIRST USED ON OPTION/MODEL: C1700 D-UA-L0100-0-1
 SIZE CODE: K CS L0100-0-ILID
 NUMBER: 1
 REV: C

REV	CHG	NO.	REV



REVISIONS	
CHK	CHANGE NO. REV

	DSN: <i>J. J. J.</i> CHK'D: <i>J. J. J.</i>	DATE: 27-JUL-83 DATE: 27-JUL-83	ENG: <i>J. J. J.</i> DATE: 27-JUL-83	TITLE: INTERPROCESSOR LINK INTERFACE (ILIE)
	DSKB: NUBLING APIL5.0R1122-JUL-83 10:55 NEXT HIGHER ASSEMBLY: 10-UA-L0100-0-1	SHEET: 1 OF 1	BOARD LOCATION:	SIZE CODE: K CS L0100-0-ILIE

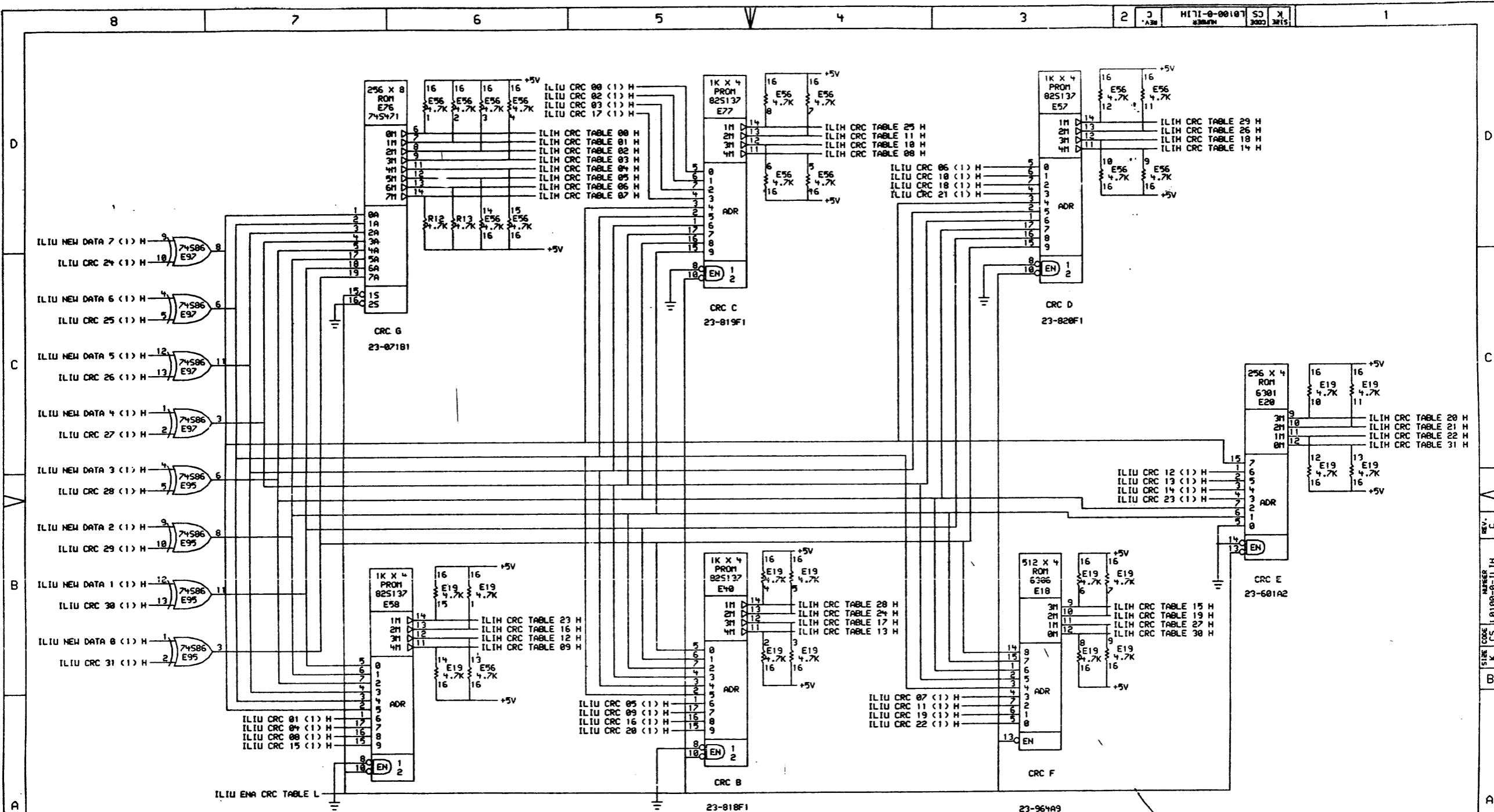


[ILIF]
 (RCVR DATA REGISTERS)

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REVISIONS		
CHK	CHANGE NO.	REV.

	DATE: 07-27-83 CHK'D: J. M. ... DATE: 07-27-83	ENG.: C. ... DATE: 07-27-83 BOARD LOCATION: 1 OF 1	DATE: 07-27-83 SHEET: 1 OF 1	TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
	DSKB: <NUEBLING>APIL6.DMI:22-JUL-83 10:52 FIRST USED ON OPTION/MODEL: C1780	NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1	SIZE CODE: K CS NUMBER: L0100-0-ILIF	REV.: C



NOTE: FOR FORWARD REFERENCING, SIGNALS ILIH CRC TABLE 00-31 H ALSO APPEAR ON SHEET ILIU.

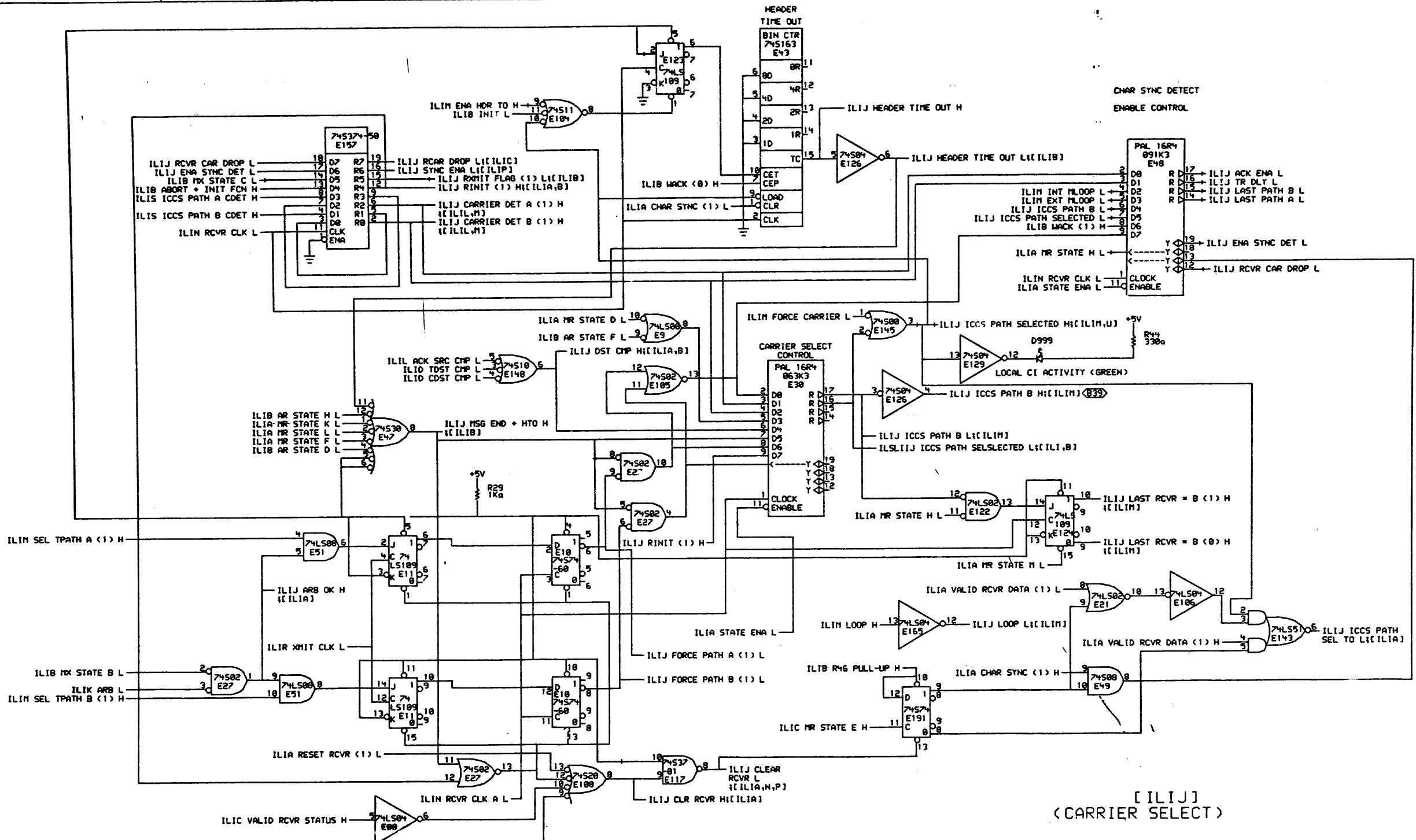
[ILIH]
(CRC LOOKUP TABLE)

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REVISIONS	
CHK	CHANGE NO. REV

	DATE: 25-02-83 CHK'D: <i>S. Mark</i>	DATE: 25-02-83 BOARD LOCATION: 4/00-01	DATE: 25-02-83 TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
	DSK8<NUEBLING>APIL7.DPL.1, SCALE 2, "D" RELEASE BOX	NEXT HIGHER ASSEMBLY: D-LIA-L0100-0-1	SIZE CODE: K CS

SIZE CODE	NUMBER	REV.
K CS	L0100-0-ILIH	C



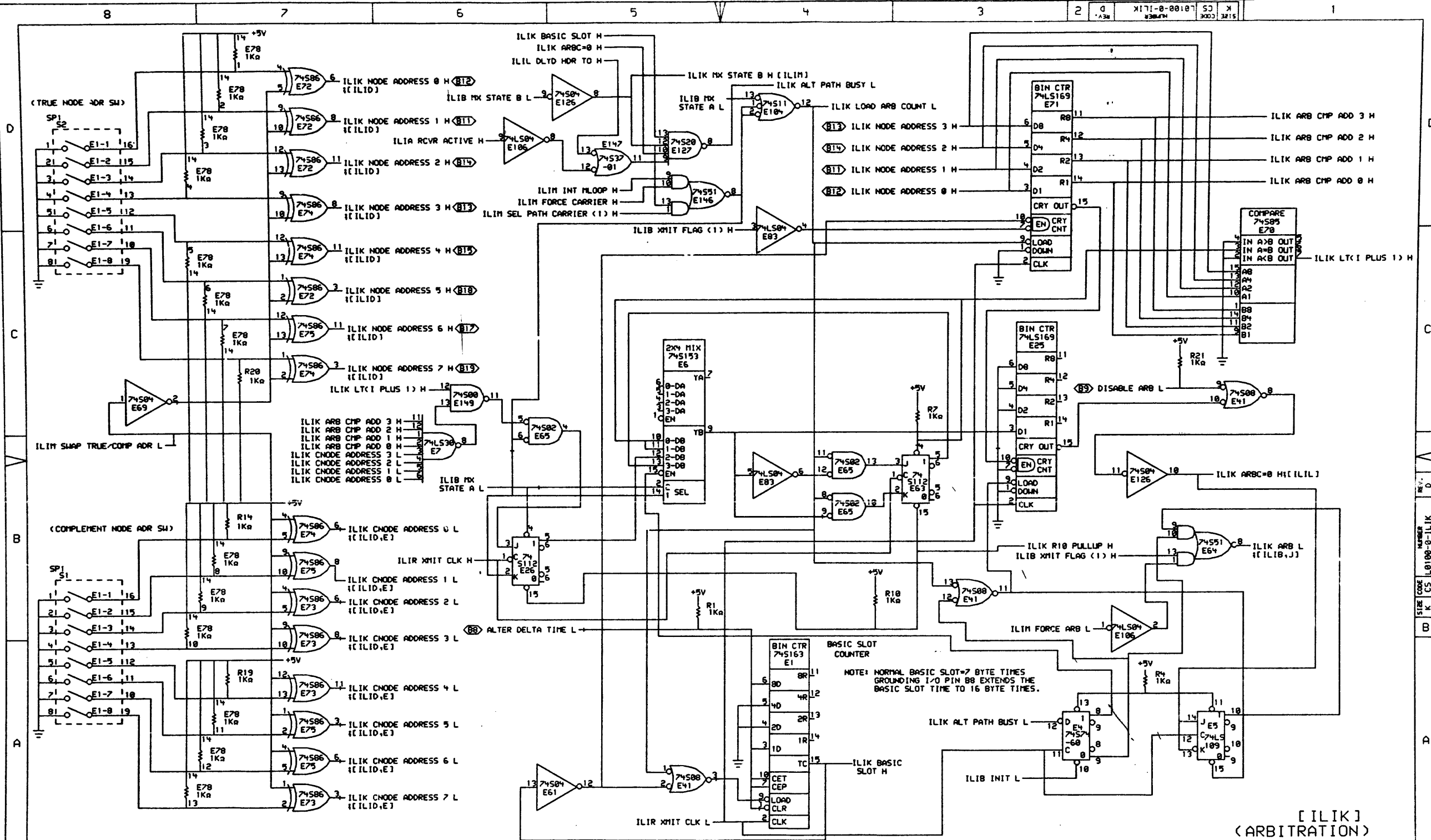
[ILIJ]
(CARRIER SELECT)

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REVISIONS	
CHK	CHANGE NO. REV

digit			DRN. H. NORMAND	DATE 02-APR-84	ENG.	DATE	TITLE
			CHK'D.	DATE	BOARD LOCATION:	SHEET 1 OF 1	
DSKDIPL0URD.EAP18.DRW 107-MAR-84 12:03			DATE	NEXT HIGHER ASSEMBLY:	SIZE CODE NUMBER		REV. D
FIRST USED ON OPTION MODEL: C1780			D-UA-L0100-0-1		K CS L0100-0-ILIJ		

REV. D
NUMBER L0100-0-ILIJ
STATE CODE K CS

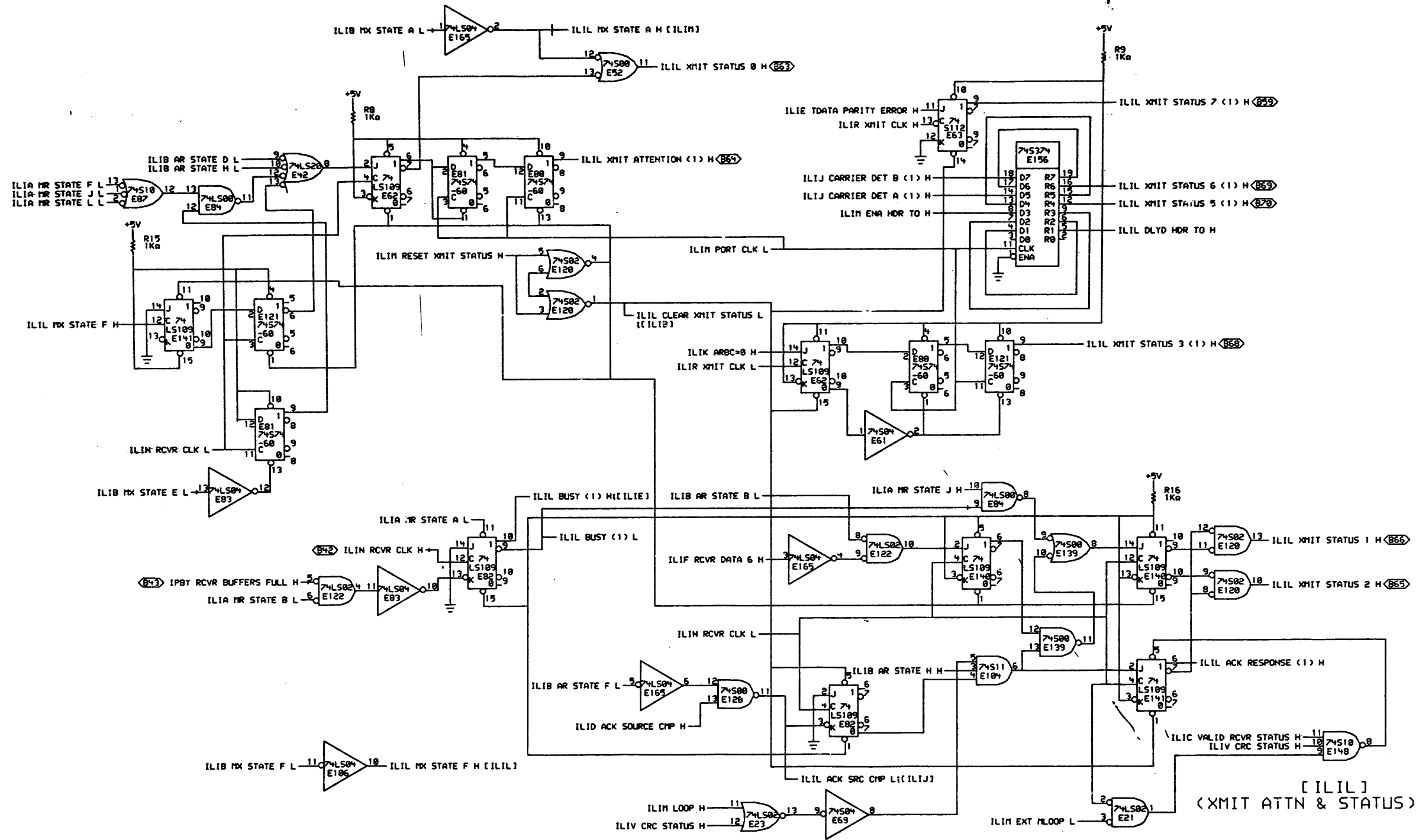


[ILIK]
(ARBITRATION)

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REVISIONS	CHK	CHANGE NO.	REV

digital DRN. H. NORMAND CHK'D. DATE BOARD LOCATION:	DATE	ENG.	DATE	TITLE:
	02-APR-84			INTERPROCESSOR LINK INTERFACE (ILI)
DSK01 (ALU) DE JAP 1.9, DRW 02-MAR-84 13:44, NEXT HIGHER ASSEMBLY: 1 FIRST USED ON OPTION/MODEL: CI780 D-LIA-L0100-0-1	SHEET	OF	NUMBER	REV.
	1	1	K CS L0100-0-ILIK	D



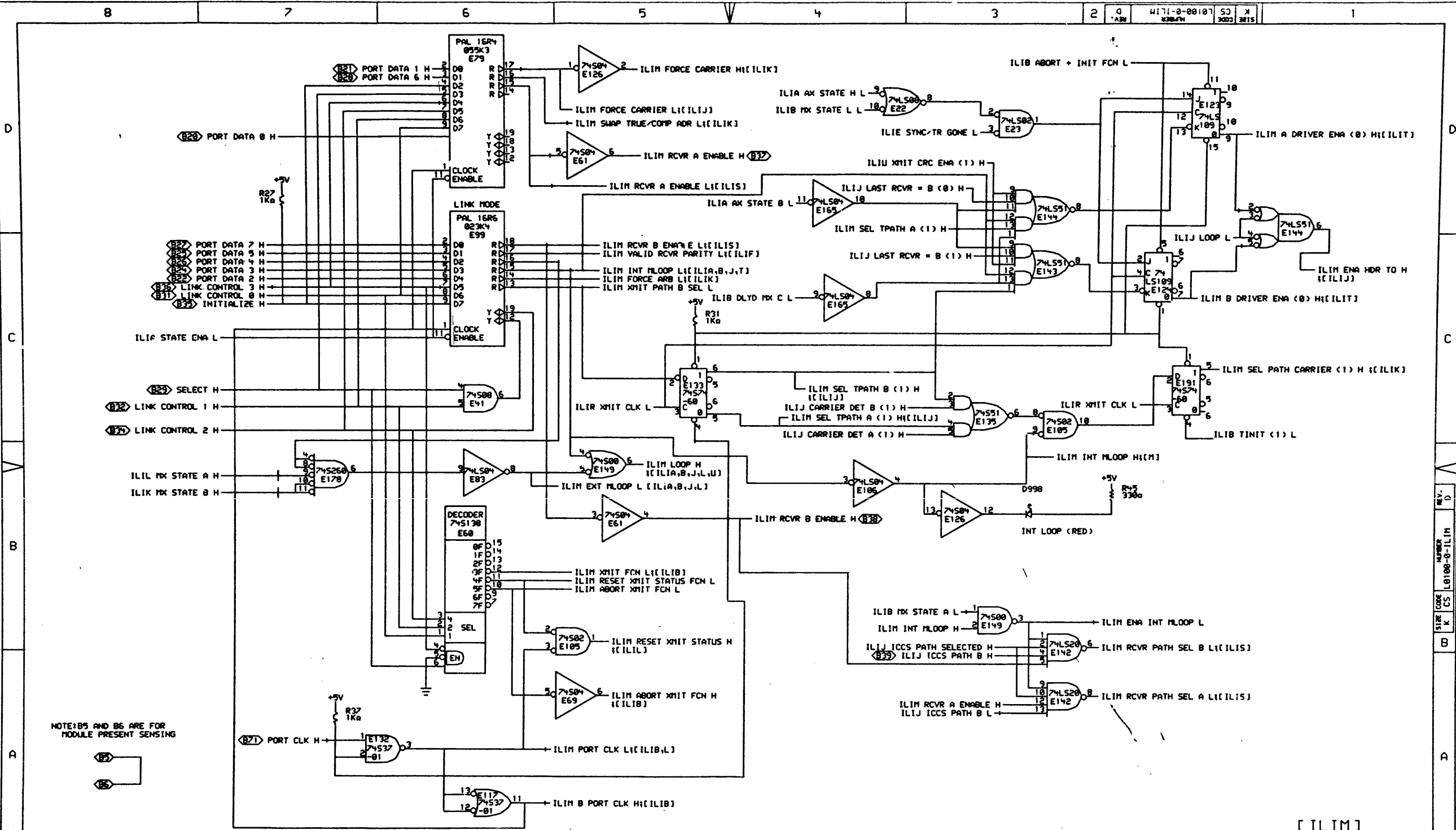
[ILIL]
 (XMIT ATTN & STATUS)

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REVISIONS		
CHK	CHANGE NO.	REV

	DATE: 02-09-84 CHK'D: J. J. J.	DATE: _____ ENG. _____	DATE: _____ TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
	DATE: _____ BOARD LOCATION: _____	DATE: _____ SHEET: _____ OF _____	DATE: _____ NEXT HIGHER ASSEMBLY: _____

OSK01 (PLURDC) XPL10.DRAW07-MAR-84 12:03
 FIRST USED ON OPTION MODEL: C1780 D-UA-L0100-0-1



NOTE: B5 AND B6 ARE FOR MODULE PRESENT SENSING

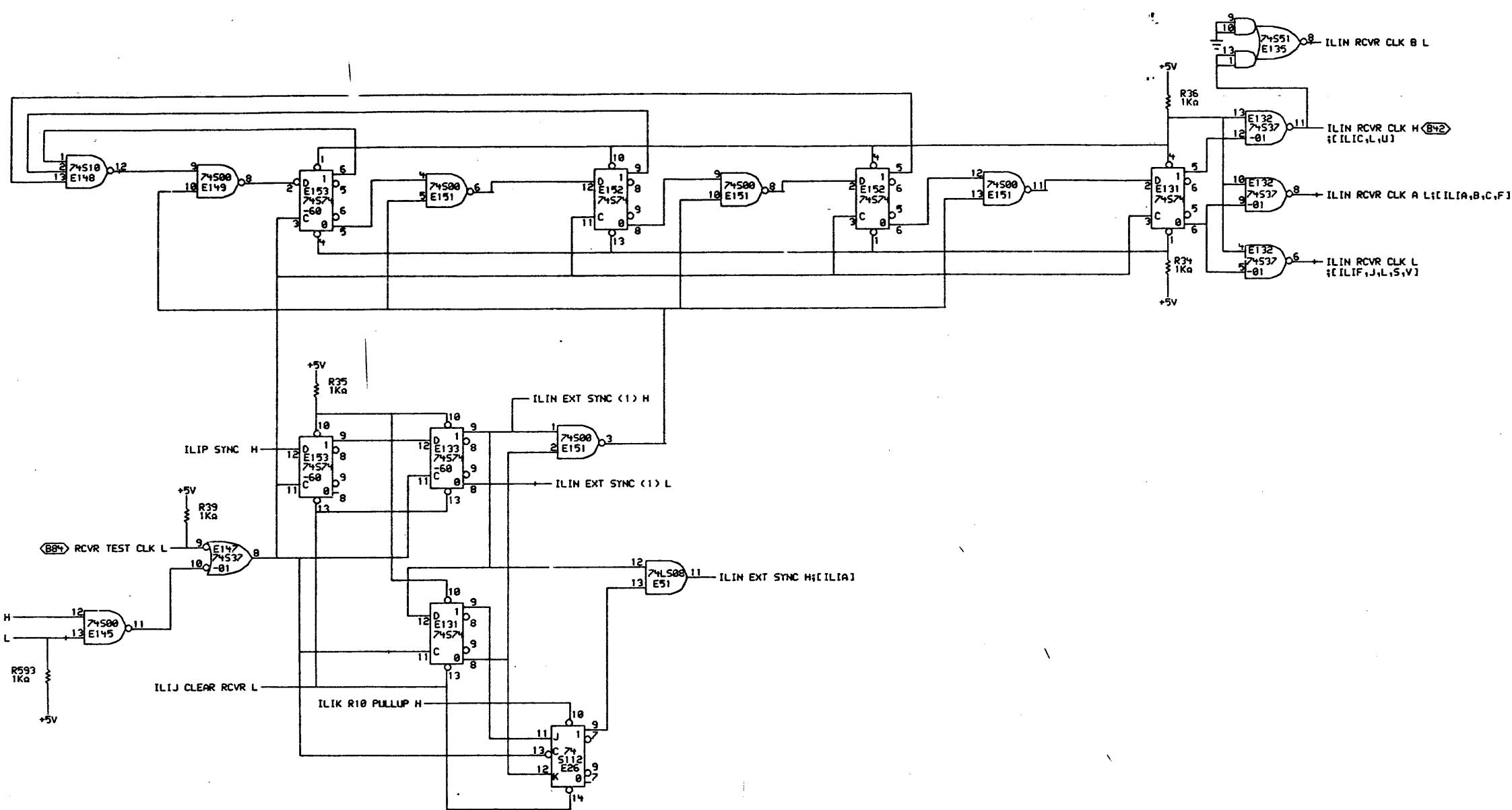
[ILIM]
 (LINK CONTROL & ICCS ENA)

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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
	DATE	BOARD LOCATION:	SHEET	OF
FIRST USED ON OPTION MODEL: C1780		NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1		SIZE CODE: K CS
NUMBER: L0100-0-ILIM			REV.:	D

REV. D
 NUMBER L0100-0-ILIM
 SIZE CODE K CS



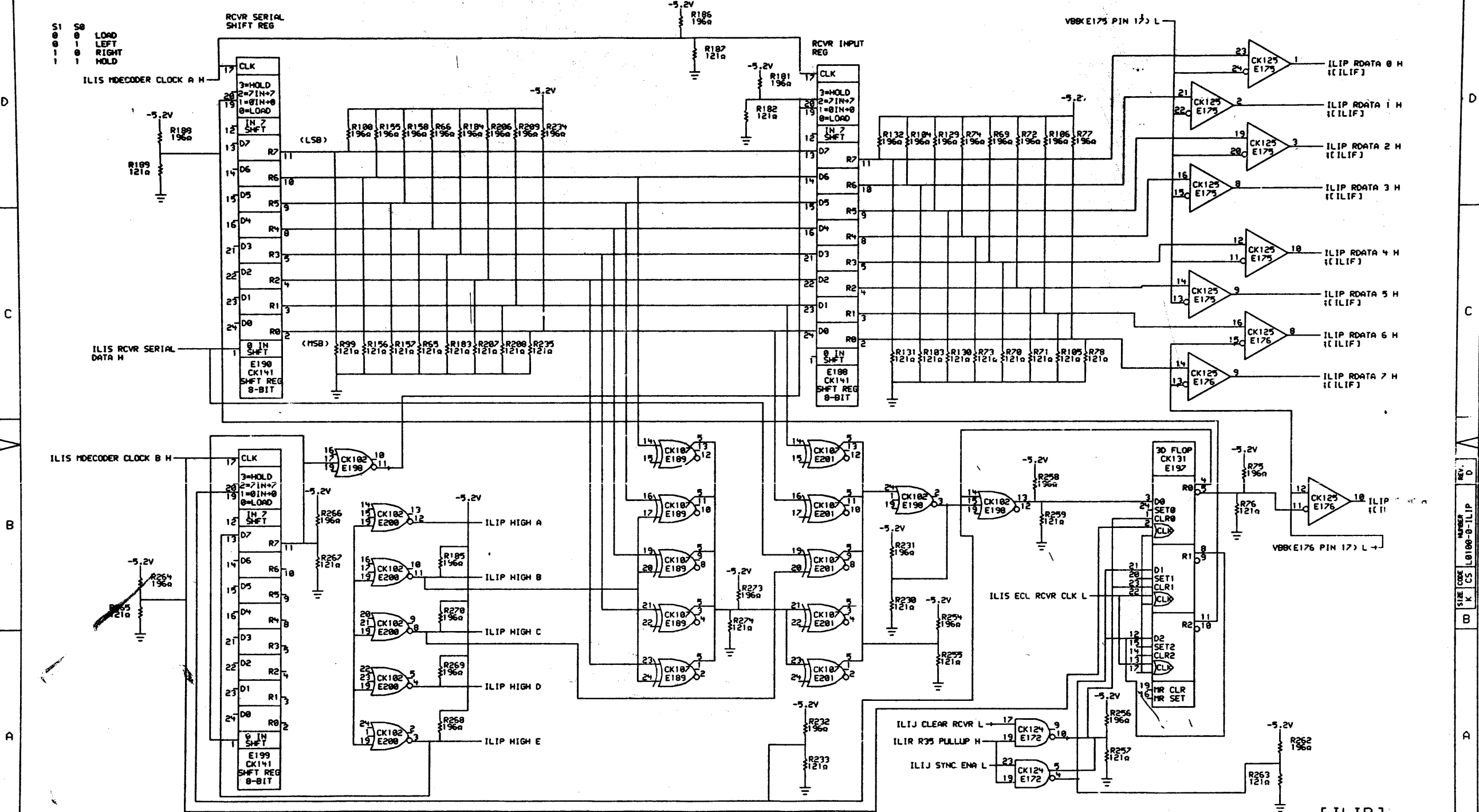
[ILIN]
(INTERNAL RCVR TIMING SOURCE)

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REVISIONS
CHK CHANGE NO. REV

	DATE: 10-11-83	ENG: J. J. 00	DATE: 3-4-83	TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
	CHK: J. J.	DATE: 12-12-83	BOARD LOCATION: 1	SIZE CODE: K CS
DSKB1 (NUEBLING) APIL12.DPL (22-AJ-83) 10:58	NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1	SHEET: 1	NUMBER: L0100-0-ILIN	REV: C
FIRST USED ON OPTION/MODEL: C1780				

S1 S0
 0 0 LOAD
 0 1 LEFT
 1 0 RIGHT
 1 1 HOLD



EXTERNAL FRAMER [ILIP] (RCVR SERL TO PARL & SYNC DETECT)

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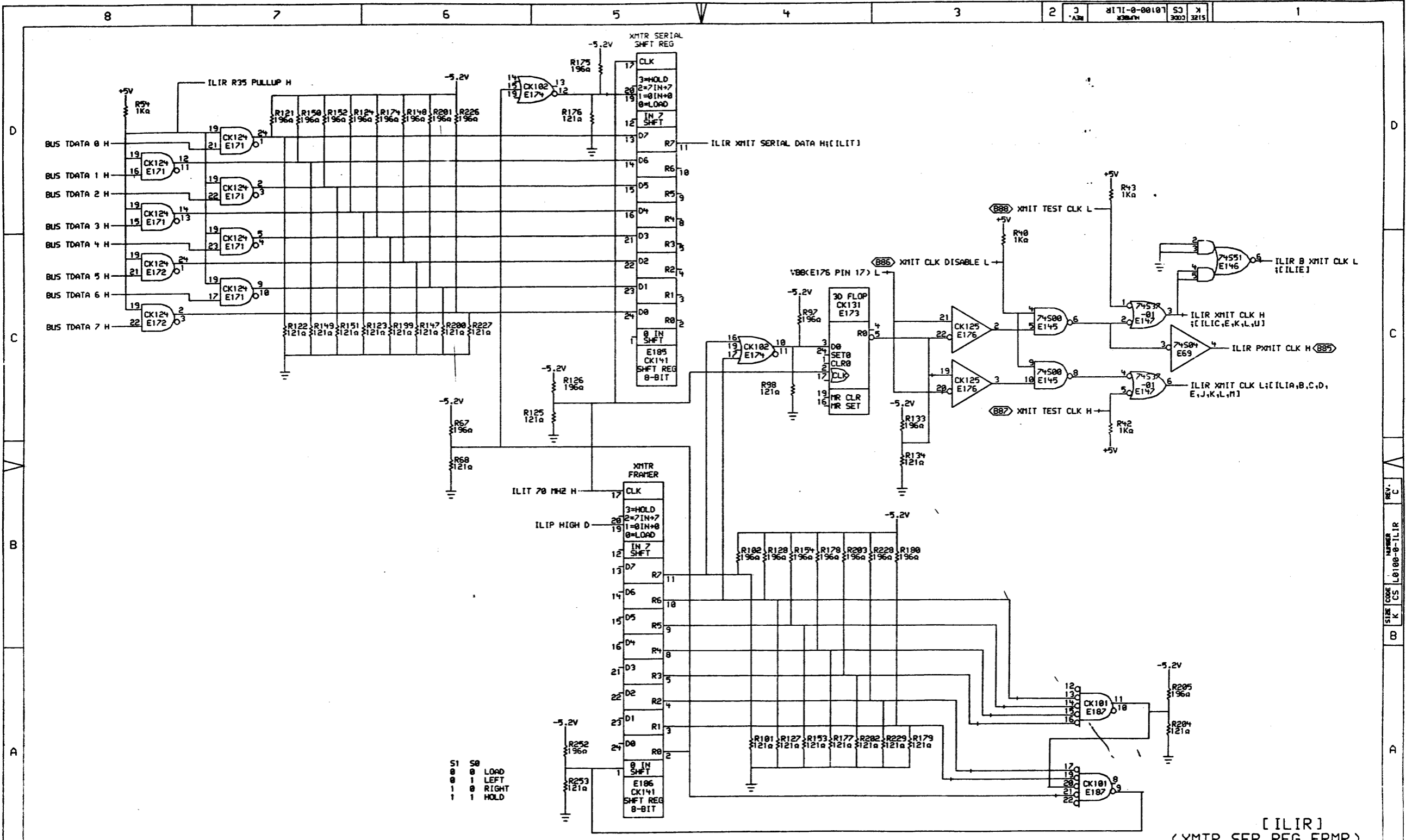
REVISIONS	
CHK	CHANGE NO. REV

DATE 82-APR-84 ENG. DATE TITLE: INTERPROCESSOR LINK INTERFACE (ILI)

DATE BOARD LOCATION: SHEET 1 OF 1 SIZE CODE NUMBER REV. D

OSKDI (PLAURDE) XAP11.3, DRW106-DEC-83 09150 NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1 K CS L0100-0-ILIP

FIRST USED ON OPTION/MODEL: C1780

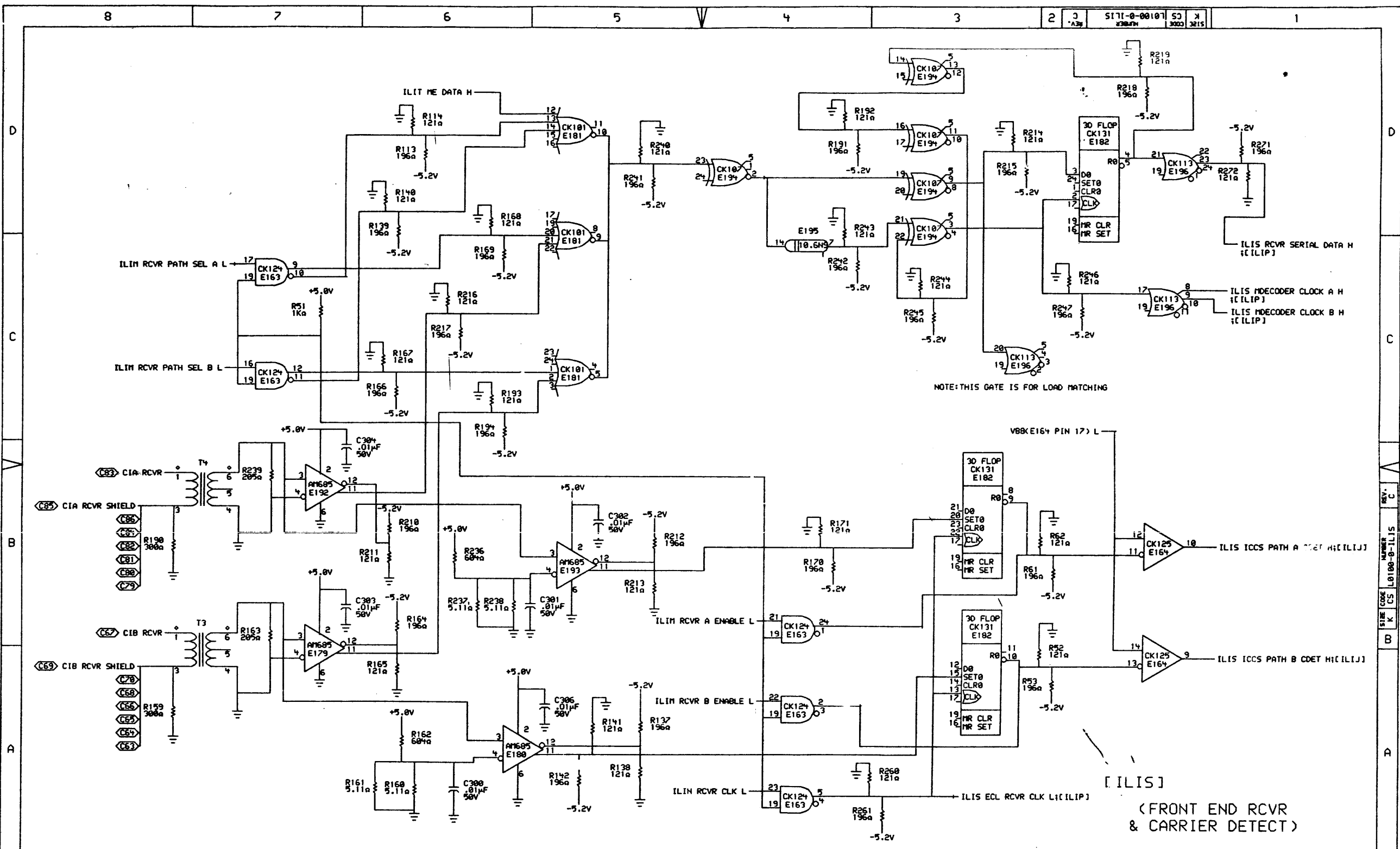


S1 S0
 0 0 LOAD
 0 1 LEFT
 1 0 RIGHT
 1 1 HOLD

[ILIR]
 (XMTR SER REG FRMR)

REVISIONS	
CHK	CHANGE NO. REV

	DRN: <i>J. J. J...</i> DATE: 27-JUL-83 CK'D: <i>[Signature]</i> DATE: 27-JUL-83	ENG: <i>R. W. N...</i> DATE: 27-JUL-83 DESIGNED LOCATION: <i>[Blank]</i>	TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
	DSKB: <NUEBLING>APIL14.DPL1 FIRST USED ON OPTION/MODEL: C1780	V. 1.00 NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1	SIZE CODE: K CS NUMBER: L0100-0-ILIR



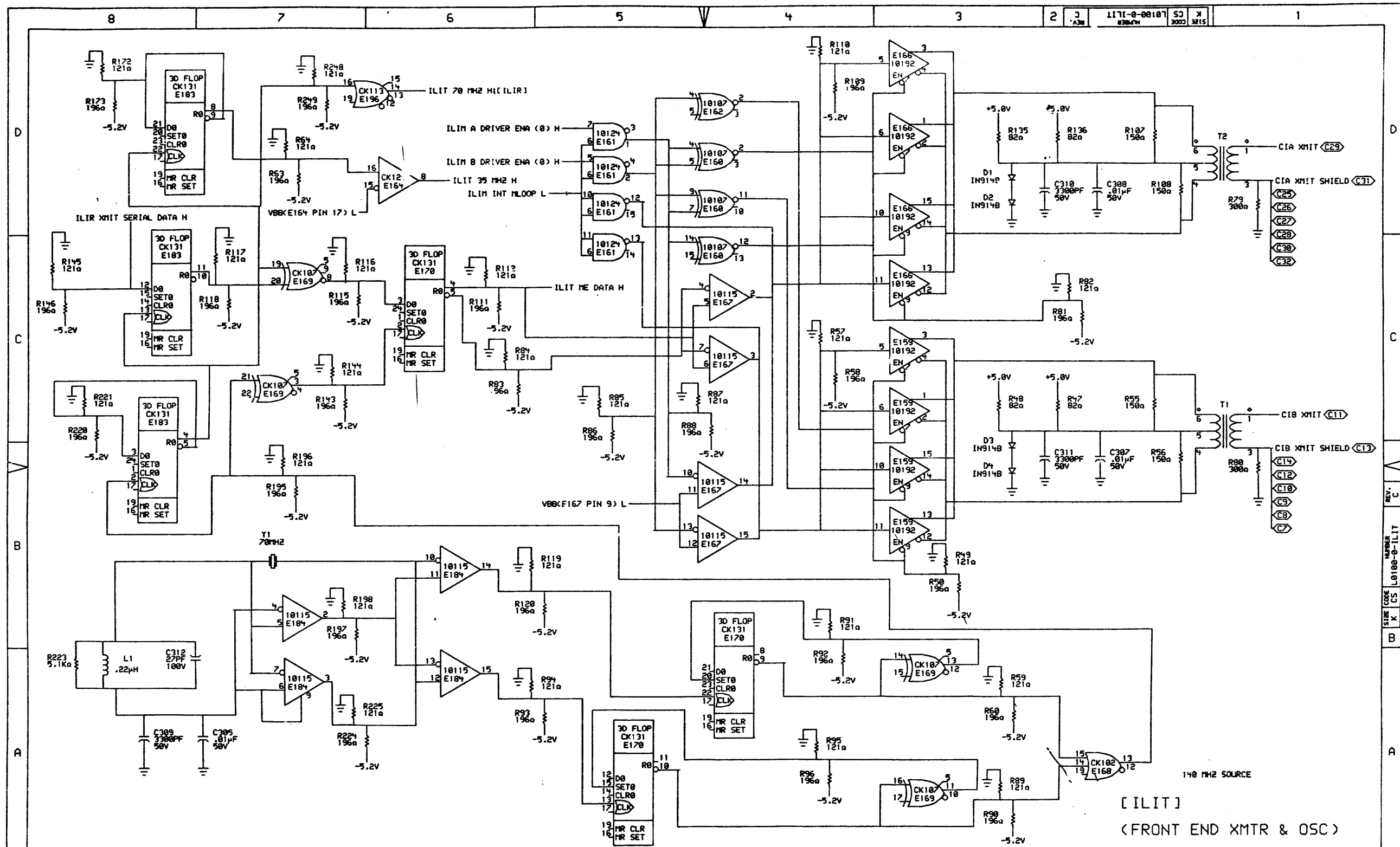
NOTE: THIS GATE IS FOR LOAD MATCHING

[ILIS]
 (FRONT END RCVR
 & CARRIER DETECT)

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REVISIONS	
CHK	CHANGE NO. REV

	DRW: J. Family	DATE: 25-11-83	ENG: C. Neuling	DATE: 8-4-85	TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
	CHK'D: J. Family	DATE: 11-11-83	BOARD LOCATION: 4. A2. 2 SHEETS	DE: 1	SIZE: K CS L0100-0-ILIS
DSKB: (NLEBLING)APIL15.DRW122-JUL-83 10158 NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1		FIRST USED ON OPTION/MODEL: CI780		NUMBER: K CS L0100-0-ILIS	REV. C

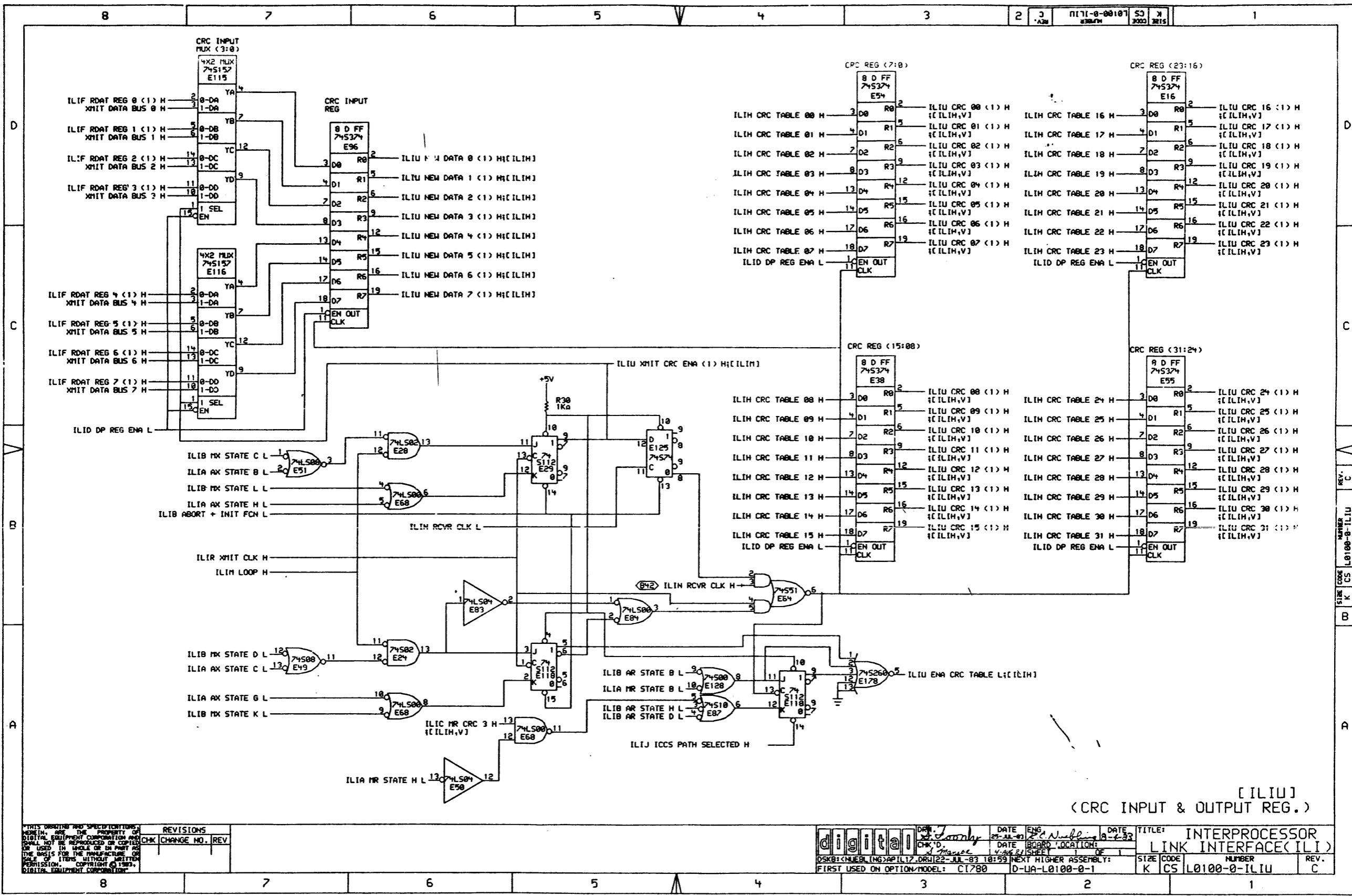


[ILIT]
(FRONT END XMIT & OSC)

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REVISIONS	
CHK	CHANGE NO. REV

	DATE: 8-2-83	ENG: J. C. Nye	DATE: 8-2-83	TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
	DATE: 8-2-83	BOARD LOCATION: 1	OF: 1	SIZE CODE: K CS
DSKB: NUBEL (NO) APIL16.DPL16-83 10:50 NEXT HIGHER ASSEMBLY:		FIRST USED ON OPTION/MODEL: C1780 D-UA-L0100-0-1		NUMBER: L0100-0-ILIT REV: C



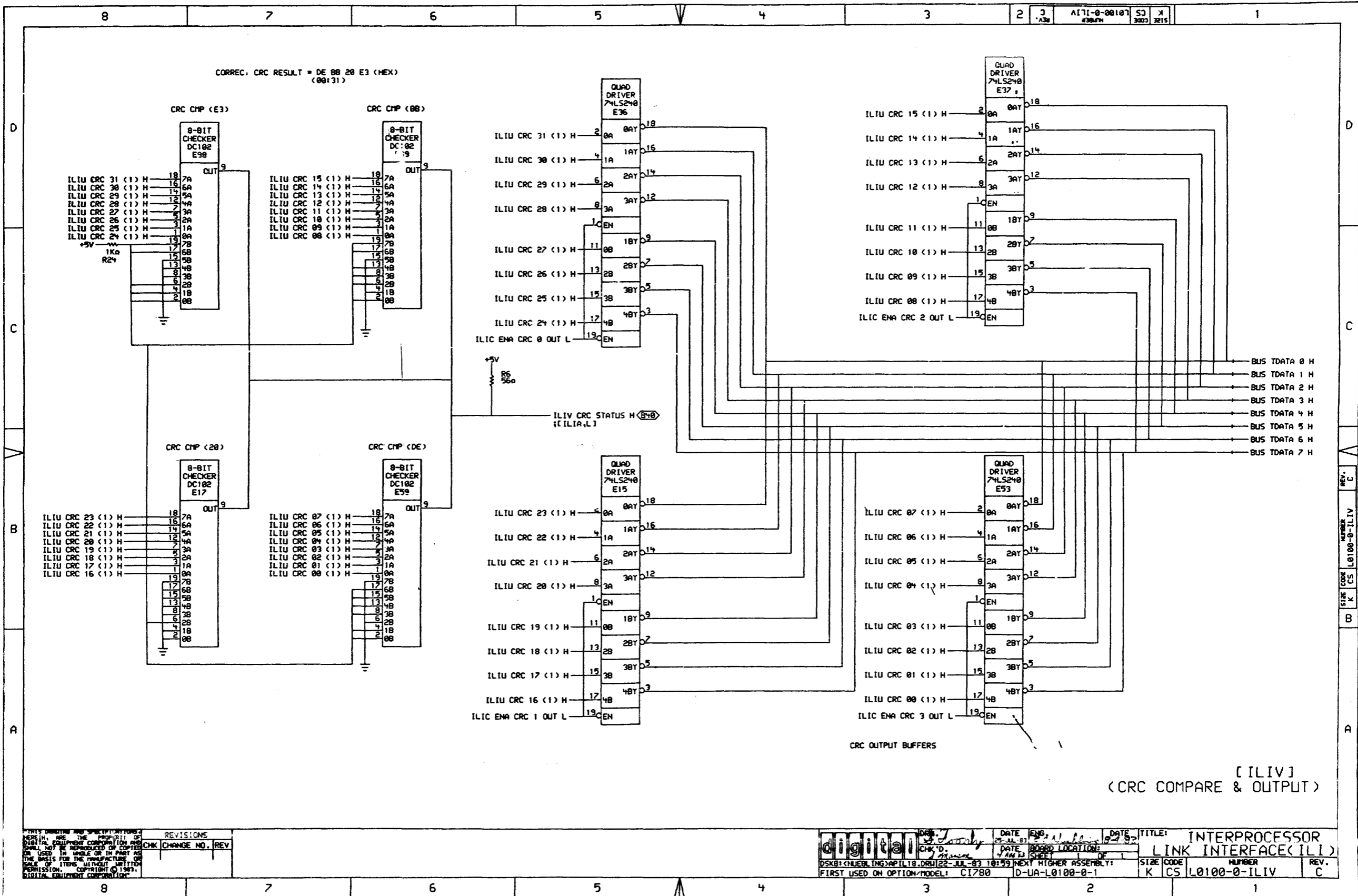
[ILIU]
(CRC INPUT & OUTPUT REG.)

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REV.	CHANGE NO.	REV.

digit@l	DATE	25-JUL-83	DATE	8-4-83
	CHK'D.	J. Family	DATE	BOARD LOCATIONS
			SHEET	1 OF 1
FIRST USED ON OPTION/MODEL:		D-LA-L0100-0-1		

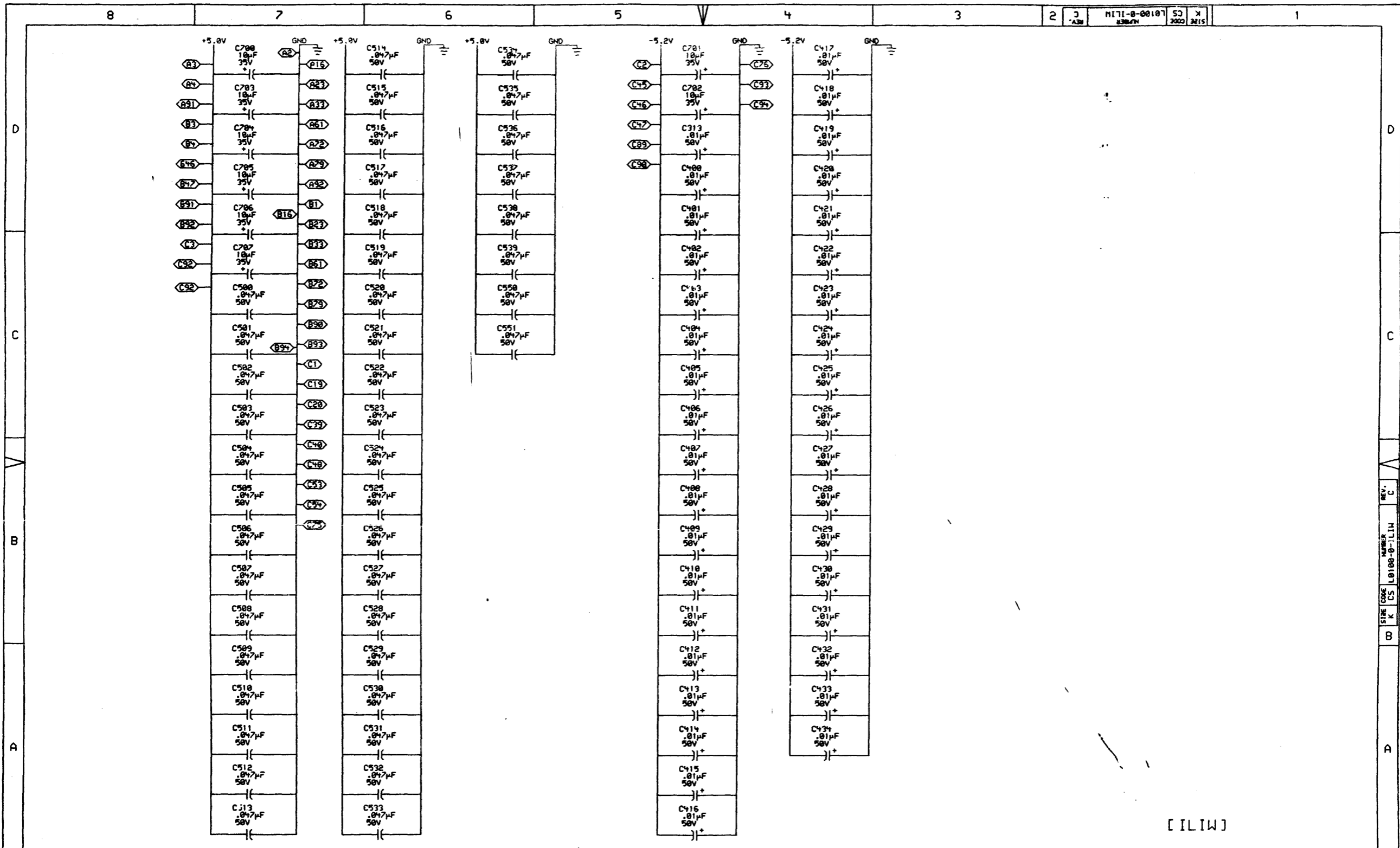
TITLE:	INTERPROCESSOR LINK INTERFACE (ILI)
SIZE CODE	K CS L0100-0-ILIU
NUMBER	
REV.	C



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REVISIONS		
CHK	CHANGE NO.	REV

DATE: 27-JUL-83	ENG: J. A. ...	DATE: 27-JUL-83	TITLE: INTERPROCESSOR LINK INTERFACE (ILIV)
CHK'D: M. ...	DATE: 27-JUL-83	BOARDS LOCATION: 4/11/83	REV: C
DSKBI:(MUEBLING)APIL18.DRW:22-JUL-83 10:59	NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1	SIZE: K	CODE: CS
FIRST USED ON OPTION MODEL: C1780		NUMBER: L0100-0-ILIV	REV: C



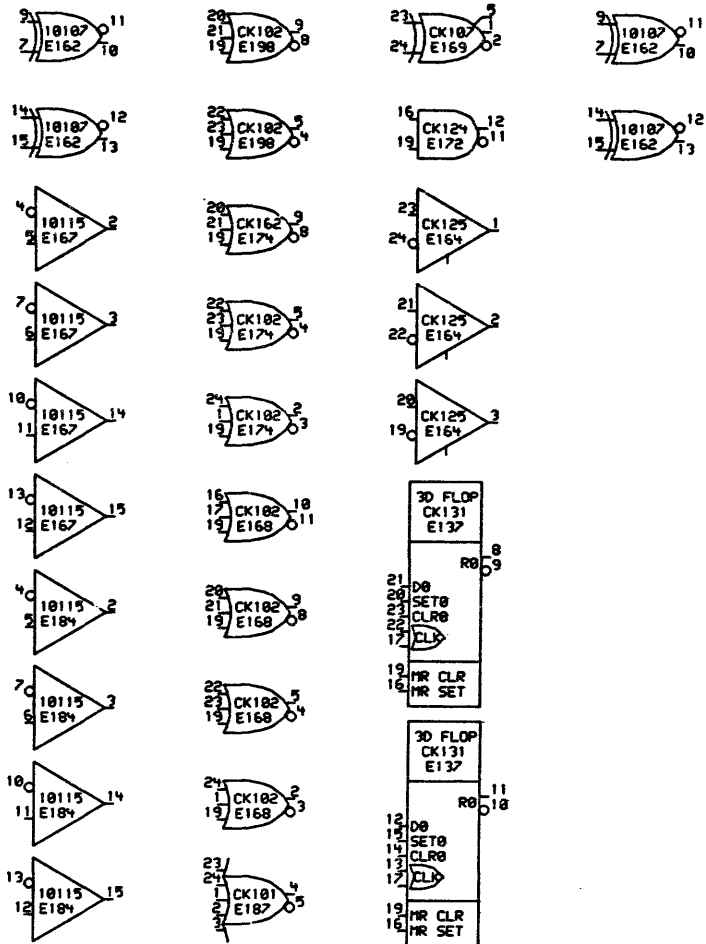
[ILIW]

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REVISIONS	
CHK	CHANGE NO. REV

	DRAWN: <i>J. L. Lundy</i> CHK'D: <i>J. L. Lundy</i>	DATE: 07-22-83 DATE: 07-22-83	ENG: <i>J. L. Lundy</i> DATE: 07-22-83	TITLE: INTERPROCESSOR LINK INTERFACE (ILI)
	SHEET: 1 OF 1 FIRST USED ON OPTION/MODEL: C1780	BOARD LOCATION: 1 NEXT HIGHER ASSEMBLY: D-UA-L0100-0-1	SIZE CODE: K CS	NUMBER: L0100-0-ILIW

SPARE GATES AND I/O PINS



[ILIX]

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REVISIONS	
CHK	CHANGE NO. REV.

digital	DATE	ENG.	DATE	TITLE:
				INTERPROCESSOR LINK INTERFACE (ILI)
	DATE	BOARD LOCATION:		
			SIZE	CODE
			K	CS
			NUMBER	REV.
			D-LA-L0100-0-1	D
			FIRST USED ON OPTION/MODEL:	
			C1780	

DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS																			
				1	2	3	4	5	6	7	8	9	10	11	12	13							
B-DD-L0109-0-0	1		PILA (Generic) Drawing Directory	A																			
B-DD-L0109-0-E	1		Drawing Directory (E-rev. etch)	F																			

NOTES:
 Rev Note: All revisions noted are the minimum acceptable level.
 Explanation Note: This document ties together all module packages that are useable as an L0109.
 Next Higher Document Number (s): B-DD-HSC50-0-BDS

REVISION HISTORY	ECO NO.	REV.
	Initial Release	A

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USED ON OPTION/MODEL		DRN. <i>Shore Korman</i>	DATE <i>22 JUNE 83</i>
HSC50		CHK'D <i>[Signature]</i>	DATE <i>5 July 83</i>
		DES. ENG. <i>[Signature]</i>	DATE <i>24 May 83</i>
		RESP. ENG. <i>[Signature]</i>	DATE <i>24 May 83</i>
		MEG. ENG. <i>[Signature]</i>	DATE <i>13 Jul 83</i>

TITLE PILA (Generic)		
DOCUMENT NUMBER		
SIZE B	CODE DD	NUMBER L0109-0-0
		REV. A
SHEET 1 OF 1		

BDD L0109-0-E

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 REGISTERED BY NUMBER 1754-

10

H	REV	10109-0-E	DD	B
		NUMBER	CODE	SIZE

DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS																							
B-DD-L0109-0-E	1		DRAWING DIRECTORY	E	F	H																					
D-UA-L0109-0-E	1		UNIT ASSEMBLY	E	F	H																					
K-PL-L0109-0-DBPE	2		PARTS LIST	E	F	H																					
K-CS-L0109-0-E	11		CIRCUIT SCHEMATICS	E	F	F																					
K-PC-L0109-0-DBI	-		PC DATA BASE	E	E	E																					
K-BD-L0109-0-E	1		BLOCK DIAGRAM	E	F	F																					
K-TD-L0109-0-E	5		TIMING DIAGRAM	E	F	F																					
B-DD-5014931-0-E	1		DRAWING DIRECTORY	E	F	F																					
		L0109-00	PILA MODULE (E REV ETCH)	-	E1	E2																					

NOTES:

EXPLANATION NOTE: THIS DOCUMENT TRACKS DOCUMENTATION FOR USE ON A L0109-00 BASED UPON THE USE OF "E" REVISION ETCH.

NEXT HIGHER ASSEMBLY NUMBER(S): B-DD-L0109-0-0.

DATE	ECO NO.	REV.	RELEASE AT REV. E. S.L. 7 JAN 83			
1837	L0109-CX001	E		F	H	
5016	L0109-CX002	F				

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USED ON OPTION/MODEL		DRN. S. Lehman	DATE 7 JAN 83	TITLE
HSC50		CHK'D Sue Bamber	DATE 13 JAN 83	PILA
		DES. ENG. A. Holland	DATE 13-1-83	DOCUMENT NUMBER
		RESP. ENG. A. Holland	DATE 13-1-83	SIZE B DD NUMBER L0109-0-E REV. H
		MFG. ENG. S. Corbett	DATE 14 JAN 83	SHEET 1 OF 1

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ECO L0109-CX001

COMPONENT DELETES:

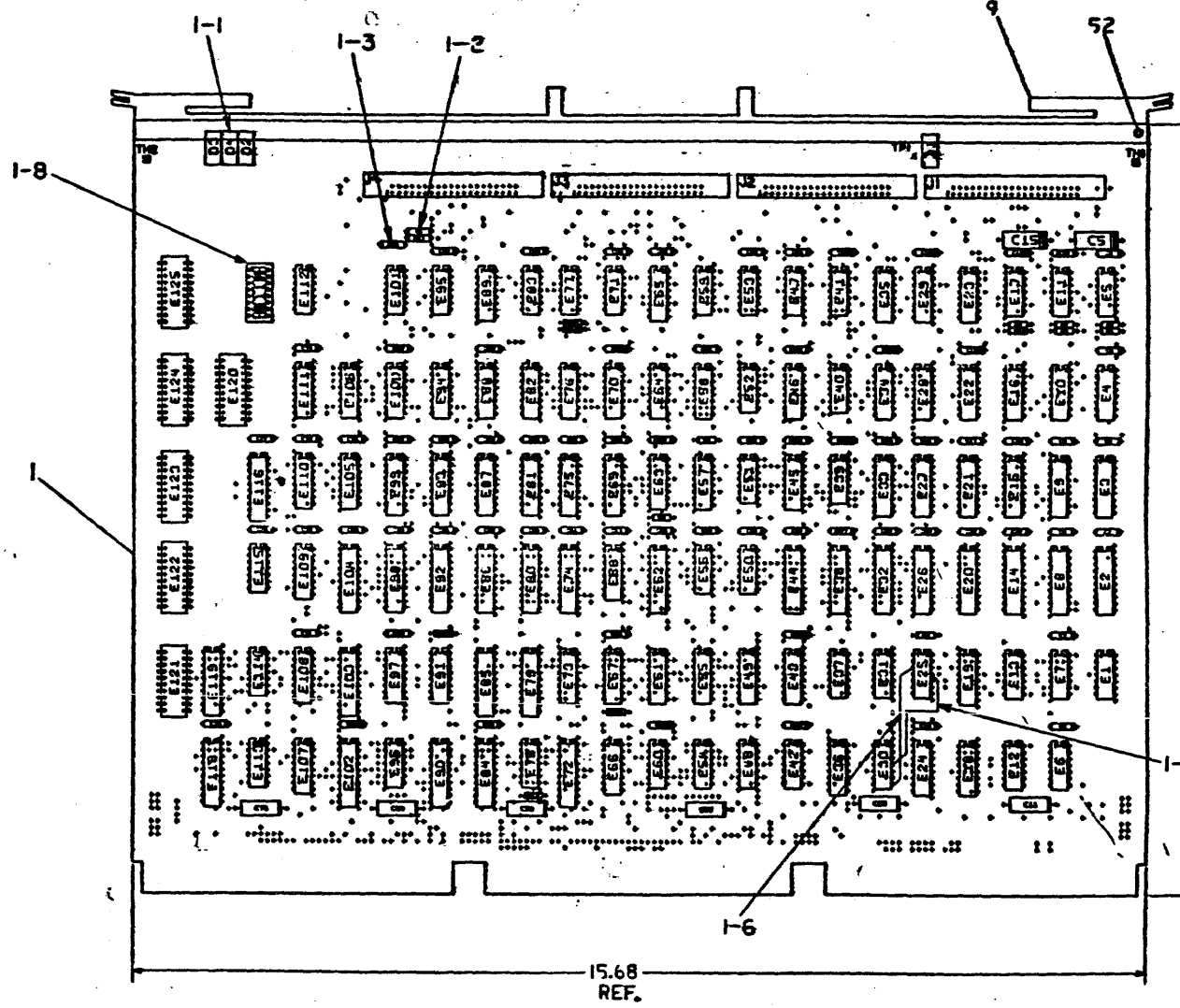
- I-1. DELETE D4, (1117373-02).
- I-2. DELETE W1, (9009185-00).
- I-3. DELETE R16, (1300309-00).

WIRE ADDS: SIDE 1

- I-6. ADD WIRE FROM E25-3 TO E30-8.
- I-7. ADD WIRE FROM E25-11 TO E30-11.

SET SWITCHES

I-8. SET E117 AS:
 0-0-0-0-0-0-0-0-0-0-0-0-0-0-0-0-0-0



NOTES: 1. THE FOLLOWING ARE SPARES: E120, E121, E122, E123, E124, E125, J1-M.
 2. YP1 IS UNUSED.

STEP	# Y AXIS	STEP	TIMES
REPORT	# X AXIS	STEP	TIMES

DATE	CHANGE NO.	REV.	BY	CHK.

PART NUMBER: 00000
 NAME: M. J. JAMES
 S. CHOW
 WORKING TITLE: DESIGN ENGINEER
 PROJECT: L0109-CX001
 DRAWING NO.: 2-22-77

ETCH REV. 1

SIGNATURES		DATE	TITLE
DRN.	<i>[Signature]</i>	11/22	digital
CHK.	<i>[Signature]</i>	11/22	
MECH. ENG.	<i>[Signature]</i>	11/22	
PROJ. ENG.	<i>[Signature]</i>	11/22	
PROD.	<i>[Signature]</i>	11/22	
SCALE: 1/1		SHT. 1 OF 1	SIZE CODE: H
NEXT HIGHER DOCUMENT: DD-L0109-0-E		NUMBER: H	

DJA L0109-0-E H

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	VARIATION REVISION LEVEL:	QTY PER VARIATION	REFERENCE DESIGNATOR
						00	
1	B-DD-5014931-0-E	5014931-00		DRILL & ETCH BOARD-PILA		1	
2		1012784-00		.047 MFD 50V +80-20%	CER	70	CONT CONT
3		1016549-00		47 MFD 10V +50-10%	AL EL	2	C1-C4,C6-C10,C12-C14,C16-C22, C24-C38,C40-C52,C54-C63,C65-C74, C76-C78
4		1017472-00		10 MFD 35V +50-10%	AL EL	6	C5,C15 C11,C23,C39,C53,C64,C75
5		1117373-00		LED ASSY GREEN		1	D3
6		1117373-01		LED ASSY RED		1	D2
7		1117373-02		LED ASSY, YELLOW		1	D1
8		1211164-04		SW,DIP 09POS/1PST 5VDC100MA F		1	E117
9		1216988-02		HANDLE,MODULE,HEX TWO EJECTORS		1	
10		1300005-03		R NETWORK 13-10K 5.0 % 14PIN		1	E115
11		1300309-00		390.0 .25 W 5.0 %	CF	5	R2,R4,R8,R11,R12
12		1300365-00		1.0 K .25 W 5.0 %	CF	2	R7,R14
13		1301423-00		6.80 K .25 W 5.0 %	CF	2	R5,R6
14		1300479-00		10.0 K .25 W 5.0 %	CF	3	R1,R3,R17
15		1311522-00		200.0 .25 W 5.0 %	CF	4	R9,R10,R13,R15
16		1910532-00		74S00 NAND GATE-QUAD 2IN		1	E40
17		1910534-00		74S04 INVERTER GATE-HEX 1I		2	E52,E71
18		1910537-00		74S11 AND GATE-TRIPLE 3INP		2	E7,E47
19		1910544-00		74S74 FF-D DUAL,EDGE TRIGG		1	E13
20		1910550-00		74S174 FF-D HEX		1	E55
21		1910957-00		74S175 FF-D QUAD COMMON CLO		2	E65,E89
22		1911573-00		74S280 PARITY GEN/CHKR,9BIT		1	E114
23		1911675-00		74S138 DECODER/DEMUX 3-8 LI		2	E91,E97
24		1911676-00		74S139 DECODER-DUAL TWO-INP		1	E61
25		1911712-00		74S51 AND-OR GATE-INVERT D		3	E60,E78,E96
26		1912388-00		74S02 NOR GATE-QUAD 2IN,PO		1	E56
27		1912389-00		74S08 AND GATE-QUAD 2IN,PO		2	E54,E101
28		1912728-00		74S251 MUX 1 OF 8 TRI-STA		1	E107

REVISION HISTORY		BASIC PART NO: L0109		DRN:	SBOURBEAU	DATE: 13-JAN-83	DIGITAL			
ENG	ECD NUMBER	REV	SECTION A OF A	CHK'D:	D WILSON	DATE: 13-JAN-83	TITLE PARTS LIST			
RH	INITIAL	E	SECTION.VARIATION INDEX	DES.ENG:	R HOLLAND	DATE: 13-JAN-83	DOCUMENT NUMBER			
SC	L0109-CX001	F	[A] 00	RESP.ENG.:	R HOLLAND	DATE: 13-JAN-83	SIZE	CODE	NUMBER	REV
TF	L0109-CX002	H	[B]	MFG.ENG.:	L CONDON	DATE: 13-JAN-83	K	PL	L0109-0-DBPE	H
TFP	2-22-84		[C]			RELEASE DATE:		22-FEB-84		
			[D]							
			[E]							
			[F]							
			[G]							
			[H]							
			[I]							
			[J]							
			[K]							
			[L]							
			[M]							
			[N]							
				ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
				D-UA-L0109-0-E	B-DD-L0109-0-E		Z2182H.PLS		15	

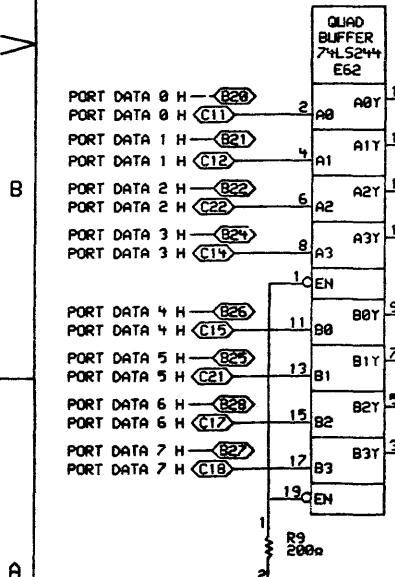
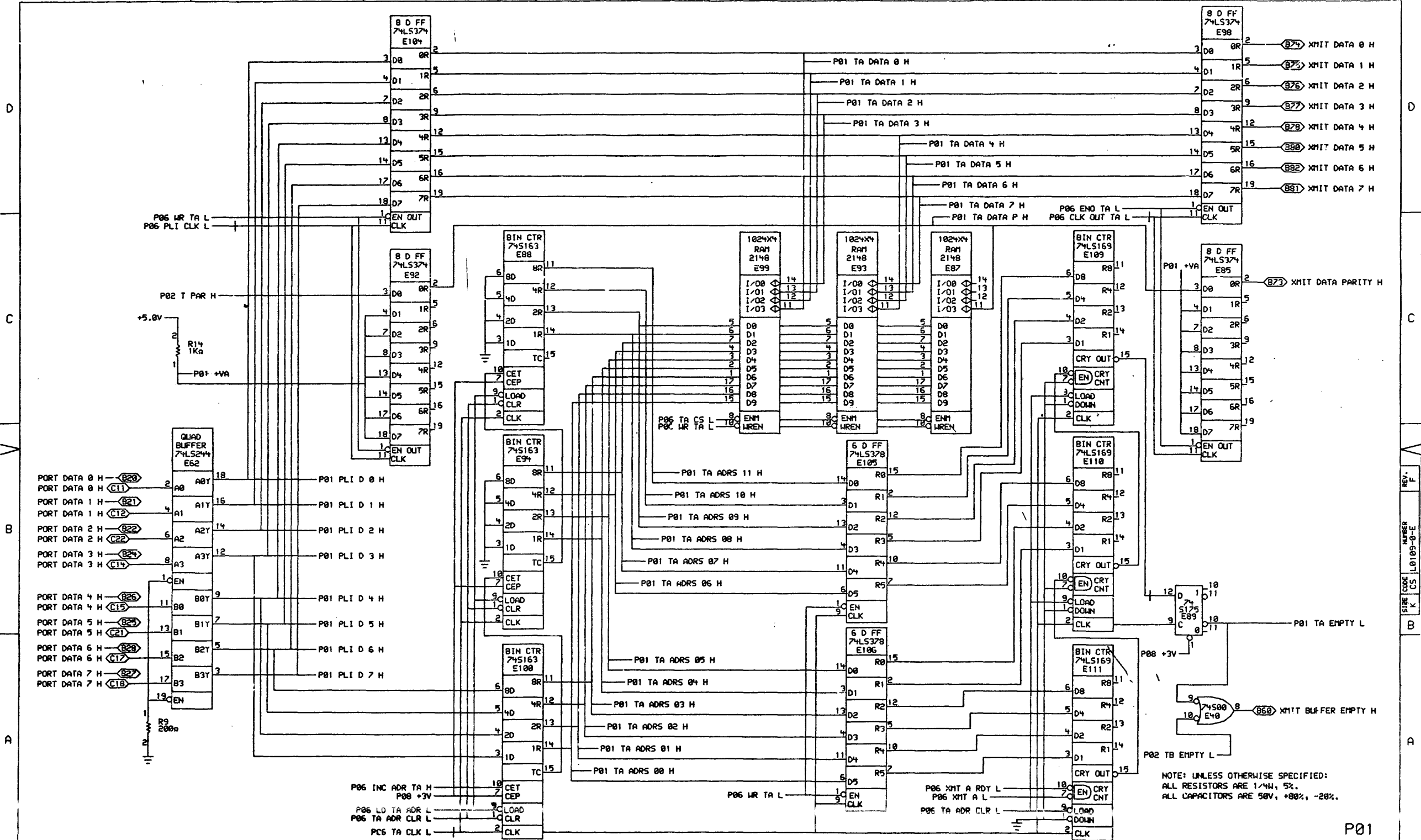
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LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	VARIATION REVISION LEVEL:	QTY PER VARIATION	REFERENCE DESIGNATOR
						00	
29	29	1912746-00	DEC	74S37	NAND GATE-QUAD 2IN	1	E113
30	30	1912799-00		LS00	NAND-GATE-QUAD 2IN,P	5	E5,E12,E51,E53,E77
31	31	1912801-00		LS02	NOR-GATE-QUAD 2IN	2	E11,E31
32	32	1912805-00		LS08	AND GATE-QUAD 2IN,PO	3	E1,E42,E48
33	33	1912807-00		LS10	NAND GATE-TRIPLE 3IN	1	E39
34	34	1912808-00		LS11	AND GATE-TRIPLE 3IN	1	E17
35	35	1912810-00		LS20	NAND GATE-DUAL 4IN	2	E83,E95
36	36	1912813-00		LS27	NOR GATE-TRIPLE 3IN	3	E6,E37,E59
37	37	1912816-00		LS32	OR GATE-QUAD 2IN,POS	1	E30
38	38	1912824-00		LS74	FF-D DUAL,EDGE TRIGG	1	E25
39	39	1912833-00		LS109	FF-JK DUAL,POS EDGE	5	E18,E19,E24,E46,F108
40	40	1912847-00		LS157	MUX 1 OF 2(QUAD)	4	E36,E66,E67,E73
41	41	1912851-00		LS169	COUNTER,SYNCH. UP/DO	9	E23,E29,E35,E57,E58,E68,E109,
							CONT E110,E111
42	42	1912853-00		LS175	FF-D QUAD	1	E45
43	43	1913340-00		74S32	OR GATE-QUAD 2IN	2	E50,E112
44	44	1913777-00		LS240	DRIVER,LINE,OCTAL,T	1	E102
45	45	1914082-00		74S163	COUNTER,SYNCH UP/DOW	14	E4,E10,E16,E22,E28,E34,E70,E76,
							CONT E82,E88,E94,E100,E43,E49
46	46	1914085-00		74S260	NOR GATE-DUAL,POS	1	E41
47	47	1914214-00		LS374	FF-D OCTAL EDGE TRIG	14	E2,E8,E14,E44,E74,E79,E80,E85,
							CONT E86,E90,E92,E98,E103,E104
48	48	1914867-00			FF-D HEX, COMMON REG	4	E63,E64,E105,E106
49	49	1915193-00		LS244	DRIVER,LINE,OCTAL,T	1	E62
50	50	1920208-01		74S244	OCTAL BUFFER,TRI-STA	4	E72,E84,E118,E119
51	51	2116957-02			1K MOS RAM 70NS 1	12	E3,E9,E15,E21,E27,E33,E69,E75,
							CONT E81,E87,E93,E99
52	52	9000024-01			EYELET,ROLLED 0.1210DX0.192	12	
53	53	9009149-01			*** THIS ITEM IS NOT USED ***	-	
54	54	1913462-01		74S240	OCTAL BUFFER,INVERTI	1	E116
55	55	1913671-01		74S374	FF-D,OCTAL,TRI STATE	4	E20,E26,E32,E38
56	56	9009185-00			*** THIS ITEM IS NOT USED ***	-	
57	57	9105740-55	F		WIRE(WRAP) 30AWG KYNAR UL14	A/R	

D	I	G	I	T	A	L	TITLE	SECTION A	OF	A	SIZE	CODE	DOCUMENT NUMBER	REV
							PILA				K	PL	L0109-0-DBPE	H

D
C
B
A

D
C
B
A

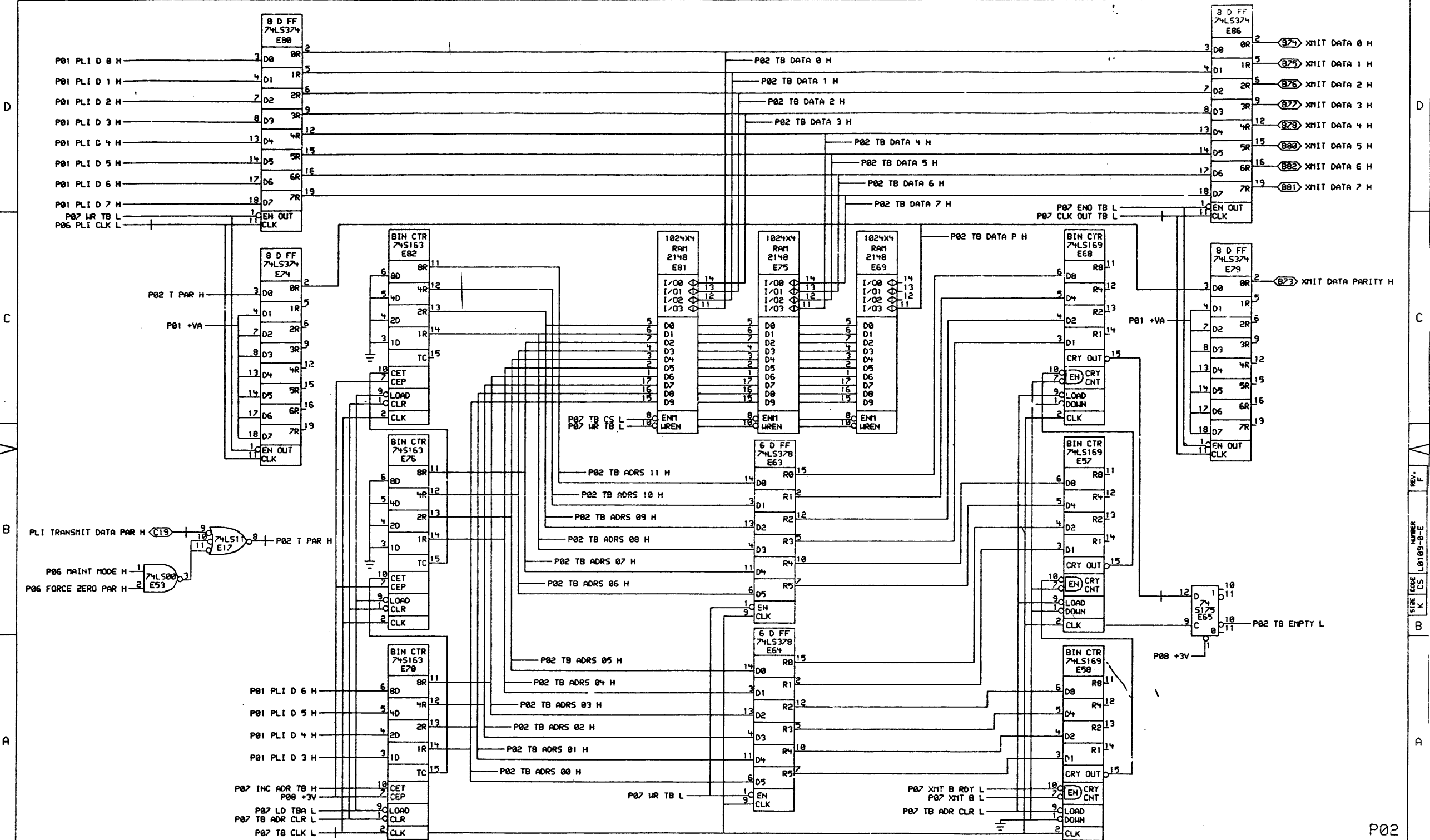


NOTE: UNLESS OTHERWISE SPECIFIED:
ALL RESISTORS ARE 1/4W, 5%.
ALL CAPACITORS ARE 50V, +80%, -20%.

P01

REVISIONS		
CHK	CHANGE NO.	REV
1837	L0109-CX001	F
	WLS 28 JUN 83	
	S. CHOW	

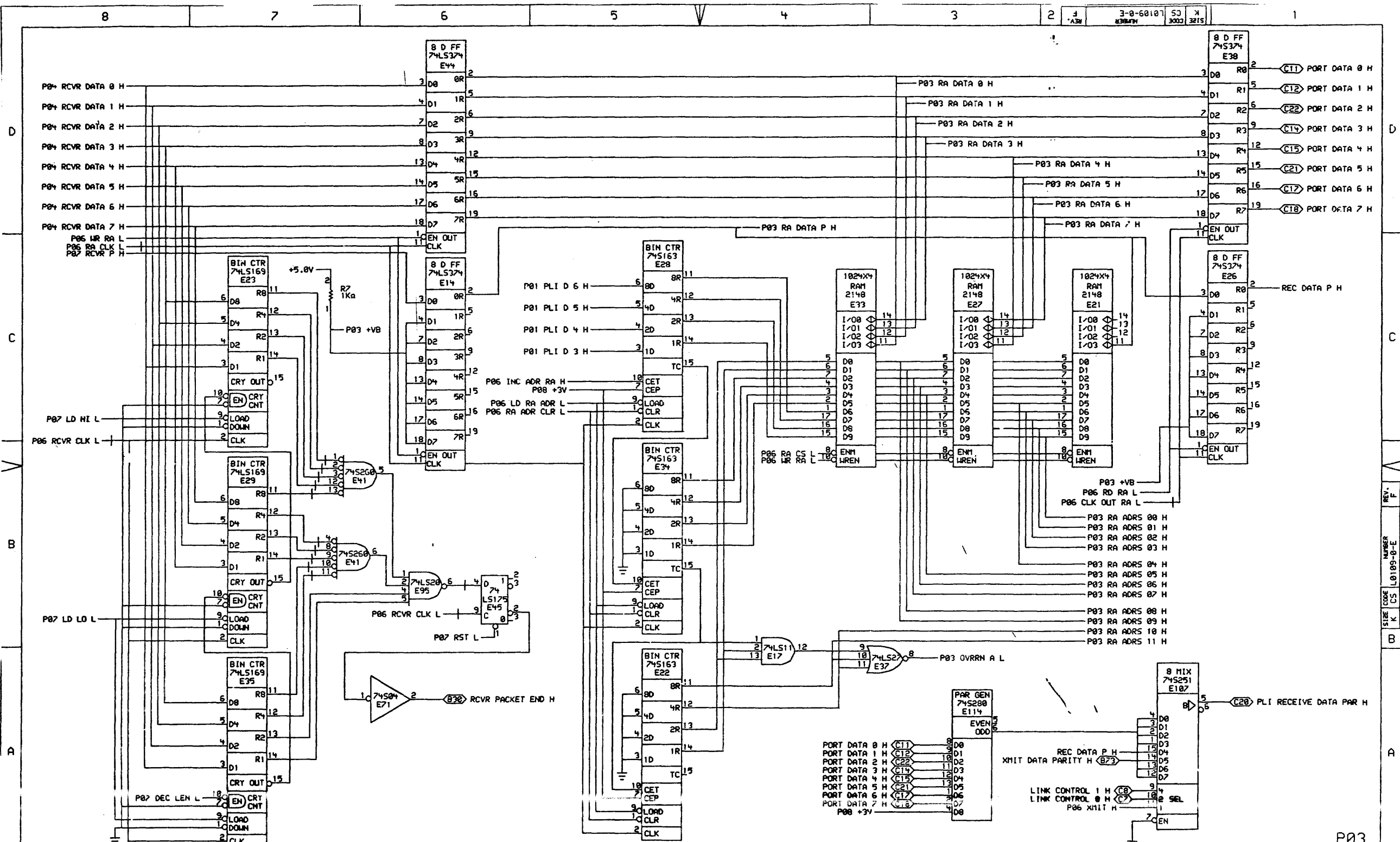
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	CHW	DATE 1/2/83	SHEET 11	OF 11	(TRANSMIT BUFFER A)
FIRST USED ON OPTION MODEL: HSC50			NEXT HIGHER ASSEMBLY: 18-DD-L0109-0-E		SIZE CODE K CS L0109-0-E



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REVISIONS	
CHK	CHANGE NO. / REV

	DRN. R. HOLLAND	DATE 27-JUN-83	ENG. S. CHOW	DATE 27-JUN-83	TITLE: PILA (TRANSMIT BUFFER B)
	CHK'D.	DATE 27-JUN-83	BOARD LOCATION: SHEET 2 OF 11	SIZE CODE: K CS	NUMBER: L0109-0-E
FIRST USED ON OPTION/MODEL: HSC50		NEXT HIGHER ASSEMBLY: B-DD-L0109-0-E		REV. F	

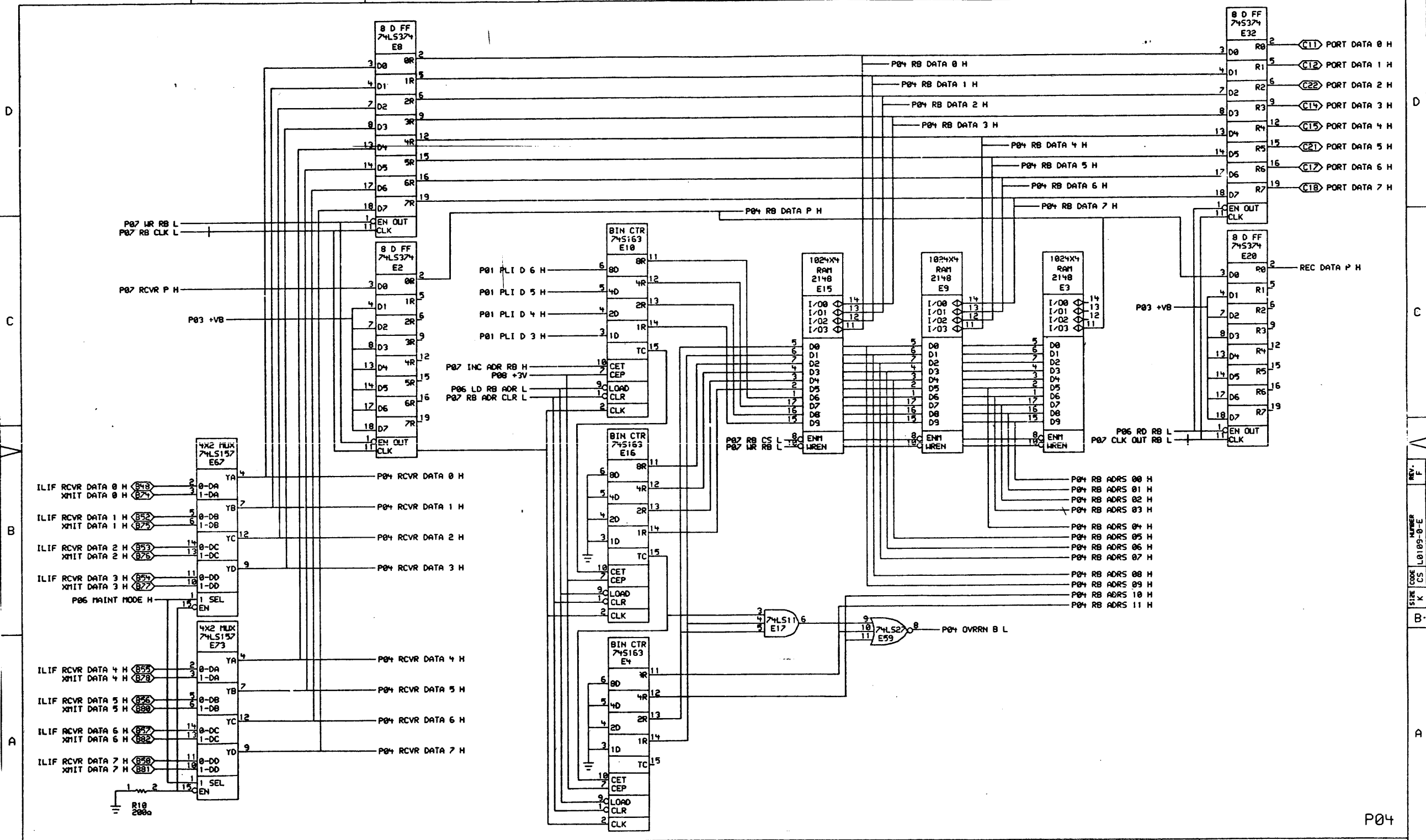


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REVISIONS	
CHK	CHANGE NO. REV

	DRN. R. HOLLAND	DATE 23-JUN-83	ENG. S. CHOW	DATE 23-JUN-83	TITLE: PILA (RECEIVE BUFFER A)
	CHK'D.	DATE	BOARD LOCATION: SHEET 3 OF 11	SIZE CODE: K CS L0109-0-E	NUMBER: REV. F
FIRST USED ON OPTION/MODEL: HSC50		NEXT HIGHER ASSEMBLY: 8-00-L0109-0-E			

P03



D

C

B

A

D

C

F

B

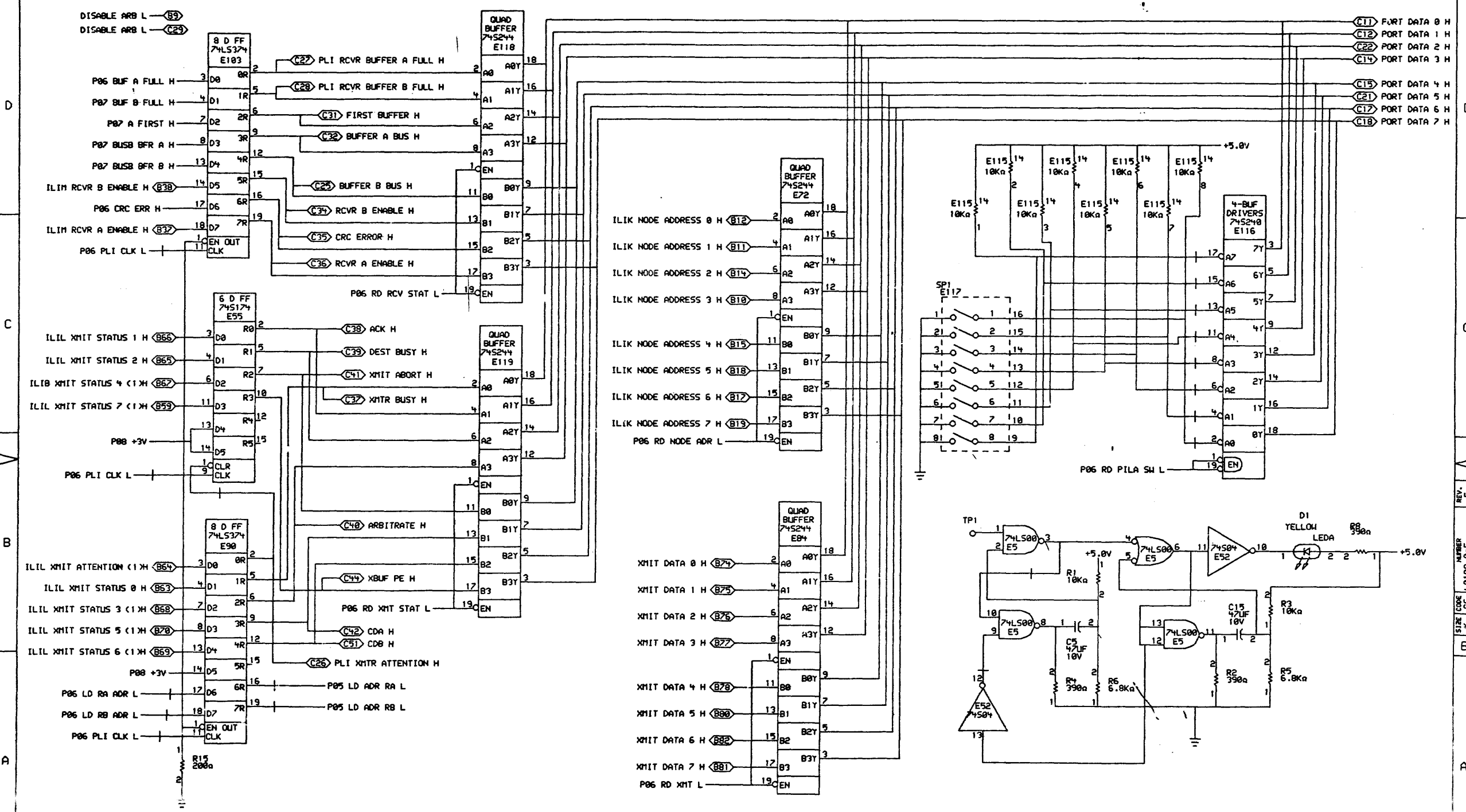
A

P04

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REVISIONS		
CHK	CHANGE NO.	REV

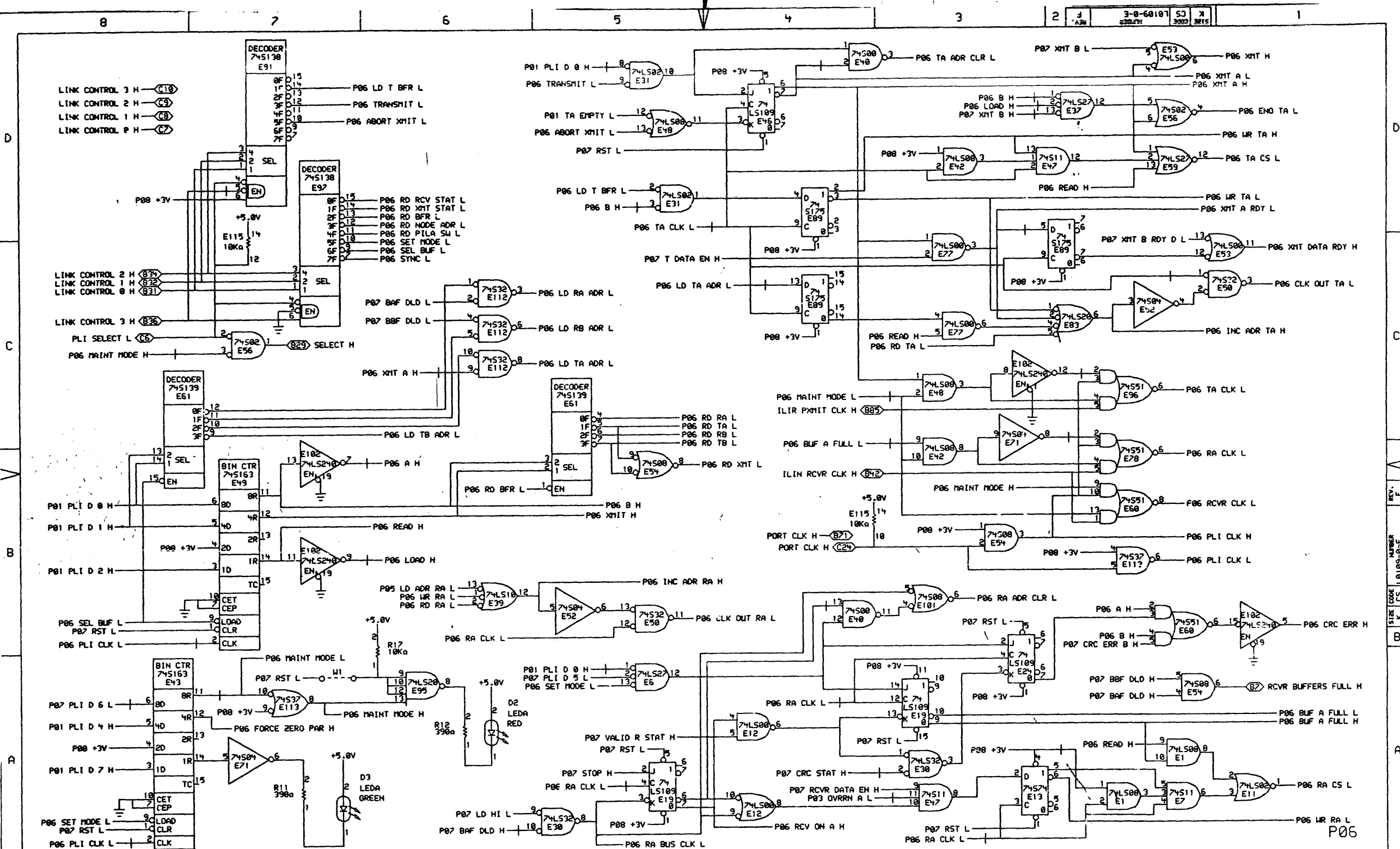
	DRN. R. HOLLAND	DATE 23-JUN-83	ENG. S. CHOU	DATE 23-JUN-83	TITLE: PILA (RECEIVE BUFFER B)
	CHK'D.	DATE	BOARD LOCATION: SHEET 4 OF 11		
FIRST USED ON OPTION/MODEL: HSC50		NEXT HIGHER ASSEMBLY: B-DD-L0109-0-E		SIZE CODE K CS	NUMBER L0109-0-E
				REV. F	



P05

REVISIONS	
CHK	CHANGE NO. REV

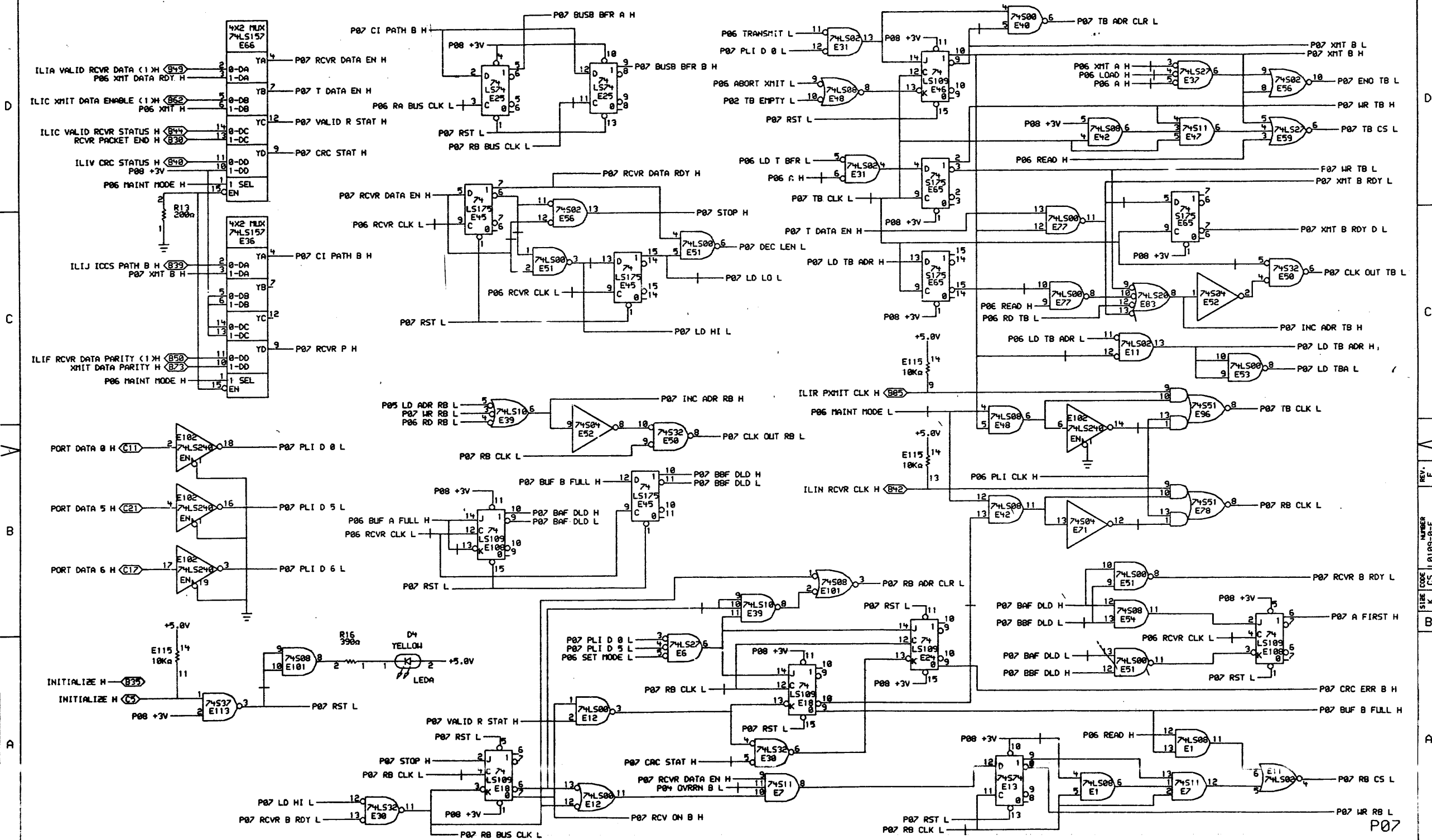
digital	DRN. R. HOLLAND	DATE 23-JUN-83	ENG. S. CHOW	DATE 23-JUN-83	TITLE: PILA (STATUS & MSC)
	CHK'D.	DATE	BOARD LOCATION: SHEET 3 OF 11	SIZE CODE K CS	NUMBER L0109-0-E
SOPT:CS05,CH05,PILA,DRN 123-JUN-83 10:53 NEXT HIGHER ASSEMBLY: FIRST USED ON OPTION/MODEL: HSC50 B-DD-L0109-0-E					REV. F



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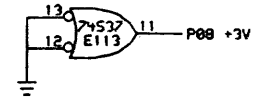
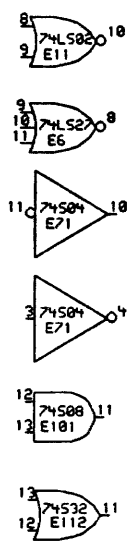
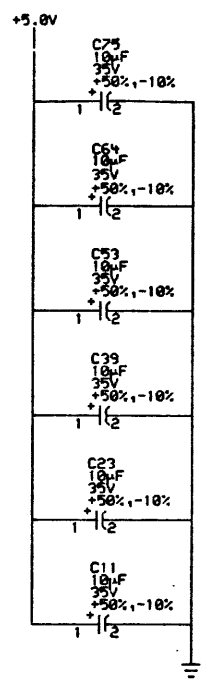
REVISIONS	
CHK	CHANGE NO. REV

	DRN. R. HOLLAND	DATE 23-JUN-82	ENG. S. CHOW	DATE 23-JUN-82	TITLE: PILA
	CHK'D.	DATE	BOARD LOCATION: SHEET 6 OF 11		(BUFFER A CONTROL)
50APYL5LDS.CHOWPILAB06.DWG 123-JUN-82 NEXT HIGHER ASSEMBLY: B-DD-L0109-0-E			SIZE CODE K CS		NUMBER L0109-0-E
FIRST USED ON OPTION/MODEL: HSC50				REV. F	

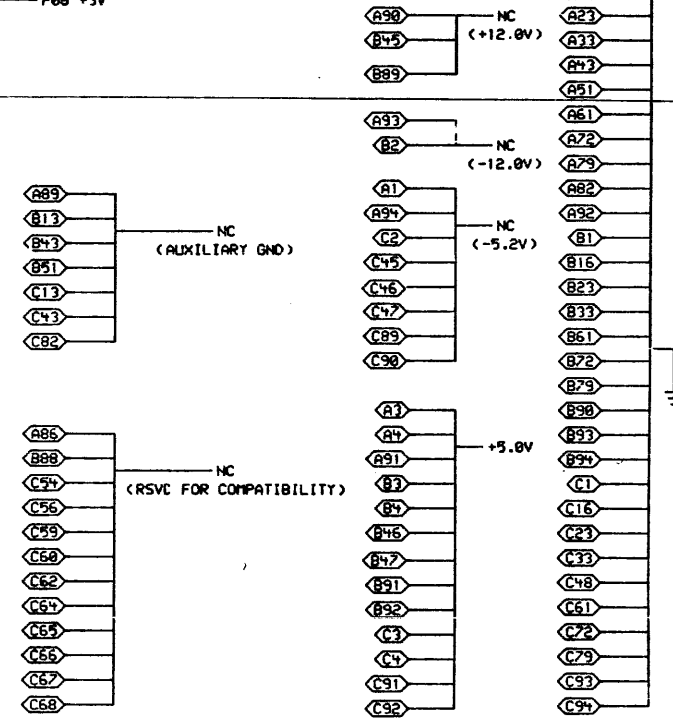


REV.	CHG	NO.	REV.

	DRN. R. HOLLAND	DATE 23-JUN-83	ENG. S. CHOW	DATE 23-JUN-83	TITLE: PILA (BUFFER B CONTROL)
	CHK'D.	DATE	DESIGN LOCATION	SHEET 11	
FIRST USED ON OPTION/MODEL: HSC50			NEXT HIGHER ASSEMBLY: B-DD-L0109-0-E		SIZE CODE NUMBER REV. K CS L0109-0-E F



NOTE: THE +12.0V, -12.0V & -5.2V EDGE PINS ARE INFORMATIONAL ONLY. THEY ARE NOT TO BE CONNECTED.



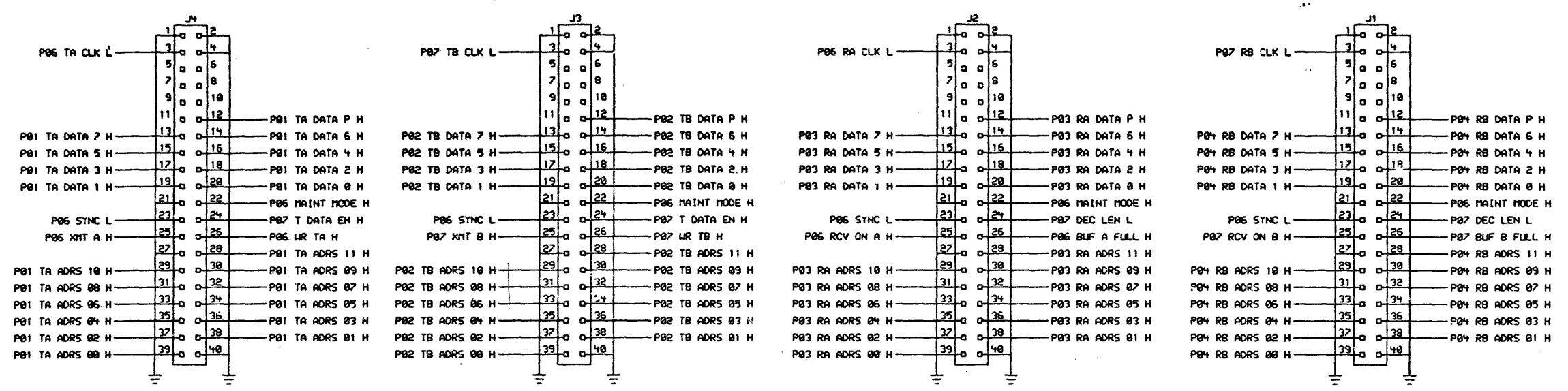
- C81 — NC
- C83 — NC
- C85 — NC
- C87 — NC
- C88 — NC

P08

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. R. HOLLAND	DATE 83-06-03	ENG. S. CHOW	DATE 83-06-03	TITLE: PILA
	CHK'D.	DATE	BOARD LOCATION: B DE 11	SHEET	(CAPS & POWER PINS)
FIRST USED ON OPTION MODEL: HSC50					SIZE CODE K CS
NEXT HIGHER ASSEMBLY: B-DD-L0109-0-E					NUMBER L0109-0-E
					REV. F



P09

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. R. HOLLAND	DATE 23-JUN-83	ENG. S. CHOW	DATE 23-JUN-83	TITLE: PILA (CONNECTORS)
	CHK'D.	DATE	BOARD LOCATION: SHEET 9 OF 11		
FIRST USED ON OPTION/MODEL: HSC50		NEXT HIGHER ASSEMBLY: B-DD-L0109-0-E		SIZE CODE K CS	NUMBER L0109-0-E
				REV. F	

KEY:	Vertical location (A-D)		Direction of line (Left, Right, Up, Down) or electrical (Input, Output, Both) or backplane pin (Pin)			
	Schematic Sheet		Horizontal location (1-8)			
+5.0V	6-A7,D 6-B3,D 8-B8,R 8-B8,R	1-C8,R 6-B6,D 8-C1,L 8-D8,R	3-C7,R 6-D7,D 8-C8,R 8-C8,R	5-B1,L 7-A6,L 8-C8,R 8-D5,D	5-D2,L 7-B8,D 8-D5,D	6-A6,D 7-C3,D 8-D8,R
ACK H		5-C7,L <C38>				
ARBITRATE H		5-B7,L <C40>				
BUFFER A BUS H		5-D7,L <C32>				
BUFFER B BUS H		5-D7,L <C25>				
CDA H		5-B7,L <C42>				
CDB H		5-B7,L <C51>				
CRC ERROR H		5-C7,L <C35>				
CRC STATUS H,ILIV		7-D8,R <B40>				
DEST BUSY H		5-C7,L <C39>				
DISABLE ARB L		5-D8,R 5-D8,R				
FIRST BUFFER H		5-D7,L <C31>				
ICCS PATH B H,ILIJ		7-C8,R <B39>				
INITIALIZE H		7-A8,R 7-A8,R <C5>				
LINK CONTROL 0 H		6-D8,R 6-C8,R <B31>	3-A2,R <C7>			
LINK CONTROL 1 H		6-D8,R 6-C8,R <B32>	3-A2,R <C8>			
LINK CONTROL 2 H		6-D8,R 6-C8,R <B34>				
LINK CONTROL 3 H		6-D8,R 6-C8,R <B36>				
NC		8-B3,L 8-B3,L 8-B3,L 8-B3,L 8-B3,L 8-C1,L 8-C1,L 8-C2,L 8-C2,L 8-D1,L				
NODE ADDRESS 0 H,ILIK		5-C4,R <B12>				
NODE ADDRESS 1 H,ILIK		5-C4,R <B11>				
NODE ADDRESS 2 H,ILIK		5-C4,R <B14>				
NODE ADDRESS 3 H,ILIK		5-C4,R <B10>				
NODE ADDRESS 4 H,ILIK		5-C4,R <B15>				
NODE ADDRESS 5 H,ILIK		5-C4,R <B18>				
NODE ADDRESS 6 H,ILIK		5-C4,R <B17>				
NODE ADDRESS 7 H,ILIK		5-C4,R <B19>				
P01 +VA		1-C2,D 1-C7,L 2-C2,R 2-C7,R				
P01 PLI D 0 H		1-B7,L 2-D8,R 6-A5,R 6-B8,R 6-D5,R				
P01 PLI D 1 H		1-B7,L 2-D8,R 6-B8,R				
P01 PLI D 2 H		1-B7,L 2-D8,R 6-B8,R				
P01 PLI D 3 H		1-B7,L 2-A7,R 2-D8,R 3-C5,R 4-C5,R				
P01 PLI D 4 H		1-B7,L 2-A7,R 2-D8,R 3-C5,R 4-C5,R 6-A8,R				
P01 PLI D 5 H		1-B7,L 2-A7,R 2-D8,R 3-C5,R 4-C5,R				
P01 PLI D 6 H		1-A7,L 2-A7,R 2-D8,R 3-C5,R 4-C5,R				
P01 PLI D 7 H		1-A7,L 2-D8,R 6-A8,R				
P01 TA ADRS 00 H		1-A5,L 9-C8,R				
P01 TA ADRS 01 H		1-A5,L 9-C7,L				
P01 TA ADRS 02 H		1-A5,L 9-C8,R				
P01 TA ADRS 03 H		1-A5,L 9-C7,L				
P01 TA ADRS 04 H		1-A5,L 9-C6,R				
P01 TA ADRS 05 H		1-A5,L 9-C7,L				
P01 TA ADRS 06 H		1-B5,L 9-C8,R				
P01 TA ADRS 07 H		1-B5,L 9-C7,L				
P01 TA ADRS 08 H		1-B5,L 9-C8,R				
P01 TA ADRS 09 H		1-B5,L 9-C7,L				
P01 TA ADRS 10 H		1-B5,L 9-C8,R				
P01 TA ADRS 11 H		1-B5,L 9-C7,L				
P01 TA DATA 0 H		1-D4,L 9-C7,L				
P01 TA DATA 1 H		1-D4,L 9-C8,R				
P01 TA DATA 2 H		1-D4,L 9-C7,L				

P01 TA DATA 3 H	1-D4,L 9-C8,R
P01 TA DATA 4 H	1-D3,L 9-C7,L
P01 TA DATA 5 H	1-D3,L 9-C8,R
P01 TA DATA 6 H	1-D3,L 9-C7,L
P01 TA DATA 7 H	1-D3,L 9-C8,R
P01 TA DATA P H	1-C3,L 9-C7,L
P01 TA EMPTY L	1-B1,L 6-D5,R
P02 T PAR H	1-C7,R 2-B7,L 2-C7,R
P02 TB ADRS 00 H	2-A6,L 9-C6,R
P02 TB ADRS 01 H	2-A6,L 9-C5,L
P02 TB ADRS 02 H	2-A6,L 9-C6,R
P02 TB ADRS 03 H	2-A6,L 9-C5,L
P02 TB ADRS 04 H	2-A6,L 9-C6,R
P02 TB ADRS 05 H	2-A6,L 9-C5,L
P02 TB ADRS 06 H	2-B6,L 9-C6,R
P02 TB ADRS 07 H	2-B5,L 9-C5,L
P02 TB ADRS 08 H	2-B5,L 9-C6,R
P02 TB ADRS 09 H	2-B5,L 9-C5,L
P02 TB ADRS 10 H	2-B5,L 9-C6,R
P02 TB ADRS 11 H	2-B5,L 9-C5,L
P02 TB DATA 0 H	2-D4,L 9-C5,L
P02 TB DATA 1 H	2-D4,L 9-C6,R
P02 TB DATA 2 H	2-D4,L 9-C5,L
P02 TB DATA 3 H	2-D4,L 9-C6,R
P02 TB DATA 4 H	2-D4,L 9-C5,L
P02 TB DATA 5 H	2-D4,L 9-C6,R
P02 TB DATA 6 H	2-D4,L 9-C5,L
P02 TB DATA 7 H	2-D4,L 9-C6,R
P02 TB DATA P H	2-C3,L 9-C5,L
P02 TB EMPTY L	1-A2,R 2-B1,L 7-D4,R 4-C2,R 4-C7,R
P03 +VB	3-B2,R 3-C7,L
P03 OVRRN A L	3-A3,L 6-A3,R
P03 RA ADRS 00 H	3-B2,L 9-C4,R
P03 RA ADRS 01 H	3-B2,L 9-C3,L
P03 RA ADRS 02 H	3-B2,L 9-C4,R
P03 RA ADRS 03 H	3-B2,L 9-C3,L
P03 RA ADRS 04 H	3-B2,L 9-C4,R
P03 RA ADRS 05 H	3-B2,L 9-C3,L
P03 RA ADRS 06 H	3-B2,L 9-C4,R
P03 RA ADRS 07 H	3-B2,L 9-C3,L
P03 RA ADRS 08 H	3-B2,L 9-C4,R
P03 RA ADRS 09 H	3-B2,L 9-C3,L
P03 RA ADRS 10 H	3-B2,L 9-C4,R
P03 RA ADRS 11 H	3-B2,L 9-C3,L
P03 RA DATA 0 H	3-D3,L 9-C3,L
P03 RA DATA 1 H	3-D3,L 9-C4,R
P03 RA DATA 2 H	3-D3,L 9-C3,L
P03 RA DATA 3 H	3-D3,L 9-C4,R
P03 RA DATA 4 H	3-D3,L 9-C3,L
P03 RA DATA 5 H	3-D3,L 9-C4,R
P03 RA DATA 6 H	3-D2,L 9-C3,L
P03 RA DATA 7 H	3-D2,L 9-C4,R
P03 RA DATA P H	3-D4,L 9-C3,L
P04 OVRRN B L	4-B3,L 7-A4,R
P04 RB ADRS 00 H	4-B2,L 9-C2,R
P04 RB ADRS 01 H	4-B2,L 9-C2,L
P04 RB ADRS 02 H	4-B2,L 9-C2,R
P04 RB ADRS 03 H	4-B2,L 9-C2,L
P04 RB ADRS 04 H	4-B2,L 9-C2,R
P04 RB ADRS 05 H	4-B2,L 9-C2,L
P04 RB ADRS 06 H	4-B2,L 9-C2,R

P04 RB ADRS 07 H	4-B2,L 9-C2,L
P04 RB ADRS 08 H	4-B2,L 9-C2,R
P04 RB ADRS 09 H	4-B2,L 9-C2,L
P04 RB ADRS 10 H	4-B2,L 9-C2,R
P04 RB ADRS 11 H	4-B2,L 9-C2,L
P04 RB DATA 0 H	4-D4,L 9-C2,L
P04 RB DATA 1 H	4-D3,L 9-C2,R
P04 RB DATA 2 H	4-D3,L 9-C2,L
P04 RB DATA 3 H	4-D3,L 9-C2,R
P04 RB DATA 4 H	4-D3,L 9-C2,L
P04 RB DATA 5 H	4-D3,L 9-C2,R
P04 RB DATA 6 H	4-D3,L 9-C2,L
P04 RB DATA 7 H	4-D3,L 9-C2,R
P04 RB DATA P H	4-D4,L 9-C2,L
P04 RCVR DATA 0 H	3-D8,R 4-B6,L
P04 RCVR DATA 1 H	3-D8,R 4-B6,L
P04 RCVR DATA 2 H	3-D8,R 4-B6,L
P04 RCVR DATA 3 H	3-D8,R 4-B6,L
P04 RCVR DATA 4 H	3-D8,R 4-A6,L
P04 RCVR DATA 5 H	3-D8,R 4-A6,L
P04 RCVR DATA 6 H	3-D8,R 4-A6,L
P04 RCVR DATA 7 H	3-D8,R 4-A6,L
P05 LD ADR RA L	5-A7,L 6-B6,R
P05 LD ADR RB L	5-A7,L 7-C6,R
P06 A H	6-B2,R 6-B6,L 7-D2,R 7-D4,R
P06 ABORT XMIT L	6-D5,R 6-D7,L 7-D4,R
P06 B H	6-B2,R 6-B5,L 6-D3,R 6-D5,R
P06 BUF A FULL H	5-D8,R 6-A1,L 7-B6,R 9-C3,L
P06 BUF A FULL L	6-A1,L 6-B3,R
P06 CLK OUT RA L	3-B2,R 6-B5,L
P06 CLK OUT TA L	1-C2,R 6-C1,L
P06 CRC ERR H	5-D8,R 6-B1,L
P06 END TA L	1-D2,R 6-D1,L
P06 FORCE ZERO PAR H	6-A7,L
P06 INC ADR RA H	3-C5,R 6-B5,L
P06 INC ADR TA H	1-A6,R 6-C2,L
P06 LD RA ADR L	3-C5,R 5-A8,R 6-C5,L
P06 LD RB ADR L	4-C6,R 5-A8,R 6-C5,L
P06 LD T BFR L	6-D5,R 6-D6,L 7-D4,R
P06 LD TA ADR L	1-A6,R 6-C4,R 6-C5,L
P06 LD TB ADR L	6-C6,L 7-C2,R
P06 LOAD H	6-B6,L 6-D3,R 7-D2,R
P06 MAINT MODE H	2-B8,R 4-B8,R 6-A7,L 6-B3,R 6-C8,R 7-C8,R
P06 MAINT MODE L	7-D8,R 9-C2,L 9-C3,L 9-C5,L 9-C7,L
P06 RA ADR CLR L	6-A7,L 6-C4,R 7-C3,R
P06 RA BUS CLK L	6-B2,L 6-B8,R
P06 RA CLK L	3-C3,R 6-B3,L 6-A3,R 6-A4,R 6-A5,R 6-B2,L 6-B6,R
P06 RA CS L	3-B4,R 6-A1,L
P06 RCV ON A H	6-A4,L 9-C4,R
P06 RCVR CLK L	3-C8,R 6-B2,L 7-A2,R 7-B6,R 7-C5,R 7-C6,R
P06 RD BFR L	6-B5,R 6-D6,L
P06 RD NODE ADR L	5-B5,R 6-D6,L
P06 RD PILA SW L	5-B2,R 6-D6,L
P06 RD RA L	3-B2,R 6-B6,R 6-C5,L
P06 RD RB L	4-B2,R 6-C5,L 7-C6,R

D
C
V
B
A

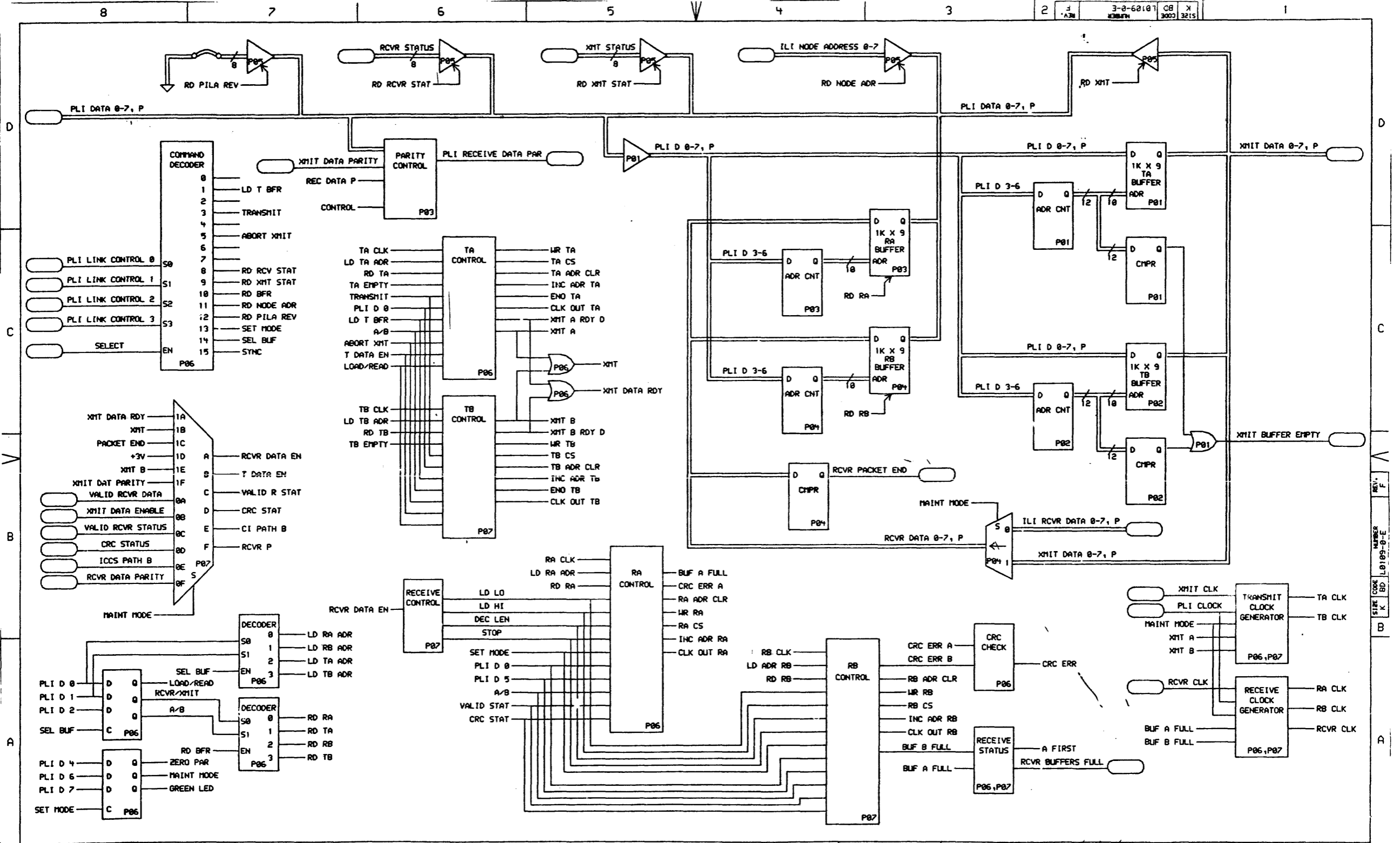
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. R. HOLLAND	DATE 23-JUN-83	ENG. S. CHOW	DATE 23-JUN-83	TITLE: PILA
	CHK'D.	DATE	BOARD LOCATION:	SHEET 10 OF 11	(Signal Cross Ref.)
FIRST USED ON OPTION MODEL: HSC50			NEXT HIGHER ASSEMBLY: B-DD-L0109-0-E		SIZE CODE K CS
			NUMBER L0109-0-E		REV. F

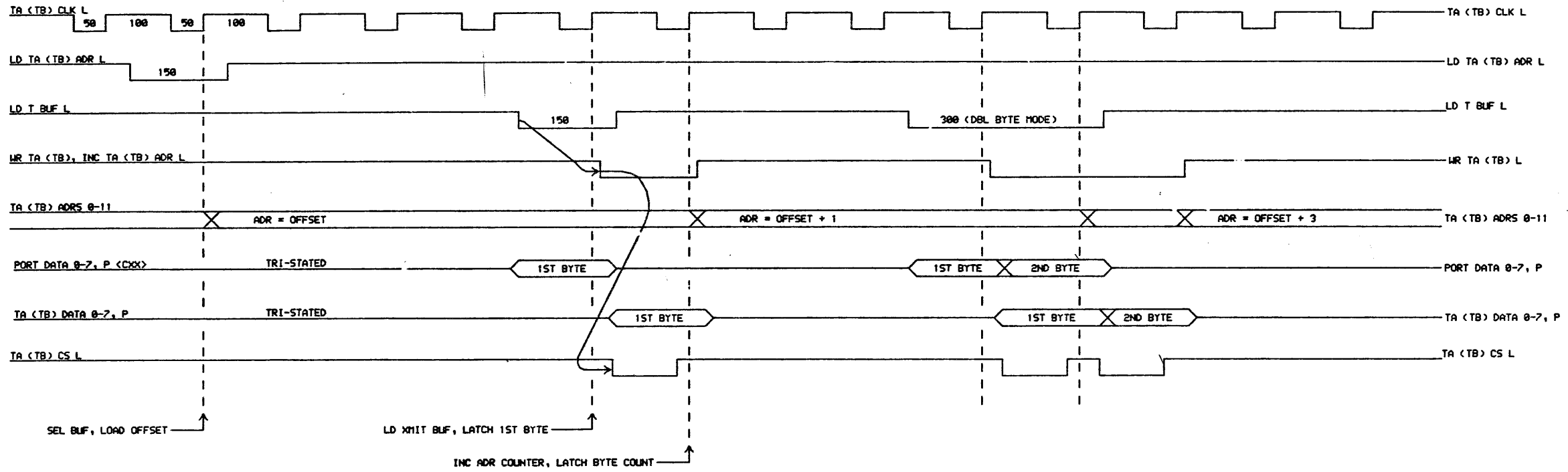
8		7		6		5		4		3		2		1	
P06 RD RCV STAT L	5-C6,R	6-D6,L													
P06 RD TA L	6-C3,R	6-C5,L													
P06 RD TB L	6-B5,L	7-C3,R													
P06 RD XMT L	5-A5,R	6-B4,L													
P06 RD XMT STAT L	5-B6,R	6-D6,L													
P06 READ H	6-A2,R	6-B6,L	6-C3,R	6-D2,R	7-A2,R	7-C3,R									
	7-D2,R														
P06 SEL BUF L	5-B8,R	6-C6,L													
P06 SET MODE L	6-A5,R	6-A8,R	6-C6,L	7-A5,R											
P06 SYNC L	6-C6,L	9-C2,R	9-C4,R	9-C6,R	9-C8,R										
P06 TA ADR CLR L	1-A3,R	1-A6,R	6-D3,L												
P06 TA CLK L	1-A6,R	6-C2,L	6-D4,R	9-D8,R											
P06 TA CS L	1-C4,R	6-D1,L													
P06 TRANSMIT L	6-D5,R	6-D6,L	7-D4,R												
P06 WR RA L	3-B4,R	3-C8,R	6-A1,L	6-B6,R											
P06 WR TA H	6-D1,L	9-C7,L													
P06 WR TA L	1-A4,R	1-B4,R	1-C7,R	6-D2,L											
P06 XMIT H	3-A2,R	6-B5,L													
P06 XMT A H	6-C6,R	6-D2,L	7-D2,R	9-C8,R											
P06 XMT A L	1-A3,R	6-D2,L													
P06 XMT A RDY L	1-A3,R	6-D2,L													
P06 XMT DATA RDY H	6-C1,L	7-D8,R													
P06 XMT H	6-D1,L	7-D8,R													
P07 A FIRST H	5-D8,R	7-B1,L													
P07 BAF DLD H	6-A2,R	6-A6,R	7-B3,R	7-B6,L											
P07 BAF DLD L	6-C6,R	7-A2,R	7-B6,L												
P07 BBF DLD H	6-A2,R	7-A2,R	7-B5,L												
P07 BBF DLD L	6-C6,R	7-B3,R	7-B5,L												
P07 BUF B FULL H	5-D8,R	7-A1,L	7-B5,R	9-C2,L											
P07 BUSB BFR A H	5-D8,R	7-D5,L													
P07 BUSB BFR B H	5-D8,R	7-D5,L													
P07 CI PATH B H	7-C7,L	7-D6,R													
P07 CLK OUT RB L	4-B2,R	7-B4,L													
P07 CLK OUT TB L	2-C2,R	7-C1,L													
P07 CRC ERR B H	6-B2,R	7-A1,L													
P07 CRC STAT H	6-A4,R	7-A5,R	7-D7,L												
P07 DEC LEN L	3-A7,R	7-C4,L	9-C2,L	9-C3,L											
P07 ENO TB L	2-D2,R	7-D1,L													
P07 INC ADR RB H	4-C6,R	7-C5,L													
P07 INC ADR TB H	2-A7,R	7-C1,L													
P07 LD HI L	3-C8,R	6-A6,R	7-A7,R	7-C5,L											
P07 LD LO L	3-B8,R	7-C4,L													
P07 LD TB ADR H	7-C1,L	7-C4,R													
P07 LD TBA L	2-A7,R	7-C1,L													
P07 PLI D 0 L	7-A5,R	7-B7,L	7-D4,R												
P07 PLI D 5 L	6-A5,R	7-A5,R	7-B7,L												
P07 PLI D 6 L	6-A8,R	7-B7,L													
P07 RB ADR CLR L	4-C6,R	7-B4,L													
P07 RB BUS CLK L	7-A6,L	7-D6,R													
P07 RB CLK L	4-C8,R	7-A3,R	7-A5,R	7-A6,R	7-B1,L	7-B6,R									
	9-D2,R														
P07 RB CS L	4-B4,R	7-A1,L													
P07 RCV ON B H	7-A5,L	9-C2,R													
P07 RCVR B RDY L	7-A7,R	7-B1,L													
P07 RCVR DATA EN H	6-A3,R	7-A4,R	7-D6,R	7-D7,L											
P07 RCVR DATA RDY H	7-D5,L														
P07 RCVR P H	3-C8,R	4-C8,R	7-C7,L												
P07 RST L	3-B6,R	6-A3,R	6-A3,R	6-A5,R	6-A7,R	6-A8,R									
	6-D5,R	7-A1,R	7-A3,R	7-A4,R	7-A6,R	7-A7,L									
	7-B3,R	7-B6,R	7-C6,R	7-D4,R	7-D6,R										
P07 STOP H	6-A5,R	7-A6,R	7-C5,L												
P07 T DATA EN H	6-C4,R	7-C4,R	7-D7,L	9-C5,L	9-C7,L										
P07 TB ADR CLR L	2-A3,R	2-A7,R	7-D2,L												
P07 TB CLK L	2-A7,R	7-C1,L	7-D4,R	9-D6,R											
P07 TB CS L	2-C5,R	7-D1,L													
P07 VALID R STAT H	6-A5,R	7-A6,R	7-D7,L												
P07 WR RB L	4-B4,R	4-C8,R	7-A1,L	7-C6,R											
P07 WR TB H	7-D1,L	9-C5,L													
P07 WR TB L	2-A5,R	2-B5,R	2-C8,R	7-D1,L											
P07 XMT B H	6-D3,R	7-C8,R	7-D1,L	9-C6,R											
P07 XMT B L	2-A3,R	6-D2,R	7-D1,L												
P07 XMT B RDY D L	6-C2,R	7-C1,L													
P07 XMT B RDY L	2-A3,R	7-D1,L													
P08 +3V															
	4-C6,R	5-A8,R	1-A2,R	1-A6,R	2-A2,R	2-A7,R	3-A3,R	3-C5,R							
	5-B8,R	6-A3,R	6-A3,R	6-A3,R	6-A5,R	6-A7,R									
	6-A8,R	6-B2,R	6-B3,R	6-B7,R	6-C2,R	6-C4,R	6-C4,R	6-D3,R							
	6-D4,R	6-D8,R	7-A3,R	7-A3,R	7-A4,R	7-A6,R	7-A8,R	7-B1,R							
	7-B6,R	7-C2,R	7-C3,R	7-C3,R	7-D3,R	7-D3,R	7-D6,R	7-D8,R							
			8-D3,L												
PLI RCVR BUFFER A FULL H	5-D7,L	<C27>													
PLI RCVR BUFFER B FULL H	5-D7,L	<C28>													
PLI RECEIVE DATA PAR H	3-A1,L	<C20>													
PLI SELECT L	6-C8,R	<C6>													
PLI TRANSMIT DATA PAR H	2-B8,R	<C19>													
PLI XMTR ATTENTION H	5-A7,L	<C26>													
PORT CLK H	6-B4,R	6-B4,R	<C24>												
PORT DATA 0 H	1-B8,R	1-B8,R	<C11>	3-A3,R	<C11>										
			3-D1,L	<C11>	4-D1,L	<C11>	5-D1,L	<C11>	7-B8,R	<C11>					
PORT DATA 1 H	1-B8,R	1-B8,R	<C12>	3-A3,R	<C12>										
			3-D1,L	<C12>	4-D1,L	<C12>	5-D1,L	<C12>							
PORT DATA 2 H	1-B8,R	1-B8,R	<C22>	3-A3,R	<C22>										
			3-D1,L	<C22>	4-D1,L	<C22>	5-D1,L	<C22>							
PORT DATA 3 H	1-B8,R	1-B8,R	<C14>	3-A3,R	<C14>										
			3-D1,L	<C14>	4-D1,L	<C14>	5-D1,L	<C14>							
PORT DATA 4 H	1-B8,R	1-B8,R	<C15>	3-A3,R	<C15>										
			3-D1,L	<C15>	4-D1,L	<C15>	5-D1,L	<C15>							
PORT DATA 5 H	1-B8,R	1-B8,R	<C21>	3-A3,R	<C21>										
			3-D1,L	<C21>	4-D1,L	<C21>	5-D1,L	<C21>	7-B8,R	<C21>					
PORT DATA 6 H	1-A8,R	1-A8,R	<C17>	3-A3,R	<C17>										
			3-D1,L	<C17>	4-D1,L	<C17>	5-D1,L	<C17>	7-B8,R	<C17>					
PORT DATA 7 H	1-A8,R	1-A8,R	<C18>	3-A3,R	<C18>										
			3-D1,L	<C18>	4-D1,L	<C18>	5-D1,L	<C18>							
PXMIT CLK H,ILIR	6-C3,R	<B85>	7-C3,R	<B85>											
RCVR A ENABLE H	5-C7,L	<C36>													
RCVR A ENABLE H,ILIM	5-C8,R	<B37>													
RCVR B ENABLE H	5-D7,L	<C34>													
RCVR B ENABLE H,ILIM	5-D8,R	<B38>													
RCVR BUFFERS FULL H	6-A1,L	<B7>													
RCVR CLK H,ILIN	6-B3,R	<B42>	7-B3,R	<B42>											
RCVR DATA 0 H,ILIF	4-B8,R	<B40>													



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REVISIONS	
CHK	CHANGE NO. / REV
1857	L0109-C200R / W.S. 28 JUN 83 / F

	DRN. S. CHOW	DATE 28-JUN-83	ENG. S. CHOW	DATE 28-JUN-83	TITLE: PILA (BLOCK DIAGRAM)
		DATE 1/15/83	BOARD LOCATION: 1	SHEET 1	SIZE CODE NUMBER REV. K BD L0109-0-E F
FIRST USED ON OPTION MODEL: HSC50			NEXT HIGHER ASSEMBLY: B-DD-L0109-0-E		



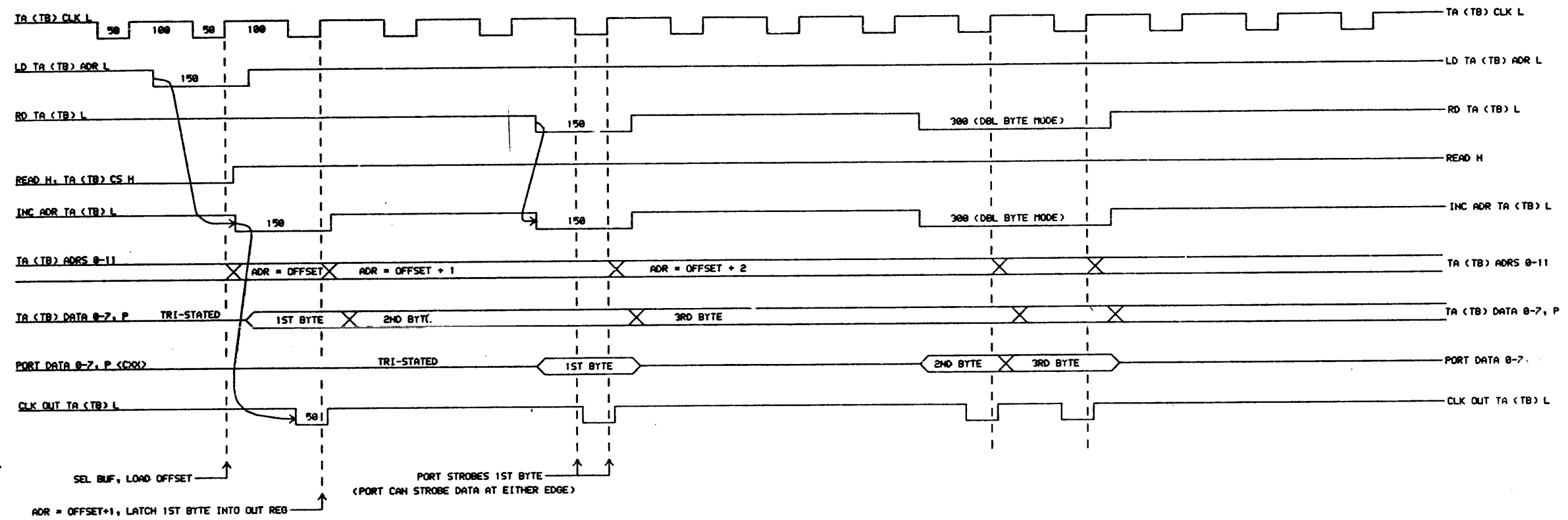
LOAD XMIT BUFFER SEQUENCE

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REVISIONS		
CHK	CHANGE NO.	REV
	1857	1
	1858	2
	1859	3
	1860	4
	1861	5
	1862	6
	1863	7
	1864	8
	1865	9
	1866	10
	1867	11
	1868	12
	1869	13
	1870	14
	1871	15
	1872	16
	1873	17
	1874	18
	1875	19
	1876	20
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	1891	35
	1892	36
	1893	37
	1894	38
	1895	39
	1896	40
	1897	41
	1898	42
	1899	43
	1900	44
	1901	45
	1902	46
	1903	47
	1904	48
	1905	49
	1906	50

digital ORN. S. CHOW DATE 28-JAN-83 ENG S. CHOW DATE 28-JAN-83
 CHK'D. DATE BOARD LOCATION: SHEET 5
 SOA: P. C. CHOW, P. T. D. JUN-83 14:23 NEXT HIGHER ASSEMBLY
 FIRST USED ON OPTION/MODEL: HSC50 B-DD-L0109-0-E

TITLE: PILA TIMING		NUMBER		REV.	
(LOAD XMIT BUFFER)		K TD L0109-0-E		F	
SIZE	CODE	NUMBER	REV.		
K	TD	L0109-0-E	F		

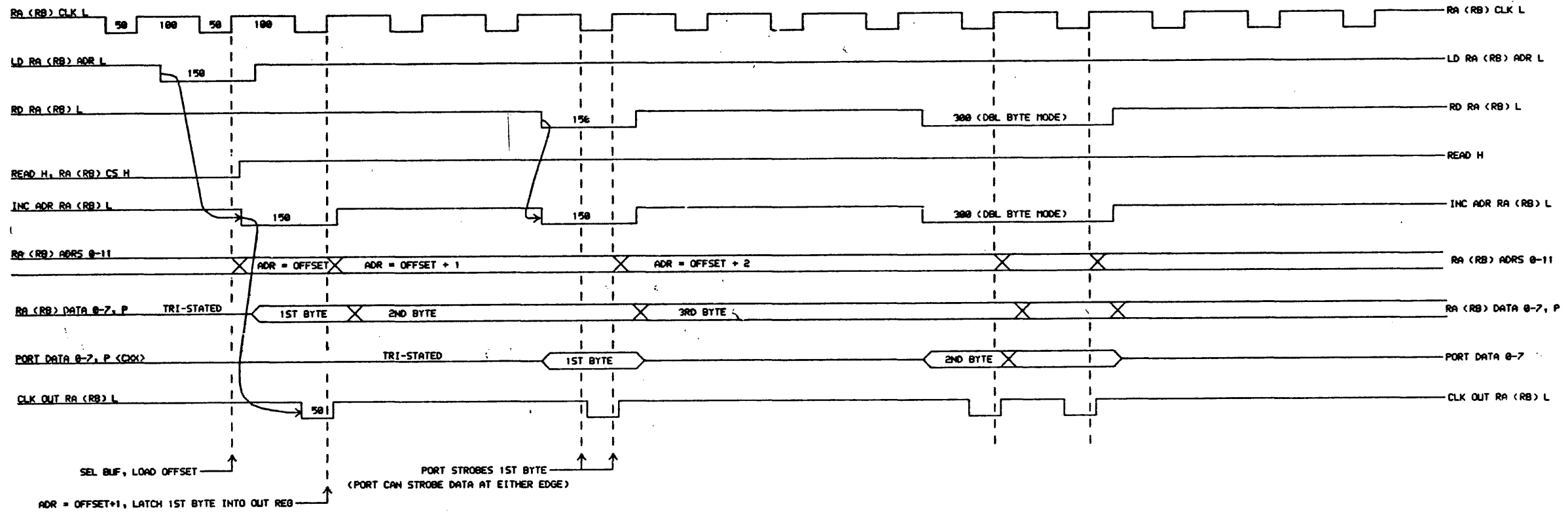


READ XMIT BUFFER SEQUENCE

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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. S. CHOU	DATE 28-JUN-82	ENG. S. CHOU	DATE 28-JUN-82	TITLE: PILA TIMING
	CHK'D.	DATE	BOARD LOCATION:	SHEET 2 OF 5	(READ XMIT BUFFER)
COPY: (SUDS, CHOU) P2, DRN/28-JUN-83 10:23			NEXT HIGHER ASSEMBLY:		SIZE CODE K
FIRST USED ON OPTION/MODEL: HSC50			8-DD-L0109-0-E		NUMBER TD L0109-0-E
					REV. F



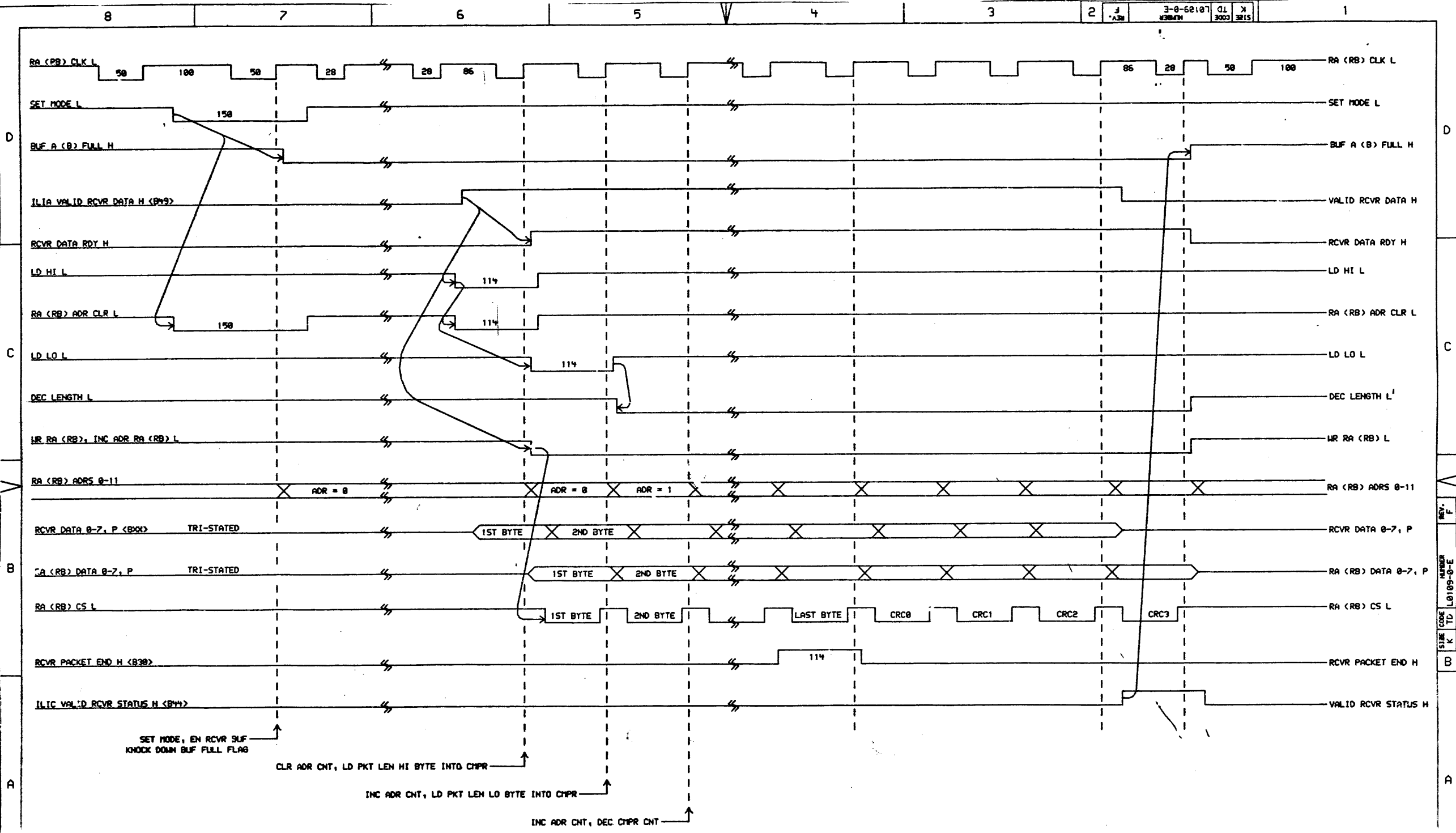
READ RECEIVE BUFFER SEQUENCE

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN: S. CHOW	DATE: 28-JUN-83	ENG: S. CHOW	DATE: 28-JUN-83	TITLE: PILA TIMING (RD RCVR BUFFER)
	CHK'D:	DATE: 28-JUN-83	BOSSO LOCATION: 4	SHEET: 4	SIZE CODE: K
COPY: S. CHOW: P14, DRN: 28-JUN-83 10124		NEXT HIGHER ASSEMBLY: B-DD-L0109-0-E		NUMBER: L0109-0-E	REV: F
FIRST USED ON OPTION/MODEL: HSC50					

REV. F
NUMBER L0109-0-E
SIZE CODE K
D



RECEIVE SEQUENCE

SET MODE, EN RCVR SUF
KNOCK DOWN BUF FULL FLAG

CLR ADR CNT, LD PKT LEN HI BYTE INTO CHPR

INC ADR CNT, LD PKT LEN LO BYTE INTO CHPR

INC ADR CNT, DEC CHPR CNT

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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. S. CHOM	DATE 28-JUN-83	ENG. S. CHOM	DATE 28-JUN-83	TITLE: PILA TIMING (RECEIVE)
	CHK'D.	DATE	BOARD LOCATION	SHEET	
FIRST USED ON OPTION/MODEL: HSC30		NEXT HIGHER ASSEMBLY: B-00-L0109-0-E		SIZE CODE K TD	NUMBER L0109-0-E

SIZE CODE K TD	NUMBER L0109-0-E	REV. F
----------------	------------------	--------

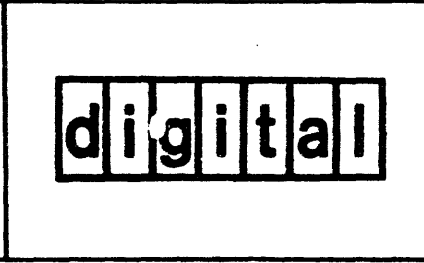
DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS															
B-DD-5014931-0-E	1		DRAWING DIRECTORY	E	F														
	-	5014931-00	PC ETCH BOARD	E	E														
D-MD-5014931-0-E	7		MECHANICAL DRAWING	E	F														
D-EC-5014931 0-E	3		ETCH CUT DRAWING	E	F														

NOTES:

REVISION HISTORY		ECO NO.	REV.
DATE	RELEASE AT		
1/19/83	REV. E S.L. TIAN 83	E	F
1937	L0109 - CX001		

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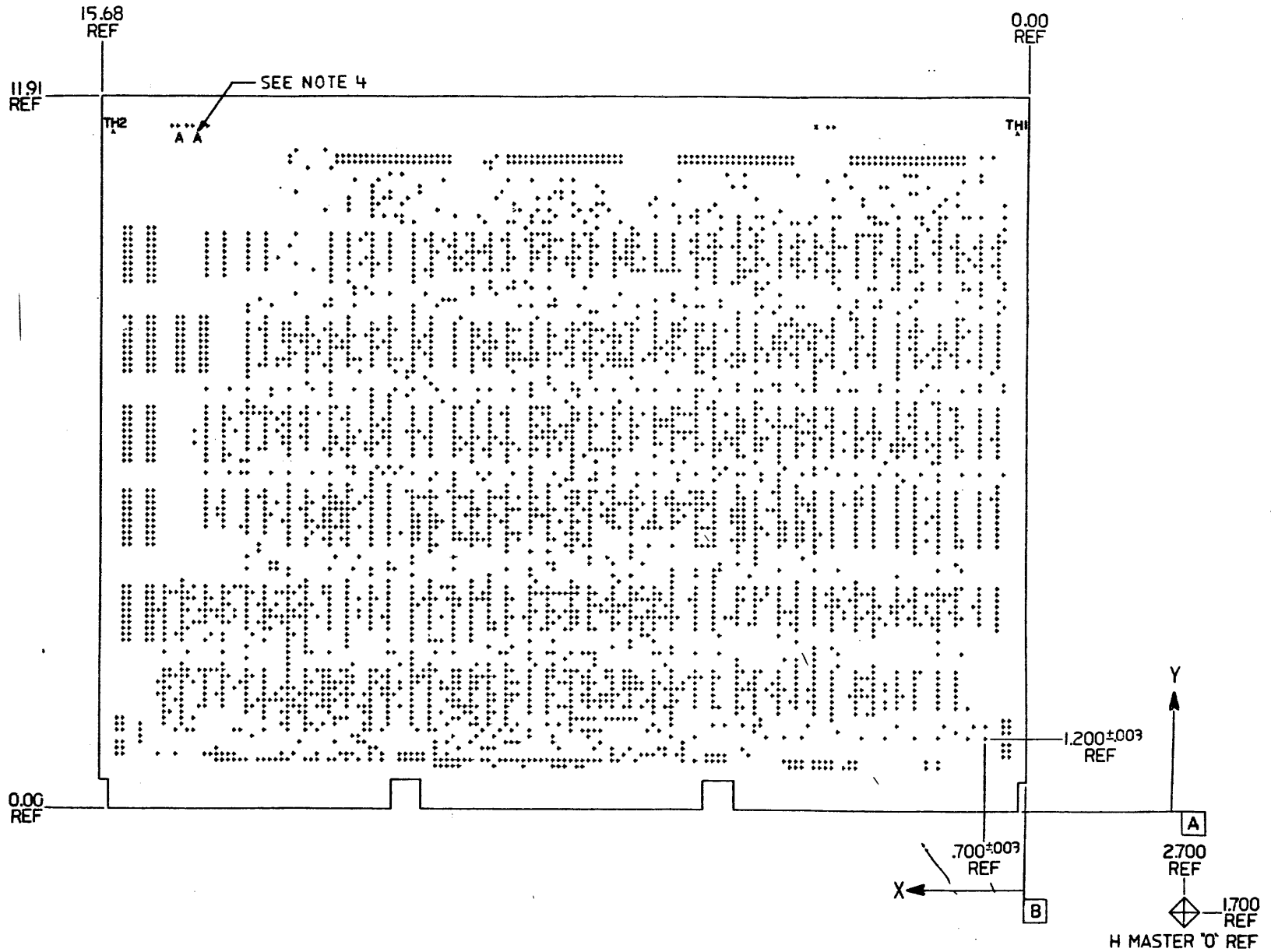


USED ON OPTION/MODEL
HSC50

DRN. <i>S. Lehman</i>	DATE 7 JAN 83
CHK'D <i>Shirley Bourbeau</i>	DATE 13 JAN 83
DES. ENG. <i>R. Noelland</i>	DATE 13-1-83
RESP. ENG. <i>R. Noelland</i>	DATE 13-1-83
MFG. ENG. <i>D. Coulson</i>	DATE 14 JAN 83

TITLE	PRINTED CIRCUIT BOARD (L0109)
DOCUMENT NUMBER	NUMBER 5014931-0-E
SIZE	CODE DD
REV. F	SHEET 1 OF 1

VIEWED FROM SIDE 1
COMPONENT SIDE



FOR HOLE TOLERANCES USE DEC STD 176										DESIGN INFORMATION		FABRICATION INFORMATION		TOLERANCES			
SYMBOL	+	A	X	+	Z	M	A	Q	*	CIRCUIT SIZE: X 15.68 Y 11.91 INCHES	FABRICATE BOARD PER DMD 5014931-0-0 AS SHOWN ()	INCHES UNLESS SPECIFIED		SIGNATURES		DATE	
FIN HOLE SIZE	.038	.157	.032							CIRCUIT TYPE: PPE () PTH () ML ()	SOLDER MASK: SIDE 1 () SIDE 2 () NONE ()	.XXX ± .010	ANGLES	DRN: Eric Jansen		11 NOV 88	
PLATED	X	X								CIRCUIT HOLE SIZE ()		.XX ± .005	± 0 DEG 30 MIN	CHK'D: Eric Jansen		12-1-88	
NON PLATED		X								TECHNOLOGY: 500 HOLEX13 () H20 () OTHER ()	SPECIFICATIONS AND STANDARDS:	.X ± .100		PRGJ ENG: R. A. Holladay		12-1-88	
BTY.	R260	Z	J							UL REQUIREMENTS LPWR () HPWR ()	MATERIALS AND WORKMANSHIP FOR ALL FABRICATED PRINTED WIRING			MFG ENG: Eric Jansen		11 Nov 88	
OFF GRD HOLE	0	0	0							CIRCUIT OUTLINE & FINGER DETAIL PER DMD 5014931-0-0 AS SHOWN ()	BOARDS MUST MEET OR EXCEED THE REQUIREMENTS OF DEC STD 176.			SCALE: 1/1			
DRILL SIZE										LAYER CONSTRUCTION PER DMD 5014931-0-0 AS SHOWN ()	SPECIAL NOTES:			SHEET 1 OF 7			
NOTE: ALL HOLE LOCATIONS ARE DESIGNED ON .025 GRID INCREMENTS FROM DATUM UNLESS SYMBOL IS CIRCLED.										ARTWORK LAYOUT: MANUAL () CAD ()	1. AUTO THEIVING REQUIRED.	TITLE: DRILL & ETCH DRAWING		SIZE CODE NUMBER REV			
										ENG SPECIAL FEATURES: L1: 109 / ORL: 128	2. POWER & GROUND TABS DO NOT NEED GOLD PLATING.	D MD 5014931-0-E		1 WO# 290508E			
										SPECIAL NOTES:	3. NON-CONTROLLED IMPEDANCE.	ETCH REV: E					
											4. "A" PADS ON LAYER 4 ARE TEST POINTS ONLY-NO HOLES.						
											5. SEE SOLDER MASK REWORK NOTES ON PAGES 2 & 7.						

CHANGE NO. REV. E
RELEASED AT: 187 LANS-01691
DATE: 7 JAN 89
BY: B. HOLLAND
DESIGNED BY: R. A. Holladay
CHECKED BY: Eric Jansen
DATE: 12-1-88

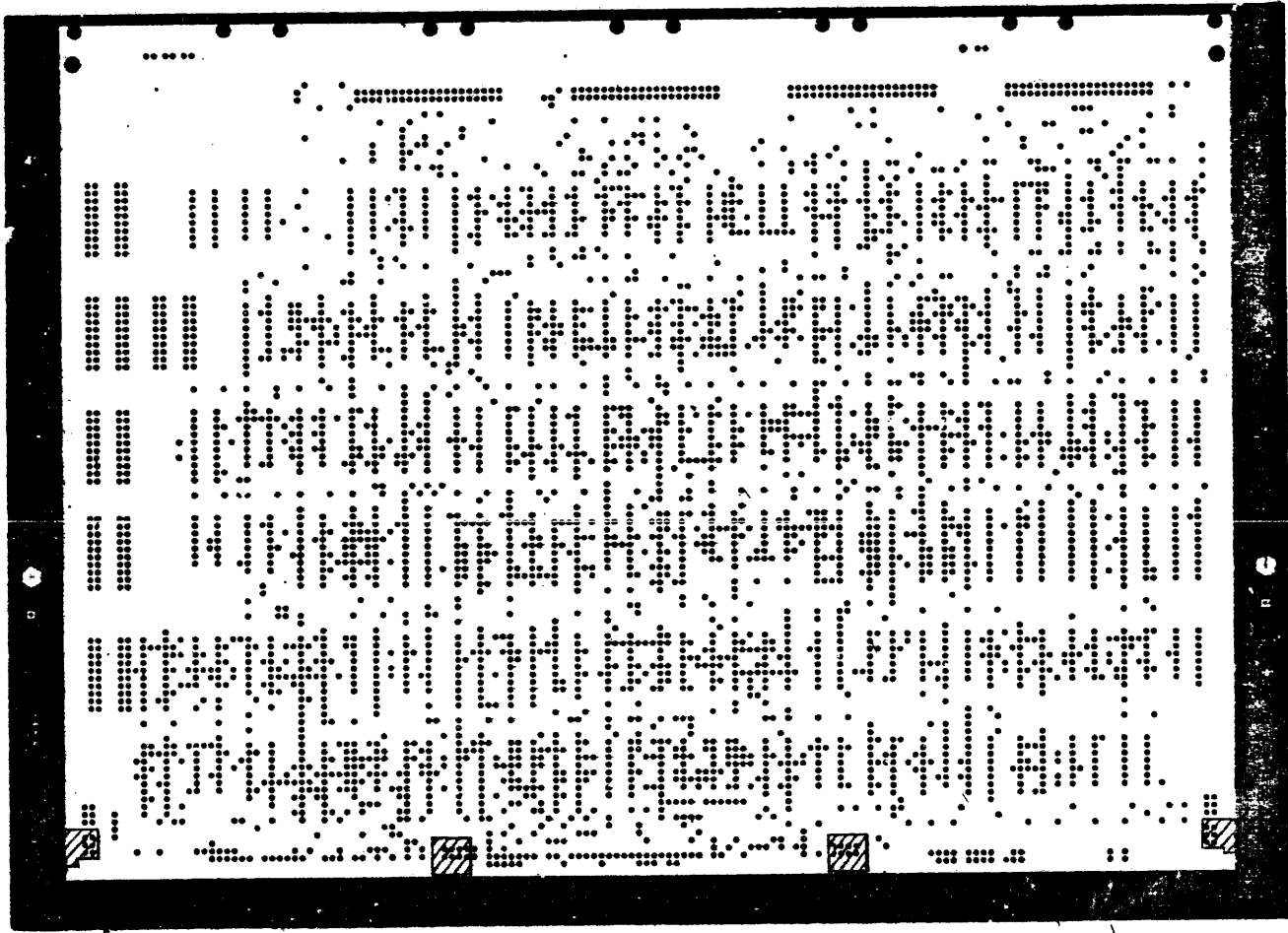
DMD 5014931-0-0

1 of 7

1 WO# 290508E

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SOLDER MASK SIDE 1



▨ INDICATES SOLDER MASK REWORK

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	DRILL & ETCH DRAWING	SIZE CODE	D MD	NUMBER	5014931-0-E	REV.	F
SCALE	1/1	SHEET	2	OF	7	DIST.	

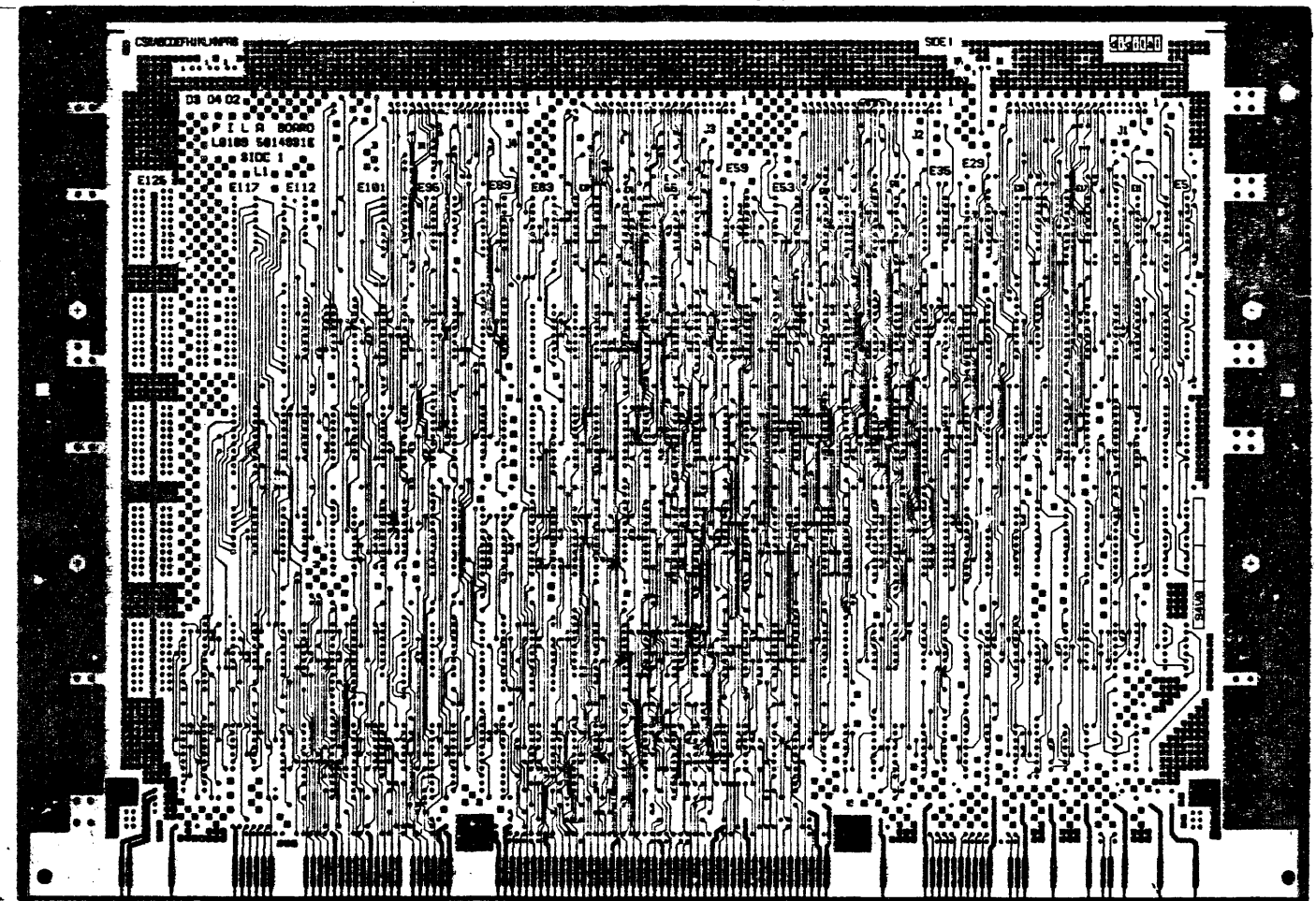
REV. F E-0-0-E DMD 5014931-0-E

DMDS014931-0-0

Sht 2 of 7

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LAYER 1



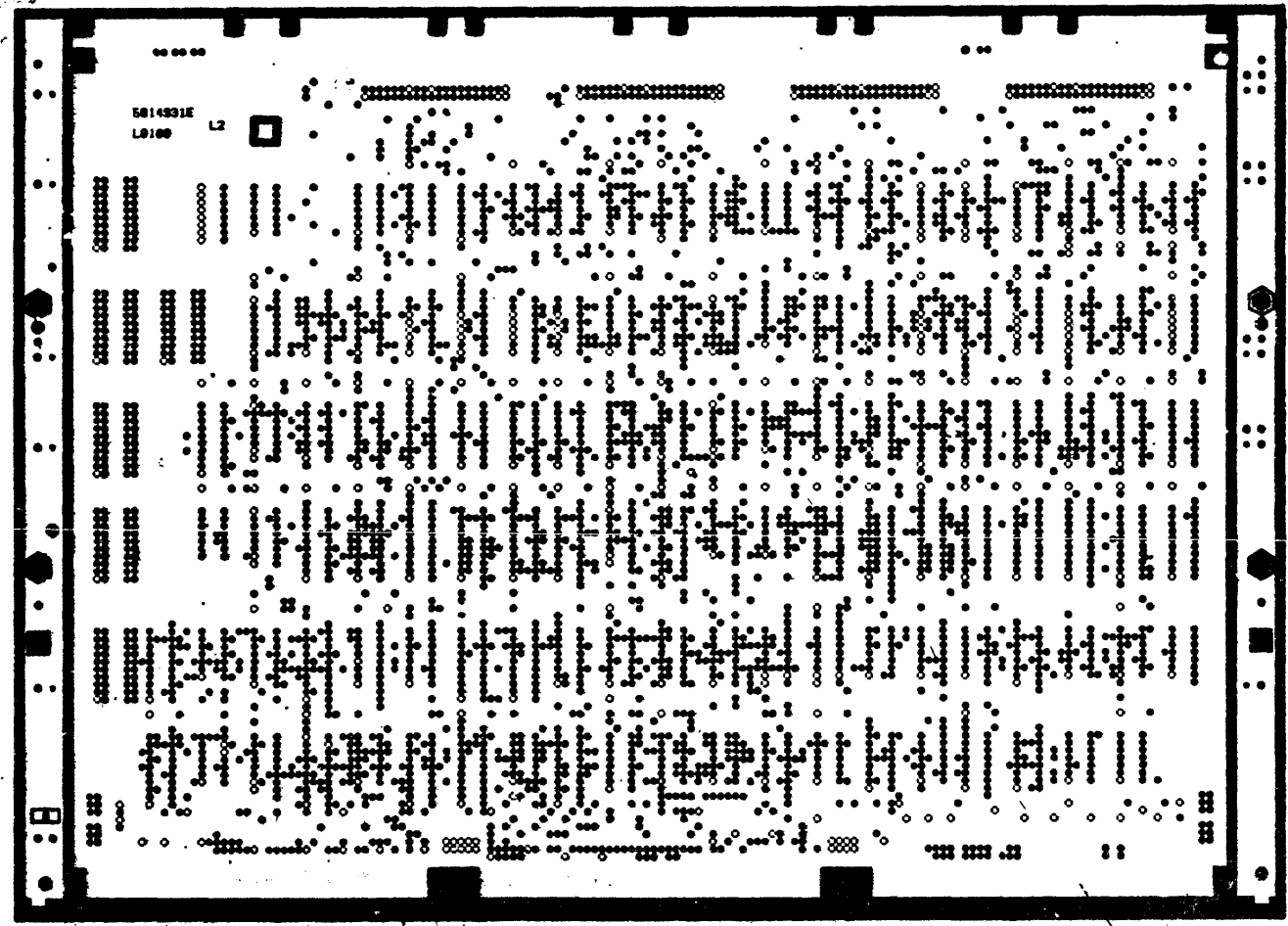
F E-0-1664105 QM Q

DMD5014931-0-0

SLT
3 of 7

REVISIONS			TITLE				SIZE CODE		NUMBER		REV.
CHK	CHANGE NO.	REV.	DRILL & ETCH DRAWING				D MD	5014931-0-E		F	
			SCALE 1/1				SHEET 3 OF 7		DIST.		

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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	DRILL & ETCH DRAWING	SIZE CODE	DMD	NUMBER	5014931-0-E	REV.	F
SCALE	1/1	SHEET	4	OF	7	DIST.	

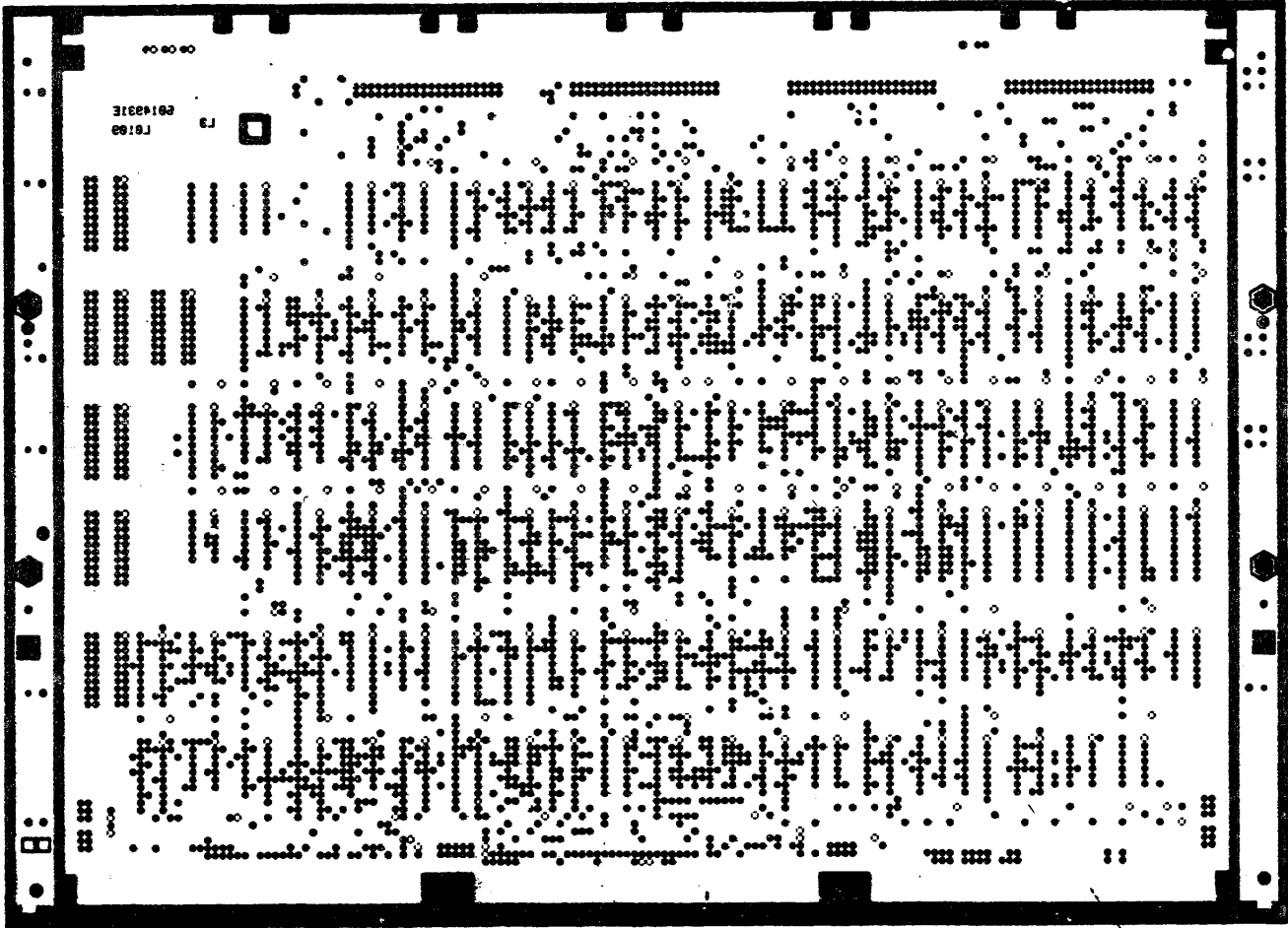
DMD 5014931-0-E F

DMD 5014931-0-0

Sht 4 of 7

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F 3-0-1664105 DMD 2



D
C
B
A

D
C
B
A

DMD5014931-0-0

SST
547

REVISIONS		
CHK	CHANGE NO.	REV.

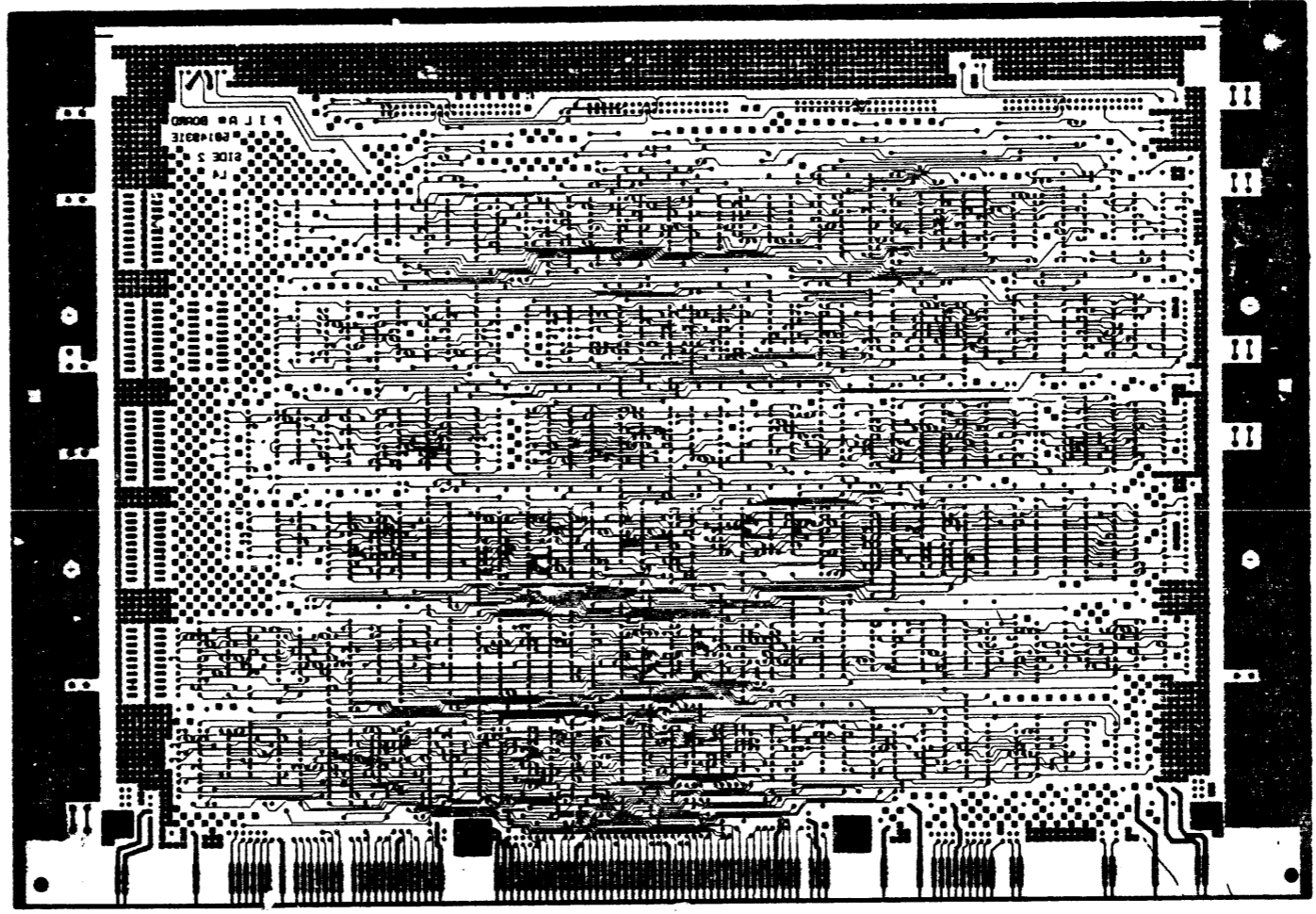
TITLE	DRILL & ETCH DRAWING	SIZE CODE	DMD	NUMBER	5014931-0-E	REV.	F
SCALE	1/1	SHEET	5	OF	7	DIST.	

8 7 6 5 4 3 2 1

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5014931-0-E DMD 2

LAYER 4



3-0-166105 DMD

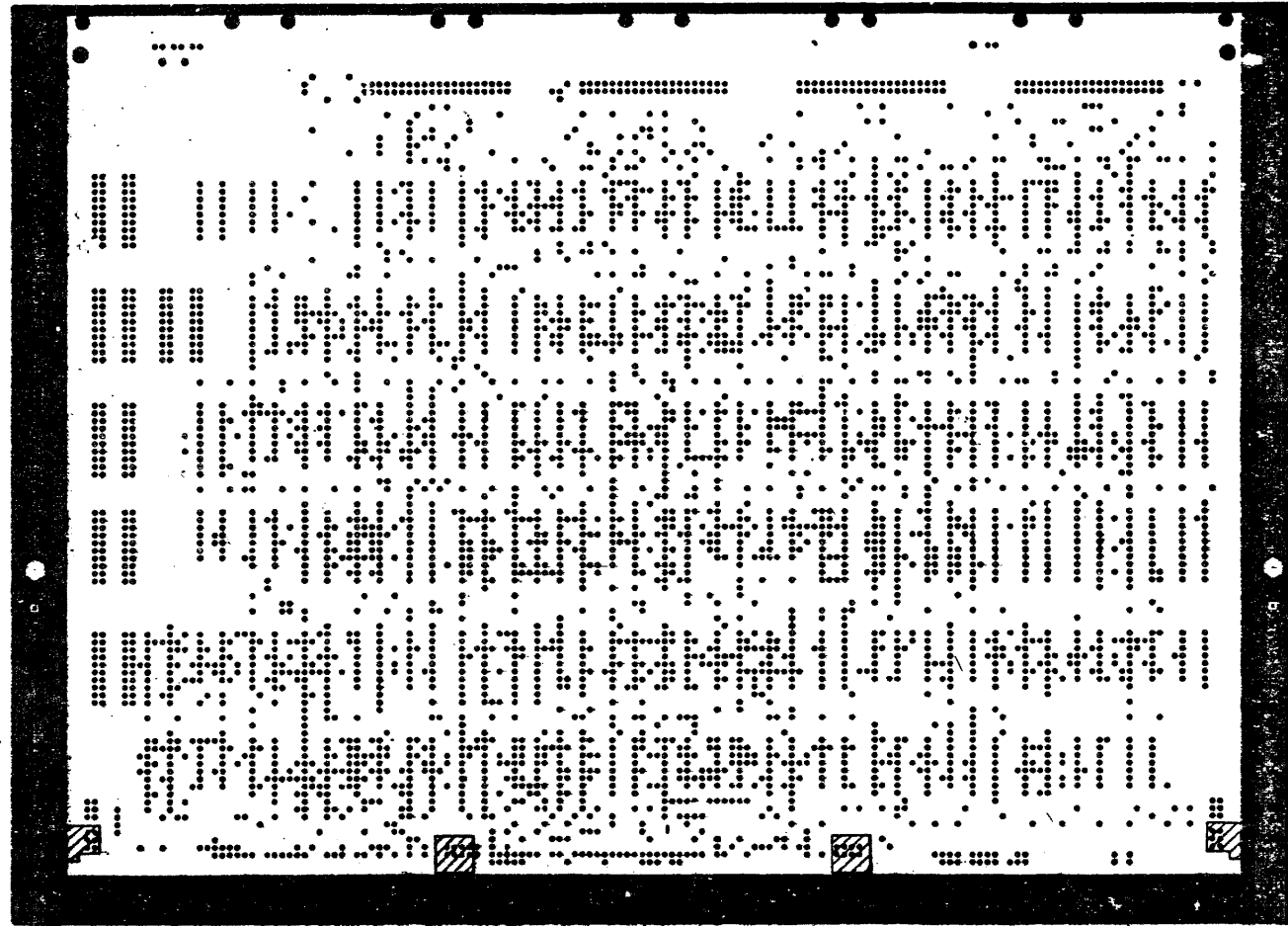
DMD5014931-0-0

S&T
6077

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	DRILL & ETCH DRAWING	SIZE CODE	DMD	NUMBER	5014931-0-E	REV.	F
SCALE	1/1	SHEET	6 OF 7	DIST.			

SOLDER MASK SIDE 2



▨ INDICATES SOLDER MASK REWORK

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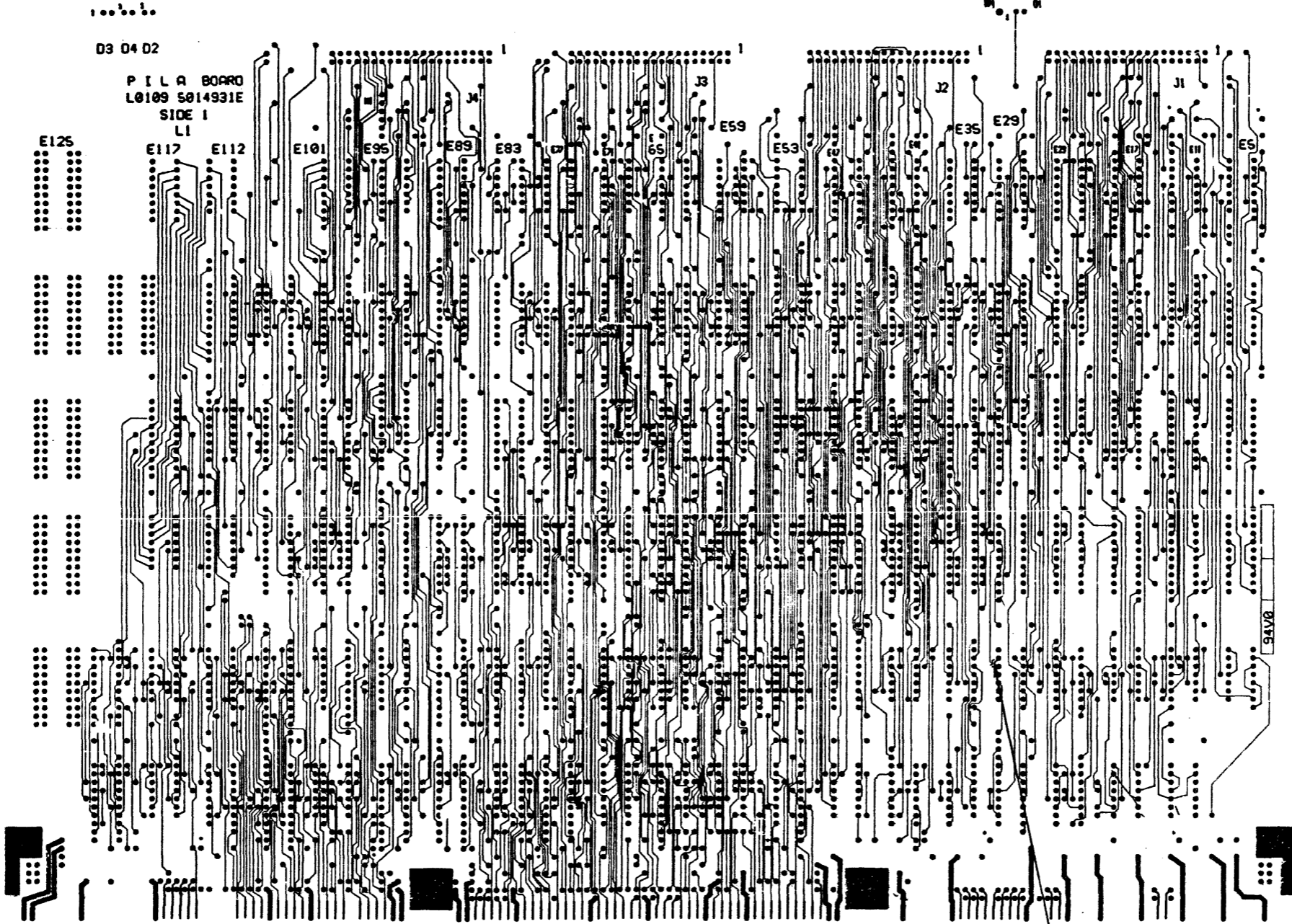
REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	SIZE/CODE	NUMBER	REV.
DRILL & ETCH DRAWING	DMD	5014931-0-E	F
SCALE 1/1	SHEET 7 OF 7	DIST.	

DMD 5014931-0-0

SAT 7/9/7

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REV	DATE	BY	CHK	DESCRIPTION
E	12/14/82	M.S. CHOW	B. HOLLAND	RELEASE AT W.S. 7 JAN, 83
F	12/14/82	M.S. CHOW	B. HOLLAND	RELEASE AT W.S. 14 JUNE 83

CHK'D <i>[Signature]</i> 12/14/82	20Dec.82	FIRST USED ON	HSC 50	digital
ENGR <i>[Signature]</i> 12/14/82		TITLE	ETCH CUT DRAWING	
PROD. <i>[Signature]</i> 12/14/82		NUMBER	5014931-0-E	REV F
NEXT HIGHER ASSY.		SCALE	1.5/1	
B-DD-5014931-0-E		SIZE	D EC	
SHEET 1 OF 3		DIGT.		

DEC 5014931-0-0

slf
1982

3-0-0-E
DEC 5014931-0-E

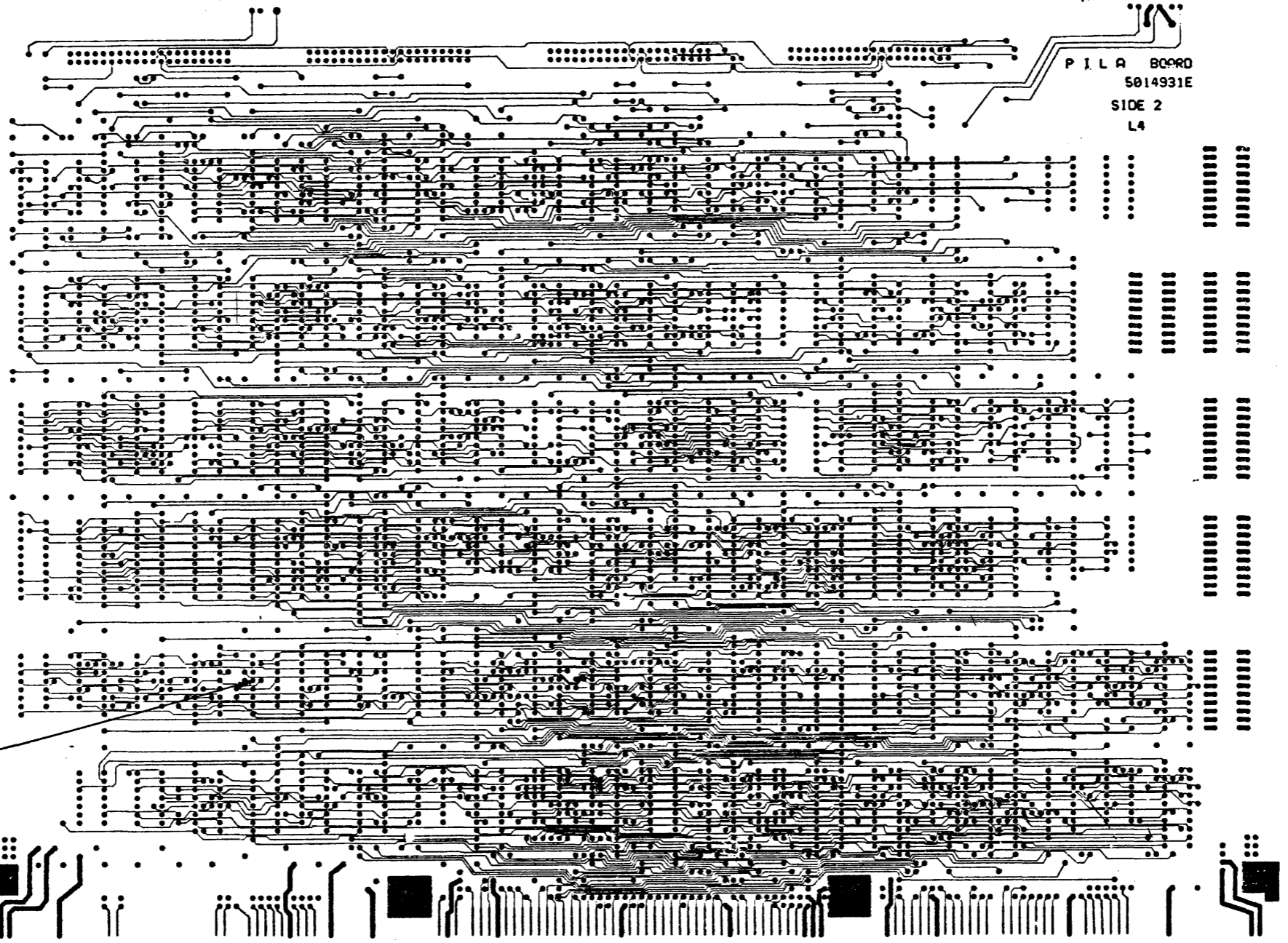
D
C
B
A
REV F
3-0-0-E
DEC 5014931-0-0

8 7 6 5 4 3 2 1

7 6 5 4 3 2 1

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DEC 5014931-0-E 2



D
C
B
A

D
C
B
A

DEC 5014931-0-0

SAT
242

REVISIONS		
CHK	CHANGE NO	REV

8 7 6 5 4 3 2 1

TITLE	SIZE CODE	NUMBER	REV.
ETCH CUT DRAWING	DEC	5014931-0-E	F
SCALE 1.5/1	SHEET 2 OF 3	DIST.	

8 7 6 5 4 3 2 1

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REWORK TERMS:

DISCONNECT—SEPARATE THE COMPONENT LEG FROM EXISTING ETCH CONNECTIONS OF THE MODULE BY EITHER BREAKING ETCH CONTINUITY TO THE PTH, OR BY DRILLING (SEE BELOW) THE PTH ITSELF, OR OTHER SUITABLE PROCEDURE.

DRILL—PRIOR TO INSTALLING COMPONENT, DRILL HOLE WITH .059 (.050-.060) INCH BIT. APPLY EPOXY IN THE HOLE AND LET CURE. DRILL THE CENTER OF EPOXY WITH .038 (.030-.040) INCH BIT.

(X & Y COORDINATES ARE FROM H MASTER '0')

ECO L0109-CX001

ETCH CUTS: SIDE 1

I-4. DISCONNECT E25-3, (X=6300, Y=5200).

ETCH CUTS: SIDE 2

I-5. DISCONNECT E25-11, (X=6000, Y=5100).

D

C

B

A

D

C

B

A

REV. F
 NUMBER 5014931-0-E
 DATE DEC

REVISIONS		
CHK	CHANGE NO.	REV.

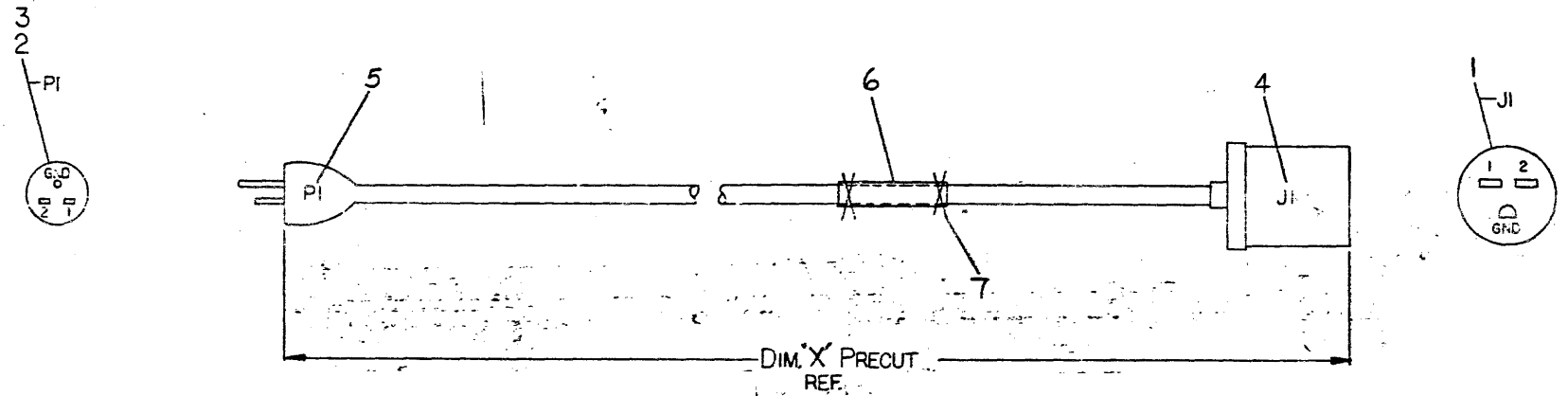
TITLE	ETCH CUT DRAWING	SIZE CODE	DEC	NUMBER	5014931-0-E	REV.	F
SCALE	---	SHEET	3 OF 3	DIST.			

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WIRED TABLE						
ITEM NO	DESCRIPTION	FROM	TO		REMARKS	
NO	AWG	COLOR	CONN WITH	CONN WITH		
2	—	BLK	PI-1	J1-1	—	
OR	—	WHT	PI-2	J1-2	—	
3	—	GRN	PI-GND	J1-GND	—	

LEGEND	
NUMBER	DIM X VARIATION
7011432-1	3 FT ± 1.5 IN.
7011432-2	6 FT ± 3.0 IN.
7011432-3	9 FT ± 3.0 IN.

NOTES:
 1. POWER CORD INSULATED JACKET SHALL BE CAPTIVE WITHIN THE RECEPTACLE (ITEM #1) STRAIN RELIEF WITH NO WIRE INSULATION VISIBLE.



REV. 1	DATE	BY
1	01/14/75	J. PRUD'ENT
2	02/18/75	J. PRUD'ENT

QTY	DESCRIPTION	DWG. PART NO.	ITEM NO.
2	TIES, CABLE	9007031	7
1	LABEL, CABLE I.D.	9009532	6
1	POWER WARN. DECAL (WHT/CLR)	A-DC-7409372-1-0	5
1	POWER CONN DECAL (WHT/CLR)	A-DC-7409373-1-0	4
1	POWER CORD 230V	1700016-09	3
1	POWER CRD 230V	1700016-06	2
1	RECEPTACLE, AC 3WIRE	9003855	1

THIRD ANGLE PROJECTION	DRN. M. C. L. W.	5-22-75	FIRST USED ON	KL10 digital
	CHKD. P. R. D.	5-20-75	TITLE	POWER CORD
REMOVES BURRS AND BREAK SHARP CORNERS	ENG. J. P. R. D.	6-12-75	PROJ. ENL. J. P. R. D.	6-12-75
DO NOT SCALE DWG	PROD. B. P. R. D.	6-16-75	SCALE	1:1
MATERIAL SEE PARTS LIST	FINISH	---	SIZE CODE	D AD
			NUMBER	7011432-0-0
			REV.	A

PART NO. 7011432-0-0

WIRE TABLE

ITEM NO	DESCRIPTION	FROM			TO			REMARKS		
		AWG	COLOR	POINT	CONNECTION	WITH	POINT		CONNECTION	WITH
3	TWP	20	RED	1	PI-9	5	7	P2-1	6	
4	20	20	BLK	2	PI-7	5	8	P2-2	6	2.75 LG
10	20	20	ORG	5	PI-5	5	6		5	

LEGEND

PART NO	VARIATION	REV
7020352-00	AS SHOWN	A2

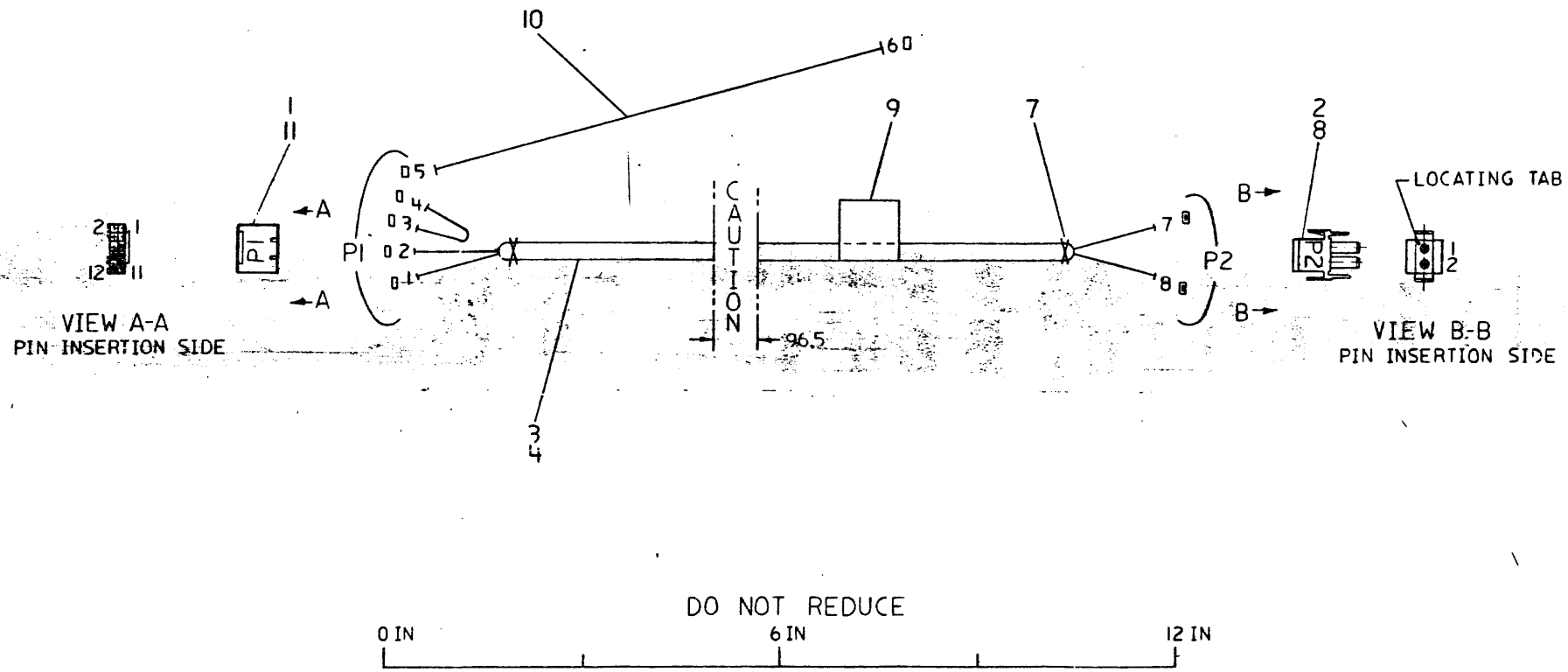
NOTES:

- UNLESS OTHERWISE NOTED ALL LENGTHS ARE IN INCHES.
- CONNECTOR TO BE LABELED WITH COMPONENT IDENTIFIERS.

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1982 DIGITAL EQUIPMENT CORPORATION

ITEM NO	SYMBOL	PART NO. (REF)
5	□	I215121-04
6	⊗	I212170-00



CAUTION: OFF SHEET PARTS LIST EXISTS. SEE K-PL-7020352-0-DBP.

REVISION HISTORY

DATE	ECO NUMBER	REV
	RELEASED	B
	7020352MFC01	C
	Hans Olofin	SED
	F. CAFFABIANCA	
	P. Cappabianca	02/21/83

DESCRIPTION	DRAWING NO.	PART NO.	ITEM NO.					
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)								
INCHES TOLERANCES	ANGLES ±0.07	APPLICABLE DIMENSION RANGE	DIMENSION RANGE IN INCHES					
			OVER	OVER	OVER	OVER	OVER	OVER
X = ±.1	SURFACE QUALITY	(CHECK ONE)	TO	TO	TO	TO	TO	
XX = ±.02		<input type="checkbox"/>	±.02	±.03	±.05	±.10		
XXX = ±.005	MICROINCHES	<input type="checkbox"/>	±.004	±.005	±.012	±.016	±.04	
THIRD ANGLE PROJECTION	DATE 1 OCT. 82	TITLE HARNESS DC VOLTAGE MONITOR						
DO NOT SCALE DRAWING	DATE 10 DEC 82	DOCUMENT NUMBER						
REMOVE BURRS AND BREAK SHARP CORNERS	DATE 13 APR 83	DIA 7020352-0-0						
MATERIAL	DATE 2-11-83	SCALE						
SEE PARTS LIST	FINISH	SHEET						

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY	PER VARIATION
					VARIATION REVISION LEVEL:	00	A2
1	1		1214112-03		CONN,P+S 12POS(2X06).100CC HSG	1	
2	2		1212167-02		MATE-N-LOK 02SKT(1X02).250CC HSG	1	
3	3		9107789-02		WIRE,TWP 20AWG(07/28)PVC 300V	A/R	
4	4		9107460-00		WIRE, 20AWG(07/28)IPVC 150V	A/R	
5	5		1215121-04		CONN,P&S 01SKT 24-20AWG	6	
6	6		1212170-00		MATE-N-LOK 01SKT 20-14AWG .0850D	2	
7	7		9007031-00	B	TIE,CABLE BUNDL.DIA 0- 3/4"=101	2	
8	8	A-DC-7409872-0-0	7409872-02		PWR HARNESS DECAL, BLACK ON CLEA	1	
9	9		3616073-00		LABEL,ID W/COPY VERTICAL	1	
10	10		9107460-33		WIRE, 20AWG(07/28)IPVC 150V	A/R	
11	11	A-DC-7409872-0-0	7409872-01		PWR HARNESS DECAL WHITE ON CLEAR	1	

REVISION HISTORY			BASIC PART NO: 7020352			
ENG	ECO NUMBER	REV	SECTION A OF 'A	DRN: W. ALLEN	DATE: 1 OCT 82	D I G I T A L
---	INITIAL	B	SECTION VARIATION INDEX	CHK'D: G. F.	DATE: 10 DEC 82	TITLE PARTS LIST
PC	7020352-MR001	C	[A]00	DES.ENG: P. CAPPABIANCA	DATE: 13 APR 83	HARNESS D C
			[B]	RESP.ENG.: P. CAPPABIANCA	DATE: 13 APR 83	VOLTAGE MONITOR
			[C]	MFG.ENG.: J. MCCAFFERY	DATE: 11 APR 83	DOCUMENT NUMBER
			[D]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	SIZE CODE NUMBER REV
			[E]	D-IA-7020352-0-0		K PL 7020352-0-DBP C
			[F]			RELEASE DATE: 30-OCT-84
						FILE NAME: Z5906C.PLS
						EDIT # 13

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SYMBOLS

ITEM NO.	SYMBOL	PART NO. (REF)
4	■	1209378-00
5	□	1209379-03
10	▨	1209378-03
12	▩	1209379-00
16	⊠	1212170-00

WIRE TABLE

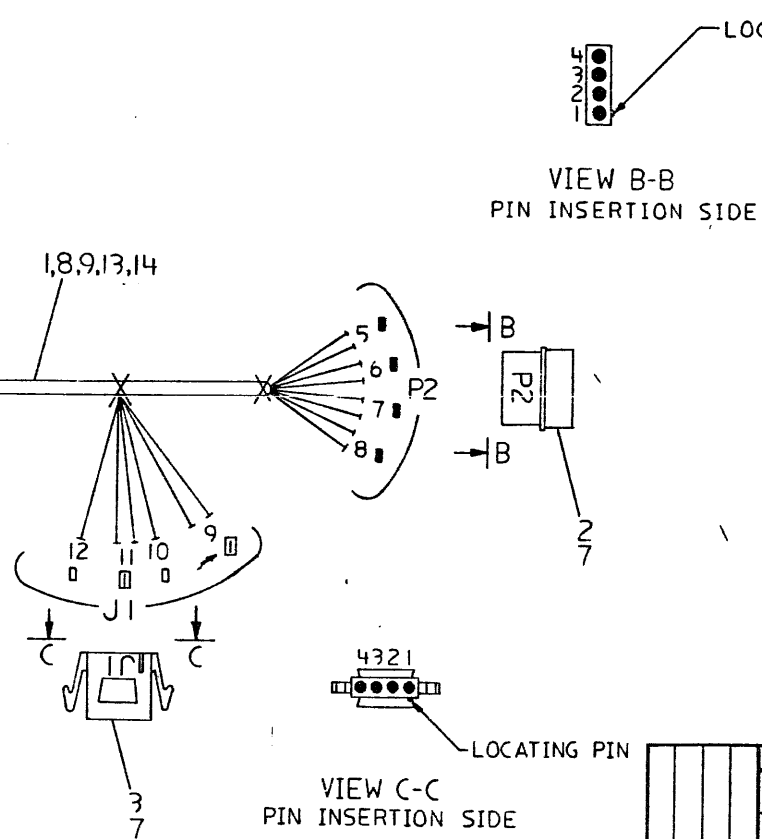
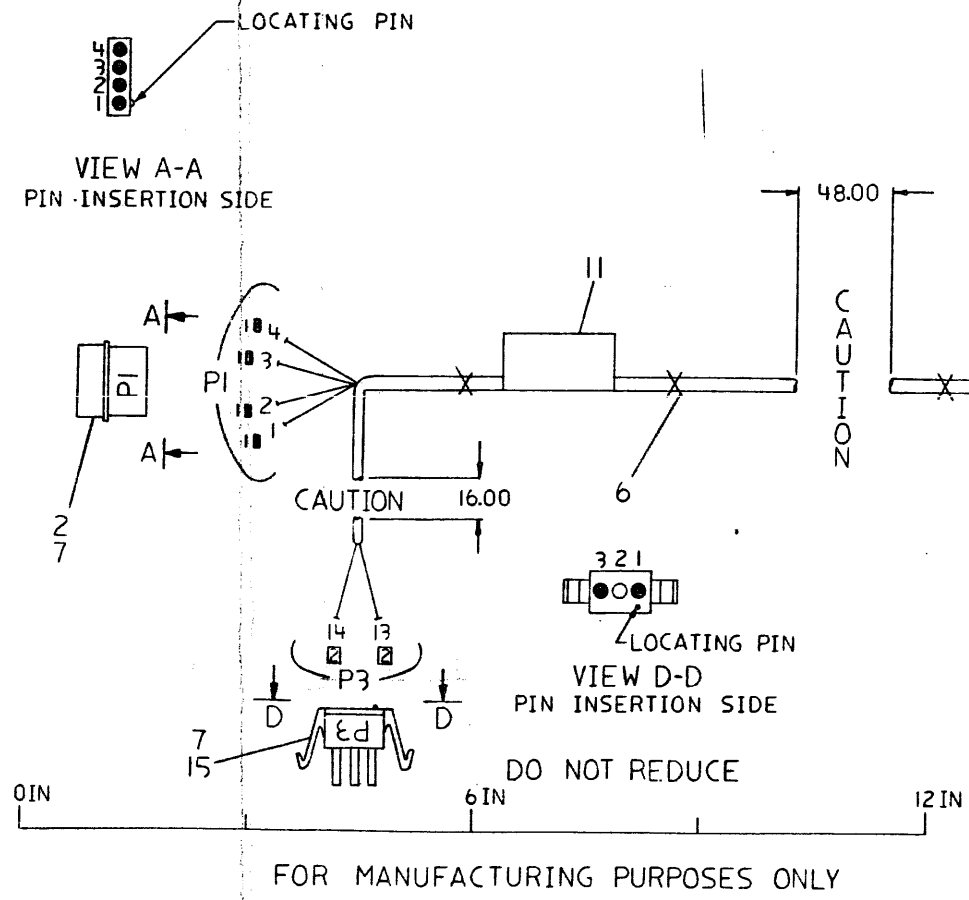
ITEM NO.	DESCRIPTION			FROM			TO			REMARKS
	AWG	COLOR	POINT	CONNECTION	WITH	POINT	CONNECTION	WITH		
9	22	BLK	1	PI-1	10	8	P2-1	4		
9	22	BLK	9	J1-1	12	13	P3-1	16		
14	18	BLK	2	PI-2	10	7	P2-2	4		
9	22	BLK	10	J1-2	5	6	P2-3	4		
8	22	ORN	3	PI-3	10	14	P3-3	16		
8	22	ORN	11	J1-3	12	5	P2-4	4		
13	18	ORN	4	PI-4	10					
1	22	VEL	12	J1-4	5					

LEGEND

PART NO	VARIATION	REV
7019862-00	AS SHOWN	B1

NOTES:

- USE TIE WRAPS (X) ITEM #6 APPROXIMATELY EVERY 3 INCHES WHEN NECESSAR AND AT EVERY BREAKOUT POINT.
- CONNECTORS TO BE LABELED WITH COMPONENT IDENTIFIERS.
- UNLESS OTHERWISE NOTED ALL LENGTHS ARE IN INCHES.



DESCRIPTION	DRAWING NO.	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)			
INCHES TOLERANCES	ANGLES ±0°30'	APPLICABLE DIMENSION RANGE	
X = ±.1	SURFACE QUALITY	OVER	OVER
XX = ±.02	✓	TO	TO
XXX = ±.005	MICROINCHES	TO	TO
QUANTITY & VARIATION	THIRD ANGLE PROJECTION	±.02	±.05
DRN: Joe Donker	DATE: 16 NOV 82	±.008	±.012
DO NOT SCALE DRAWING	REMOVE BURRS AND BREAK SHARP CORNERS	±.015	±.021
MATERIAL: SEE PARTS LIST	FINISH: 11	±.024	±.04
DATE: 14 DEC 82	DATE: 14 APR 83	TITLE: HARNESS, SWITCH VANE	
DATE: 14 APR 83	DATE: 14 APR 83	DOCUMENT NUMBER: 7019862-0-0	
DATE: 14 APR 83	DATE: 14 APR 83	SIZE CODE: DIA	NUMBER: 7019862-0-0
DATE: 14 APR 83	DATE: 14 APR 83	SCALE: 1/1	SHEET: 1 OF 20

REVISION HISTORY

REV.	DATE	DESCRIPTION
1		RELEASED

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION
1		9107350-44		WIRE, 22AWG(07/30)IPVC 150V	A/R
2		1209351-01		MATE-N-LOK 04PIN(1X04).250CC HSG	2
3		1209350-04		MATE-N-LOK 04SKT(1X04).200CC HSG	1
4		1209378-00		MATE-N-LOK 01PIN 20-14AWG .0850D	4
5		1209379-03		MATE-N-LOK 01SKT 30-22AWG .0850D	2
6		9007031-00		TIE,CABLE BUNDL.DIA 3- 3/4"=101	A/R
7	A-DC-7409873-0-0	7409873-02		PWR CONN DECAL BLACK IN CLEAR	1
8		9107350-33		WIRE, 22AWG(07/30)IPVC 150V	A/R
9		9107350-00		WIRE, 22AWG(07/30)IPVC 150V	A/R
10		1209378-03		MATE-N-LOK 01PIN 30-22AWG .0850D	4
11		3610073-00		LABEL,TD W/COPY VERTICAL	1
12		1209379-00		MATE-N-LOK 01SKT 20-14AWG .0320D	2
13		9107360-33		WIRE, 18AWG(19/30)IPVC 150V	A/R
14		9107360-00		WIRE, 18AWG(19/30)IPVC 150V	A/R
15		1212167-00		MATE-N-LOK 03SKT(1X03).250CC HSG	1
16		1212170-00		MATE-N-LOK 01SKT 20-14AWG .0850D	2

VARIATION REVISION LEVEL:

REVISION HISTORY			BASIC PART NO: 7019862								
ENGR	ECO NUMBER	REV	SECTION A OF A	DRN:	JOE DONAHER	DATE:	15 NOV 82	D I G I T A L			
---	INITIAL	IC	SECTION.VARIATION INDEX	CHK'D:	G. F.	DATE:	14 APR 83	TITLE PARTS LIST			
			CA300					HARNESS, SWITCH VANE			
			CB	DES.ENG:	P. CAPPABIANCA	DATE:	14 APR 83	DOCUMENT NUMBER			
			CC					SIZE	CODE	NUMBER	REV
			CD	RESP.ENG.:	P. CAPPABIANCA	DATE:	14 APR 83	K	PL	7019862-3-DBP	C
			CE	APG.ENG.:	J. MCCAFFERY	DATE:	11 APR 83	RELEASE DATE: 02-FEB-84			
			CF	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
				10-1A-7019862-0-0				Z5888C.PLS		9	

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MPC

DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS													
		5414506-01	MODULE REVISION	A													
D-UA-5414506-1-0	1		VOLTAGE MONITOR BOARD	A													
K-PL-5414506-1-DBP	1		VOLTAGE MONITOR BOARD (PL)	A													
D-CS-5414506-1-1	1		VOLTAGE MONITOR BOARD (CS)	A													
A-SP-5414506-1-2	4		VOLTAGE MONITOR BOARD (SP)	B													
D-MD-5014505-0-0	2		DRILL & ETCH DRAWING (MD)	A													
D-EC-5014505-0-0	1		ETCH CUT DRAWING (EC)	A													
		5014505	ETCHED CIRCUIT BOARD	C													
K-PC-5414506-0-DBC	-		P.C. DESIGN DATA BASE	A													

NOTES:

REVISIONS	DATE	CHG NO.	REV.
		X	A

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USED ON OPTION/MODEL	DRN	TITLE
KL10	M. Normand 25 Sept 81	VOLTAGE MONITOR BOARD
	CHK'D RW Coaster 1 Oct 81	
	ENG. H.E. Joseph 1 Oct 81	SIZE CODE NUMBER REV.
	PROD. [Signature]	B DD 5414506-1 A
		SHEET 1 OF 1

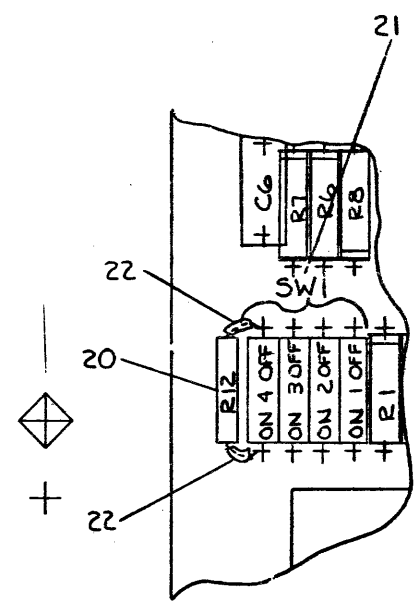
REWORK INSTRUCTIONS
 INITIAL RELEASE
 COMPONENT ADDS SIDE 1 AS SHOWN
 O-1 SW1 (P/N 1211164-00, SW DIP 1A 4POS)

- A. CLIP OFF BOTH LEADS OF SW1-4
 FLUSH WITH SWITCH BODY
- B. INSERT SWITCH INTO 6 PIN'S AT
 AT THE TOP R1.

O-2 R12 (P/N 1300447, 470K .25W 5.0%)

- A. ADD TEFLON THIN WALL TUBING
 (P/N 9107807-11 TO BOTH LEADS.
- B. INSERT R12 INTO 2 PTH'S BENEATH
 SW1-4

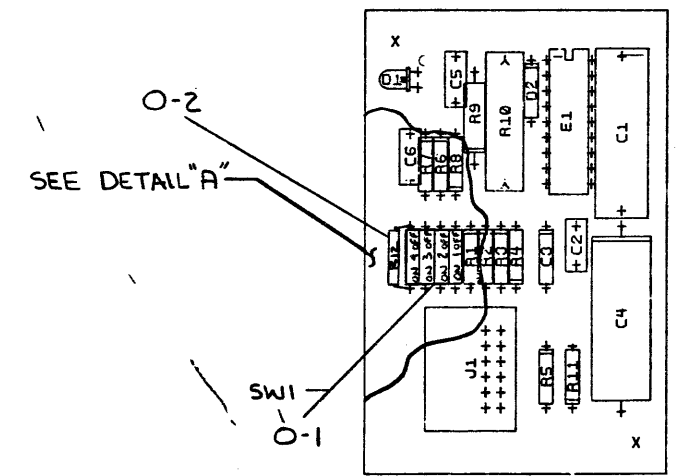
NOTE: DIRECTIONS ARE GIVEN WITH
 RESPECT TO MODULE BEING
 POSITIONED WITH CONNECTOR
 J1 TOWARD USER.
 INDICATE MODULE REVISION AS "A"



SWITCH SW1

SWITCH POSITION	VOLTAGE	±20	±15	±5
1	OFF	OFF	ON	
2	OFF	DN	OFF	
3	ON	OFF	OFF	
4	NOT USED			

DETAIL "A"
 INSTALLED SWITCH (SW1)



NOTES: 2ML74

CHG	NO	REV

STEP	E	Y	AXIS	3	STEP	3	TIMES
REPEAT	→	X	AXIS	25	STEP	4	TIMES

SIGNATURES	DATE
DRN. <i>M. Howard</i>	10/29/77
CHK'D. <i>R.W. Canten</i>	10/28/77
MECH. ENG. <i>R.C. Jones</i>	10/28/77
PROJ. ENG. <i>A.C. Jorgensen</i>	10/28/77
PRD.	10/28/77

digital	
TITLE VOLTAGE	
MONITOR EC-R	
SCALE 2/1	SIZE CODE NUMBER
SHT. 1 OF 1	0 U-5411506-1-0
NEXT HIGHER ASSY. BDD 5411502-1	

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
				01	
1	D-MD-5014505-0-0	5014505-00	DRILL AND ETCH BRD.	1	
2		1000075-00	25 MFD 25V +75-10% AL EL	1	C1
3		1000004-00	.02 MFD 100V 20% Z5U DISC	1	C2
4		1001610-00	.01 MFD 50V +80-20% Z5U CER	1	C3
5		1000080-00	50 MFD 50V +75-10% AL EL	1	C4
6		1010274-01	.22 MFD 50V +80-20% Z5U CER	2	C5,C6
7		1117373-00	LED ASSY GREEN	1	D1
8		1105796-00	1N 4004 PIV=400 I= 1A D041 SP	1	D2
9		1217652-00	HEADER 12PIN RT ANGLE, DBL RO	1	J1
10		1317925-00	6.98 K .25 W .10% RN55E-B	2	R1
11		1317924-00	36.50 K .25 W .10% RN55E-B	2	R2
12		1317923-00	51.10 K .25 W .10% RN55E-B	2	R3
13		1317922-00	732.0 .25 W .10% RN55E-B	2	R4
14		1305355-00	7.0 K .25 W .10% RN55C-B	5	R5
15		1300365-00	1.0 K .25 W 5.0 % CC	2	R6,R7
16		1300250-00	150.0 .25 W 5.0 % CC	1	R8
17		1300347-00	680.0 .50 W 5.0 % CC	1	R9
18		1300368-00	1.0 K 1.0 W 5.0 % CC	1	R10
19	SEE NOTE #1	1917657-00	3544J LOW-VOLT SUPERVISORY	1	E1
20		1300447-00	4.70 K .25 W 5.0 % CC	2	R11,R12
21		1211164-00	SW,DIP 1P 1A 4POS	1	SW1
22		9107807-11	TUBING, THIN WALL, .022ID	UL A/R	

23 NOTE: 1. THIS PART CAN BE MARKED SG2544J/DEC3544J.

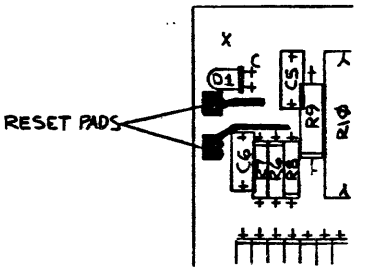
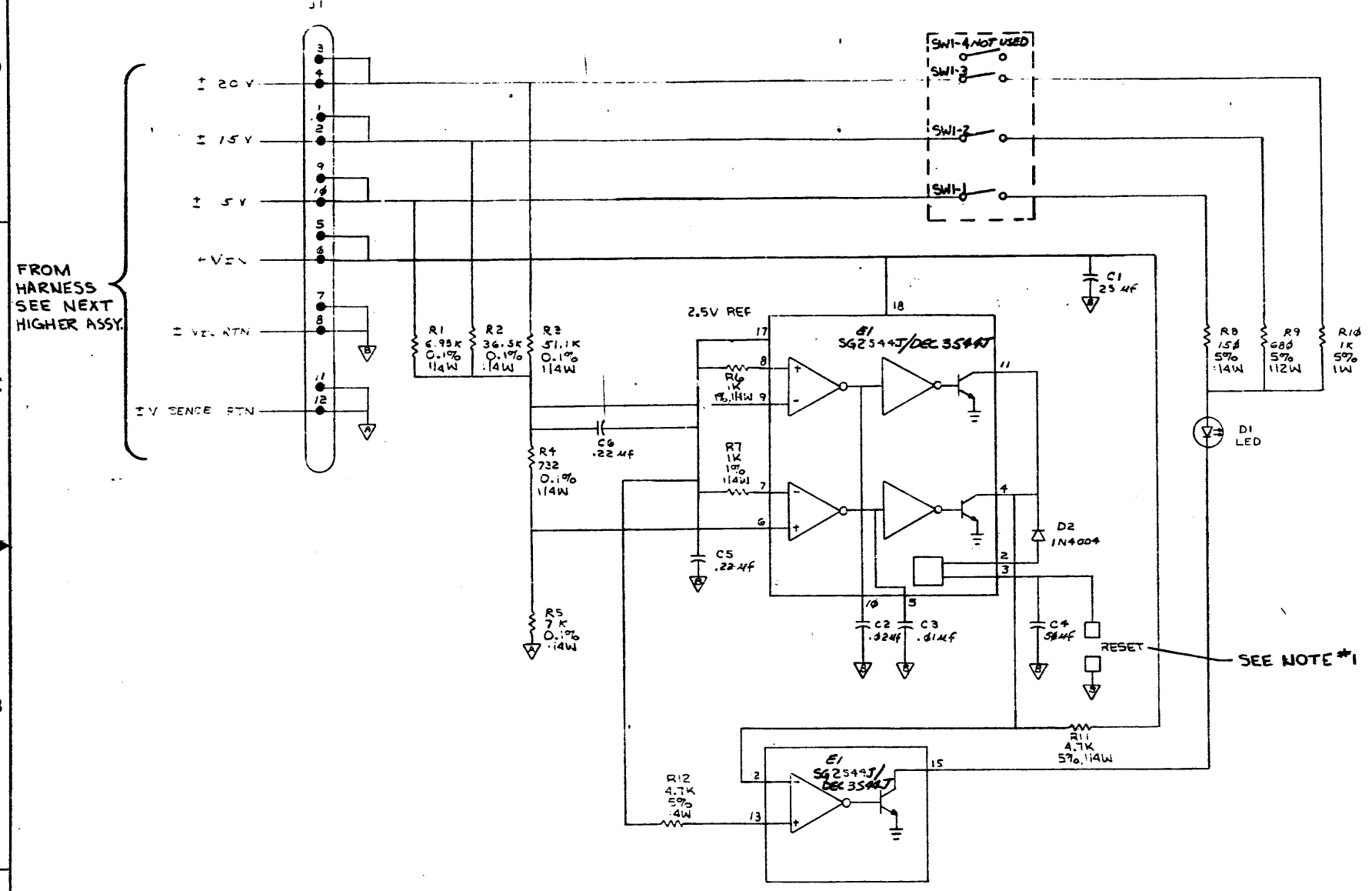
REVISION HISTORY			BASIC PART NO: 5414506		
ENG	ECO NUMBER	REV	SECTION A OF A	DRN: M, NORMAND	DATE: 22-SEP-81
---	INITIAL	A	SECTION VARIATION INDEX	CHK'D: R, W, CAUNTER	DATE: 1-OCT-81
			(A) 01	DES, ENG: R, JOSEPHSON	DATE: 1-OCT-81
			(B)	RESP, ENG: R, JOSEPHSON	DATE: 1-OCT-81
			(C)	MFG, ENG: T, CAVANAUGH	DATE: 1-OCT-81
			(D)	ASSEMBLY NUMBER: D-UA-5414506-1-0	TOP DOCUMENT NUMBER: B-DD-54145-1
			(E)		
			(F)		
			(H)		
			(J)		
			(K)		
			(L)		
			(M)		
			(N)		
			TITLE: PARTS LIST		
			5414506-01		
			VOLTAGE MONITOR BOARD		
			DOCUMENT NUMBER		
			SIZE: CODE: NUMBER: REV		
			K PL 5414506-1-DBP A		
			FILE NAME: 23208A.PLS		
			EDIT: 4		

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MR

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NOTES:
 1. JUMPER PADS TOGETHER TO RESET INDICATOR. RESET IS ALSO PROVIDED BY REMOVAL OF ALL POWER TO THE MODULE. RESET JUMPER MUST BE LESS THAN 100 OHMS MAXIMUM.



SWITCH POSITION	VOLTAGE	±20	±15	±5
1	OFF	OFF	ON	
2	OFF	ON	OFF	
3	ON	OFF	OFF	
4	NOT USED			

SWITCH SW1
 CAUTION: ONLY ONE SWITCH MUST BE ON FOR PROPER OPERATION OF THIS BOARD.

SEE NOTE #1

FROM HARNESS SEE NEXT HIGHER ASSY.

REV.	CHANGE NO.

DRN M. N. ...	FIRST USED ON	KL10	digital
CHK'G R. J. ...	TITLE	VOLTAGE MONITOR BOARD	
ENG. X. ...	PROJECT	10289	
PROJ. ENG. ...	PROJ. NO.	10289	
PROJ. ENG. ...	PROJ. NO.	10289	
NEXT HIGHER ASSY.	SCALE	NONE	REV. A
B-CD-5414506-1	SIZE CODE	D	NUMBER 5414506-1-1
SHEET 1 OF 1	DIST.		

MR 1

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE Sept. 22, 1981

TITLE VOLTAGE MONITOR

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENG *John Tivnan* APPD *Jim Josephson* SIZE A CODE SP NUMBER 5414506-1-2 REV A

DEC 16-(392)-1079-N971
DRA 107

SHEET 1 OF 5

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE VOLTAGE MONITOR

1.0 General

1.1 This is a specification for a voltage monitor printed circuit board.

When the input voltage to the module is within its range and the correct switch is placed in the "on" position, a green LED is illuminated.

When the voltage goes out of range either on an overvoltage or undervoltage condition, the green LED is extinguished and this fault is latched.

1.2 Reference Documents

DEC STD 102 - Environmental Standard.
D-UA-5414506-1-0 Voltage Monitor Board

2.0 Electrical Specifications

2.1 Input Voltages

The following voltage input ranges are acceptable for monitoring:

+5V	0- +7V DC
+15V	0- +20V DC
+20V	0- +25V DC
-5V	0- -7V DC
-15V	0- -20V DC
-20V	0- -25V DC

2.2 Power Source

This module will operate from a power source of -4.5V to -40V DC, or +4.5V to +40V DC.

2.3 Detection Band

This module will detect and extinguish the green LED display when the input voltage is out of the following bands:

SIZE A CODE SP NUMBER 5414506-1-2 REV A

DEC FORM NO EN-01022-16-N370-(381)
DRA 108

SHEET 2 OF 5

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE VOLTAGE MONITOR

Switch	Voltage	Error Band (Fault when outside this range)
SW1-1	+/-5V	$\pm 5\% \pm 1.5\%$ (3.5% to 6.5%)
SW1-2	+/-15V	$\pm 5\% \pm 1.5\%$ (3.5% to 6.5%)
SW1-3	+/-20V	$\pm 5\% \pm 1.5\%$ (3.5% to 6.5%)

2.4 Reset

Reset pads are provided on the printed circuit board for local reset. Jumper reset pads together to reset the indicator. Reset is also provided by removal of all power to the module. Reset jumper must be less than 100 ohms maximum.

2.5 Turn-On Time Delay

An undervoltage fault will be disabled for 100 ± 20 ms after the power source voltage is turned on.

2.6 Fault Duration

The over and/or under voltage condition must exist for a duration of time before detection.

Overvoltage Condition - 0.1 ms
Undervoltage Condition - 0.2 ms

2.7 Operating Power

Current drain is 16 ma during normal operation. Current drain during a fault condition is 22 ma.

2.8 Operating Voltage

Module will operate as long as power source voltage is greater than 4.5V. Duration of true indication may be lengthened by connecting power source voltage to a voltage higher than being monitored.

If the module is operated from the voltage it is monitoring, it will not indicate (at all) upon loss of the voltage.

3.0 Output

3.1 Indicator

Normal operation of the voltage is indicated by an illuminated green LED. Abnormal voltage is indicated by the lack of illumination by the green LED.

SIZE A CODE SP NUMBER 5414506-1-2 REV A

DEC FORM NO EN-01022-16-N370-(381)
DRA 108

SHEET 3 OF 5

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE VOLTAGE MONITOR

4.0 Mechanical and Physical

4.1 Size - Printed circuit board 2.0 x 3.0 in. Maximum height is 0.5 in.

4.2 Weight

4.3 Mounting - Any position

4.4 Cooling - Natural convection.
Power may range from 100 mw to 650 mw depending upon power source voltage.

4.5 Temperature Range

- Per DEC Std 102 for Class B environment

4.6 Altitude - 8000 ft - operate

4.7 Vibration - Per DEC Std 102 Class B

4.8 Mechanical Shock - Per DEC Std 102

5.0 Reliability

MTBF goal is greater than 200,000 hours for ground benign environment per MIL - HDBK 217B based on parts count.

6.0 Safety

Not applicable

7.0 Input-Output Connector

J1 Part Number 12-17652-00 Mates with 12-14112-03 connector. Contacts in mating connector are 12-151121-00

Signals

J1-1	15V INPUT
J1-2	15V INPUT
J1-3	20V INPUT
J1-2	20V INPUT
J1-5	INPUT VOLTAGE
J1-6	INPUT VOLTAGE
J1-7	INPUT VOLTAGE RETURN
J1-8	INPUT VOLTAGE RETURN
J1-9	5V INPUT
J1-10	5V INPUT

SIZE A CODE SP NUMBER 5414506-1-2 REV A

DEC FORM NO EN-01022-16-N370-(381)
DRA 108

SHEET 4 OF 5

TITLE VOLTAGE MONITOR

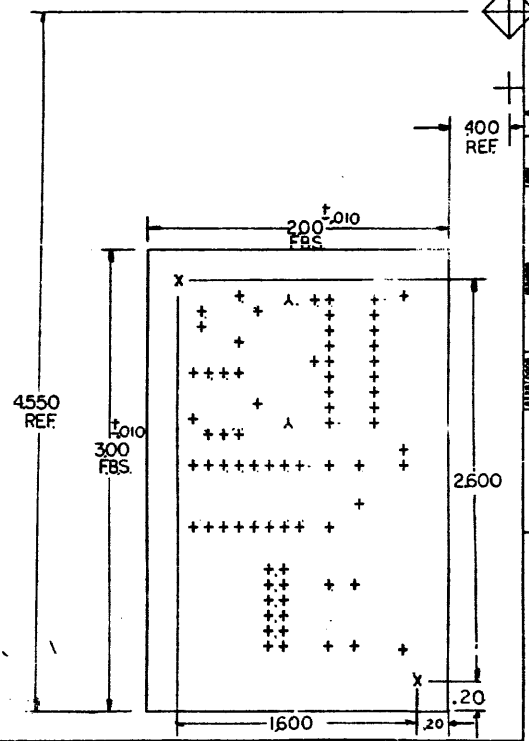
J1-11 SENSE VOLTAGE RETURN
J1-12 SENSE VOLTAGE RETURN

In addition, two printed circuit pads are provided for reset.

SIZE	CODE	NUMBER	REV
A	SP	5414506-1-2	A

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COMPONENT SIDE VIEW



NOTES: 2ML74

STEP 6	→ Y AXES	35	STEP 2 TIMES
REPORT	→ X AXES	26	STEP 3 TIMES

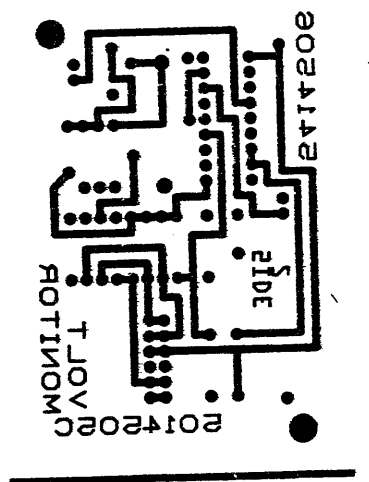
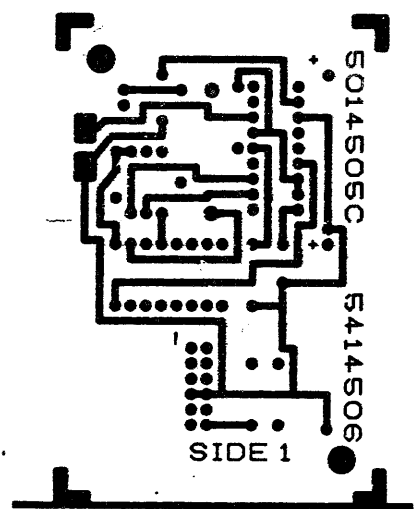
CHANGE NO REV	DIMENSIONAL TOLERANCE - INCHES
	XXX = ±.005
	XX = ±.020
	X = ±.040
	ANGLE = ±0° 30'

HOLE DATA										
SYMBOL	+	A.	X'	+	Z	M	A	□	◆	*
FIN HOLE SIZE		.042	.055	.141						
PLATED	X	X	OPT							
NON PLATED										
QTY.	74	2	2							
○-OFF GRD. HLE										
DRILL SIZE										
.025 GRID UNLESS INDICATED										

BOARD FABRICATION INFORMATION	
BOARD SIZE	10.6 QUAD
BOARD DATA	DWG. # EMD-7605819-17-0
BOARD MAT'L	FL-EP-055-E 2/2
DEC. PS	-1400000
QTY. OF LAYERS	2
PTH. X PRINT AND ETCH	
SOLDER MASK	NO SIDE 2 BOTH SIDES
GOLD. CONTACTS	NO BOTTOM TOP

SIGNATURES		DATE	TITLE	DRILL AND ETCH DRAWING
DRN.	<i>[Signature]</i>	5-18-60		
CHK.	<i>[Signature]</i>	6-20-60		
MECH. ENG.	<i>[Signature]</i>	6-22-60	SIZE CODE	NUMBER
PROJ. ENG.	<i>[Signature]</i>	6-22-60		
PROD.	<i>[Signature]</i>	6-22-60	REV	REV
SCALE	2/1			
SHT.	1 OF 2		NEXT HIGHER ASSY. DUA-5414506-0-0	ETD+ REV. C

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REVISIONS		
CHK	CHANGE NO.	REV

TITLE DRILL & ETCH DRAWING		SIZE CODE D	NUMBER MD5014505-0-0	REV. A
SCALE 2/1	SHEET 2	OF 2	DIST.	

MR 1

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION		
					10	20	45
1	1	D-AD-7018541-0-0	7018541-00	10 METER BNCI SUB -ASSY	0	0	0
			3618803-01	LABEL,CABLE CI780 RX	0	0	0
			3618807-01	LABEL,MARKER 1-16,A+B	0	0	0
			3618808-02	LABEL,CABLE CI780 TX	0	0	0
5	5	D-AD-7018541-0-0	7018541-01	20 METER BNCI SUB-ASSY	0	0	0
6	6	D-AD-7018541-0-0	7018541-02	45 METER BNCI SUB-ASSY	0	0	0

REVISION HISTORY		BASIC PART NO: OBNCI		DRN: R.J.RILEY	DATE: 19-NOV-81	D I G I T A L		
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D: M.BAPTISTE	DATE: 19-NOV-81	TITLE		PARTS LIST
---	INITIAL	A	SECTION. VARIATION INDEX	DES.ENG.: G.NORTON	DATE: 19-NOV-81	BNCI CABLE ASSEMBLY		
			[A] 10,20,45	RESP.ENG.: R.NUEBLING	DATE: 19-NOV-81	DOCUMENT NUMBER		
			[B]	MFG.ENG.: M.WARSHAW	DATE: 19-NOV-81	SIZE	CODE	NUMBER
			[C]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	K	PL	BNCI-0-DBP
			[D]	D-UA-BNCI-0-0	E-UA-5C008-AA-0			FILE NAME:
			[E]					23339A.PLS
			[F]					EDIT #
								9

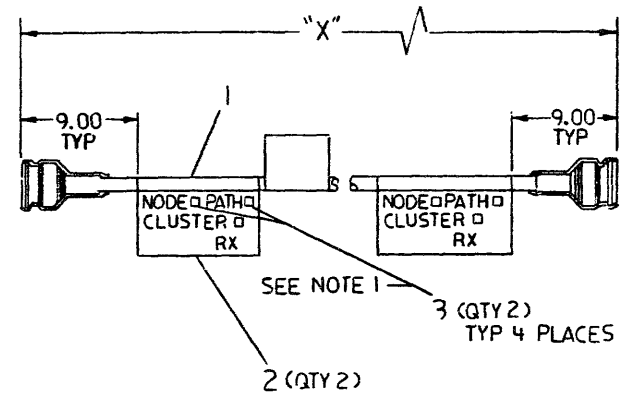
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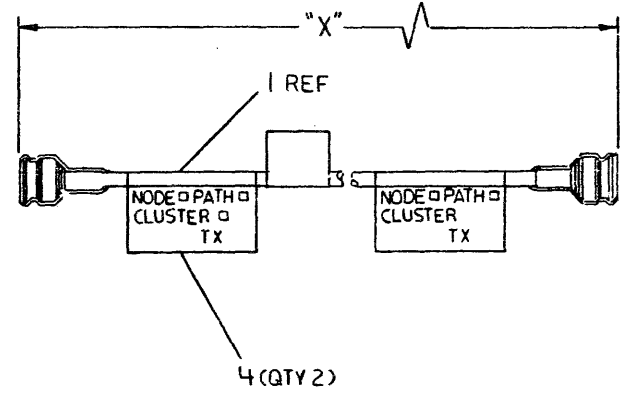
0-0-BNCI-0-DBP

DWG NUMBER	DIM X (REF)		REMARKS
	FEET	METERS	
BNCI-10	33 FT	10M	TX/RX
BNCI-20	66 FT	20M	TX/RX
BNCI-45	148 FT	45M	TX/RX

NOTES:
1. ITEM 3 TO BE PKGD WITH CABLES & PUT ON IN THE FIELD.



RX VARIATION



TX VARIATION

CAUTION: SEE OFF SHEETS PARTS LIST
K-PL-BNCI-0-DBP (Z3339A)

REV.	DATE	ECO NUMBER	DESCRIPTION

DESCRIPTION		DRAWING NO.		PART NO.		ITEM NO.		
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)								
INCHES TOLERANCES X = ± .1 XX = ± .02 XXX = ± .005	ANGLES ± 0'30"	APPLICABLE DIMENSION RANGE (CHECK ONE)	DIMENSION RANGE IN INCHES					
			OVER 0 TO 0.2	OVER 0.2 TO 1.2	OVER 1.2 TO 4.0	OVER 4.0 TO 12.0	OVER 12.0 TO 40.0	OVER 40.0 TO 80.0
SURFACE QUALITY			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
MICROINCHES			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
QUANTITY & VARIATION	THIRD ANGLE PROJECTION	DATE	DATE	TITLE	digital			
DO NOT SCALE DRAWING	REMOVE BURRS AND BREAK SHARP CORNERS	DATE	DATE	BNCI CABLE ASSY				
MATERIAL	SEE PARTS LIST	DATE	DATE	DOCUMENT NUMBER				
FINISH	F-TA-7018771-0-0	DATE	DATE	DUA BNCI-0-0				

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION		
				10	20	45
1 SUBSTIT	D-AD-7018541-0-0	7018541-00	10 METER BNCI SUB -ASSY	2	0	0
		3618803-01	LABEL,CABLE CI780 RX	0	0	0
		3618807-01	LABEL,MARKER 1-16,A+B	0	0	0
		3618808-02	LABEL,CABLE CI780 TX	0	0	0
5 SUBSTIT	D-AD-7018541-0-0	7018541-01	20 METER BNCI SUB-ASSY	0	0	0
		7018541-02	45 METER BNCI SUB-ASSY	0	0	0

REVISION HISTORY		BASIC PART NO: OBNCI		DRN: R.J.RILEY	DATE: 19-NOV-81	DIGITAL			
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D: M.BAPTISTE	DATE: 19-NOV-81	TITLE	PARTS LIST		
---	INITIAL	A	SECTION. VARIATION INDEX	DES.ENG.: G.NORTON	DATE: 19-NOV-81	BNCI CABLE ASSEMBLY			
			[A] 10,20,45	RESP.ENG.: R.NUEBLING	DATE: 19-NOV-81	DOCUMENT NUMBER			
			[B]	MFG.ENG.: M.WARSHAW	DATE: 19-NOV-81	SIZE	CODE	NUMBER	REV
			[C]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	K	PL	BNCI-0-DBP	A
			[D]	D-UA-BNCI-0-0	E-UA-SC008-AA-0	FILE NAME:			EDIT #
			[E]			Z3339A.PLS			9
			[F]						

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DRAWING NUMBER	PAGE	PART NO.	DESCRIPTION	REVISIONS
			ECO NUMBER	-
E-LA-5414793-0-0	1	5414793-00	MODULE REVISION	A1
K-PL-5414793-0-DBP	1		IPA 20-L BACKPANEL	A
D-CS-5414793-0-1	5		PARTS LIST, 5414793	A
			IPA20-L BACKPLANE INT CONN	A
K-PC-5414793-0-DBC	-		CALDEC DATA BASE	A
D-DD-5014792-0	1		DRAWING DIRECTORY, 5014792	A

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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN	E. WILSON	DATE	ENG.	DATE	TITLE
	CHK'D.	10-11-84	08-JUN-84	Pat. Corporation	08-JUN-84	DRAWING DIRECTORY
	DATE	BOARD LOCATION				5414793
	08-JUN-84	N/A				
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:		SIZE CODE		NUMBER REV.
				D DD		5414793-0 A

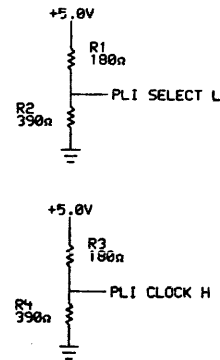
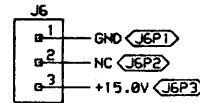
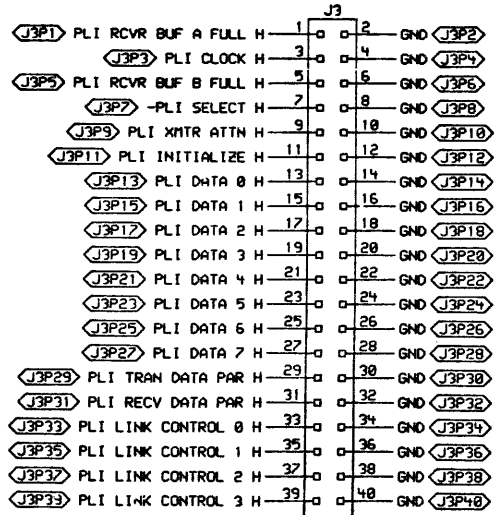
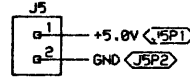
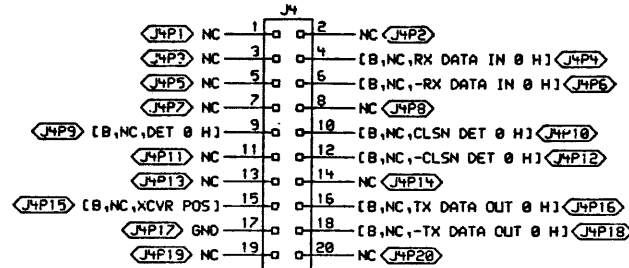
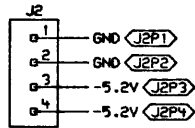
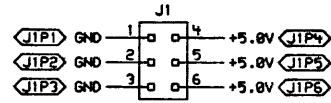
LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
				VARIATION REVISION LEVEL:	00		
					A1		
1	D-MD-5014792-0-0	5014792-00		DRILL&ETCH DRAWING	1		
2		1300309-00		390.0 .25 W 5.0 % CF	2		R2,R4
3		1301322-00		180.0 .25 W 5.0 % CF	2		R1,R3
4		1212297-12		MATE-N-LOK 06PIN(2X03).250CC HDR	1		J1
5		1212297-15		MATE-N-LOK 04PIN(1X04).250CC HDR	1		J2
6		1220547-01		PCB HEADER 40PIN(2X20).100CC STR	1		J3
7		1217869-01	A	PCB,HEADER 20PIN(2X10).100CC STR	1		J4
8		1212297-10		MATE-N-LOK 02PIN(1X02).250CC HDR	1		J5
9		1212297-14		MATE-N-LOK 03PIN(1X03).250CC HDR	1		J6
10		9009150-02		INSERT,THREAD SLF-CLNCH	4		
11		1218506-00		CONN,COAX BKP 50 OHM PLUG CLAM	4		
12	A-DC-7411881-0-0	7411881-01		DECAL	1		
13		1218421-02		CARD EDGE 94PIN(2X47).100CC STR	6		J7-J12
14		9905016-04		CARTON,DIE CUT,SELF LOCK W/FOAM	1		

REVISION HISTORY			BASIC PART NO: 5414793		
ENGI	ECO NUMBER	REV	SECTION A OF A	IDRN: J. PLANTE	DATE: 14-SEP-82
---	INITIAL	IA	SECTION VARIATION INDEX	CHK'D: R.W.CAUNTER	DATE: 1-DEC-82
			[A] 00	DES. ENG: P.CAPPABIANCA	DATE: 8-JUN-84
			[B]	RESP. ENG.: P.CAPPABIANCA	DATE: 8-JUN-84
			[C]	MPG. ENG.: S.RAYNE	DATE: 5-NOV-84
			[D]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:
			[E]	E-DA-5414793-0-0	DD-5414793-0
			[F]		FILE NAME:
			[G]		Z9869A.PLS
			[H]		EDIT #:
			[I]		14

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IPA20-L POWER AND SIGNAL INTERCONNECTIONS

- NOTES: 1. THE VARIABLE FIELDS ARE DEFINED AS FOLLOWS:
 (B, FIELD 1, FIELD 2)
 FIELD 1 = CI20; FIELD 2 = NIA20
2. NO VARIABLE SIGNAL FIELD DENOTES A COMMON SIGNAL FOR BOTH CI/NIA 20.
3. FOR BACKPANEL WIRELISTER GENERATION/CONTROL, RUN THE FOLLOWING LD FILES WITH SW B=2: IPAPWR.LD, BKPSL1.LD, AND BKPSL2.LD
4. FOR CI20 OR NIA20 BACKPANEL SIGNAL WIRELIST, RUN THE FOLLOWING LD FILES: IPAPWR.LD, IPASLT1.LD, AND IPASLT2.LD AND SET SW B=1 FOR CI20 AND B=2 FOR NIA20.

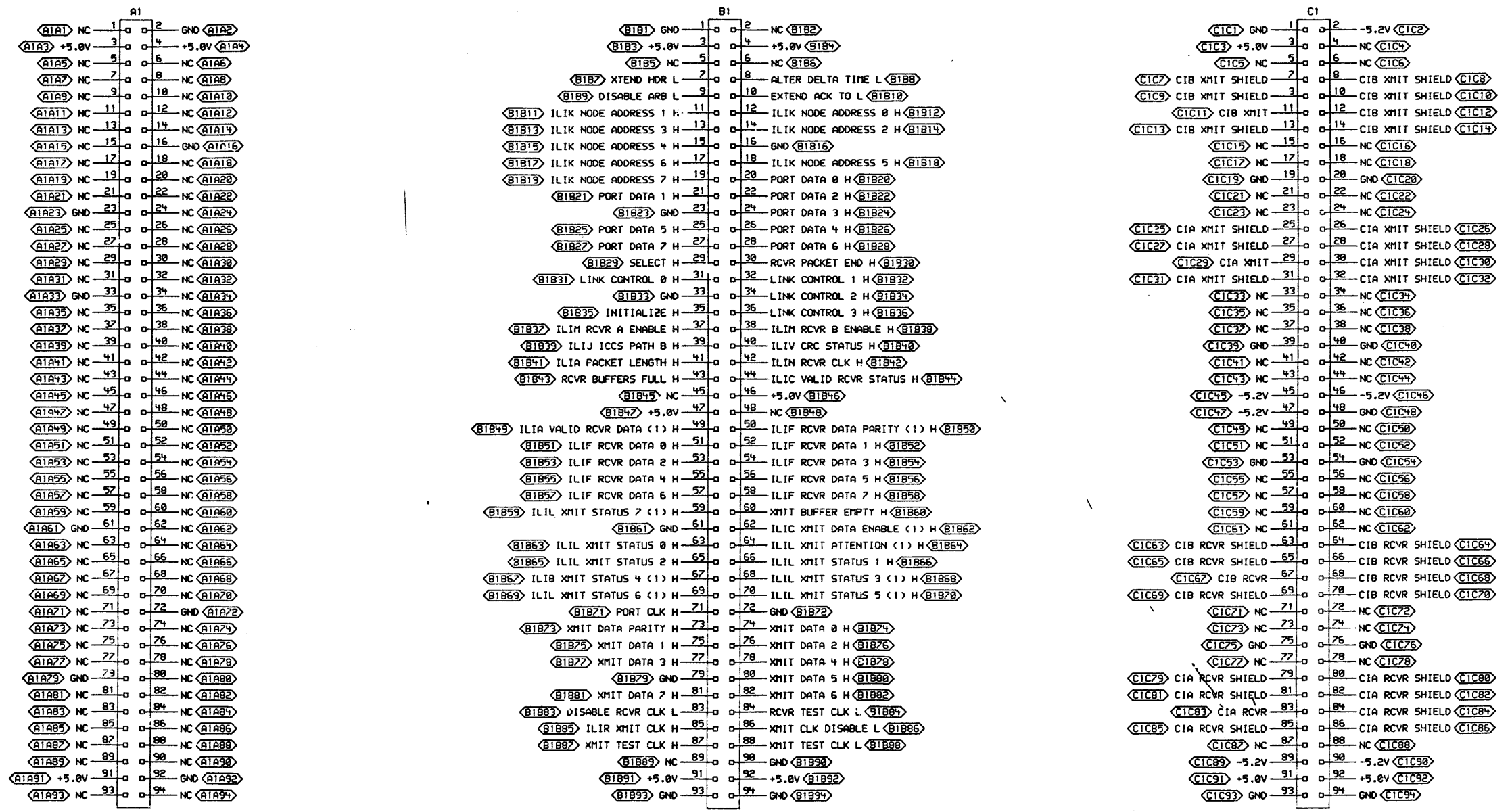


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REVISIONS	
CHK	CHANGE NO. REV.

digital	DRW. <i>R. J. Eichmann</i>	DATE 10-JUL-84	ENG. R. J. EICHMANN	DATE 10-JUL-84	TITLE: IPA20-L
	CHK'D D. DELLORCO	DATE 10-JUL-84	BOARD LOCATION:	SHEET 1 OF 5	BACKPLANE INT CONN
XTRA: (EICHMANN) IPAPWR.DRW 10-JUL-84 09:08 NEXT HIGHER ASSEMBLY: IPA20-L					SIZE CODE D CS
FIRST USED ON OPTION/MODEL: IPA20-L					NUMBER 5414793-0-1
					REV. A

L0100 (LINK) SIGNALS FOR SLOT 1
(CI20 ONLY)



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REVISIONS		
CHK	CHANGE NO.	REV.

	DATE 10-11-84	ENG. J. EICHMANN	DATE 10-11-84	TITLE: IPA20-L
	CHK'D DELLORCO	DATE 10-11-84	BOARD LOCATION: 2 OF 5	BACKPLANE INT CONN
XTRA: (EICHMANN) IPAS.T1.DRW 10-JUL-84 09:10		NEXT HIGHER ASSEMBLY: IPA20-L		SIZE CODE D CS
FIRST USED ON OPTION/MODEL: IPA20-L		NUMBER 5414793-0-1		REV. A

L0109 (PILA) SIGNALS FOR SLOT 2 (B FIELD #1)
L0072 (NIA) SIGNALS FOR SLOT 2 (B FIELD #2)

A2A1	NC	1	2	GND	A2A2
A2A3	+5.0V	3	4	+5.0V	A2A4
A2A5	NC	5	6	NC	A2A6
A2A7	NC	7	8	NC	A2A8
A2A9	NC	9	10	NC	A2A10
A2A11	NC	11	12	NC	A2A12
A2A13	GND	13	14	NC	A2A14
A2A15	NC	15	16	GND	A2A16
A2A17	NC	17	18	NC	A2A18
A2A19	NC	19	20	NC	A2A20
A2A21	NC	21	22	NC	A2A22
A2A23	GND	23	24	NC	A2A24
A2A25	NC	25	26	NC	A2A26
A2A27	NC	27	28	NC	A2A28
A2A29	NC	29	30	NC	A2A30
A2A31	NC	31	32	NC	A2A32
A2A33	GND	33	34	NC	A2A34
A2A35	NC	35	36	NC	A2A36
A2A37	NC	37	38	NC	A2A38
A2A39	NC	39	40	NC	A2A40
A2A41	NC	41	42	NC	A2A42
A2A43	GND	43	44	NC	A2A44
A2A45	NC	45	46	NC	A2A46
A2A47	NC	47	48	NC	A2A48
A2A49	NC	49	50	NC	A2A50
A2A51	GND	51	52	NC	A2A52
A2A53	NC	53	54	NC	A2A54
A2A55	NC	55	56	NC	A2A56
A2A57	NC	57	58	NC	A2A58
A2A59	NC	59	60	NC	A2A60
A2A61	GND	61	62	NC	A2A62
A2A63	NC	63	64	NC	A2A64
A2A65	NC	65	66	NC	A2A66
A2A67	NC	67	68	NC	A2A68
A2A69	NC	69	70	NC	A2A70
A2A71	NC	71	72	NC	A2A72
A2A73	NC	73	74	NC	A2A74
A2A75	NC	75	76	NC	A2A76
A2A77	NC	77	78	NC	A2A78
A2A79	GND	79	80	GND	A2A80
A2A81	NC	81	82	GND	A2A82
A2A83	NC	83	84	NC	A2A84
A2A85	NC	85	86	[B,NC,XCVR NEG]	A2A86
A2A87	NC	87	88	NC	A2A88
A2A89	NC	89	90	[B,NC,XCVR POS]	A2A90
A2A91	+5.0V	91	92	GND	A2A92
A2A93	NC	93	94	NC	A2A94

B2B1	GND	1	2	NC	B2B2
B2B3	+5.0V	3	4	+5.0V	B2B4
B2B5	NC	5	6	NC	B2B6
B2B7	[B,RCVR BUFFERS FULL H,NC]	7	8	NC	B2B8
B2B9	[B,DISABLE ARB L,NC]	9	10	[B,ILIK NODE ADDRESS 3 H,NC]	B2B10
B2B11	[B,ILIK NODE ADDRESS 1 H,NC]	11	12	[B,ILIK NODE ADDRESS 0 H,NC]	B2B12
B2B13	[B,NC,GND]	13	14	[B,ILIK NODE ADDRESS 2 H,NC]	B2B14
B2B15	[B,ILIK NODE ADDRESS 4 H,NC]	15	16	GND	B2B16
B2B17	[B,ILIK NODE ADDRESS 6 H,NC]	17	18	[B,ILIK NODE ADDRESS 5 H,NC]	B2B18
B2B19	[B,ILIK NODE ADDRESS 7 H,NC]	19	20	PORT DATA 0 H	B2B20
B2B21	PORT DATA 1 H	21	22	PORT DATA 2 H	B2B22
B2B23	GND	23	24	PORT DATA 3 H	B2B24
B2B25	PORT DATA 5 H	25	26	PORT DATA 4 H	B2B26
B2B27	PORT DATA 7 H	27	28	PORT DATA 6 H	B2B28
B2B29	[B,SELECT H,NC]	29	30	[B,RCVR PACKET END H,NC]	B2B30
B2B31	LINK CONTROL 0 H	31	32	LINK CONTROL 1 H	B2B32
B2B33	GND	33	34	LINK CONTROL 2 H	B2B34
B2B35	INITIALIZE H	35	36	LINK CONTROL 3 H	B2B36
B2B37	[B,ILIM RCVR A ENABLE H,NC]	37	38	[B,ILIM RCVR B ENABLE H,NC]	B2B38
B2B39	[B,ILIJ ICCS PATH B H,NC]	39	40	[B,ILIV CRC STATUS H,NC]	B2B40
B2B41	NC	41	42	[B,ILIM RCVR CLK H,NC]	B2B42
B2B43	[B,NC,GND]	43	44	[B,ILIC VALID RCVR STATUS H,NC]	B2B44
B2B45	NC	45	46	+5.0V	B2B46
B2B47	+5.0V	47	48	[B,ILIF RCVR DATA 0 H,NC]	B2B48
B2B49	[B,ILIA VALID RCVR DATA (1) H,NC]	49	50	[B,ILIF RCVR DATA PARITY (1) H,NC]	B2B50
B2B51	[B,NC,GND]	51	52	[B,ILIF RCVR DATA 1 H,NC]	B2B52
B2B53	[B,ILIF RCVR DATA 2 H,NC]	53	54	[B,ILIF RCVR DATA 3 H,NC]	B2B54
B2B55	[B,ILIF RCVR DATA 4 H,NC]	55	56	[B,ILIF RCVR DATA 5 H,NC]	B2B56
B2B57	[B,ILIF RCVR DATA 6 H,NC]	57	58	[B,ILIF RCVR DATA 7 H,NC]	B2B58
B2B59	[B,ILIL XMIT STATUS 7 (1) H,NC]	59	60	[B,XMIT BUFFER EMPTY H,NC]	B2B60
B2B61	GND	61	62	[B,ILIC XMIT DATA ENABLE (1) H,NC]	B2B62
B2B63	[B,ILIL XMIT STATUS 0 H,NC]	63	64	[B,ILIL XMIT ATTENTION (1) H,NC]	B2B64
B2B65	[B,ILIL XMIT STATUS 2 H,NC]	65	66	[B,ILIL XMIT STATUS 1 H,NC]	B2B66
B2B67	[B,ILIL XMIT STATUS 4 (1) H,NC]	67	68	[B,ILIL XMIT STATUS 3 (1) H,NC]	B2B68
B2B69	[B,ILIL XMIT STATUS 6 (1) H,NC]	69	70	[B,ILIL XMIT STATUS 5 (1) H,NC]	B2B70
B2B71	[B,PORT CLK H,NC]	71	72	GND	B2B72
B2B73	[B,XMIT DATA PARITY H,NC]	73	74	[B,XMIT DATA 0 H,NC]	B2B74
B2B75	[B,XMIT DATA 1 H,NC]	75	76	[B,XMIT DATA 2 H,NC]	B2B76
B2B77	[B,XMIT DATA 3 H,NC]	77	78	[B,XMIT DATA 4 H,NC]	B2B78
B2B79	GND	79	80	[B,XMIT DATA 5 H,NC]	B2B80
B2B81	[B,XMIT DATA 7 H,NC]	81	82	[B,XMIT DATA 6 H,NC]	B2B82
B2B83	NC	83	84	NC	B2B84
B2B85	[B,ILIR XMIT CLK H,NC]	85	86	NC	B2B86
B2B87	NC	87	88	NC	B2B88
B2B89	NC	89	90	GND	B2B90
B2B91	+5.0V	91	92	+5.0V	B2B92
B2B93	GND	93	94	GND	B2B94

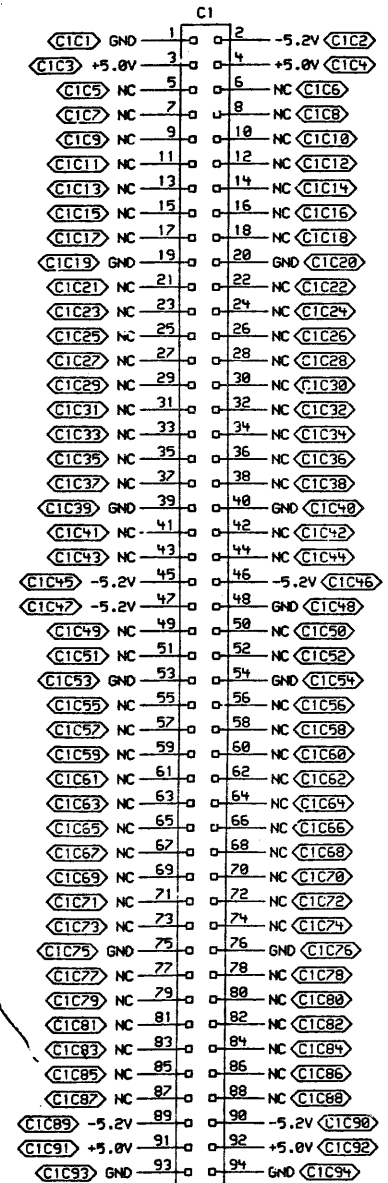
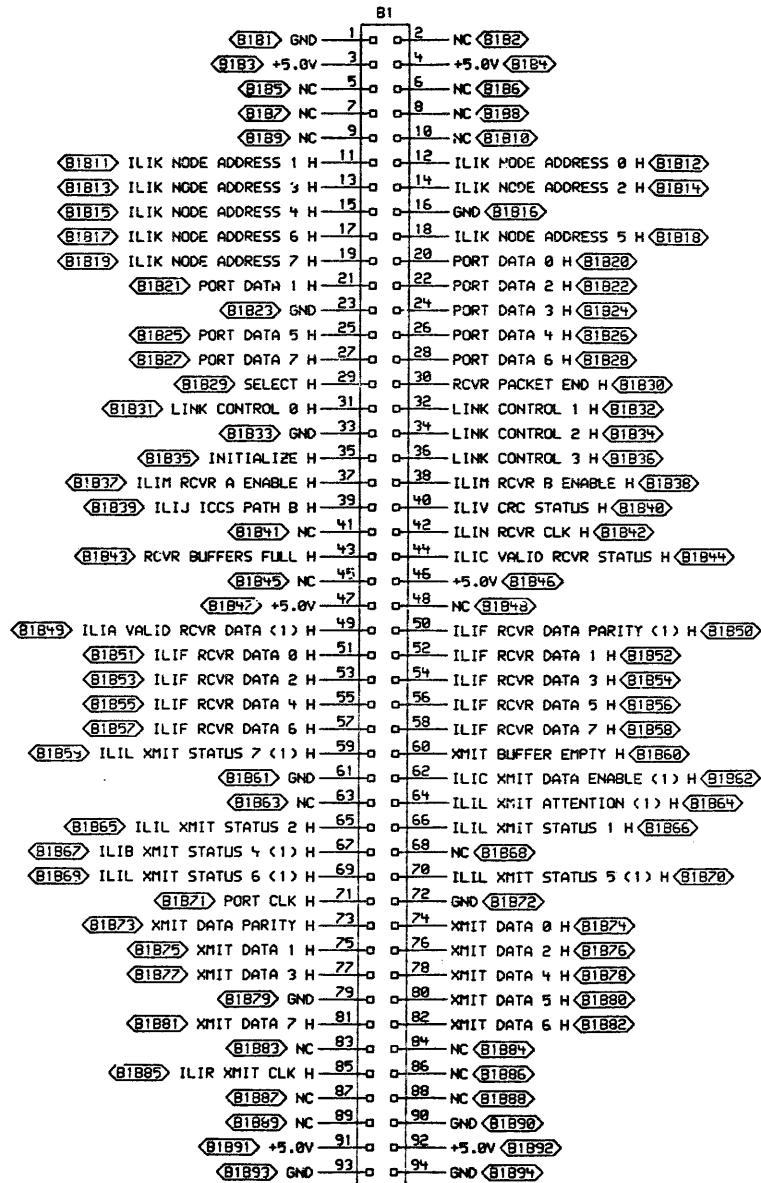
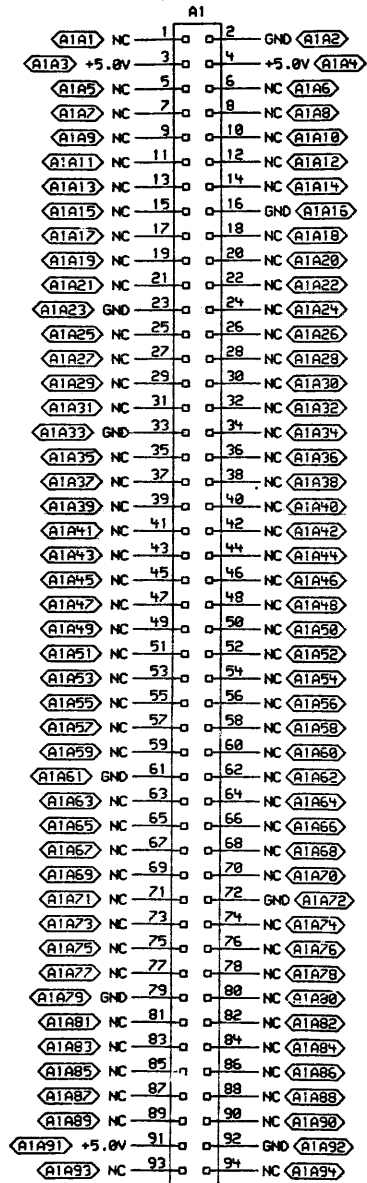
C2C1	GND	1	2	[B,NC,-5.2V]	C2C2
C2C3	+5.0V	3	4	[B,+5.0V,NC]	C2C4
C2C5	PLI INITIALIZE H	5	6	PLI SELECT L	C2C6
C2C7	PLI LINK CONTROL 0 H	7	8	PLI LINK CONTROL 1 H	C2C8
C2C9	PLI LINK CONTROL 2 H	9	10	PLI LINK CONTROL 3 H	C2C10
C2C11	PLI DATA 0 H	11	12	PLI DATA 1 H	C2C12
C2C13	[B,NC,GND]	13	14	PLI DATA 3 H	C2C14
C2C15	PLI DATA 4 H	15	16	GND	C2C16
C2C17	PLI DATA 6 H	17	18	PLI DATA 7 H	C2C18
C2C19	PLI TRAN DATA PAR H	19	20	PLI RCVR DATA PAR H	C2C20
C2C21	PLI DATA 5 H	21	22	PLI DATA 2 H	C2C22
C2C23	GND	23	24	PLI CLOCK H	C2C24
C2C25	BUFFER B BUS H	25	26	PLI XMIT ATTN H	C2C26
C2C27	PLI RCVR BUF A FULL H	27	28	PLI RCVR BUF B FULL H	C2C28
C2C29	[B,DISABLE ARB L,NC]	29	30	NC	C2C30
C2C31	[B,FIRST BUFFER H,NC]	31	32	BUFFER A BUS H	C2C32
C2C33	GND	33	34	[B,RCVR B ENABLE H,NC]	C2C34
C2C35	[B,CRC ERROR H,PLI CRC ERR H]	35	36	[B,RCVR A ENABLE H,NC]	C2C36
C2C37	XMIT BUSY H	37	38	ACK H	C2C38
C2C39	[B,DEST BUSY H,NC]	39	40	[B,ARBITRATE H,NC]	C2C40
C2C41	XMIT ABORT H	41	42	CDR H	C2C42
C2C43	[B,NC,GND]	43	44	XBUF PE H	C2C44
C2C45	[B,NC,-5.2V]	45	46	[B,NC,-5.2V]	C2C46
C2C47	[B,NC,-5.2V]	47	48	GND	C2C48
C2C49	NC	49	50	NC	C2C50
C2C51	CDR H	51	52	NC	C2C52
C2C53	NC	53	54	[B,NC,RX DATA IN 0 H]	C2C54
C2C55	NC	55	56	[B,NC,-RX DATA IN 0 H]	C2C56
C2C57	NC	57	58	NC	C2C58
C2C59	[B,NC,DET 0 H]	59	60	[B,NC,CLSN DET 0 H]	C2C60
C2C61	GND	61	62	[B,NC,-CLSN DET 0 H]	C2C62
C2C63	NC	63	64	[B,NC,XCVR NEG]	C2C64
C2C65	[B,NC,XCVR POS]	65	66	[B,NC,TX DATA OUT 0 H]	C2C66
C2C67	[B,NC,XCVR NEG]	67	68	[B,NC,-TX DATA OUT 0 H]	C2C68
C2C69	NC	69	70	NC	C2C70
C2C71	NC	71	72	GND	C2C72
C2C73	NC	73	74	NC	C2C74
C2C75	NC	75	76	NC	C2C76
C2C77	NC	77	78	NC	C2C78
C2C79	GND	79	80	NC	C2C80
C2C81	NC	81	82	NC	C2C82
C2C83	NC	83	84	NC	C2C84
C2C85	[B,NC,GND]	85	86	NC	C2C86
C2C87	[B,NC,GND]	87	88	NC	C2C88
C2C89	[B,NC,-5.2V]	89	90	[B,NC,-5.2V]	C2C90
C2C91	+5.0V	91	92	+5.0V	C2C92
C2C93	GND	93	94	GND	C2C94

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	18-JUL-84	ENG.	R.J.EICHMANN	DATE	18-JUL-84	TITLE:	IPAZ0-L			
	CHK'D	D. DELLORCO	DATE	18-JUL-84	SHEET	3	OF	5			
XTRA: EICHMANN/IPAZL2.DRW110-JUL-84 09:11 NEXT HIGHER ASSEMBLY:								SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL: IPAZ0-L								D	CS	5414793-0-1	A

IPA20-L SLOT 1 BACKPANEL CONNECTIONS

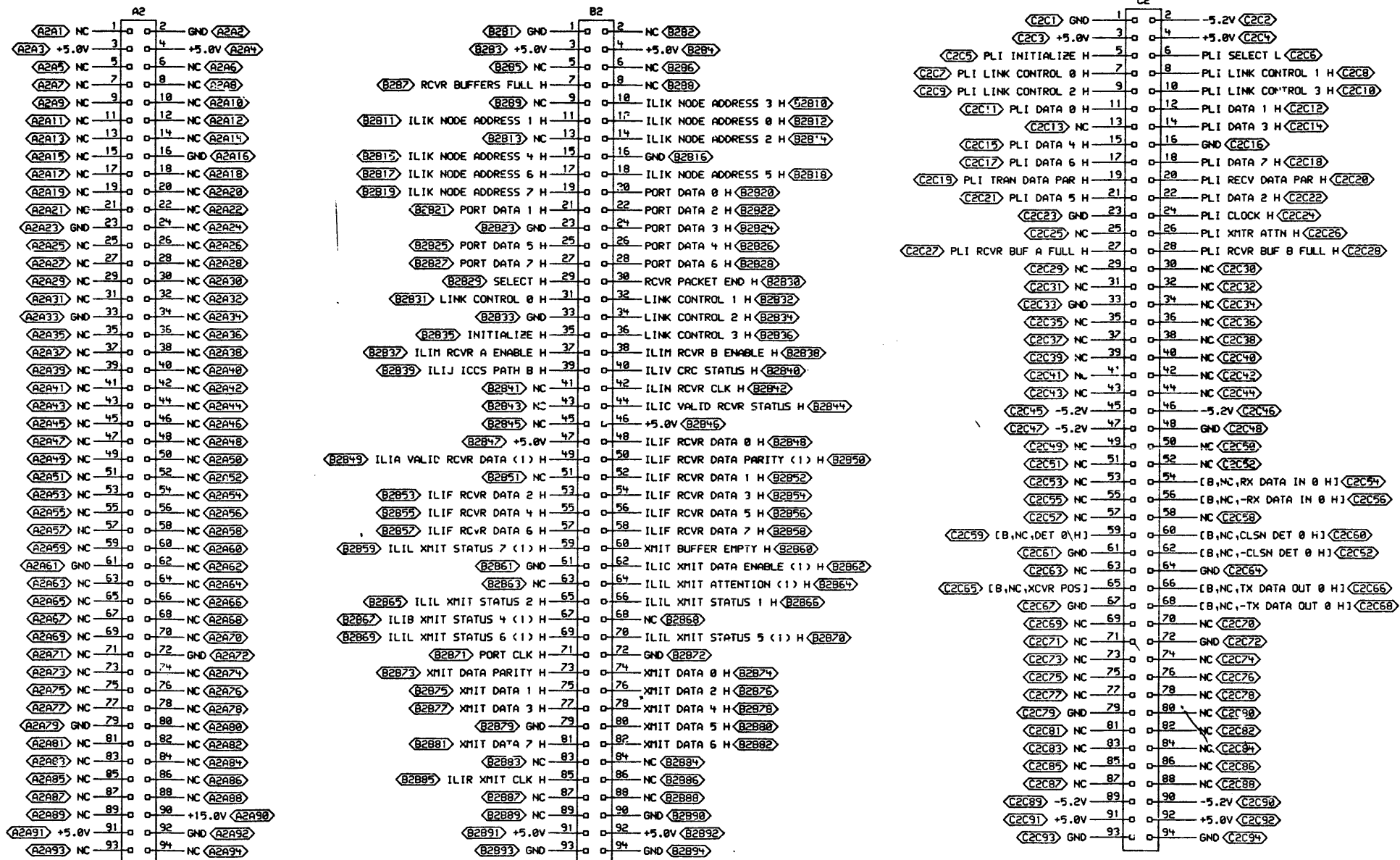


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REVISIONS	
CHK	CHANGE NO. REV

	DRN: <i>R. J. Eichmann</i> CHK'D: <i>D. Dellorco</i>	DATE: 10-JUL-84 DATE: 10-JUL-84	ENG.: R. J. EICHMANN BOARD LOCATION: 4 OF 5	DATE: 10-JUL-84 SHEET: 4 OF 5	TITLE: IPA20-L BACKPLANE INT CONN
	XTRAC(EICHMANN)SKPS1.DRW 110-JUL-84 09:12 NEXT HIGHER ASSEMBLY:	FIRST USED ON OPTION/MODEL: IPA20-L IPA20-L	SIZE CODE: D CS	NUMBER: 5414793-0-1	REV.: A

IPA20-L SLOT 2 BACKPANEL CONNECTIONS



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REVISIONS	
CHK	CHANGE NO. / REV

digital DRN: *J. J. EICHMANN*
 DATE: 10-JUL-84
 CHK'D: D. DELLORCO
 DATE: 10-JUL-84
 SHEET: 5 OF 5

DATE: 10-JUL-84	ENG: R. J. EICHMANN	DATE: 10-JUL-84	TITLE: IPA20-L
DATE: 10-JUL-84	BOARD LOCATION: 5	DATE: 10-JUL-84	BACKPLANE INT CONN
DATE: 10-JUL-84	DATE: 10-JUL-84	DATE: 10-JUL-84	SIZE CODE: D CS
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DATE: 10-JUL-84	DATE: 10-JUL-84	DATE: 10-JUL-84	REV: A

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DRAWING NUMBER	PAGE	PART NO.	DESCRIPTION	REVISIONS
			ECO NUMBER	-
E-MD-5014792-0-0	5	5014792-00	ETCHED CIRCUIT BOARD	D
E-EC-5014792-0-0	2		DRILL AND ETCH DRAWING	C
			ETCH CUT DRAWING	D

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REVISIONS		
CHK	CHANGE NO.	REV

digital
FIRST USED ON OPTION/MODEL:

DRN E. WILSON	DATE 80-11-07	ENG. R. Goshorn	DATE 80-11-07	TITLE DRAWING DIRECTORY
CHK'D W. Pierce	DATE 80-11-07	BOARD LOCATION N/A	SHEET 1	NUMBER 5014792
DSX114792.12P14.261	80-JUN-84 11:51	NEXT HIGHER ASSEMBLY: D-DD-5414793-0	SIZE D	CODE DD

SIZE	CODE	NUMBER	REV.
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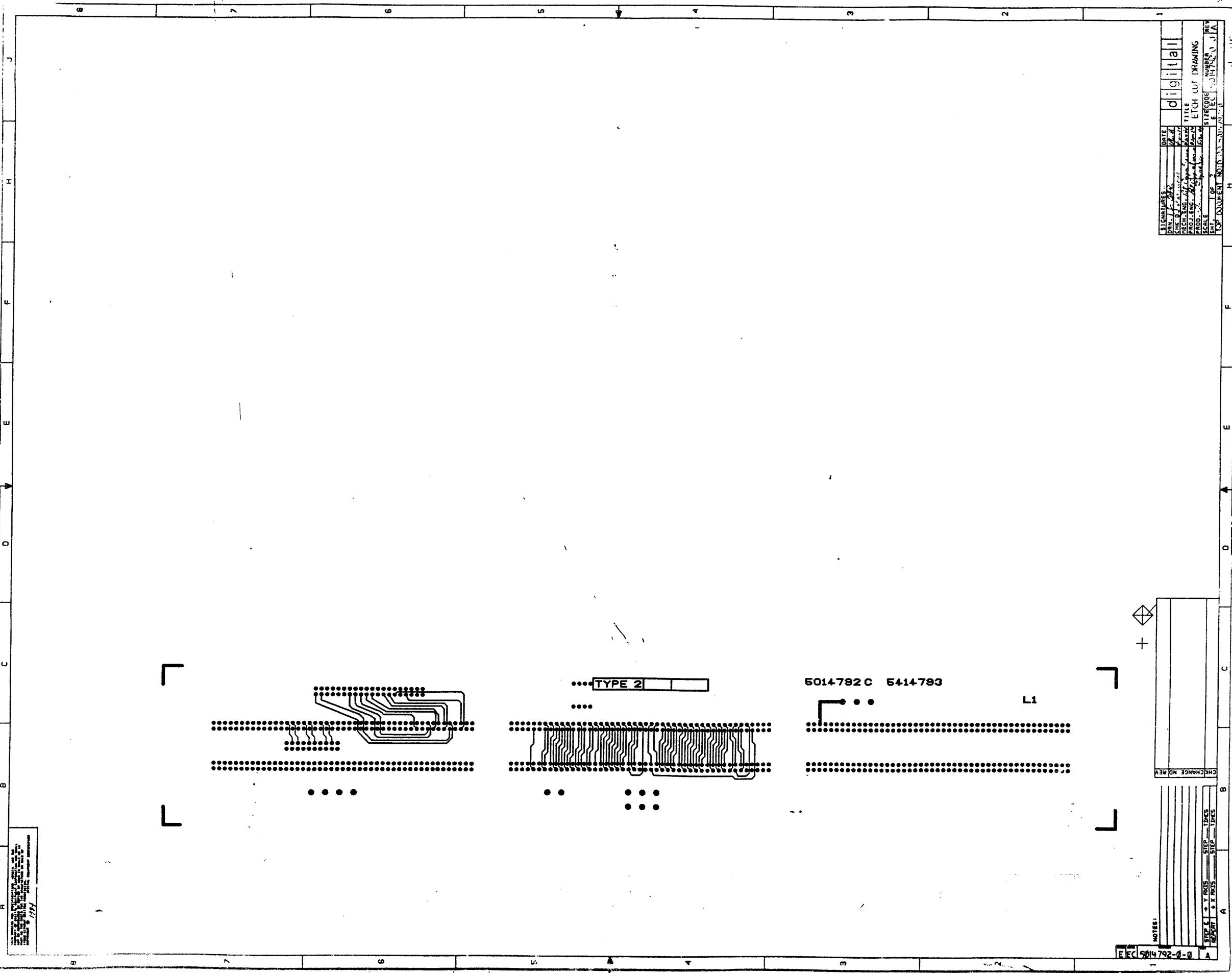
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REV. A

SIZE D
CODE DD
NUMBER 5014792-0



ALL DIMENSIONS UNLESS OTHERWISE SPECIFIED ARE IN INCHES
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 DATE 01/24/92

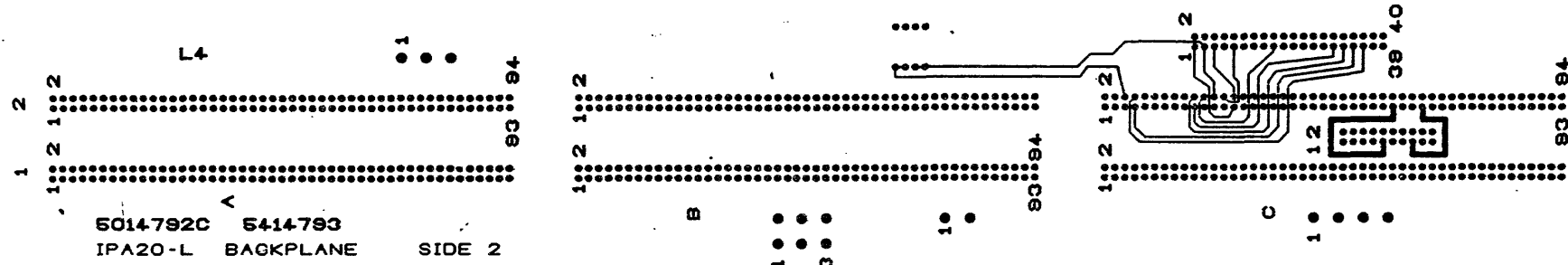
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PROJ. ENG.		ETCH CUT DRAWING
PROD. MGR.		STAGECODE
SCALE	1 OF 2	EC NUMBER
BY	DAVID R. BOYD	REV

CHG	NO	DATE	BY	REASON

STEP	DESCRIPTION	DATE
1	DESIGN	01/24/92
2	ETCH	01/24/92
3	PLATING	01/24/92
4	DRILL	01/24/92
5	FINISH	01/24/92

NOTES:
 EEC 5014792-0-0

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5014792C 5414793
IPA20-L BACKPLANE SIDE 2

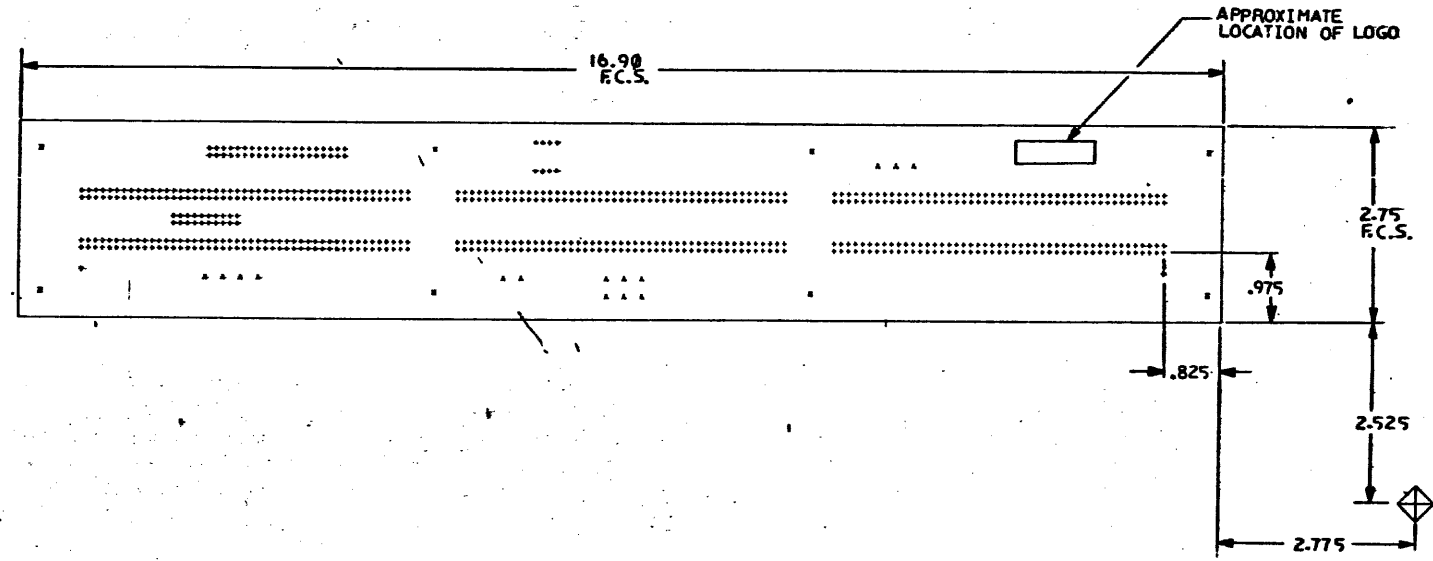
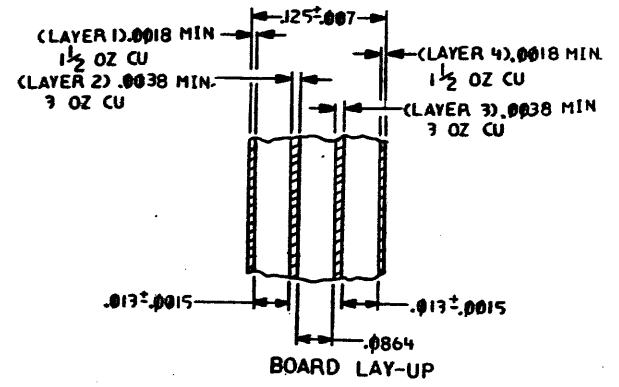
REVISIONS		
NO.	CHANGE NO.	REV.

TITLE	ETCH CUT DRAWING	NO. 5014792-0	REV. A
SCALE	2/1	SHEET 2 OF 2	DATE

5014792-0 A

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VIEWS FROM SIDE 1
COMPONENT SIDE



FOR HOLE TOLERANCES USE DEC STD 176									
SYMBOL	+	A	X	+	Z	M	A	□	*
FIN HOLE SIZE	.040	.067	.187	.125	.171				
PLATED	X	X							
NON PLATED			X	X	X				
DIY	632	15	4	2	4				
○=OFF GRID HOLE	□	□	□	□	□				
DRILL SIZE									

NOTE: ALL HOLE LOCATIONS ARE DESIGNED ON .025 GRID INCREMENTS FROM DATUM UNLESS SYMBOL IS CIRCLED.

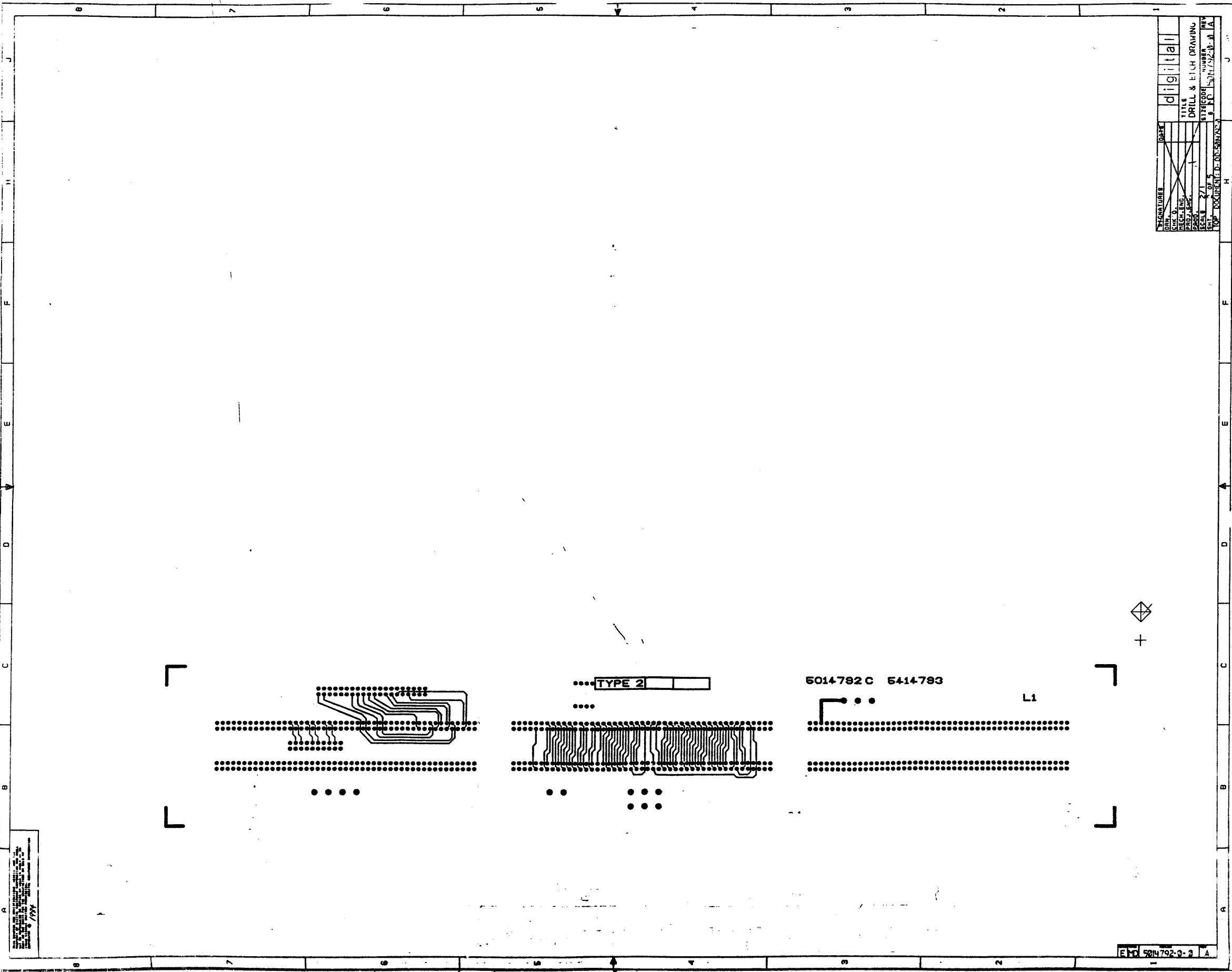
DESIGN INFORMATION	
CIRCUIT SIZE: X 16.90 Y 2.75 INCHES	
CIRCUIT TYPE: PRED PTH ML	
CIRCUIT HOISKISQ	
TECHNOLOGY: SOD HD12X13LY HD20 OTHER MSL	
UL REQUIREMENTS LPMR □ HPMR □	
CIRCUIT OUTLINE & FINGER DETAIL □ AS SHOWN	
LAYER CONSTRUCTION PER □ AS SHOWN	
ARTWORK LAYOUT: MANUAL □ CAD	
ENG SPECIAL FEATURES:	
SPECIAL NOTES:	

FABRICATION INFORMATION	
FABRICATE BOARD PER □ AS SHOWN	
SOLDER MASK, SIDE 1 □ SIDE 2 □ NONE	
SPECIFICATIONS AND STANDARDS:	
MATERIALS AND WORKMANSHIP FOR ALL FABRICATED PRINTED WIRING BOARDS MUST MEET OR EXCEED THE REQUIREMENTS OF DEC STD 176	
SPECIAL NOTES:	

TOLERANCES	
INCHES UNLESS SPECIFIED	
.XXX ± .010 ANGLES	
.XX ± .020 45 DEG 30 MIN	
.X ± .100	

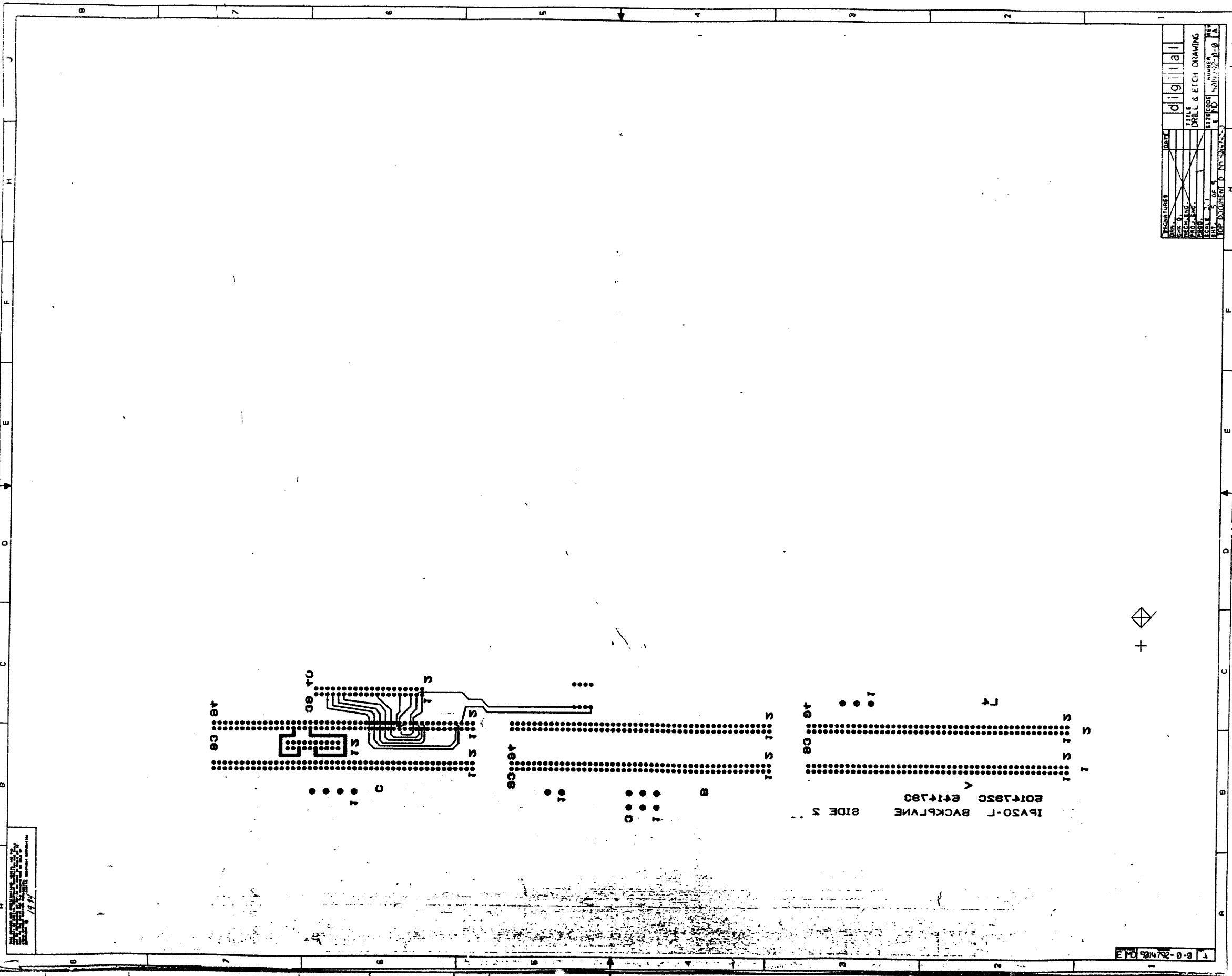
SIGNATURES	DATE
DRN: <i>H. DeG...</i>	6/14/83
CHK'D: <i>R. J. ...</i>	6/14/83
MECH ENG: <i>R. J. ...</i>	6/14/83
PROJ ENG: <i>R. J. ...</i>	6/14/83
MFG ENG: <i>...</i>	6/14/83

digital
TITLE: DRILL & ETCH DRAWING
SCALE: 1/1
SHEET 1 OF 5
TOP DOCUMENT NO: D-00-5014792-0
SIZE CODE NUMBER
0 MD5014792-0-01A
ETCH REV: C



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PROJ. ENG.		DRILL & EICH DRAWING
PROD.		STAGE CODE
SAY	271	NUMBER
	OF 5	REV
		TOP DOCUMENT NO. 5014792-0-2



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DESIGNER	DATE	digital
CHECKED		
APPROVED		
DATE		
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SCALE		
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TOP EXHAUST D. NO. 504732-0-0		REV
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