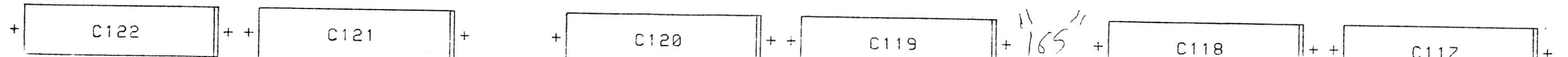
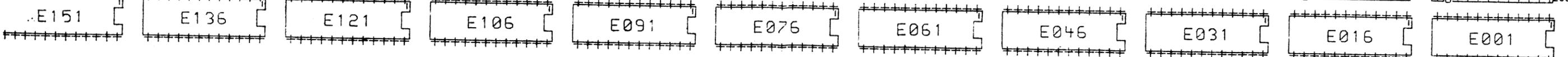
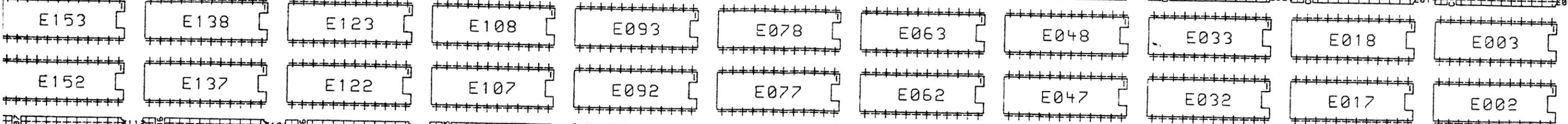
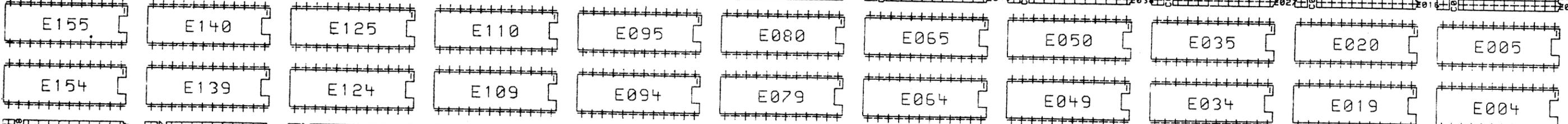
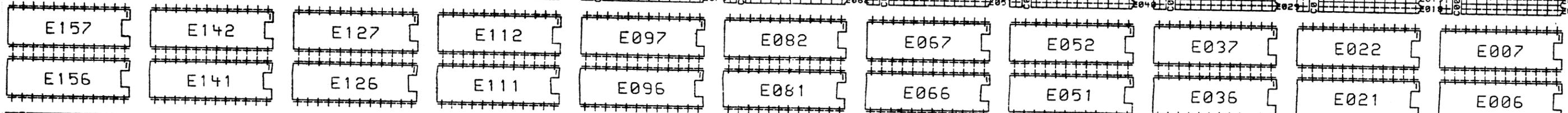
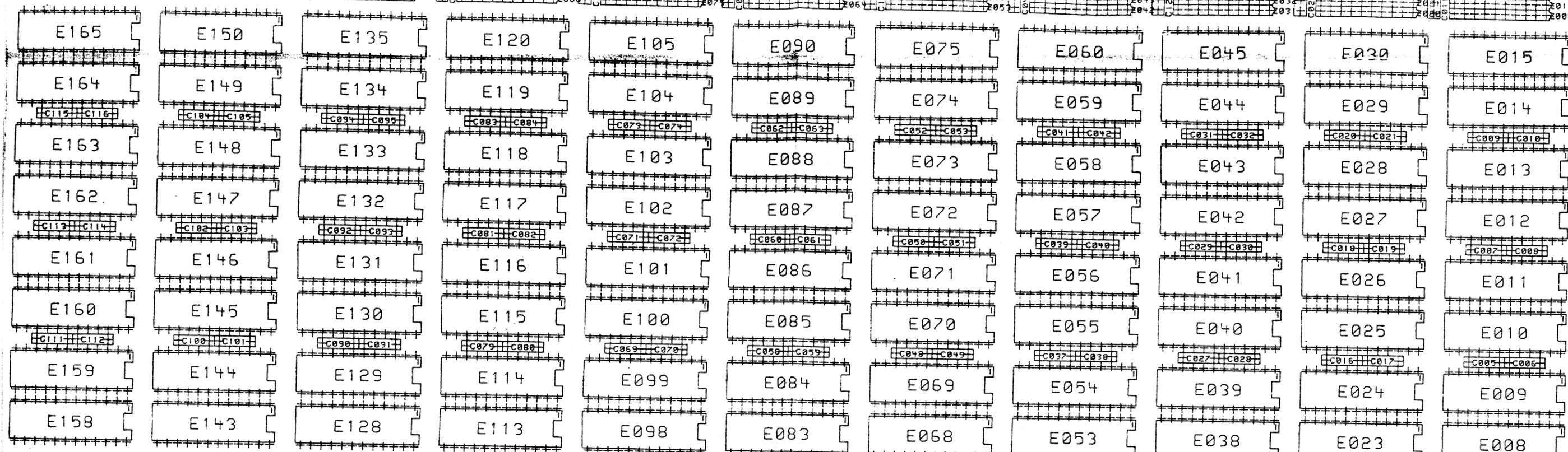
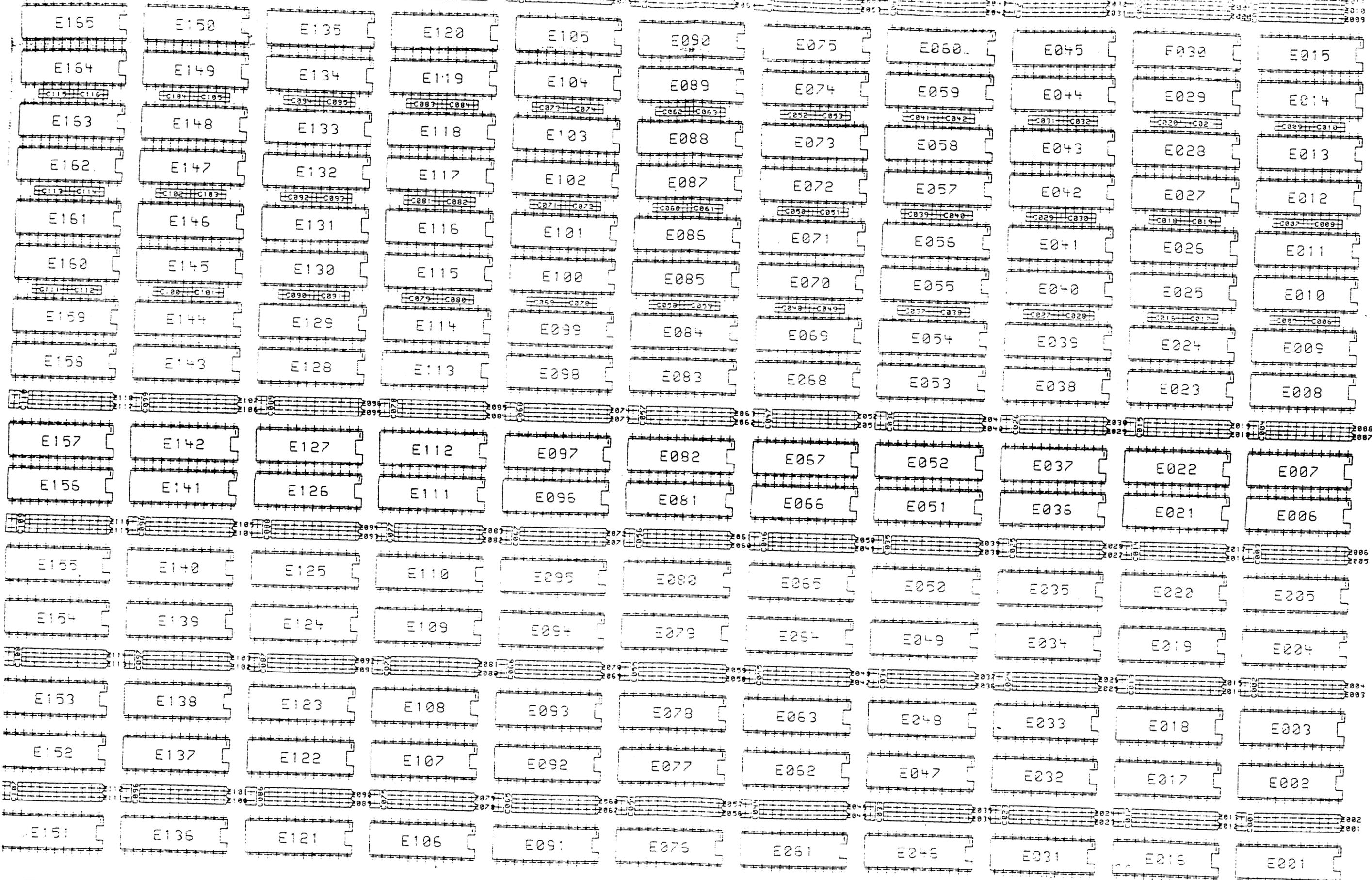


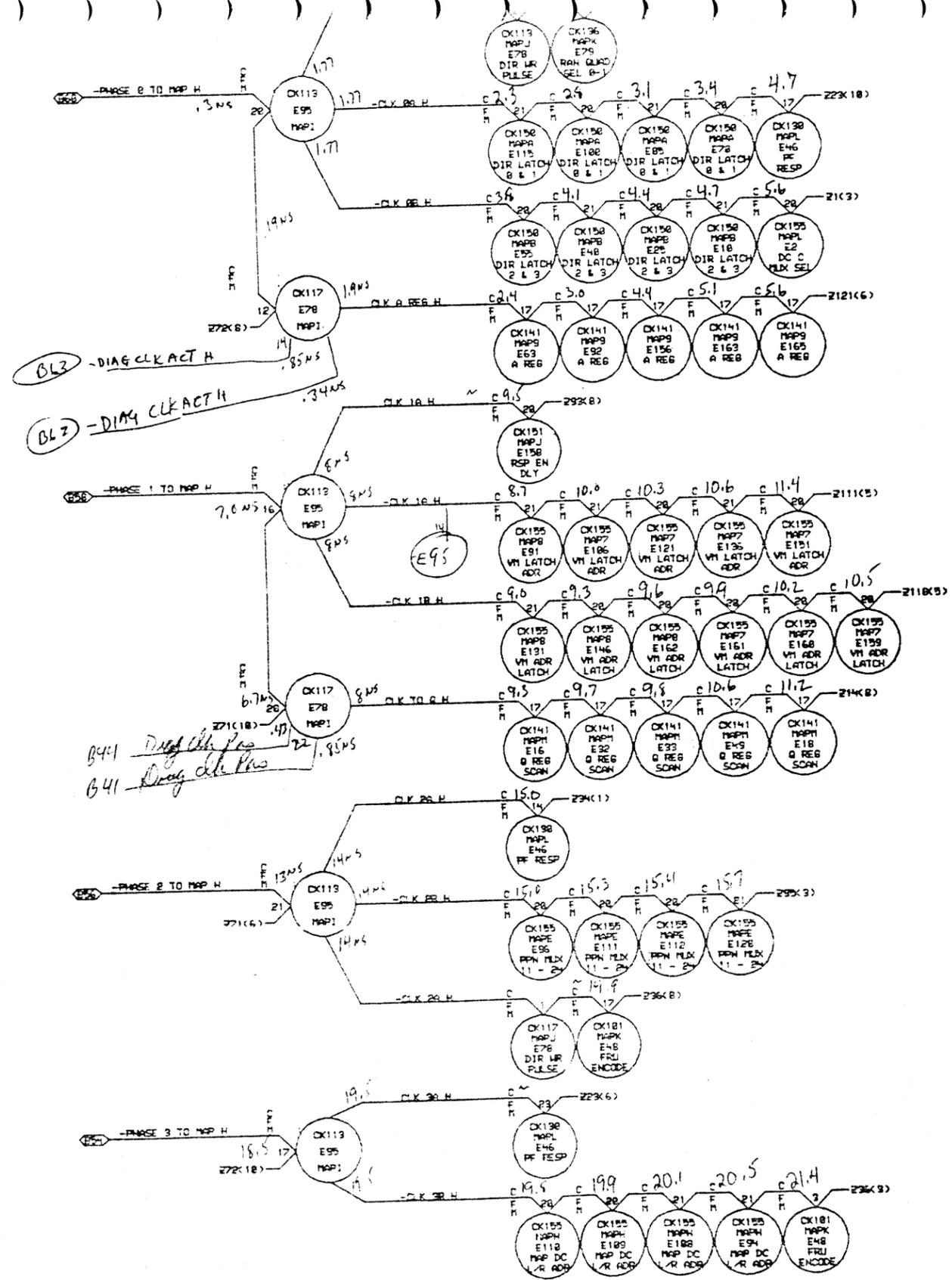
VULCAN:<2080.L0057>24MAP1  
VULCAN:<2080.L0057>24MAP2  
VULCAN:<2080.L0057>24MAP3  
VULCAN:<2080.L0057>24MAP4  
VULCAN:<2080.L0057>43MAP5  
VULCAN:<2080.L0057>24MAP6  
VULCAN:<2080.L0057>54MAP7  
VULCAN:<2080.L0057>24MAP8  
VULCAN:<2080.L0057>24MAP9  
VULCAN:<2080.L0057>24MAPA  
VULCAN:<2080.L0057>24MAPB  
VULCAN:<2080.L0057>53MAPC  
VULCAN:<2080.L0057>54MAPD  
VULCAN:<2080.L0057>24MAPE  
VULCAN:<2080.L0057>24MAPF  
VULCAN:<2080.L0057>24MAPG  
VULCAN:<2080.L0057>51MAPH  
VULCAN:<2080.L0057>52MAPI  
VULCAN:<2080.L0057>50MAPJ  
VULCAN:<2080.L0057>49MAPK  
VULCAN:<2080.L0057>53MAPL  
VULCAN:<2080.L0057>47MAPM  
VULCAN:<2080.L0057>54MAPN





MAP B

Pick  
 Here is the little  
 I was able to  
 pull together  
 Rocky

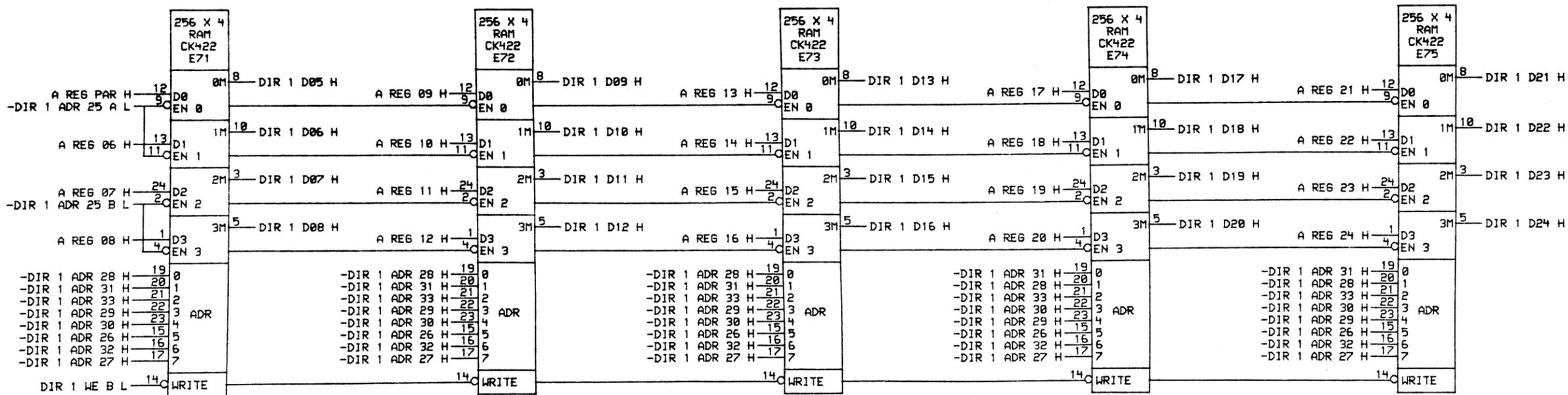
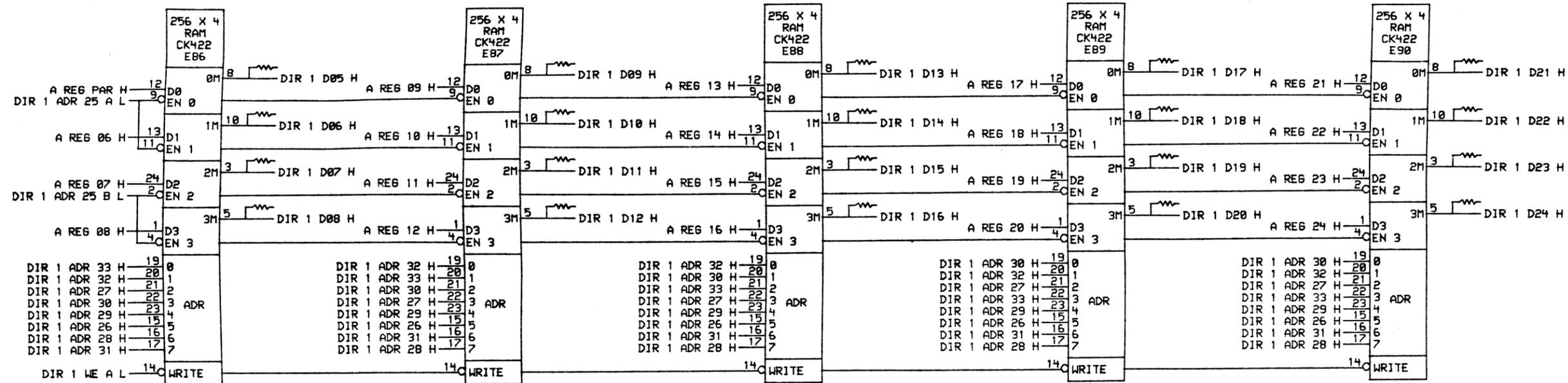


NEED CLK 1 AH at E158  
 CLK 3 AH at E46  
 CLK 2 AL at E78, E48

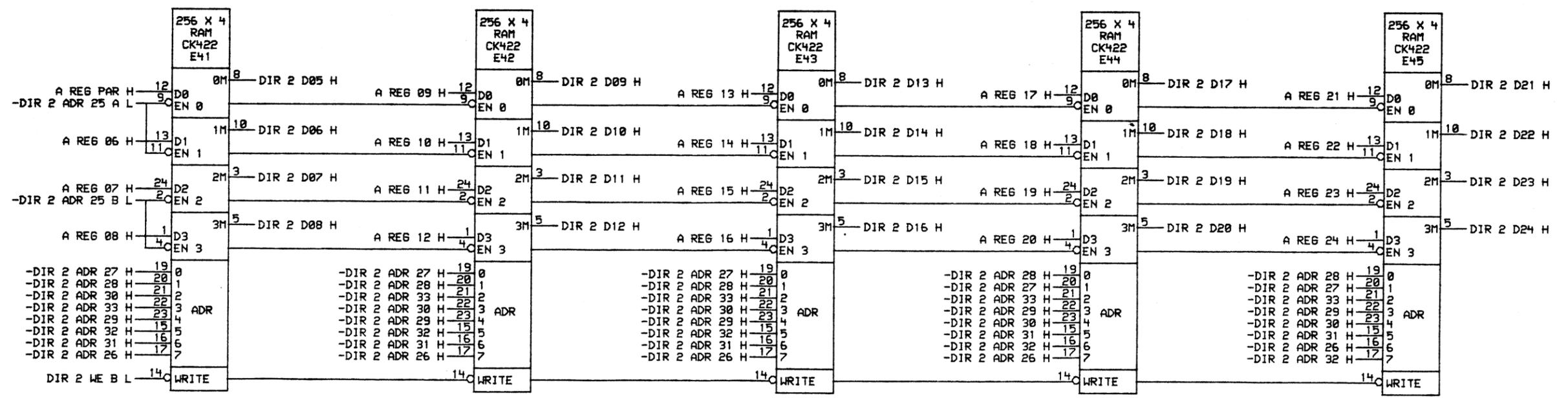
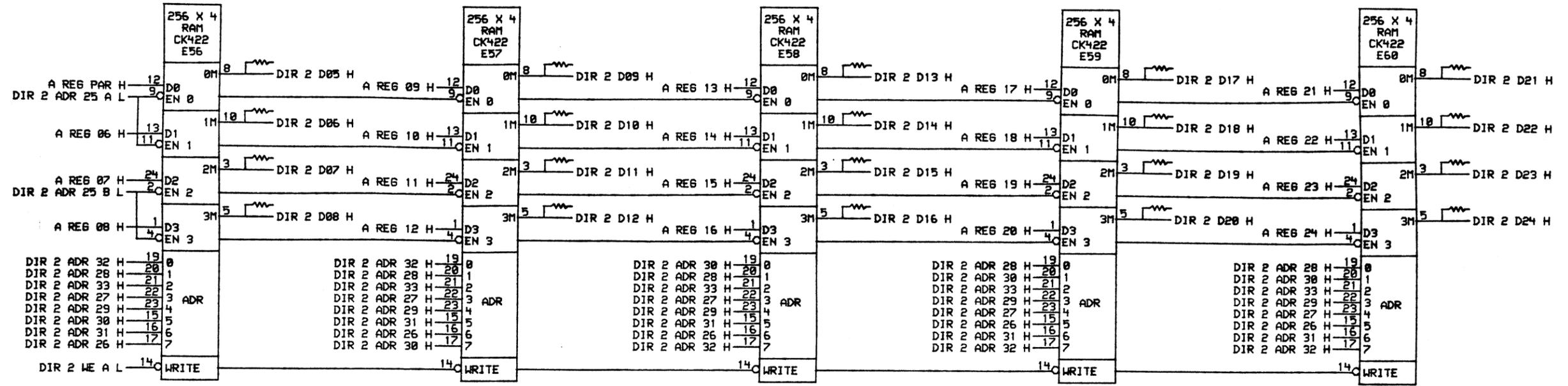
DRN.
CHK'D.
EXTRA: (ROCK BLOCKS) MAP 29, DRU 115 - JJA - B1
FIRST USED ON OPTION/MODEL:

FILE ENG.	DATE	TITLE: MAP
DESIGN LOCATION:		
REV. 05	NUMBER	REV.

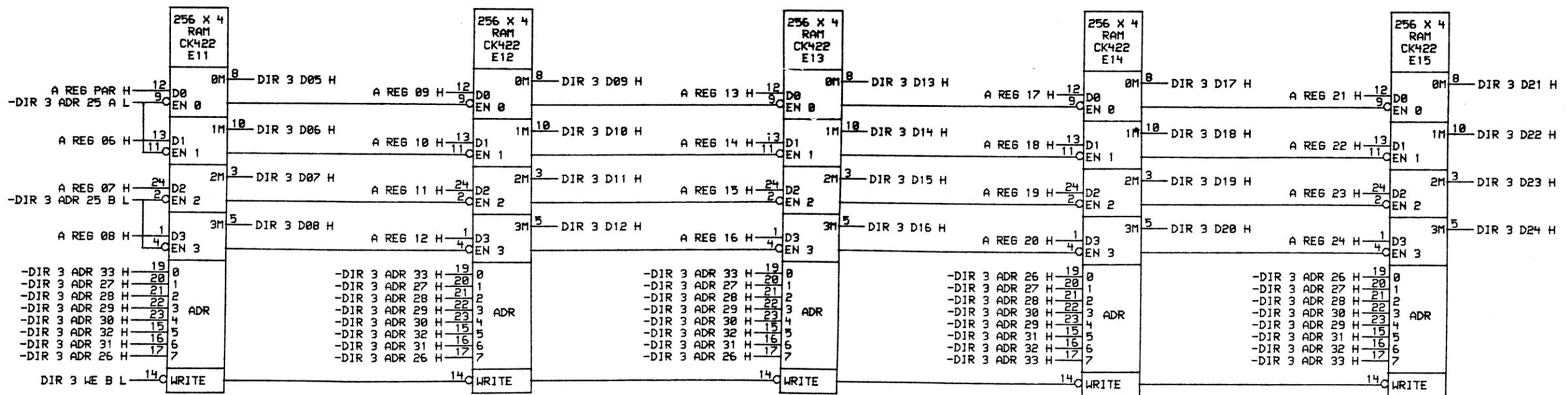
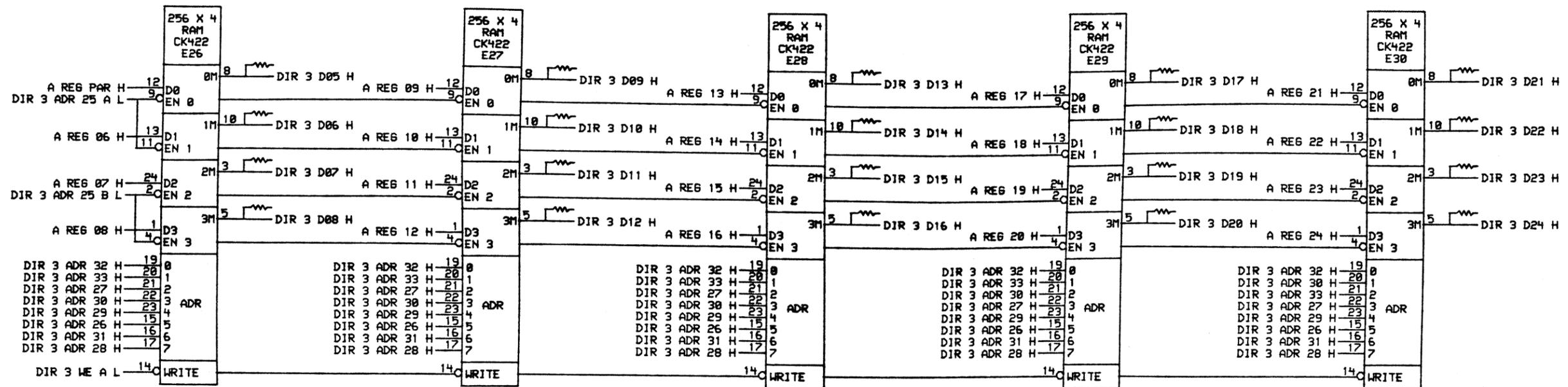




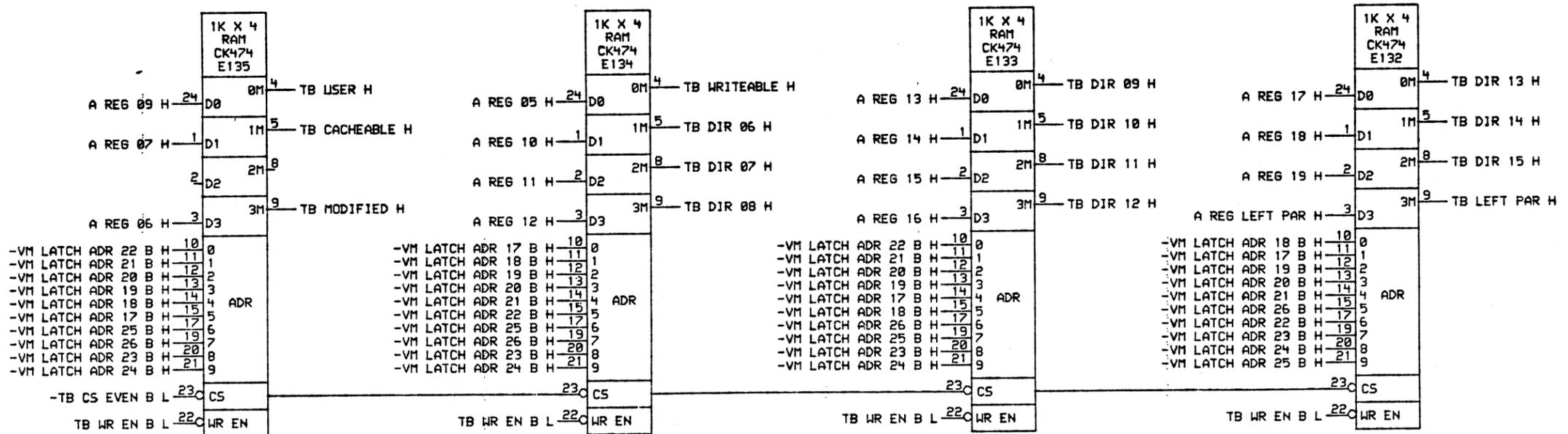
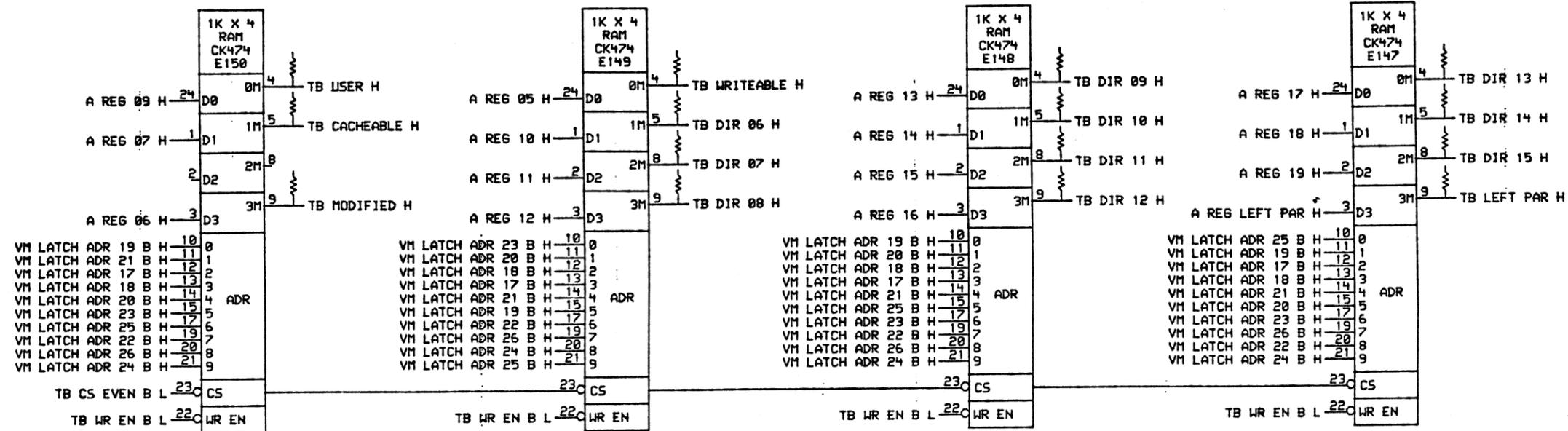
DRN.	DATE	ENG.	DATE	TITLE: CACHE DIRECTORY 1
CHK'D.	01-FEB-82			00-23
VULCAN (2000-L0057) 2-MAP2.DRW		09-MAR-81 09:02		NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: 2000		B-DD-L0057-0		SHEET 1 OF 1
SIZE	CODE	NUMBER	REV.	
D	CS	L0057-0-MAP2		



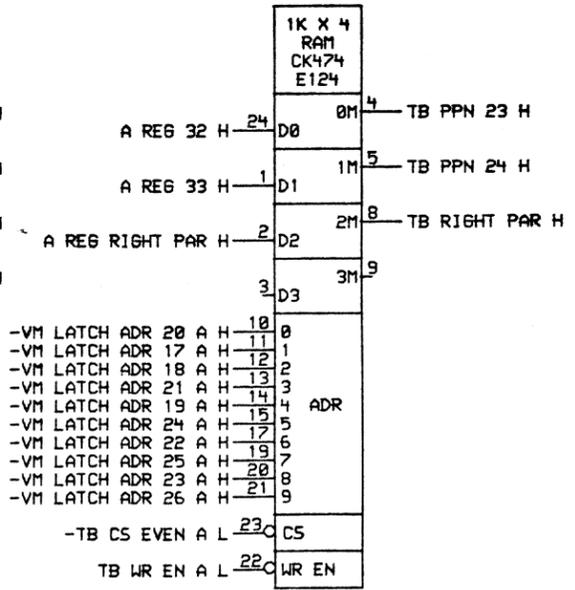
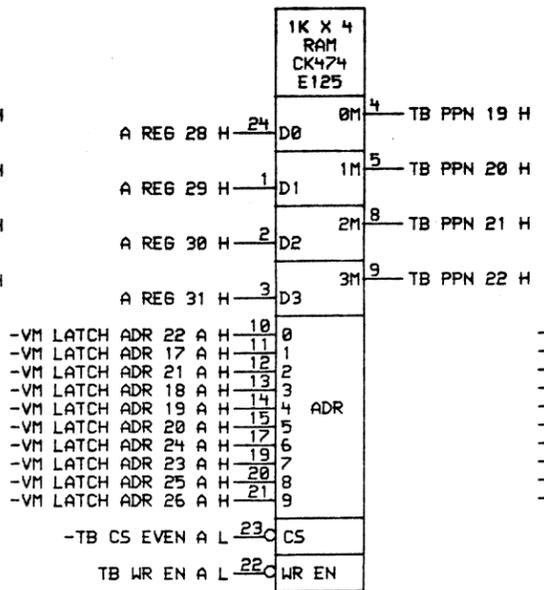
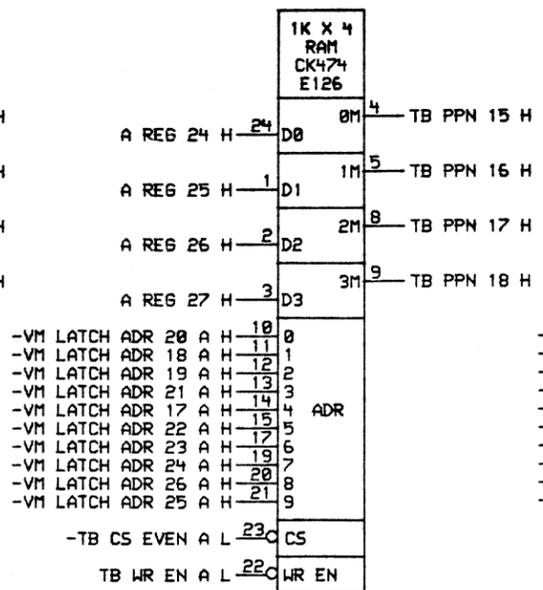
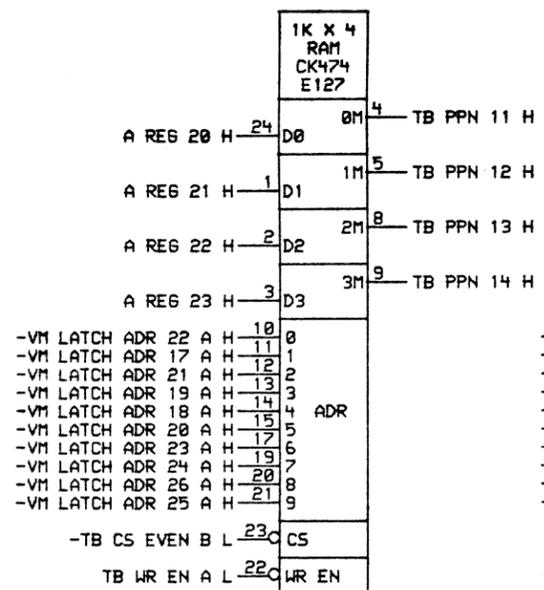
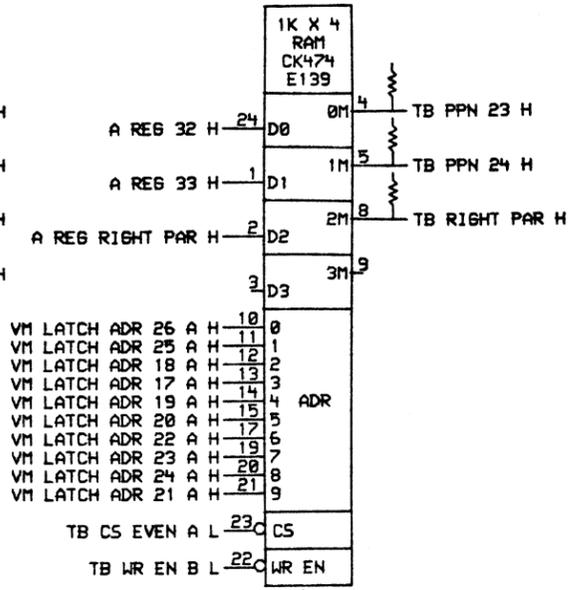
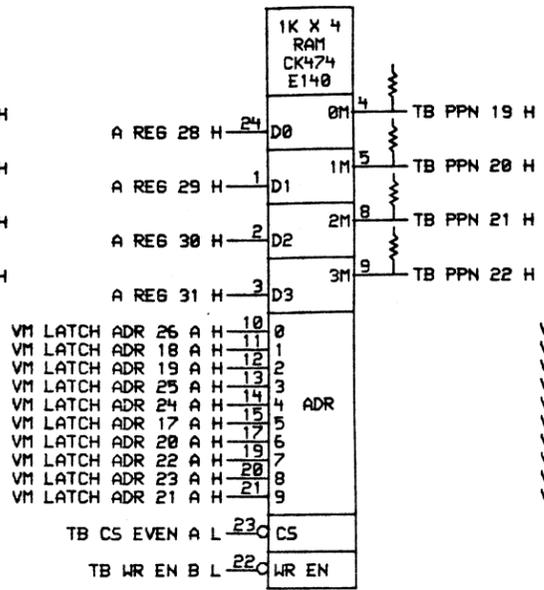
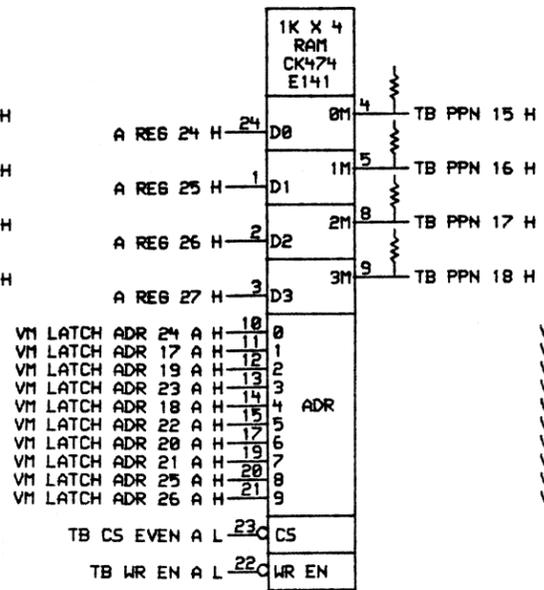
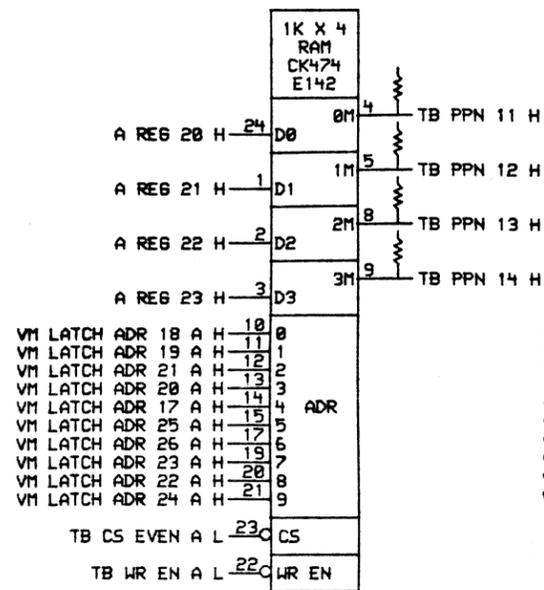
DRN.	DATE	ENG.	DATE	TITLE: CACHE DIRECTORY 2
CHK'D.	DATE	BOARD LOCATION:	SHEET	00-23
VILLAGE (2000-L0057) MAP3.DRW 09-MAR-81 09:05 NEXT HIGHER ASSEMBLY:				SIZE CODE NUMBER
FIRST USED ON OPTION MODEL: 2080				D C5 L0057-0-MAP3 REV.



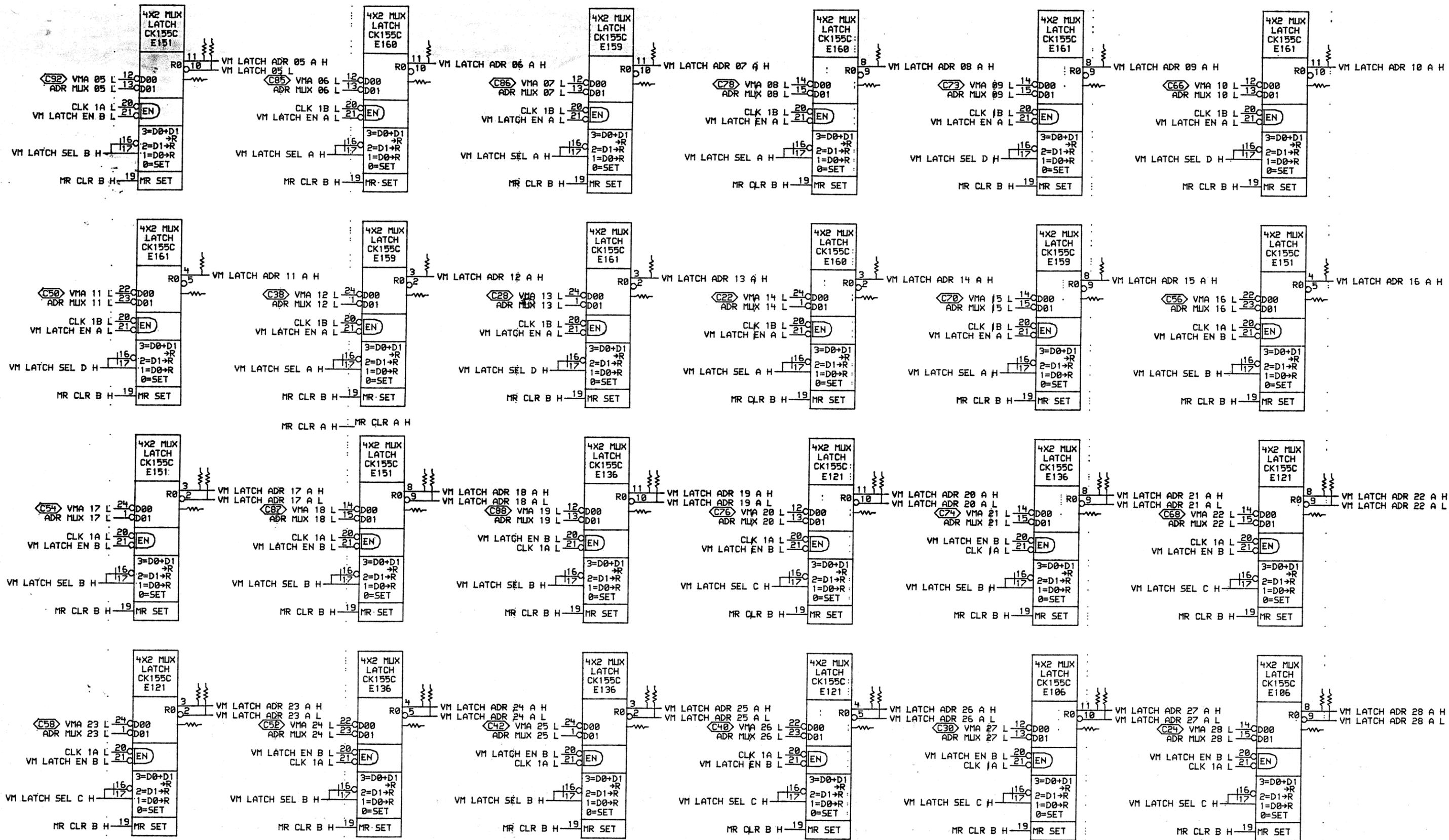
DRN.	DATE	ENG.	DATE	TITLE: CACHE DIRECTORY 3
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
VULCAN:K2888.L0057-0-MAP4.DRW		09-MAR-81 09:06	NEXT HIGHER ASSEMBLY:	SIZE CODE
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0057-0	NUMBER	REV.
		D	CS	L0057-0-MAP4



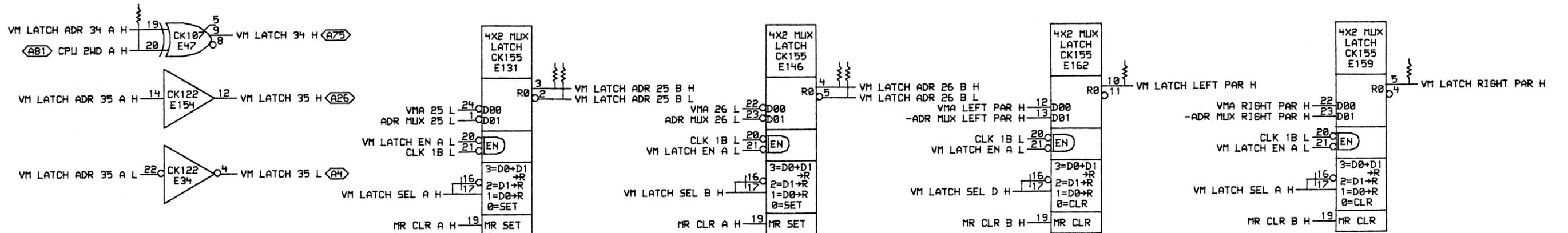
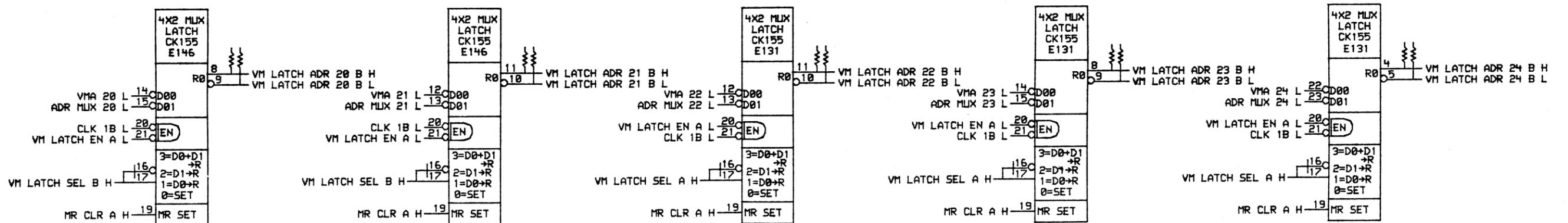
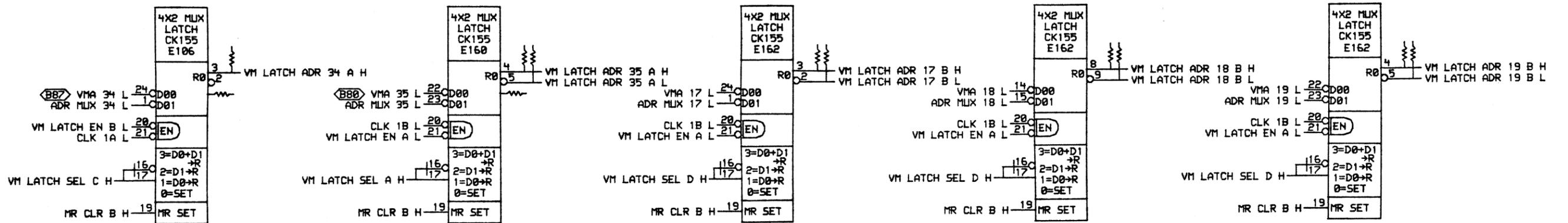
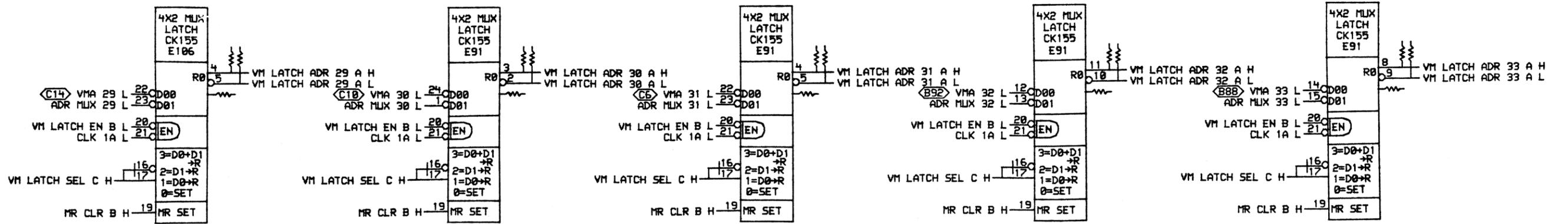
D  
 C  
 B  
 A



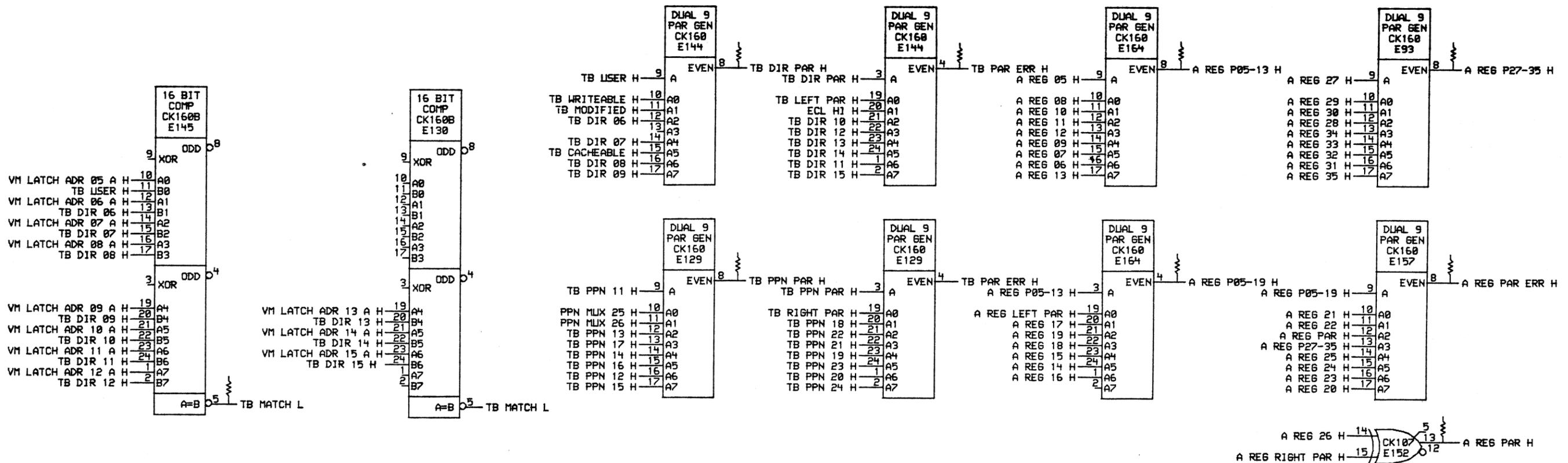
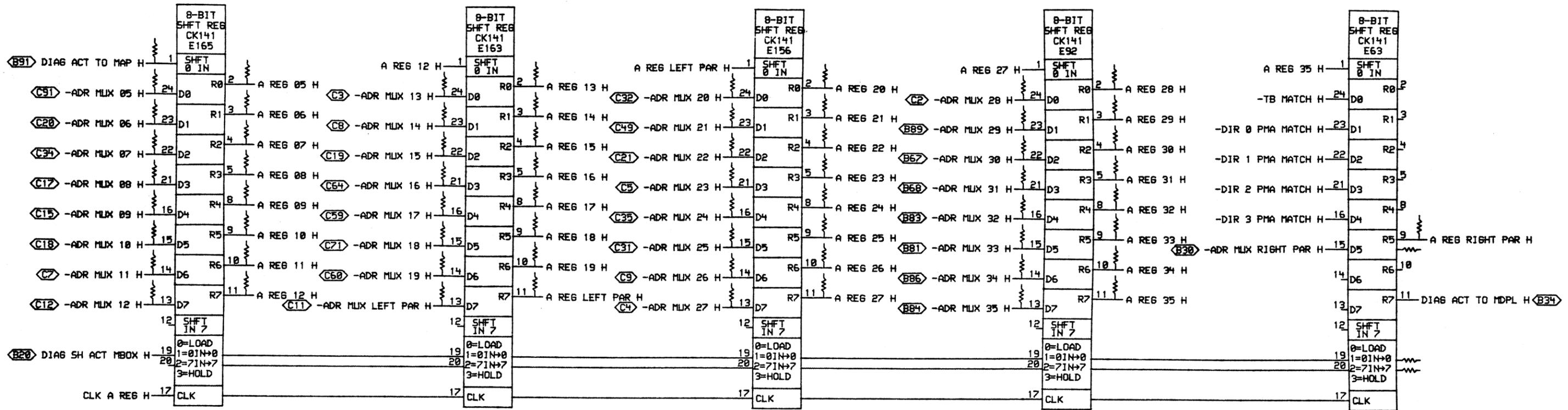
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		TB PPN
		SHEET		11-24
VULCAN (2080-L0057) 2-11-65 DRW 09-MAR-81 09:13 NEXT HIGHER ASSEMBLY:				SIZE CODE NUMBER
FIRST USED ON OPTION/MODEL: 2080 B-DD-L0057-0				D CS L0057-0-MAP6
				REV.



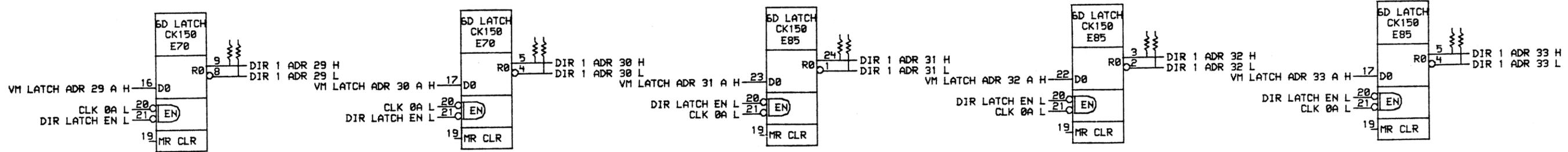
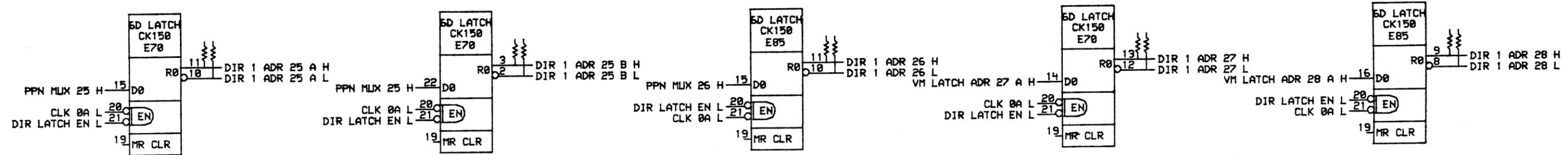
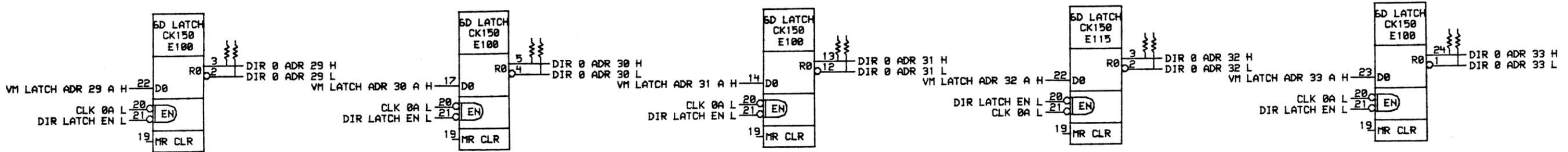
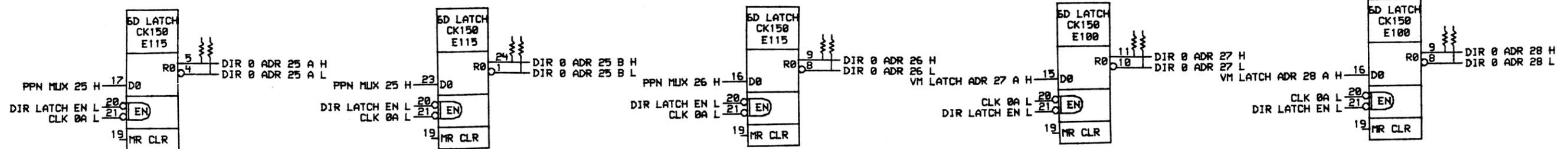
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	23-NOV-82			VM LATCH
	DATE	BOARD LOCATION:		05 A - 28 A
		SHEET	OF	
ARCH: (LHLER.PLOTS) 34477.DRW	22-NOV-82 14:27	NEXT HIGHER ASSEMBLY:		SIZE CODE
FIRST USED ON OPTION/MODEL:	2080:	B-DD-L0057-0		D C5
				NUMBER
				L0057-0-MAP7
				REV.



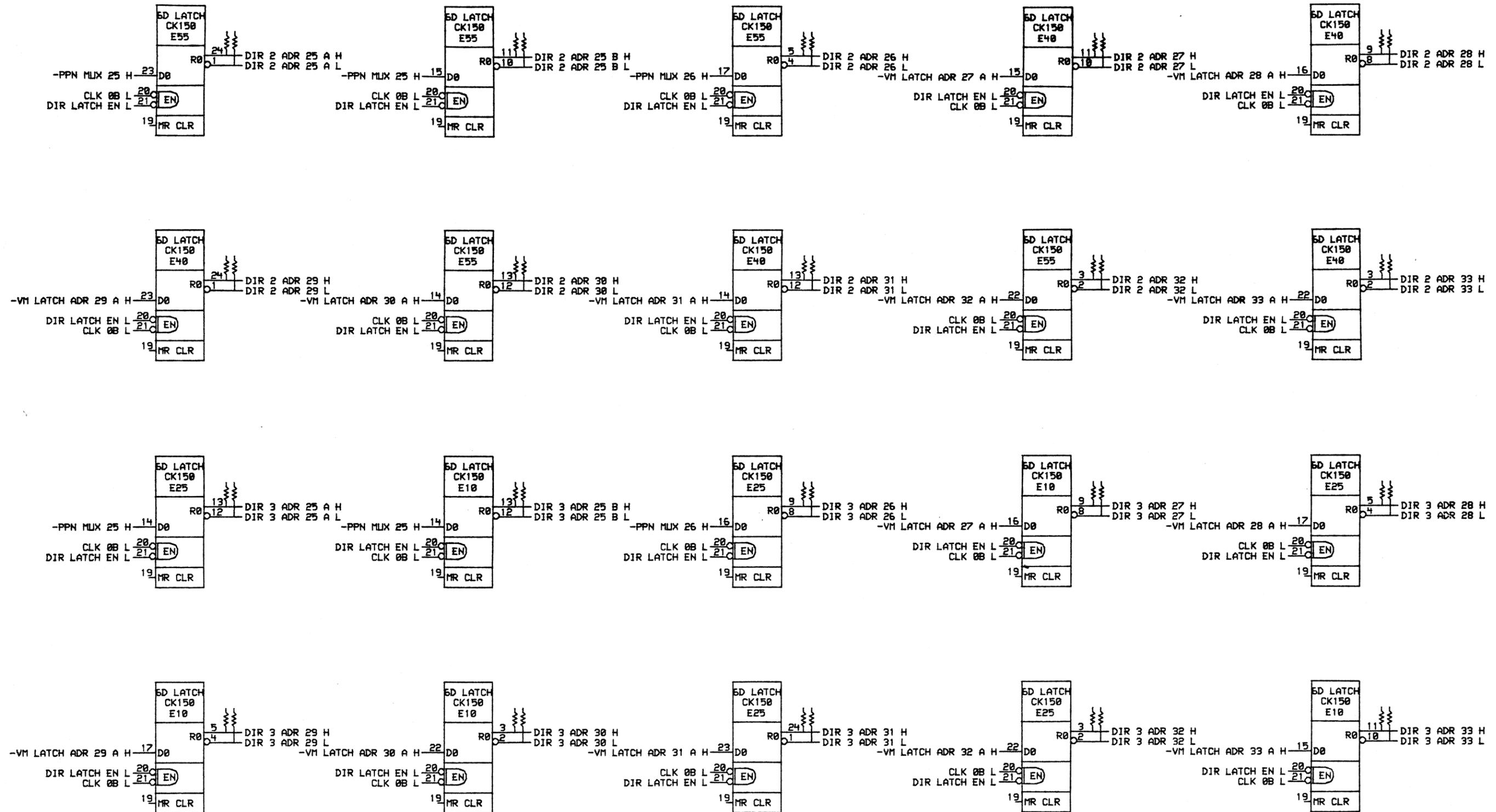
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	01-FEB-82			VM LTC 29A-35A
	DATE	BOARD LOCATION:		17B-26B + PAR
		SHEET 2 OF 2		
VIL CAN: (2000-L0057) 2411AP8.DRW	27-JAN-82 15:27	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: 2080	B-DD-L0057-0		D CS	L0057-0-MAP8
				REV.



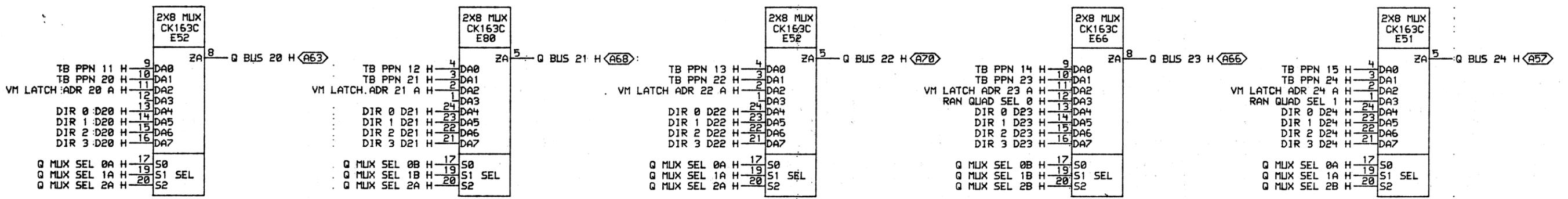
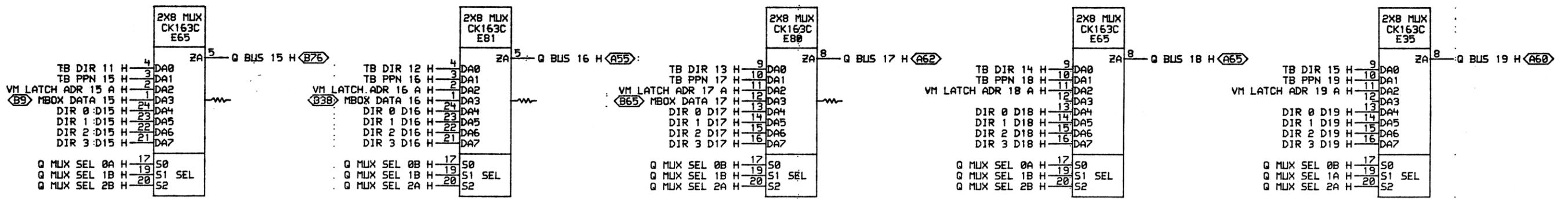
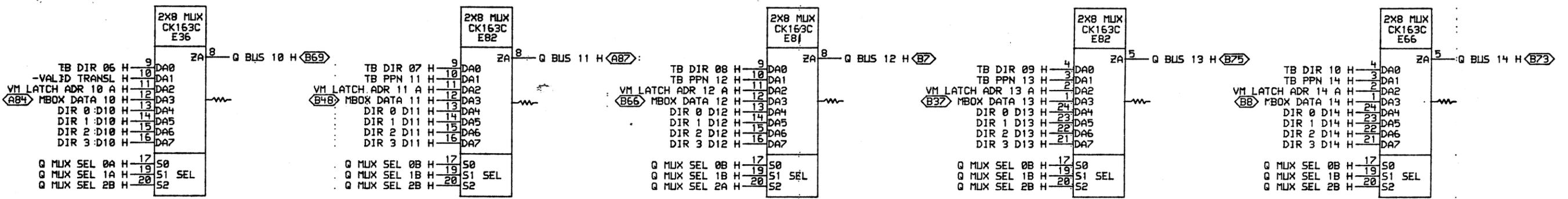
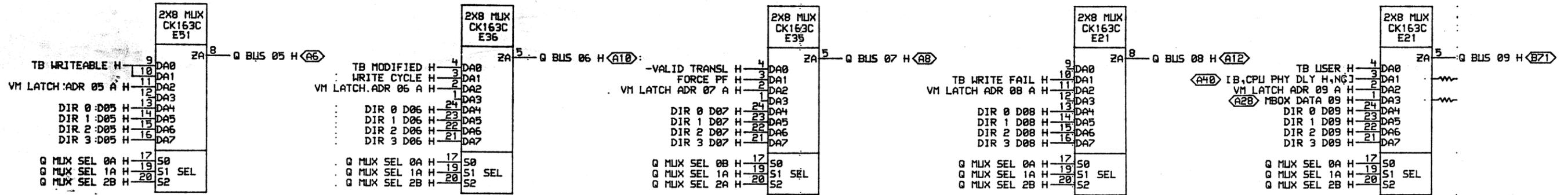
DRN.	DATE	ENG.	DATE	TITLE:	A REG		
CHK'D.	01-FEB-82	BOARD LOCATION:	SHEET	1	OF	1	
VLLCANI<2080-L0057>PMAPS.DRW				27-JAN-82 15:36	NEXT HIGHER ASSEMBLY:	B-DD-L0057-0	
FIRST USED ON OPTION/MODEL: 2080				SIZE	CODE	NUMBER	REV.
				D	CS	L0057-0-MAP9	



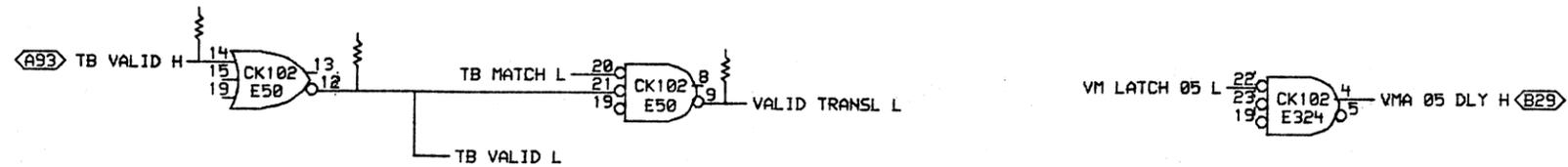
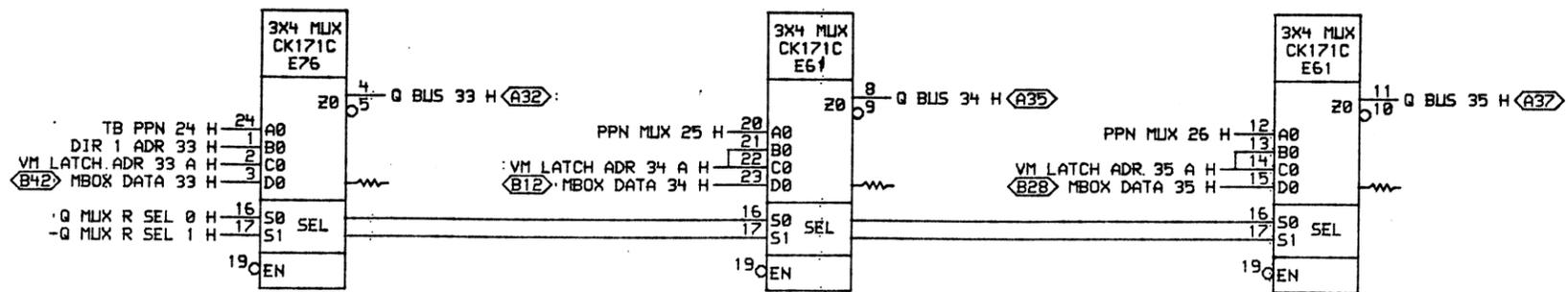
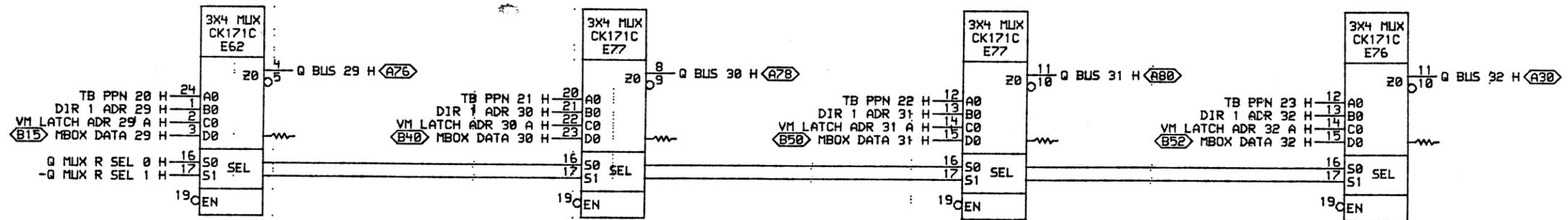
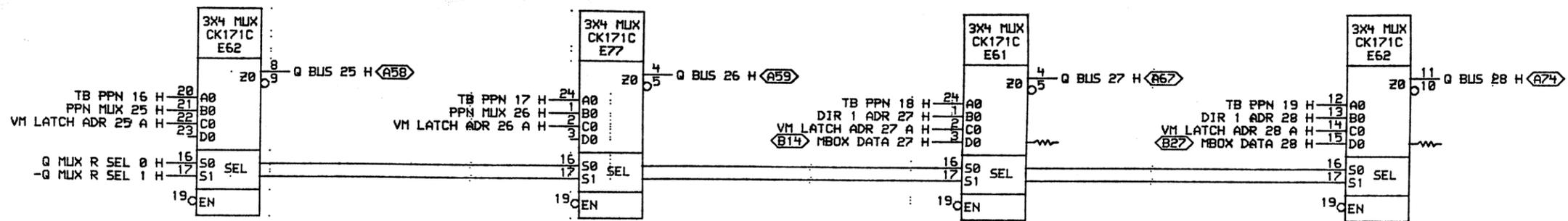
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	DIR LATH
VIL CAN: (2000-L0057) 2-MAPA.DRW	01-FEB-82 08:34	NEXT HIGHER ASSEMBLY:	1 OF 2	0 & 1
FIRST USED ON OPTION/MODEL:	2000	B-DD-L0057-0	SIZE CODE	NUMBER
			D CS	L0057-0-MAPA
				REV.



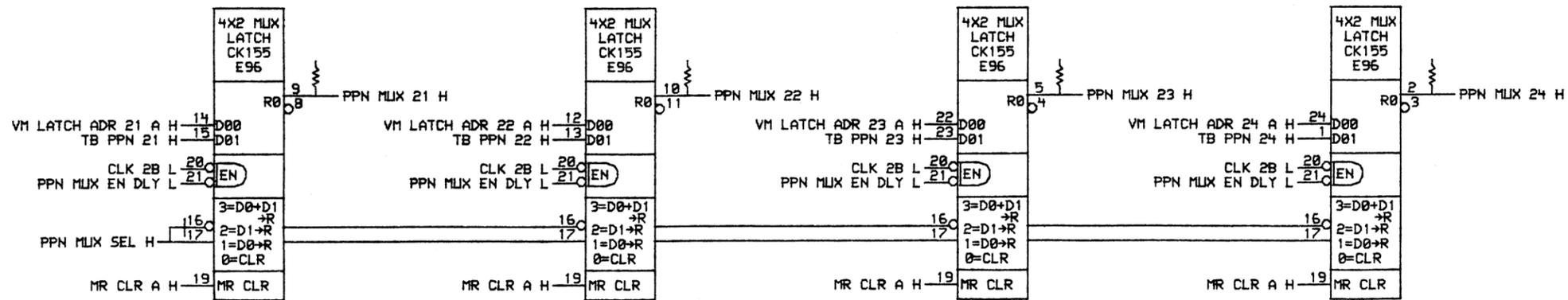
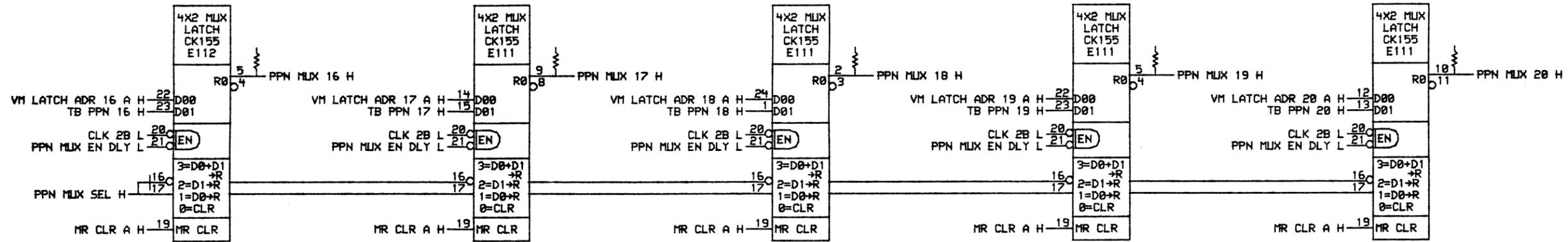
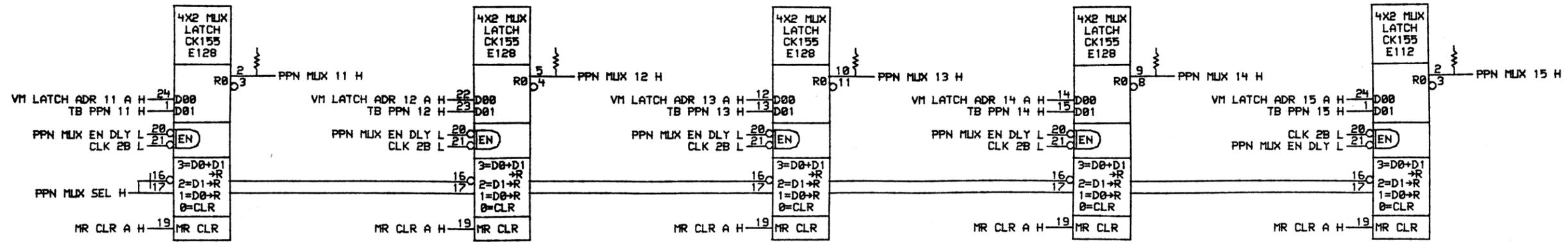
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	01-FEB-82			DIR LATCH
	DATE	BOARD LOCATION:		2 & 3
		SHEET 2 OF 2		
VULCANI (2000-L0057) 2-MAPB.DRW	01-FEB-82 08:21	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: 2000		B-DD-L0057-0	D CS	L0057-0-MAPB
			REV.	*



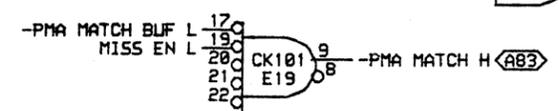
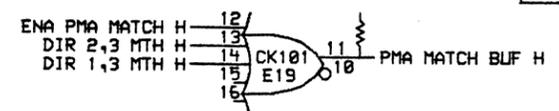
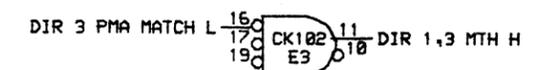
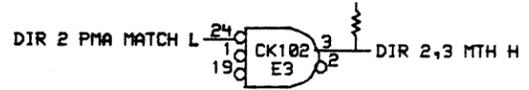
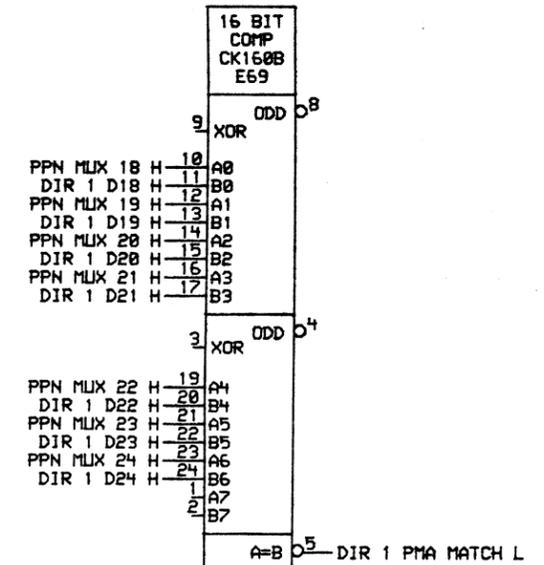
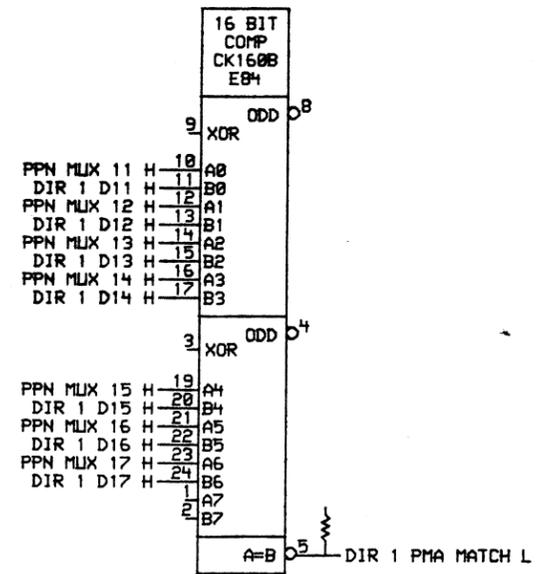
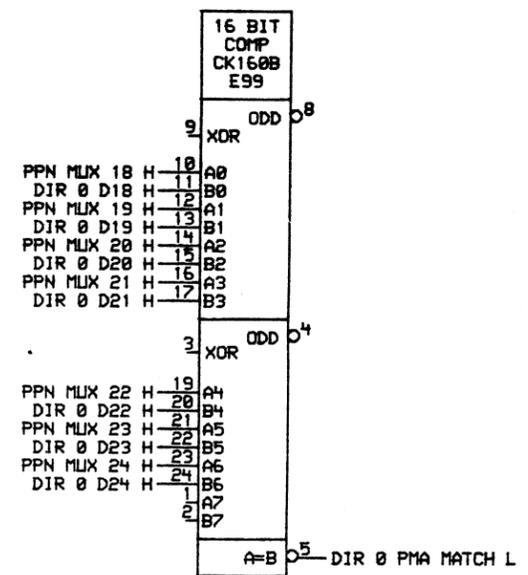
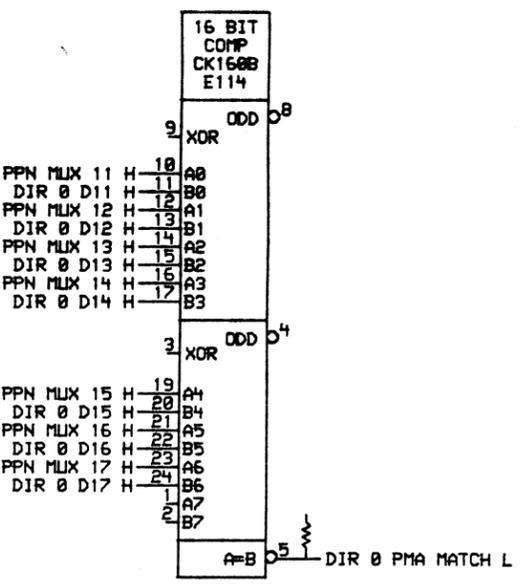
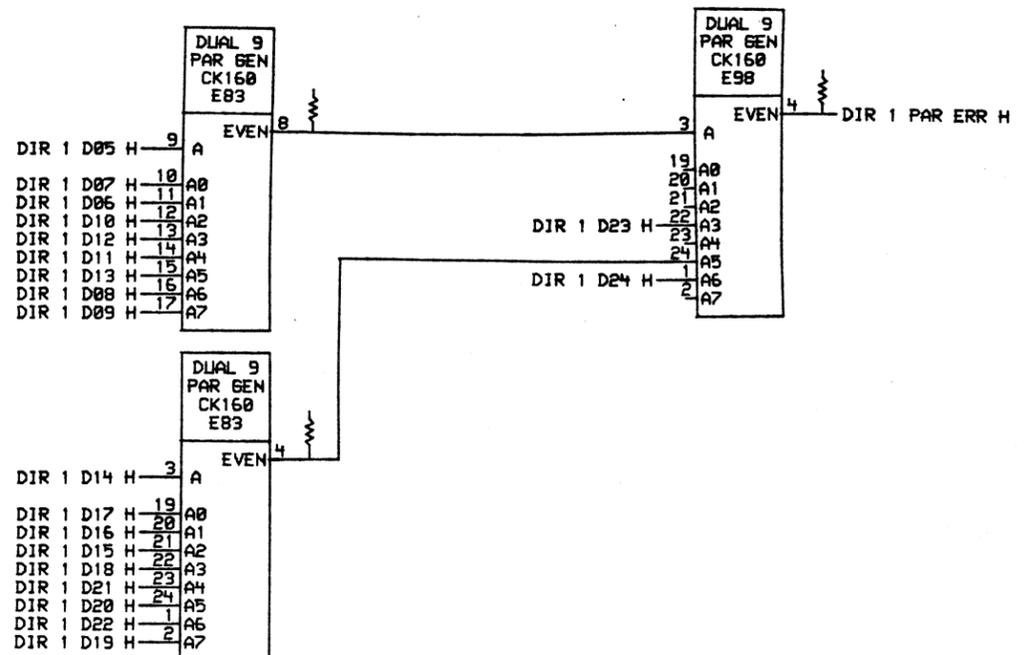
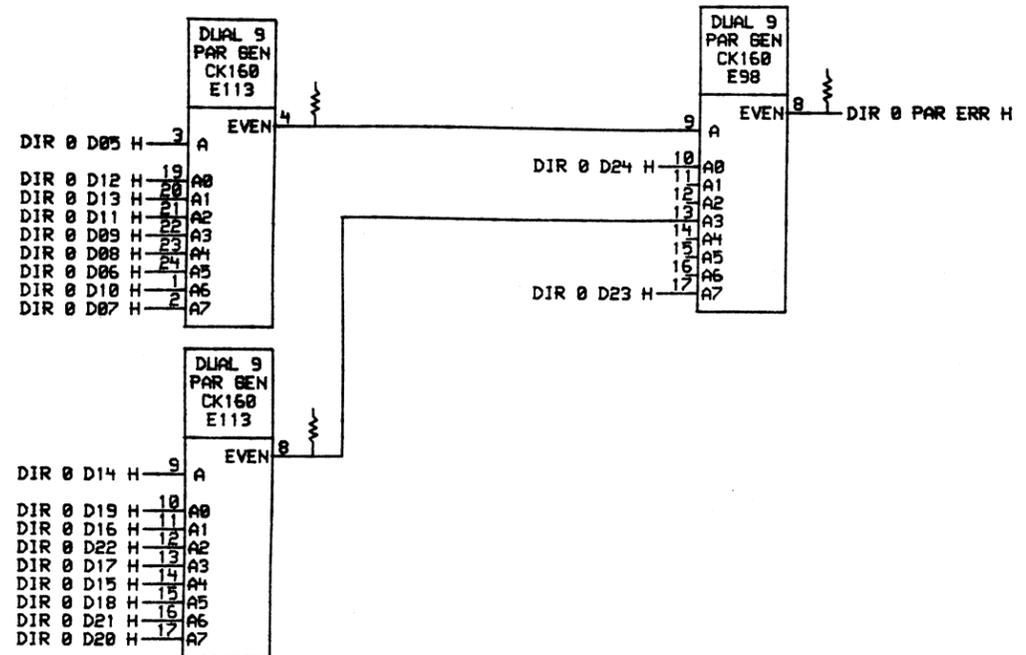
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	Q MUX 05 - 24
ARCH: <UHLER.PLOTS>33APC.DRW	15-NOV-82 14:01	NEXT HIGHER ASSEMBLY:	1 OF 2	NUMBER
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0057-0	SIZE	L0057-0-MAPC
			CODE	REV.
			D CS	*



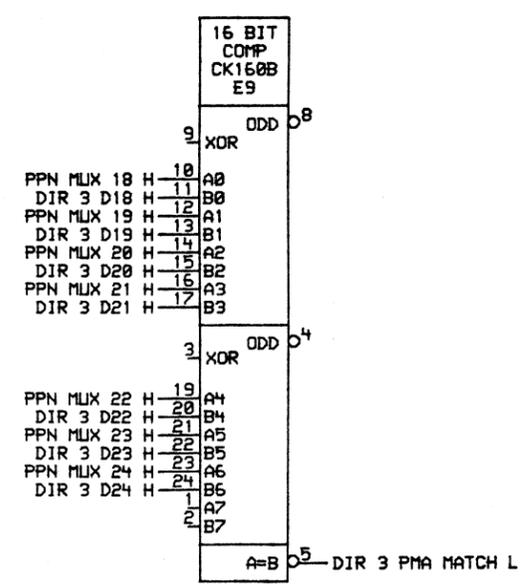
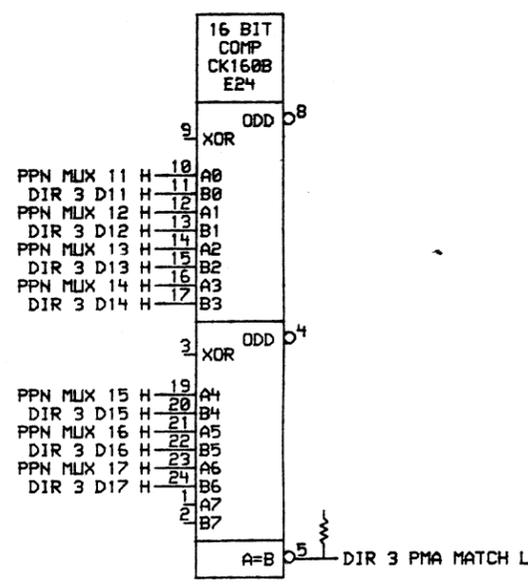
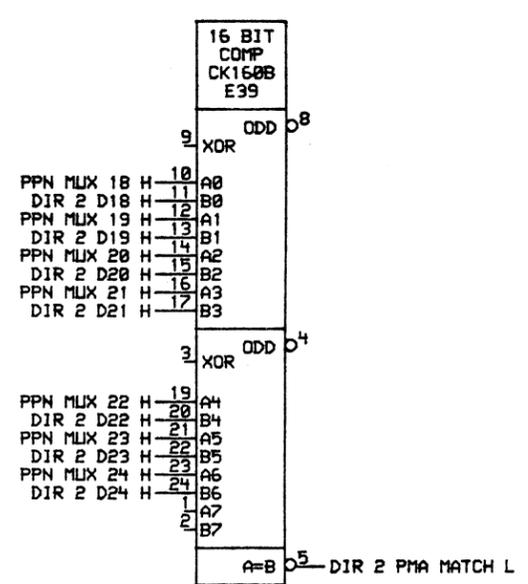
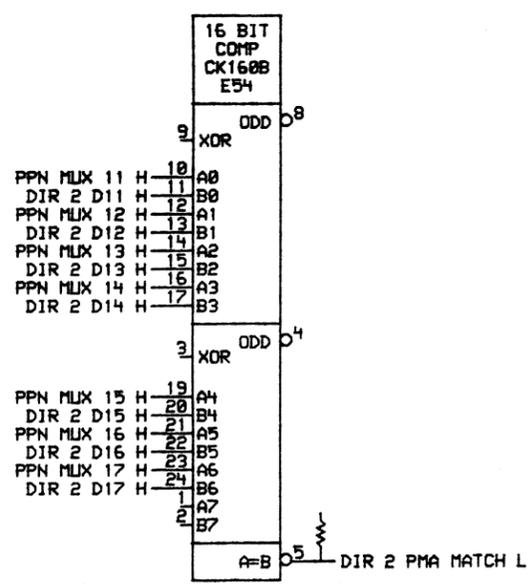
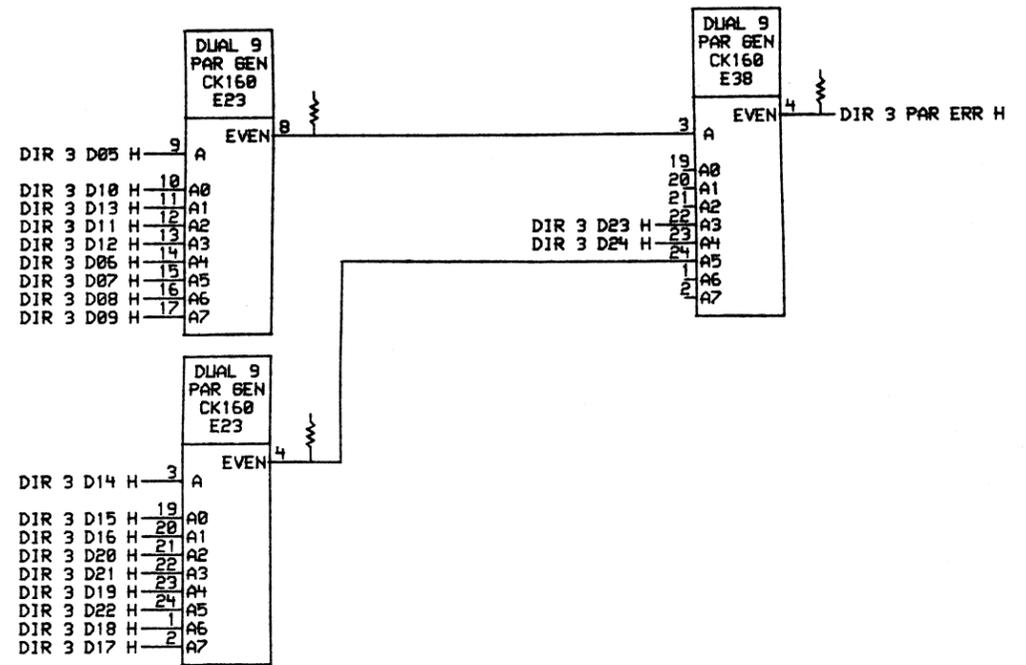
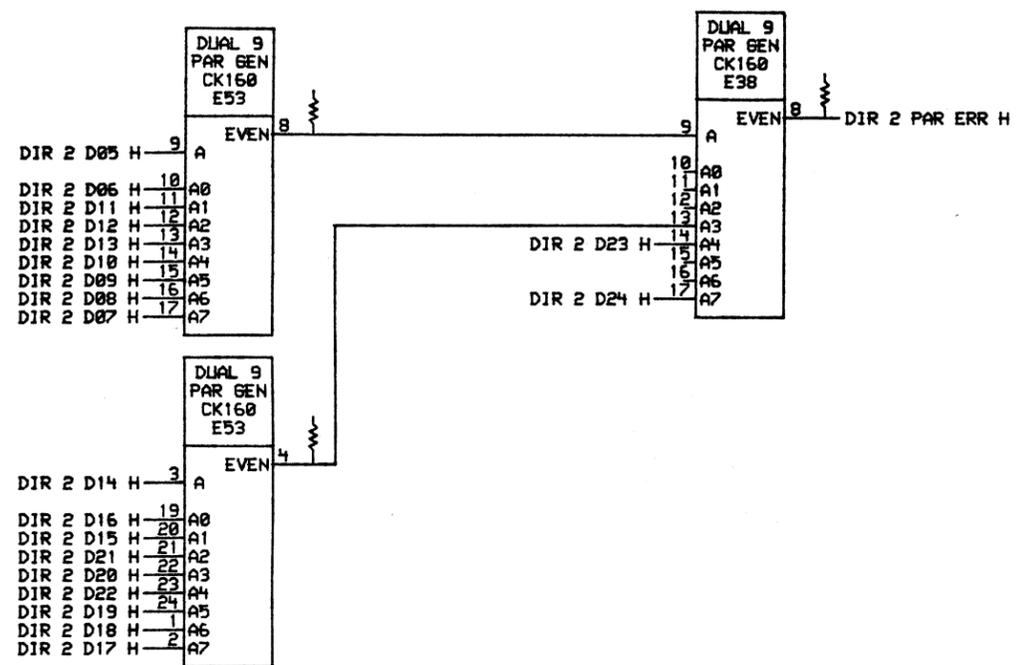
DRN.	DATE	ENG.	DATE	TITLE:	Q MUX 28-35
CHK'D.	DATE	BOARD LOCATION:	SHEET 2 OF 2	NUMBER	
ARCH: <UNLDR.PLOTS>MAPD.DRW 22-NOV-82 14:36				NEXT HIGHER ASSEMBLY:	SIZE CODE
FIRST USED ON OPTION/MODEL: 2080				B-DD-L0057-0	NUMBER L0057-0-MAPD
					REV. *



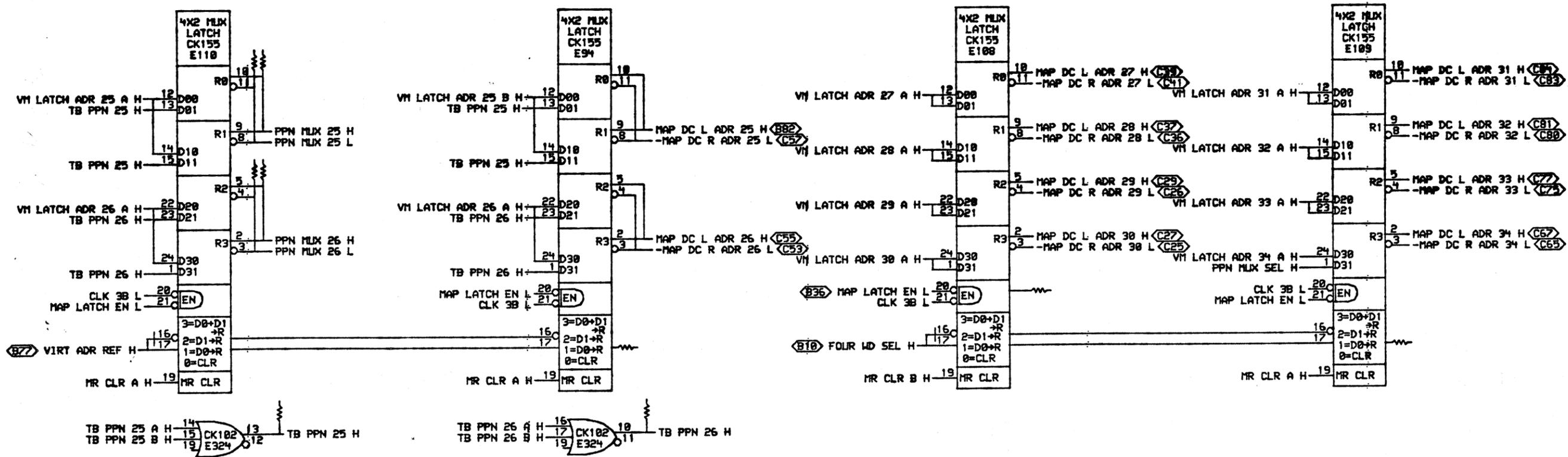
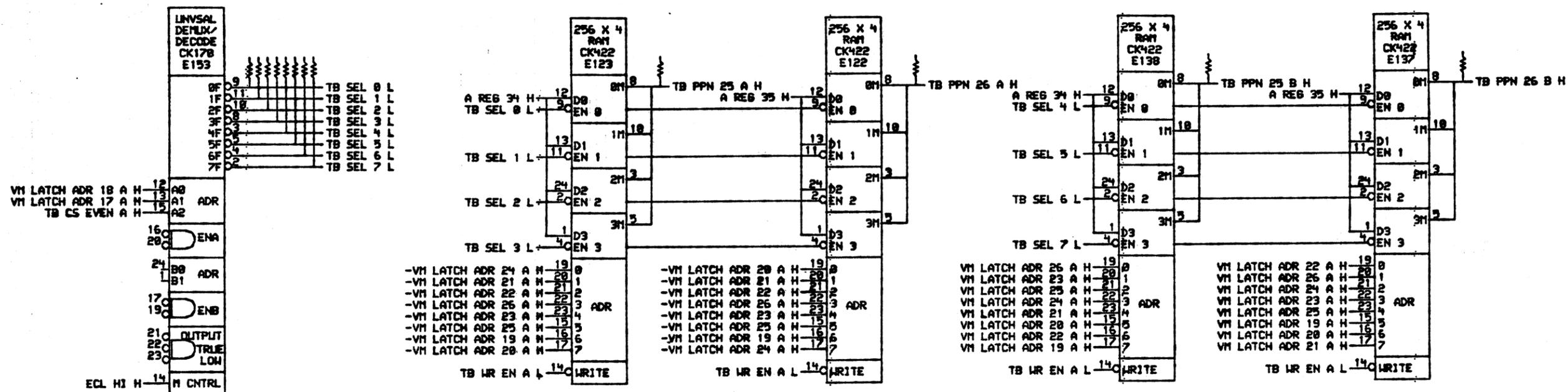
DRN.	DATE	ENG.	DATE	TITLE:	PPN MUX 11-24		
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	SIZE	CODE	NUMBER
VULCAN: <P088.L0057>24MAPE.DRW 101-FEB-82 08:05				NEXT HIGHER ASSEMBLY:	D	CS	L0057-0-MAPE
FIRST USED ON OPTION/MODEL: 2080				B-DD-L0057-0	REV.	*	

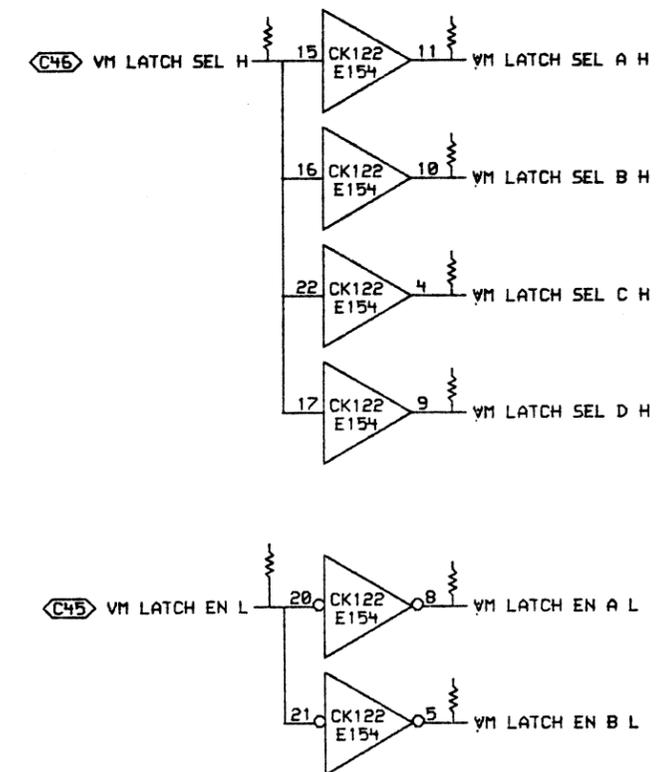
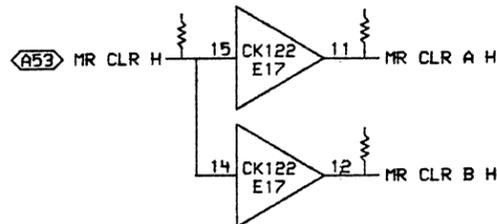
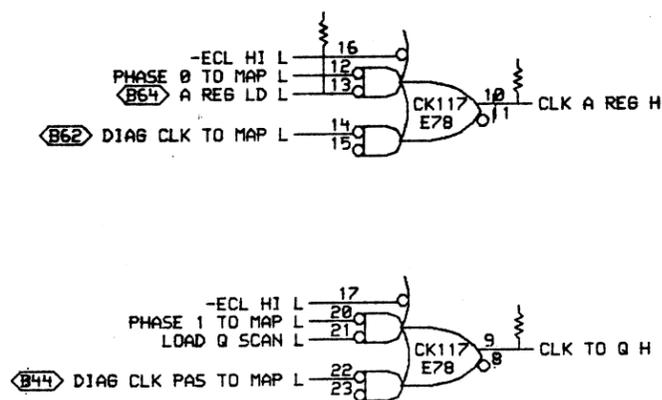
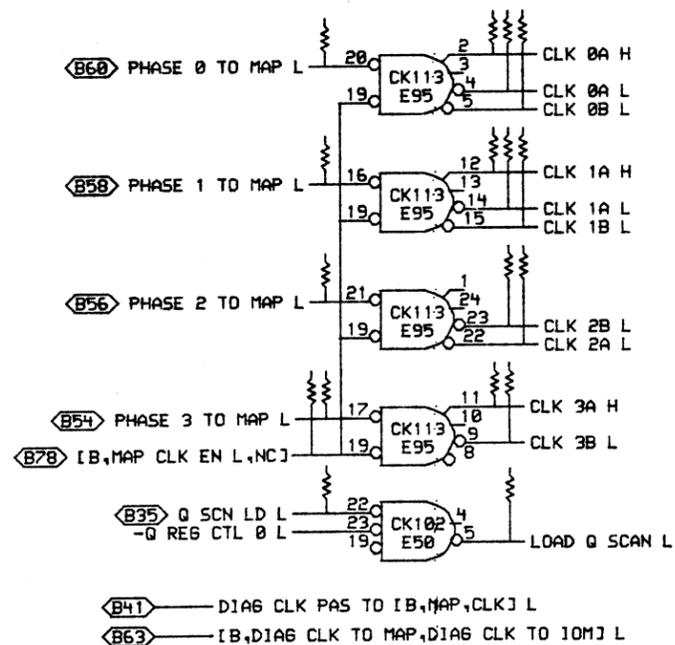
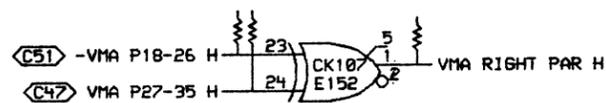
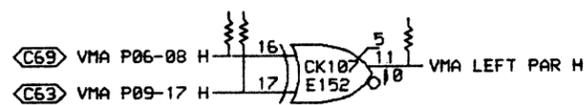
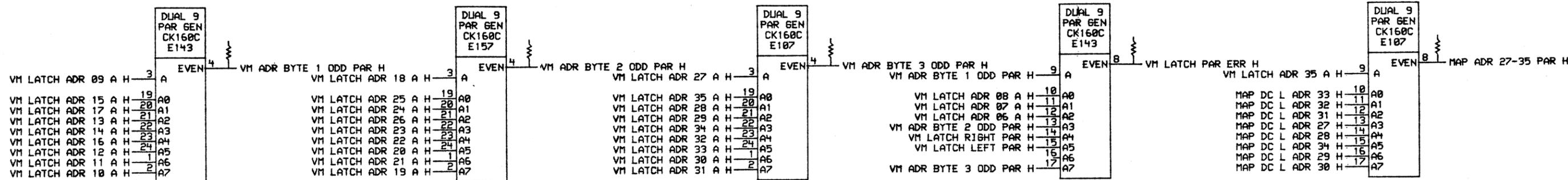


DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	01-FEB-82	BOARD LOCATION:		DIR MATCH
		SHEET	OF	0 & 1
VULCAN (2080) L0057-0 MAPF.DRW	01-FEB-82 08:51	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: 2080	B-DD-L0057-0		D	CS
			NUMBER	REV.
			L0057-0-MAPF	*



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	01-FEB-02			DIR MATCH
	DATE	BOARD LOCATION:		0-3
		SHEET	OF	
VULCAN (2000-L0057) MAP8.DRW	01-FEB-02 09:02	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: 2000	B-DD-L0057-0		D	CS
			NUMBER	REV.
			L0057-0-MAPG	*



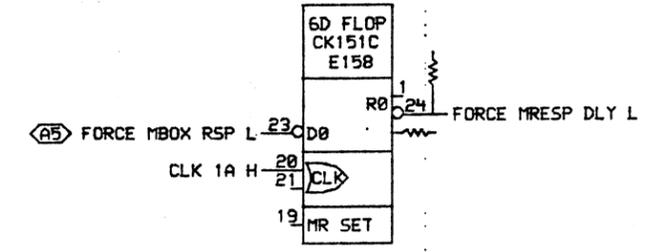
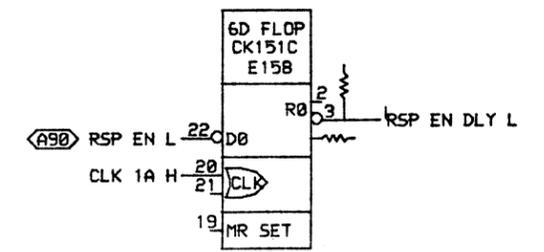
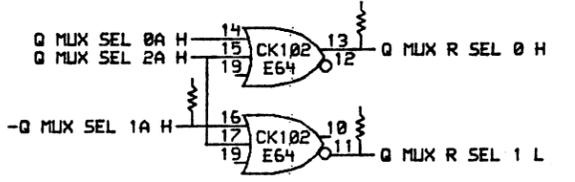
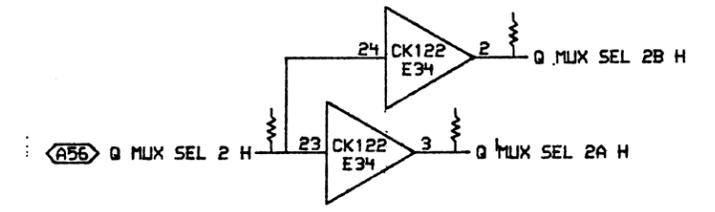
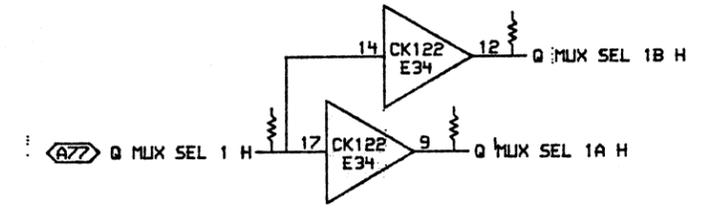
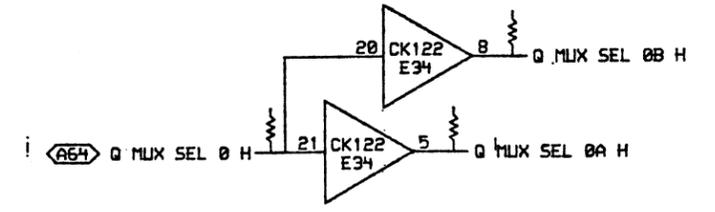
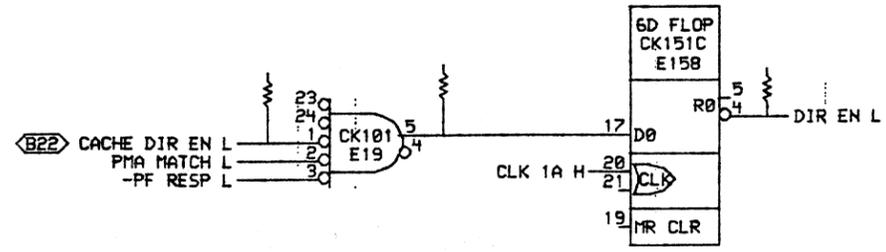
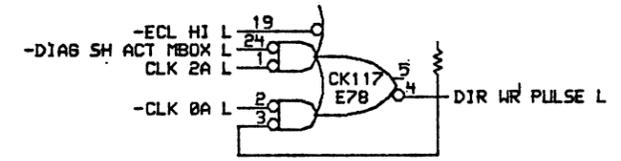
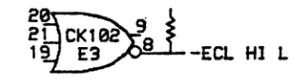
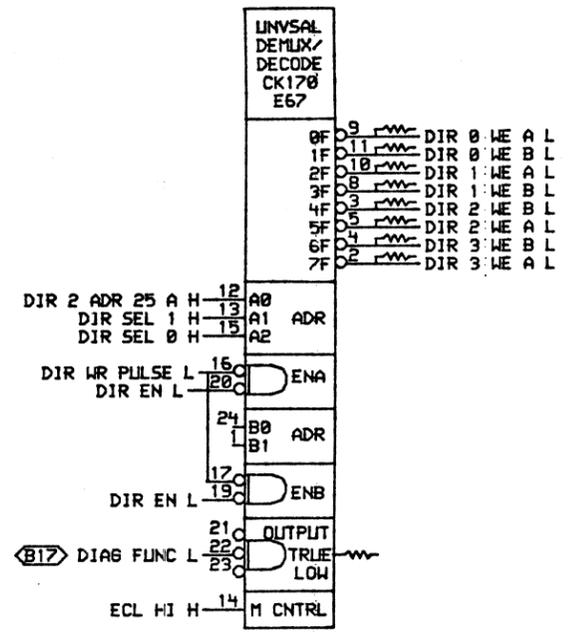
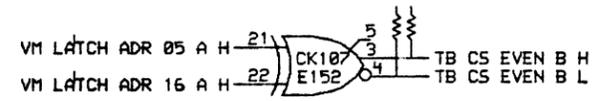
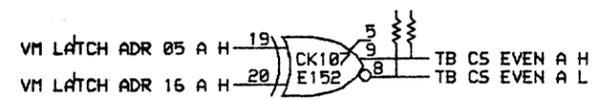
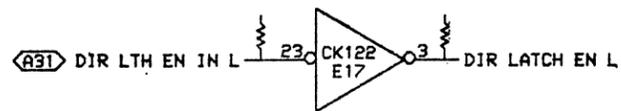
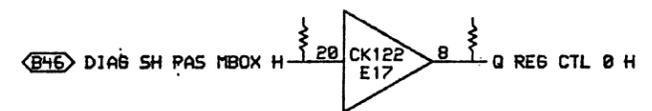
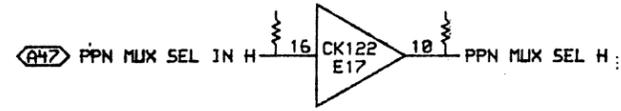
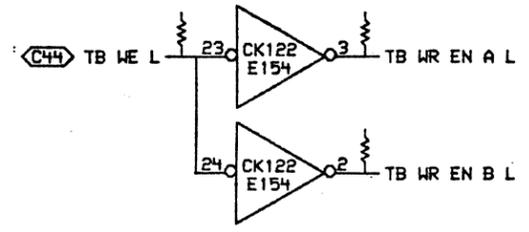


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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	10-NOV-82			CLK BUFFER
CHK'D.	DATE	BOARD LOCATION:		
FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0057-0		

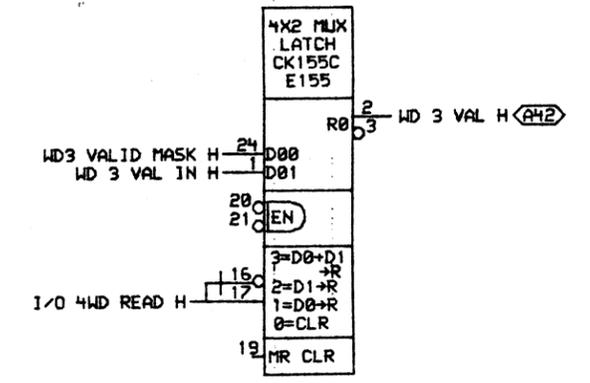
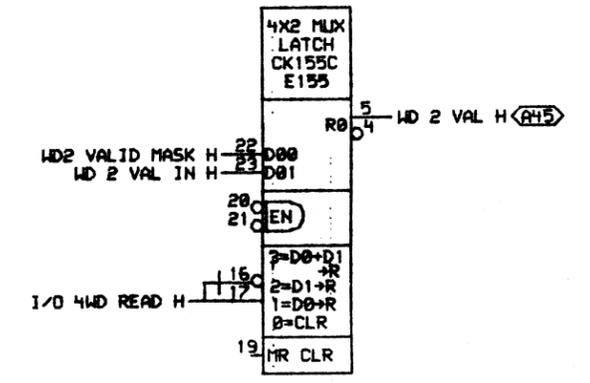
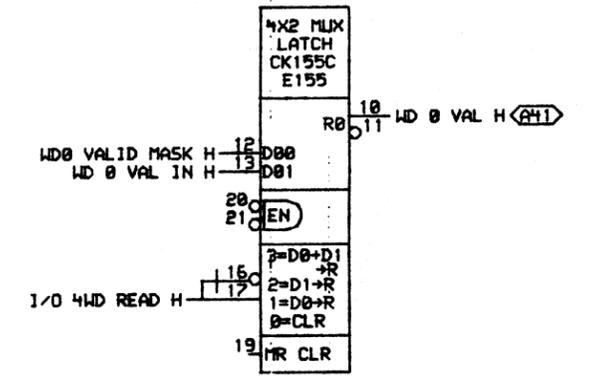
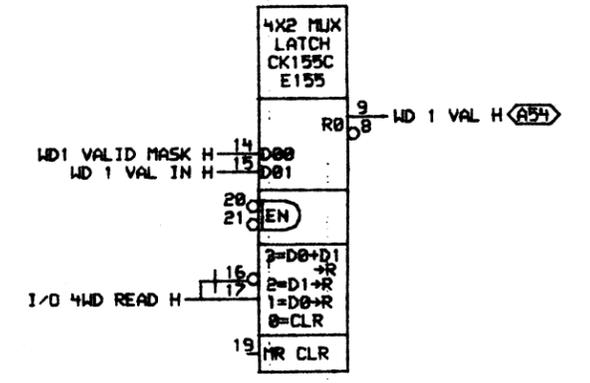
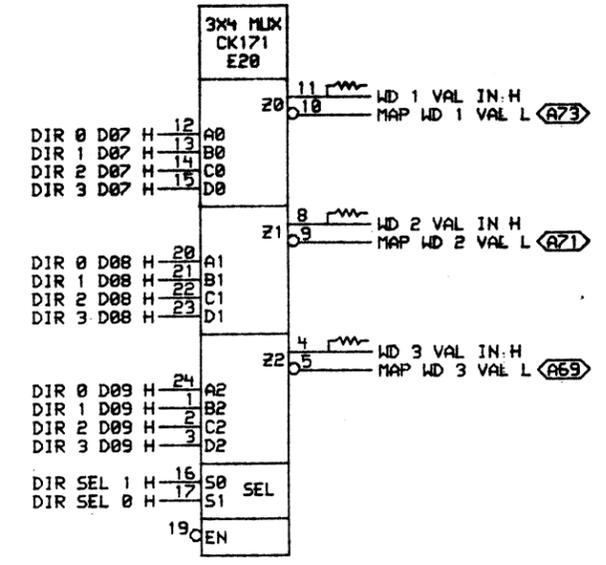
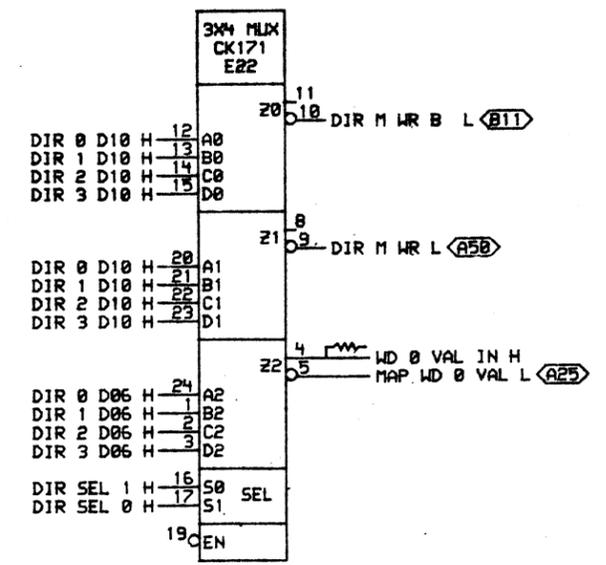
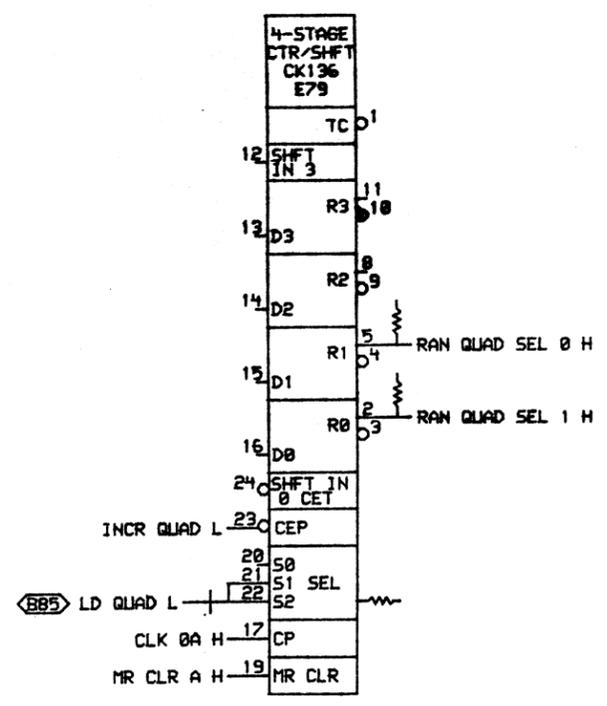
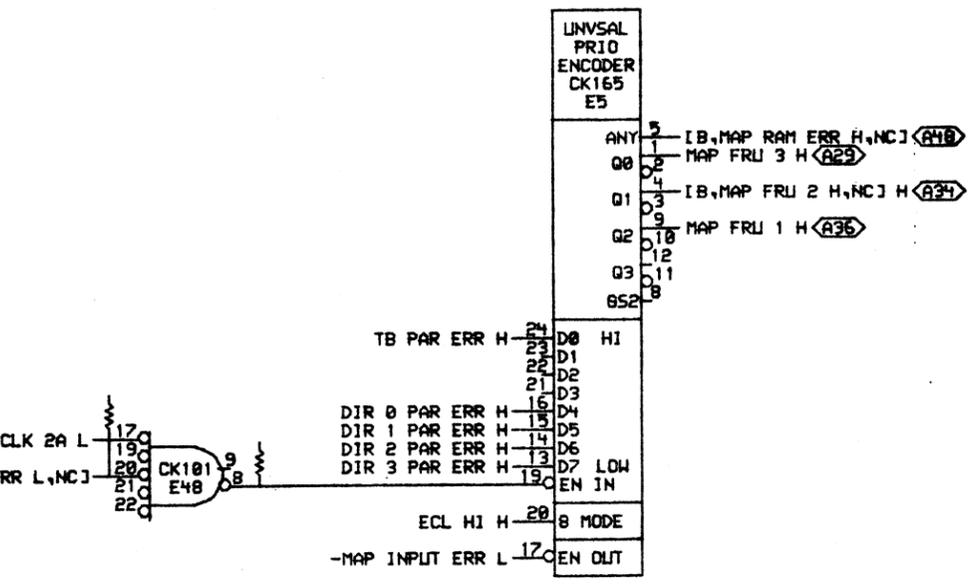
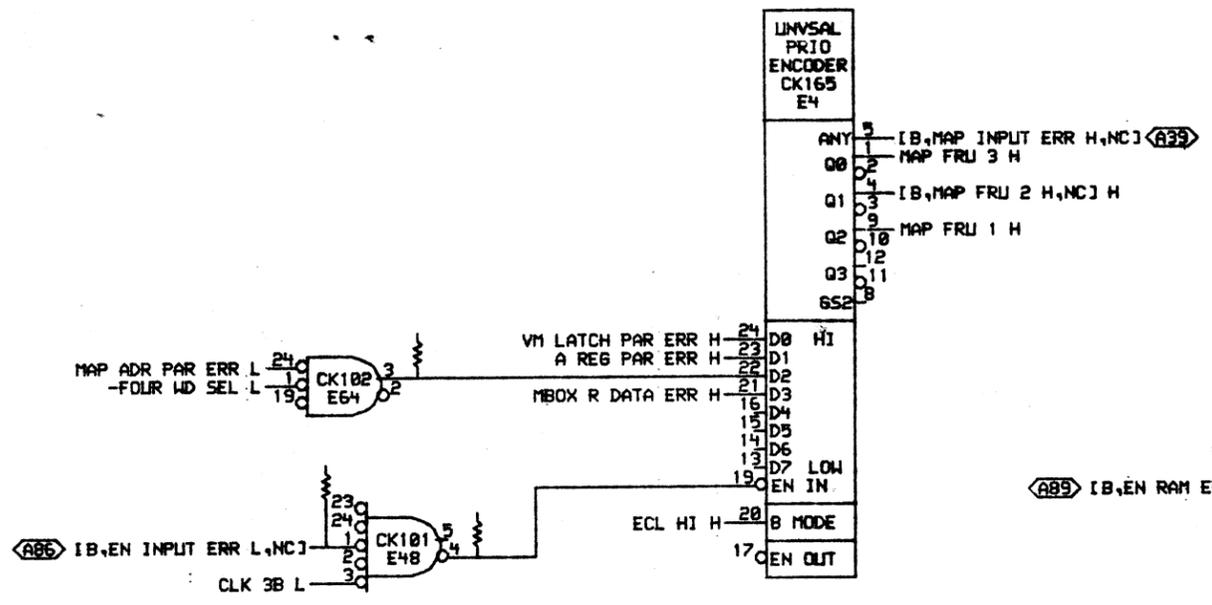
SIZE	CODE	NUMBER	REV.
D	CS	L0057-0-MAPI	*



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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE:	INPUT BUFFERS
	27-OCT-82	J. Jambly			
	DATE	BOARD LOCATION:	SHEET	OF	
FIRST USED ON OPTION/MODEL: 2000				SIZE CODE	NUMBER
				D CS	L0057-0-MAPJ
				REV.	*

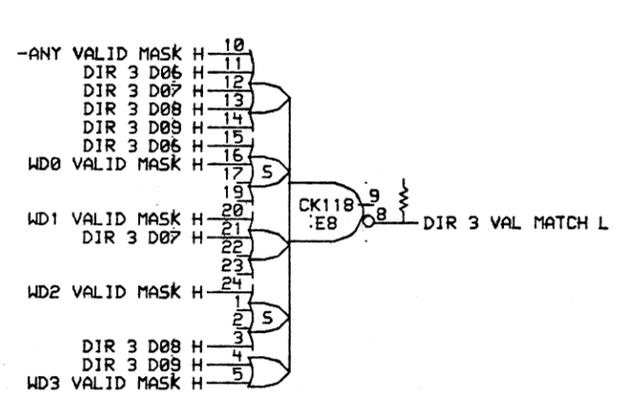
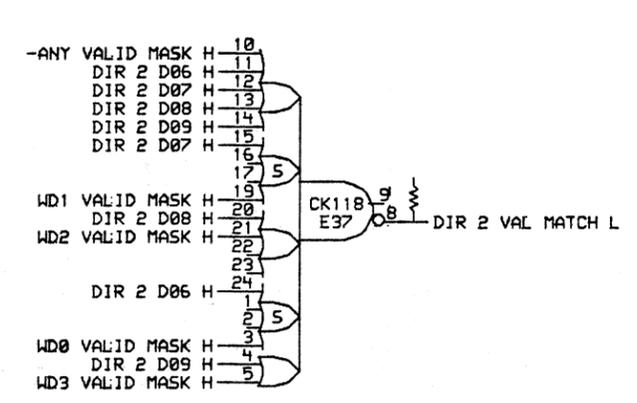
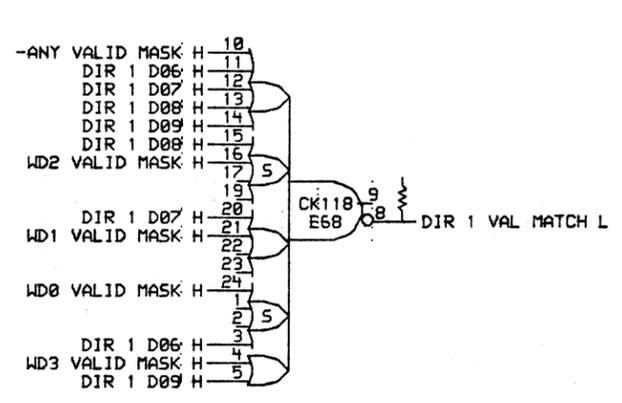
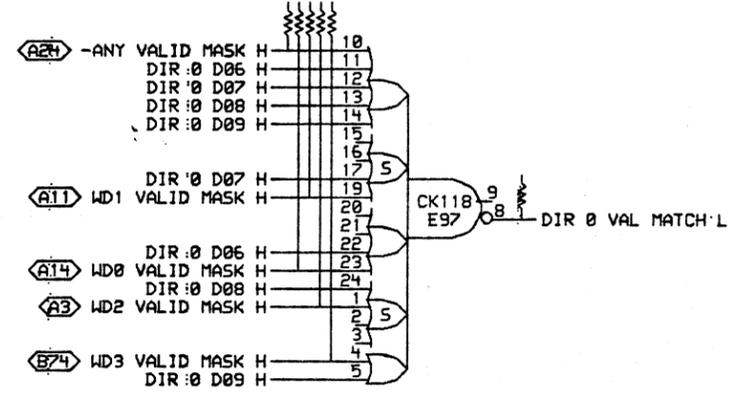
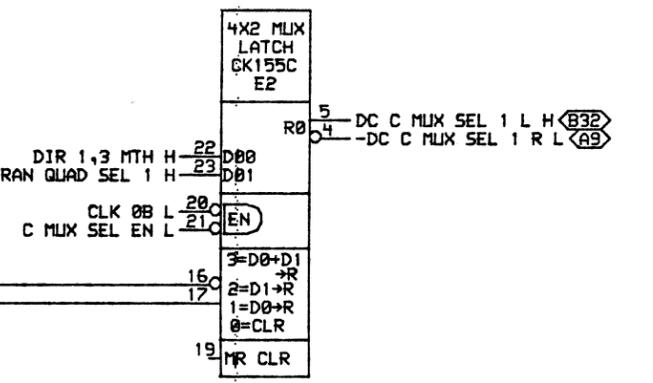
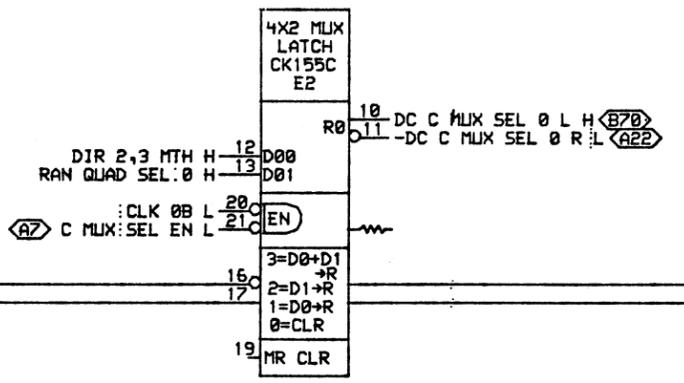
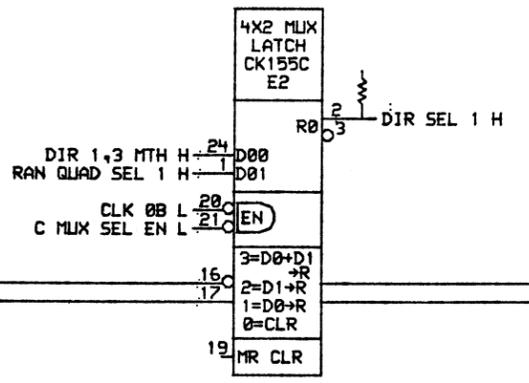
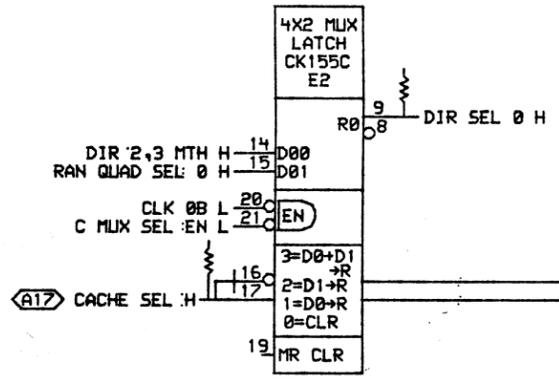
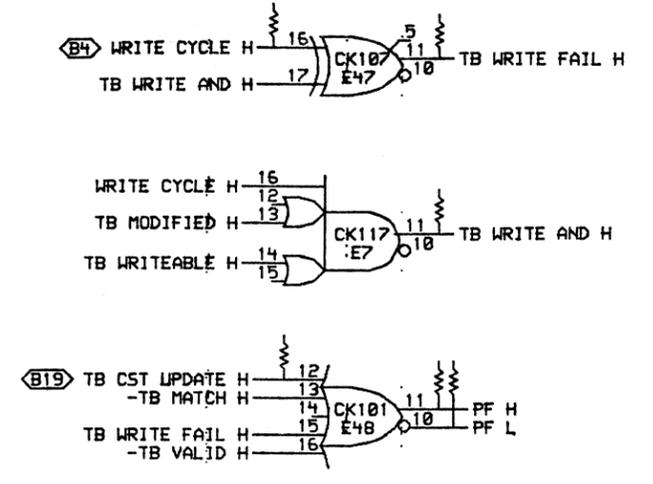
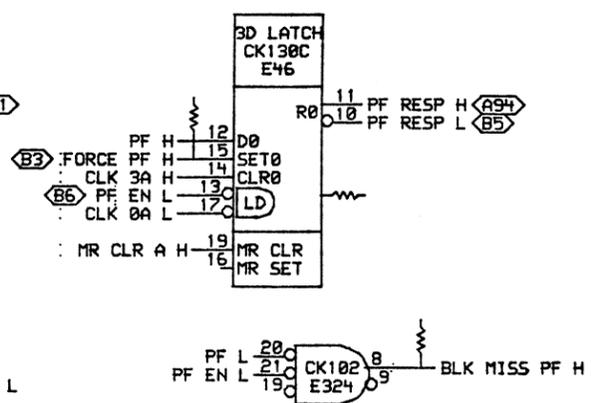
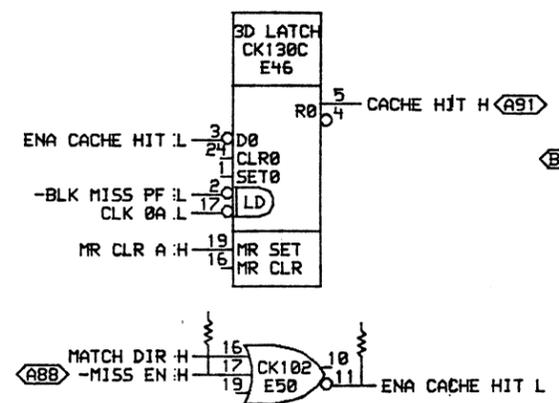
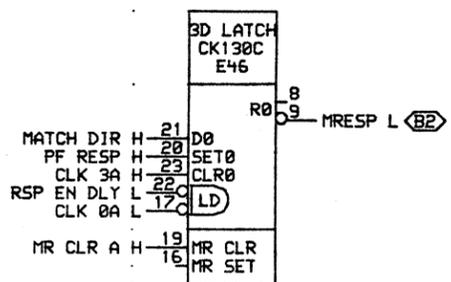
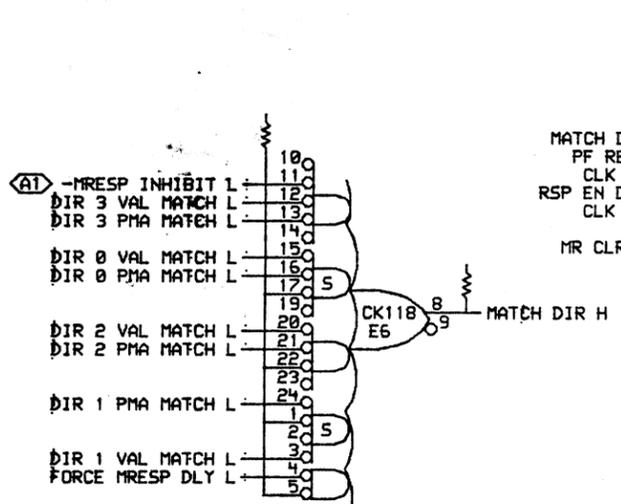


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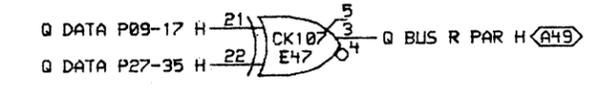
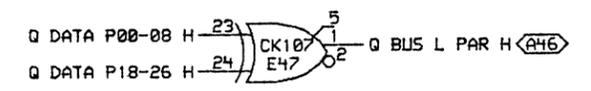
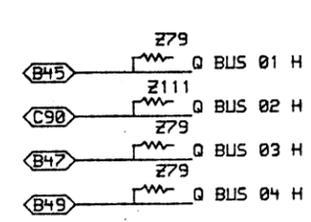
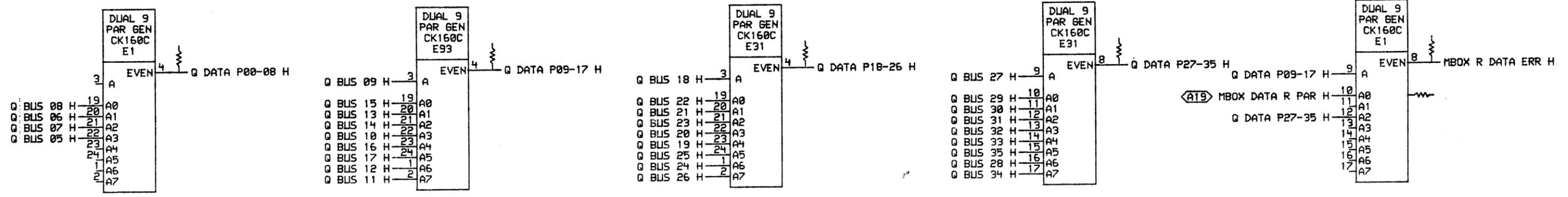
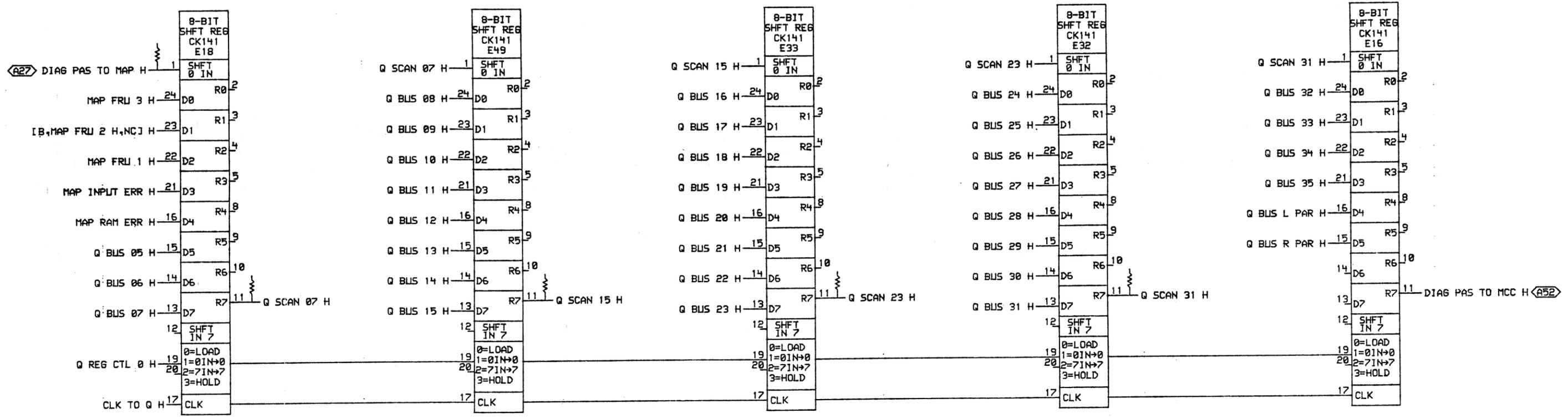
REVISIONS	
CHK	CHANGE NO. REV

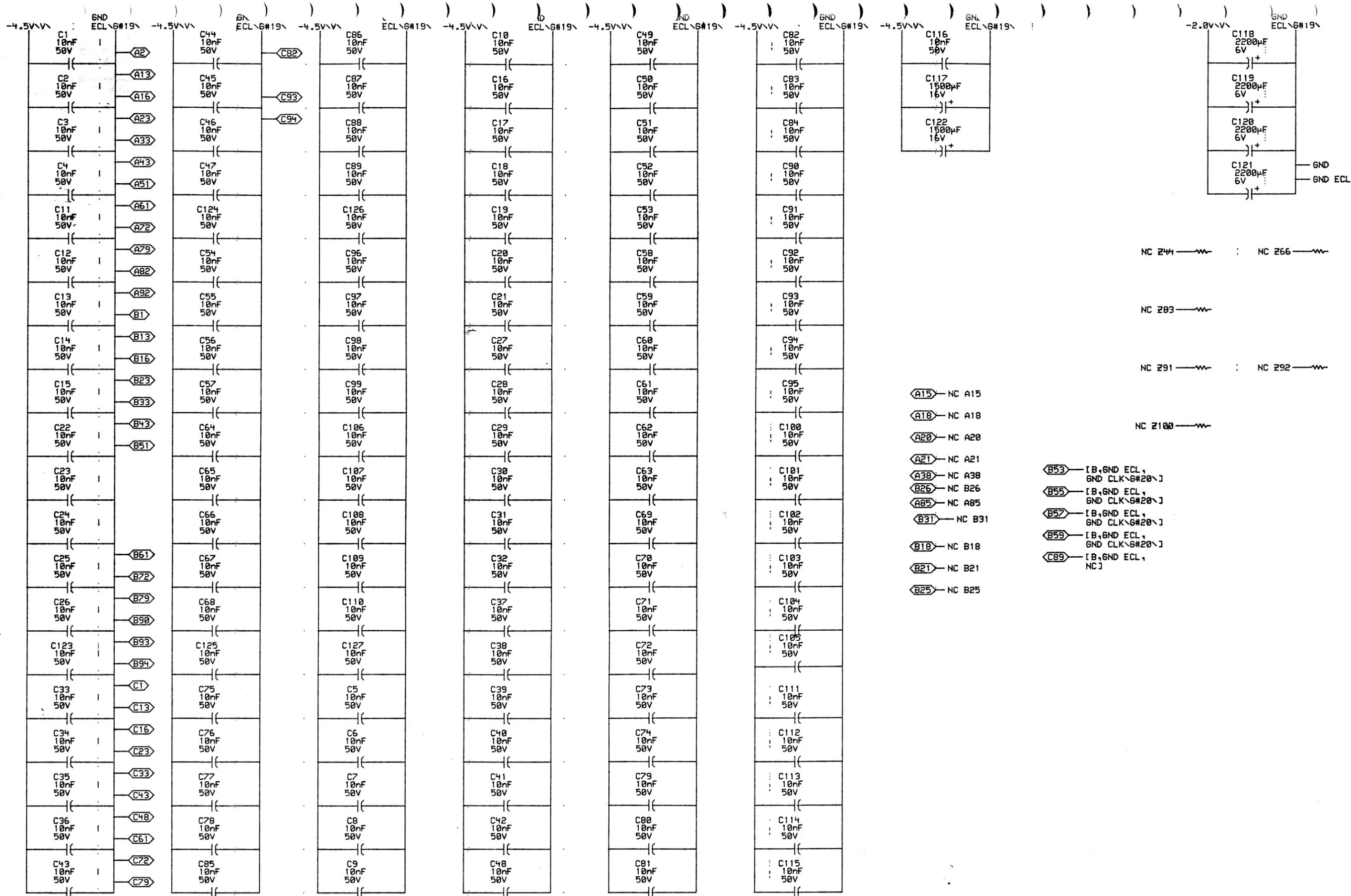
	DATE	ENG.	DATE	TITLE:
	13-OCT-82	J. J. J.	13-OCT-82	ERROR LATCHES + VALID & WRT BITS
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
VUL CAN: 2080-L0057-MAPK.DRW	12-OCT-82 07:30	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION MODEL: 2080	B-DD-L0057-0		D	CS

NUMBER	REV.
L0057-0-MAPK	*



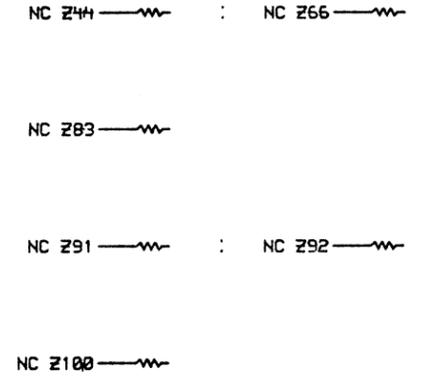
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	22-NOV-82			VALID MATCHES
ARCH: (UNLIER.PLOTS)31MAPL.DRW 15-NOV-82 16:59 NEXT HIGHER ASSEMBLY:				RSP_SEL_CTL
FIRST USED ON OPTION/MODEL: 2080	B-DD-L0057-0	SHEET 1 OF 1	SIZE CODE	NUMBER
			D CS	L0057-0-MAPL
				REV. *





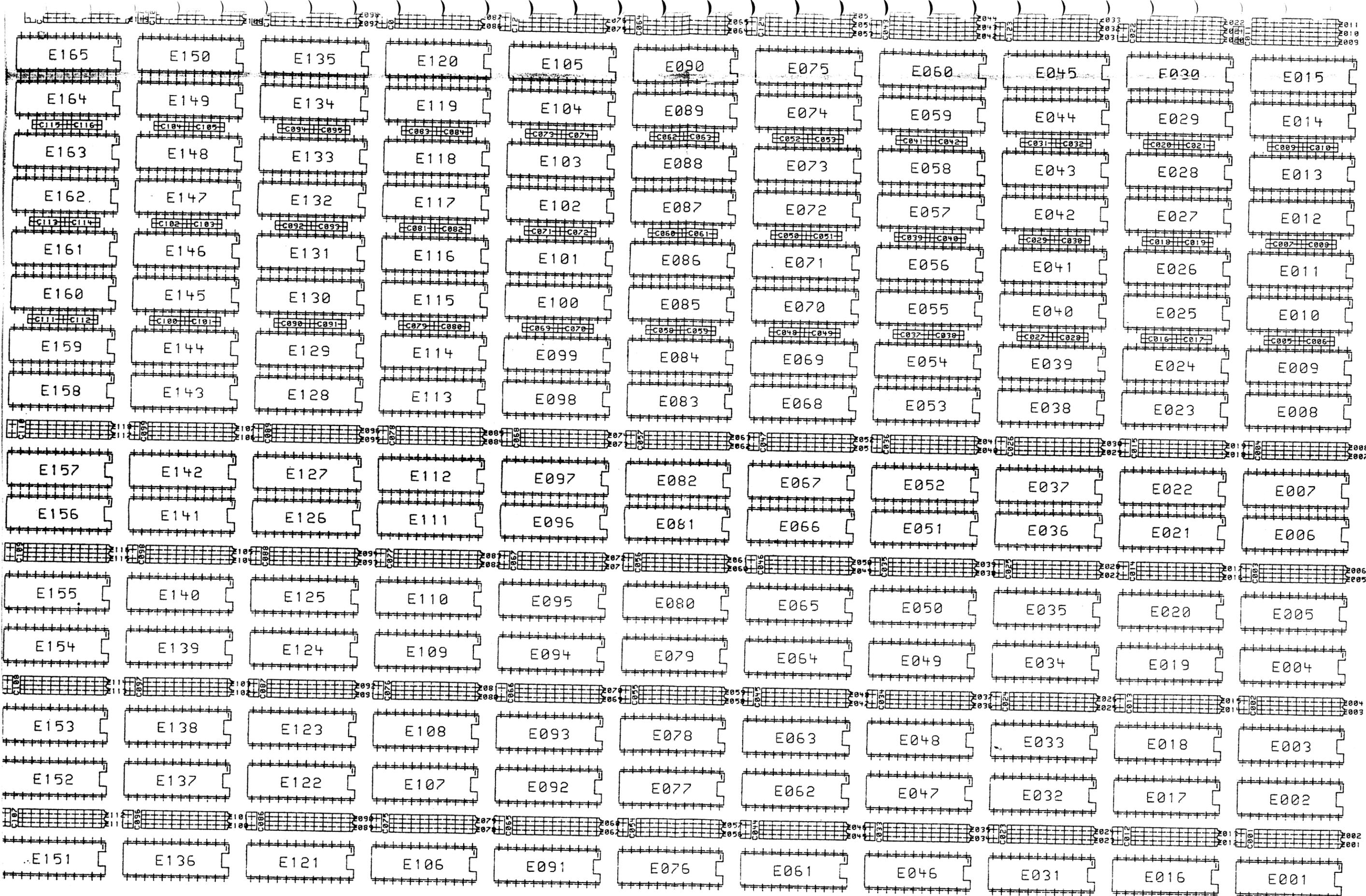
- (A15) NC A15
- (A18) NC A18
- (A20) NC A20
- (A21) NC A21
- (A38) NC A38
- (B26) NC B26
- (B85) NC B85
- (B31) NC B31
- (B18) NC B18
- (B21) NC B21
- (B25) NC B25

- (B53) [B,GND ECL, GND CLK#20]
- (B55) [B,GND ECL, GND CLK#20]
- (B57) [B,GND ECL, GND CLK#20]
- (B59) [B,GND ECL, GND CLK#20]
- (C89) [B,GND ECL, NC]

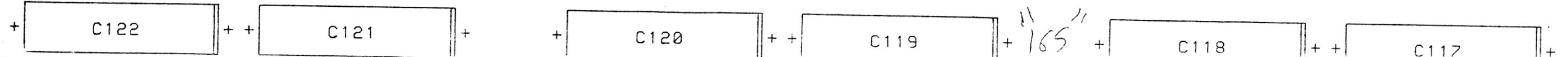


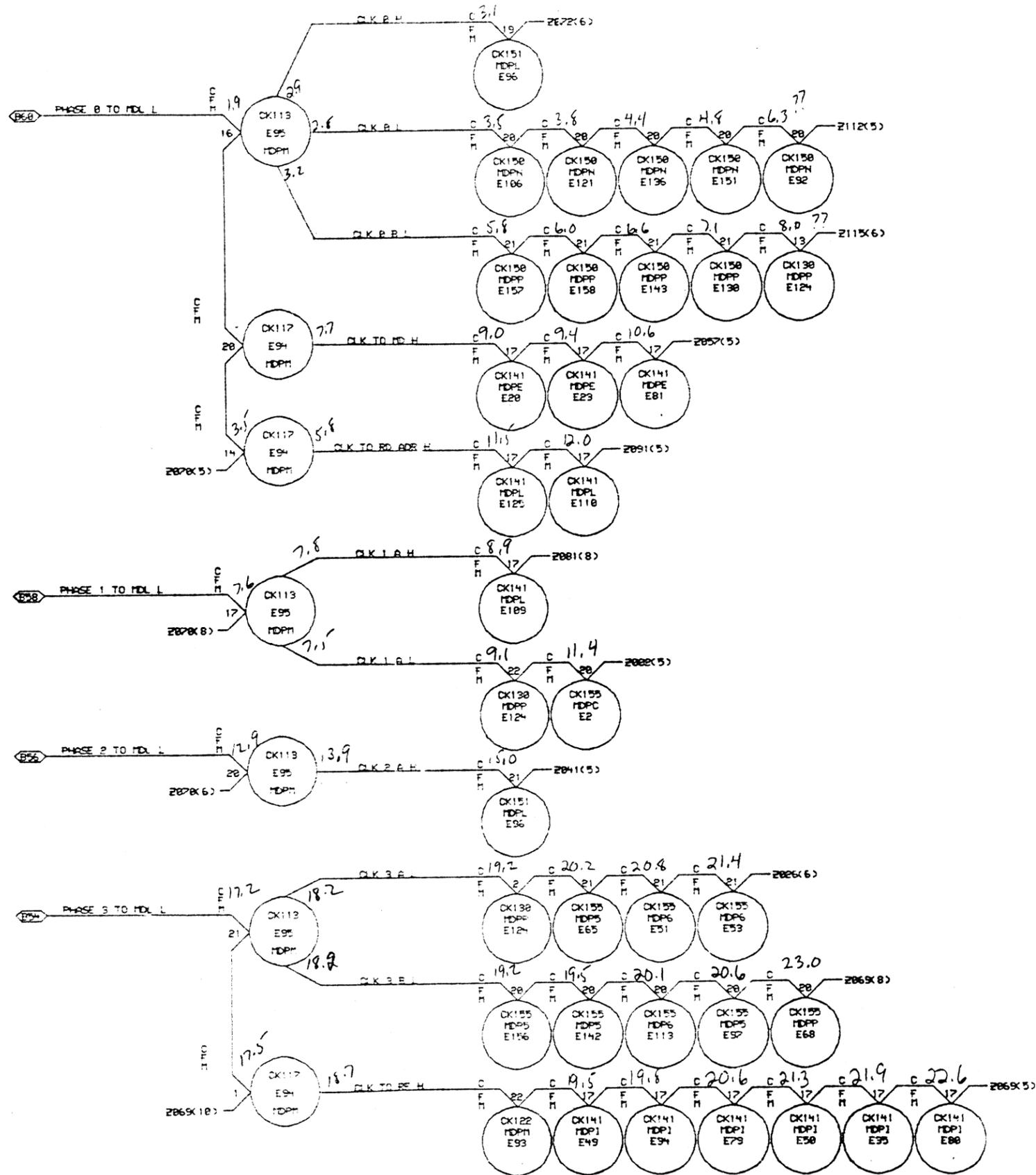
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET 1 OF 1	MAP POWER & GROUND
ARCH: C:\MLER.PLOTS\34\MAPN.DRW		22-NOV-82 14:39	NEXT HIGHER ASSEMBLY:	SIZE CODE
FIRST USED ON OPTION MODEL: 2080		B-DD-L0057-0	NUMBER	REV.
			D CS L0057-0-MAPN	*

VULCAN:<2080.L0060>19MDP1  
VULCAN:<2080.L0060>19MDP2  
VULCAN:<2080.L0060>19MDP3  
VULCAN:<2080.L0060>19MDP4  
VULCAN:<2080.L0060>19MDP5  
VULCAN:<2080.L0060>19MDP6  
VULCAN:<2080.L0060>19MDP7  
VULCAN:<2080.L0060>19MDP8  
VULCAN:<2080.L0060>26MDP9  
VULCAN:<2080.L0060>22MDPA  
VULCAN:<2080.L0060>22MDPB  
VULCAN:<2080.L0060>19MDPC  
VULCAN:<2080.L0060>19MDPD  
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VULCAN:<2080.L0060>19MDPG  
VULCAN:<2080.L0060>19MDPH  
VULCAN:<2080.L0060>26MDPI  
VULCAN:<2080.L0060>19MDPJ  
VULCAN:<2080.L0060>19MDPK  
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MDP



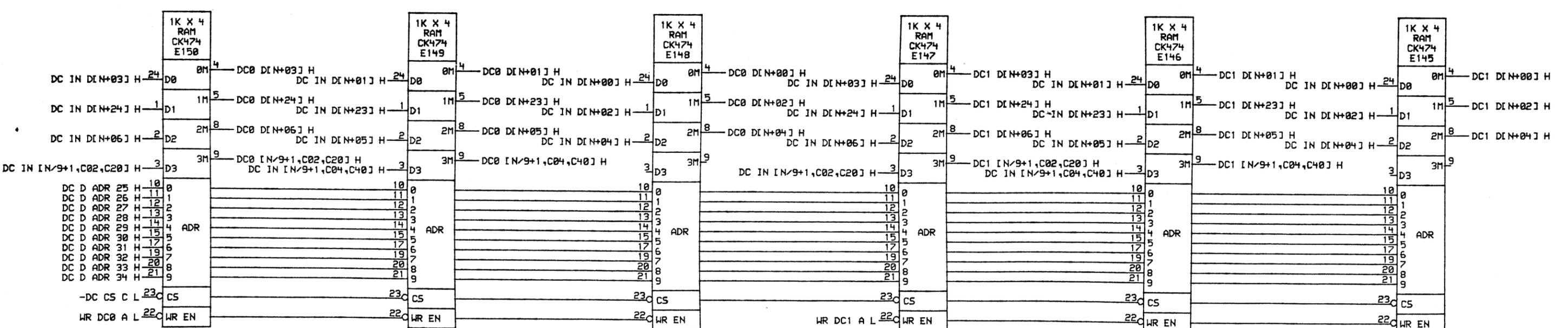
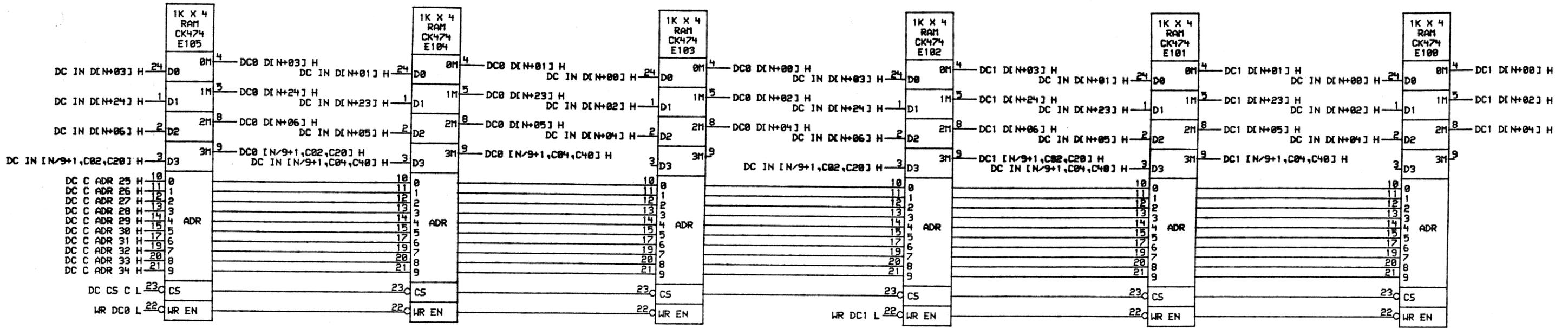


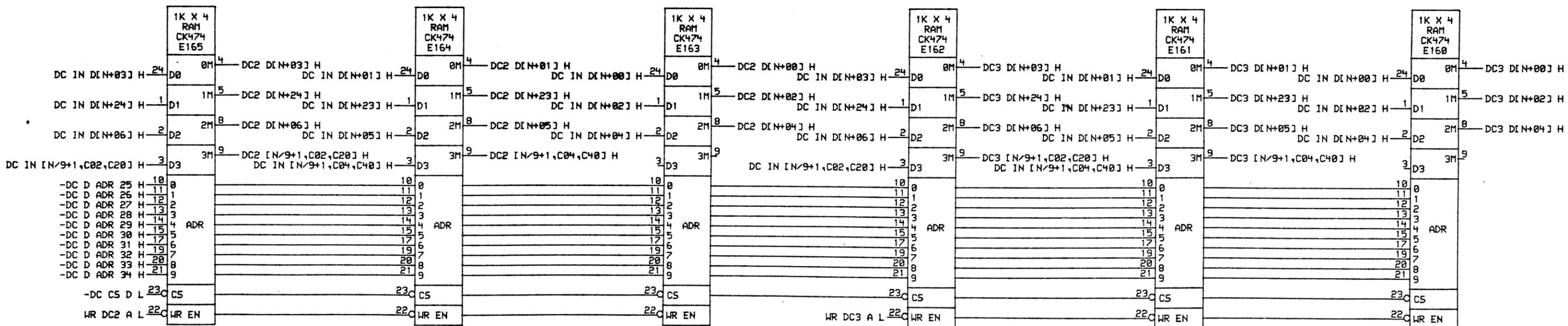
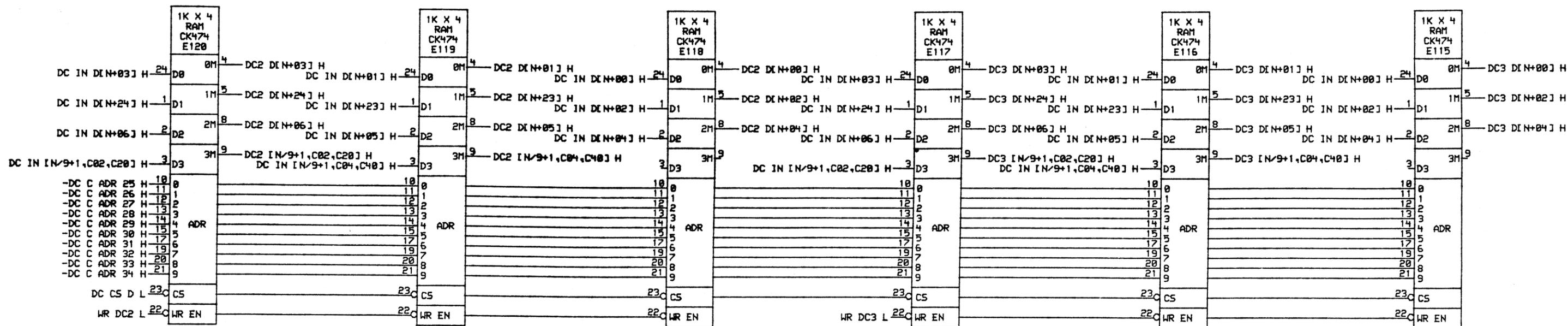
MDP  
 NEEDS LOCKING  
 ① E92 CLK 0 L  
 ② E724 CLK 0 B L  
 E96 CLK 0 H  
 ③ CLK to RF  
 ④ CLK to D Reg

CLK TO D Reg  
 19.3 19.7 20.6  
 E92 E724 E96

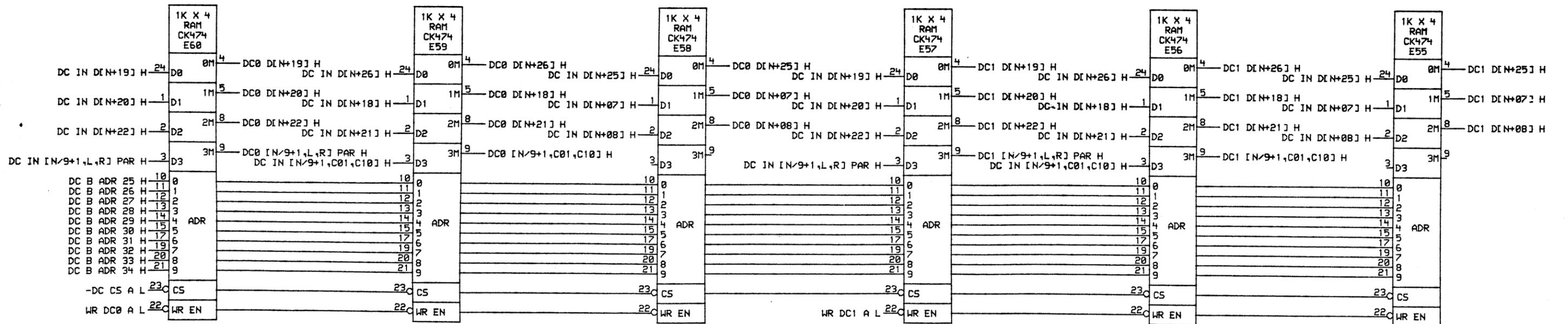
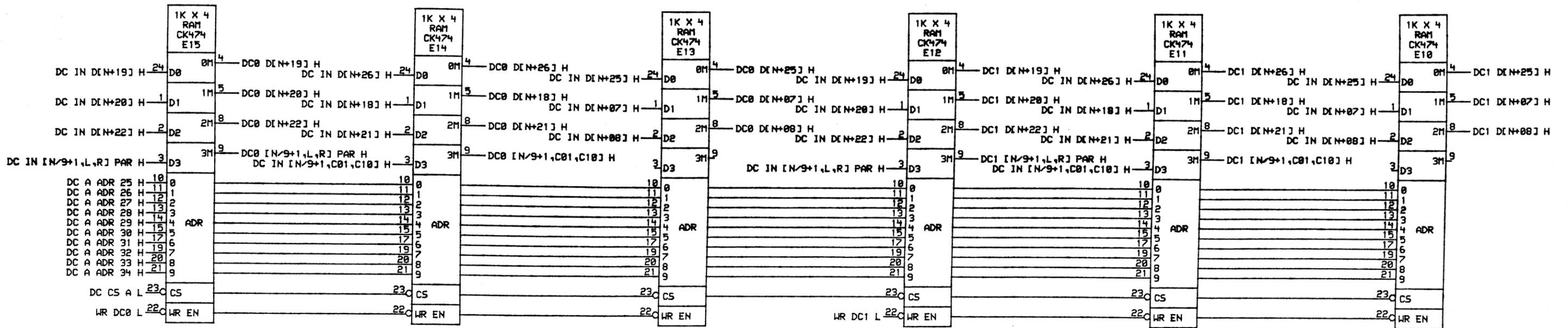
MDP

DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD	LOCATION:	
FIRST USED ON OPTION/MODEL:				SIZE (CODE)
				NUMBER
				REV.

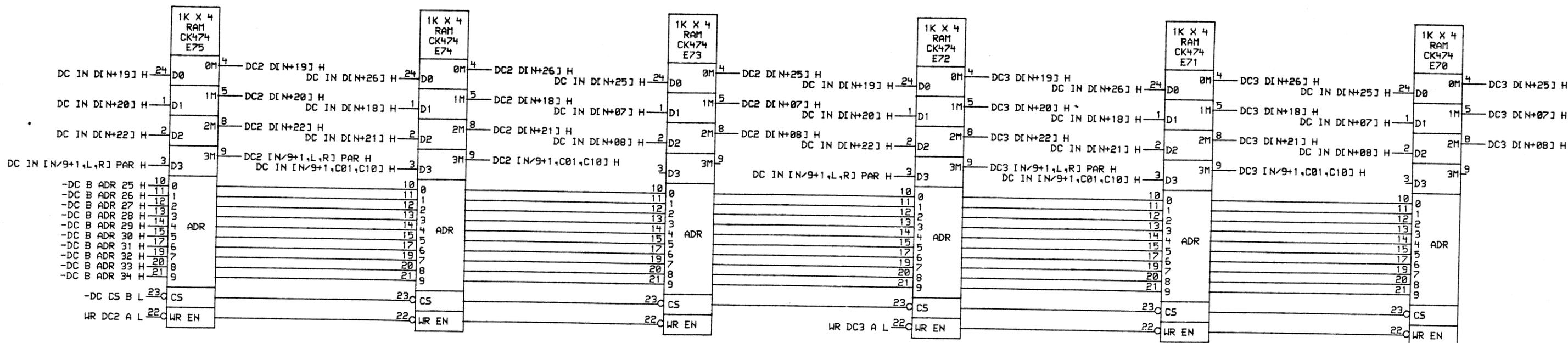
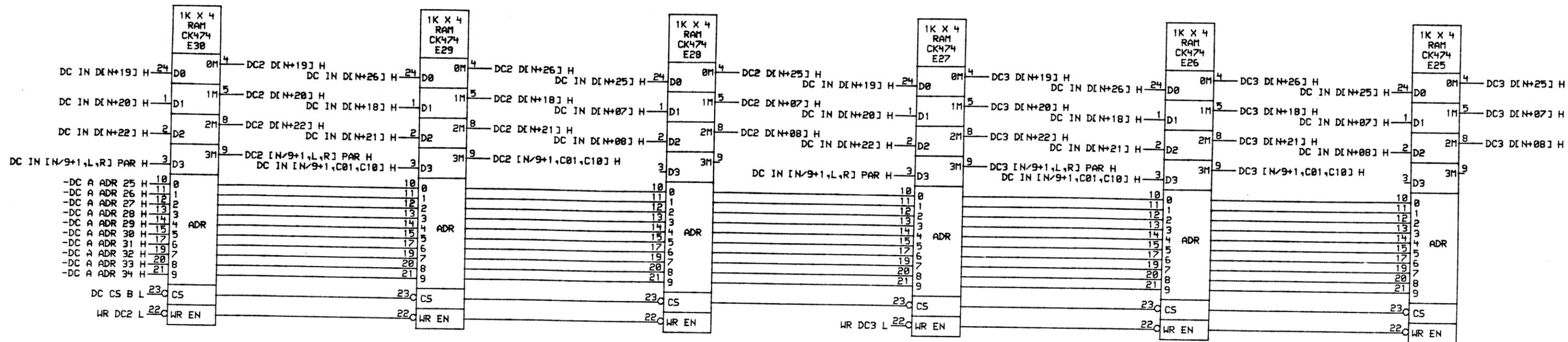




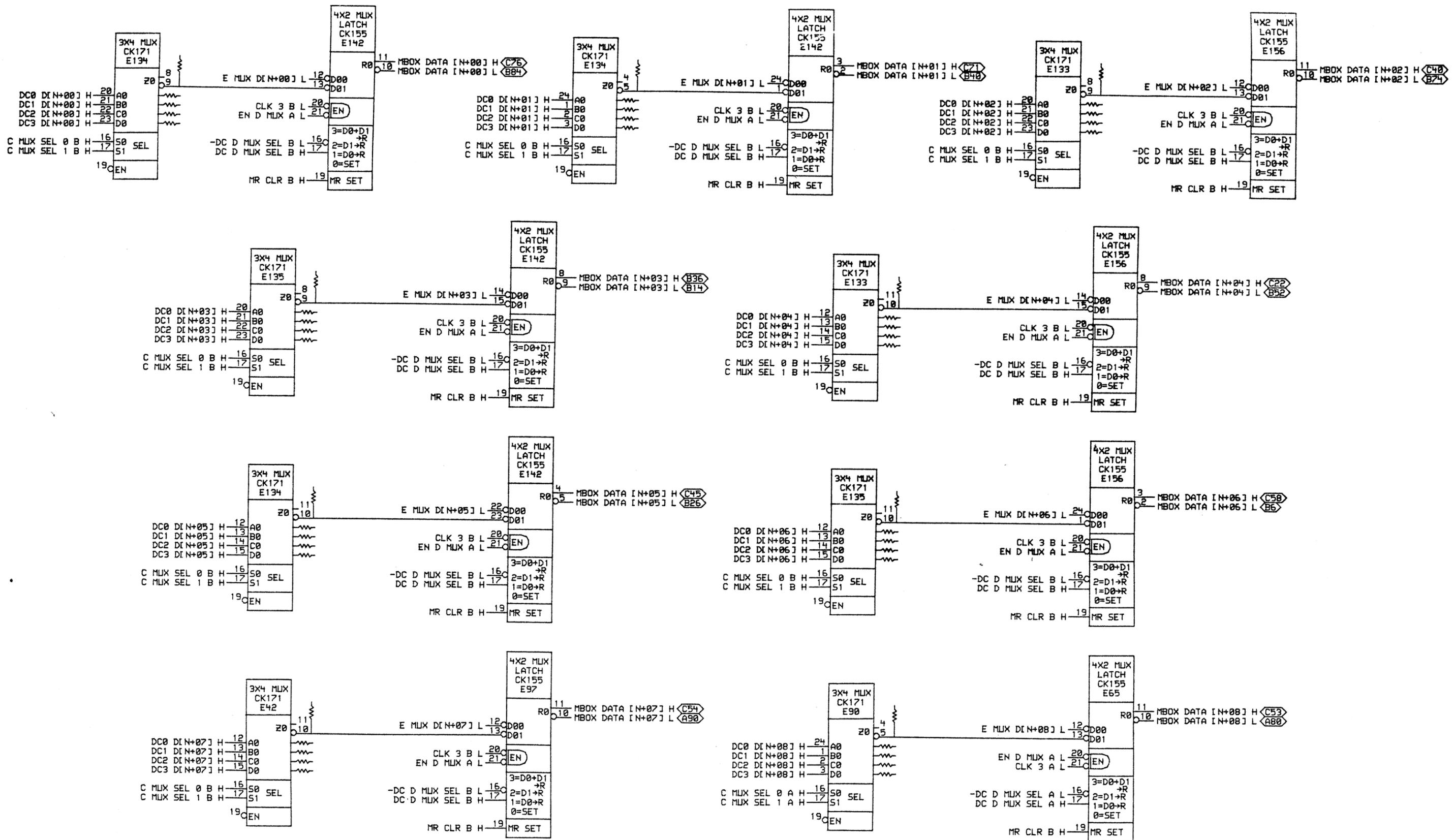
DRN.	DATE	ENG.	DATE	TITLE:	CACHE 2,3 RAM			
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	SIZE	CODE	NUMBER	REV.
VULCAN (2000-L0060) MDP2.DRN	29-JAN-82	10:42	2	4	D	CS	L0060-0-MDP2	*
FIRST USED ON OPTION/MODEL: 2080					B-DD-L0060-0			



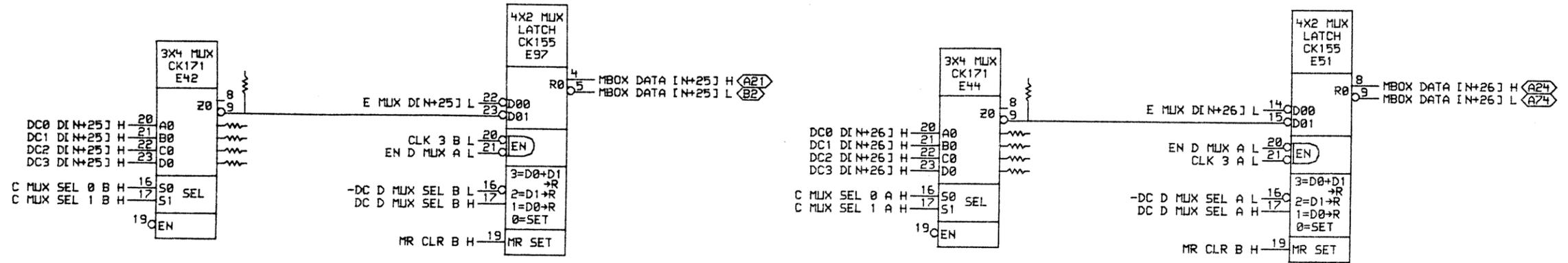
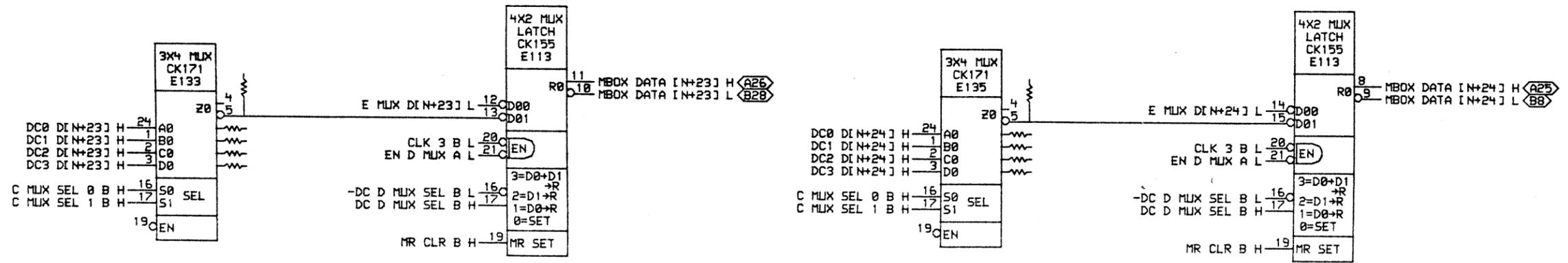
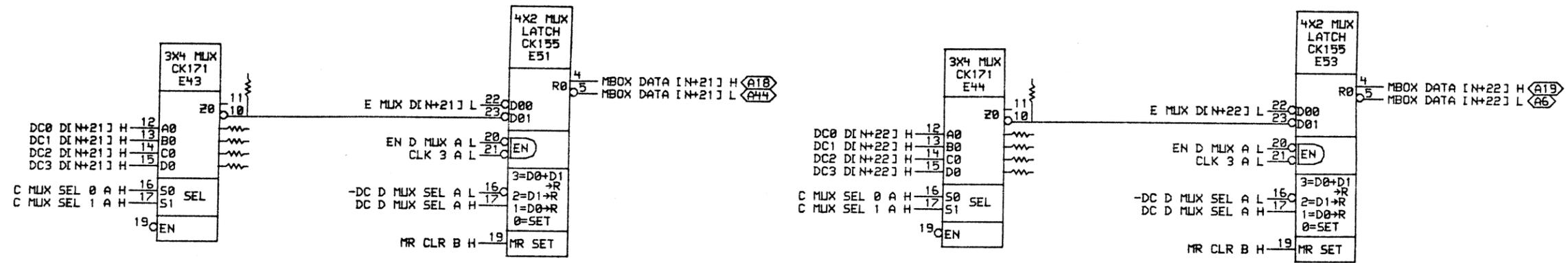
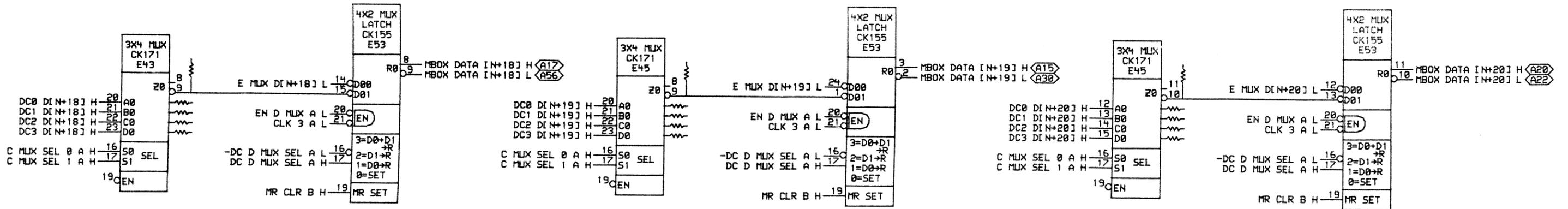
DRN.	DATE	ENG.	DATE	TITLE:	CACHE 0,1 RAM			
CHK'D.	29-JAN-82			BOARD LOCATION:				
VULCAN (P886-L0060) MDP3.DRW 129-JAN-82 10:43 NEXT HIGHER ASSEMBLY:					SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL: 2080					D	CS	L0060-0-MDP3	*



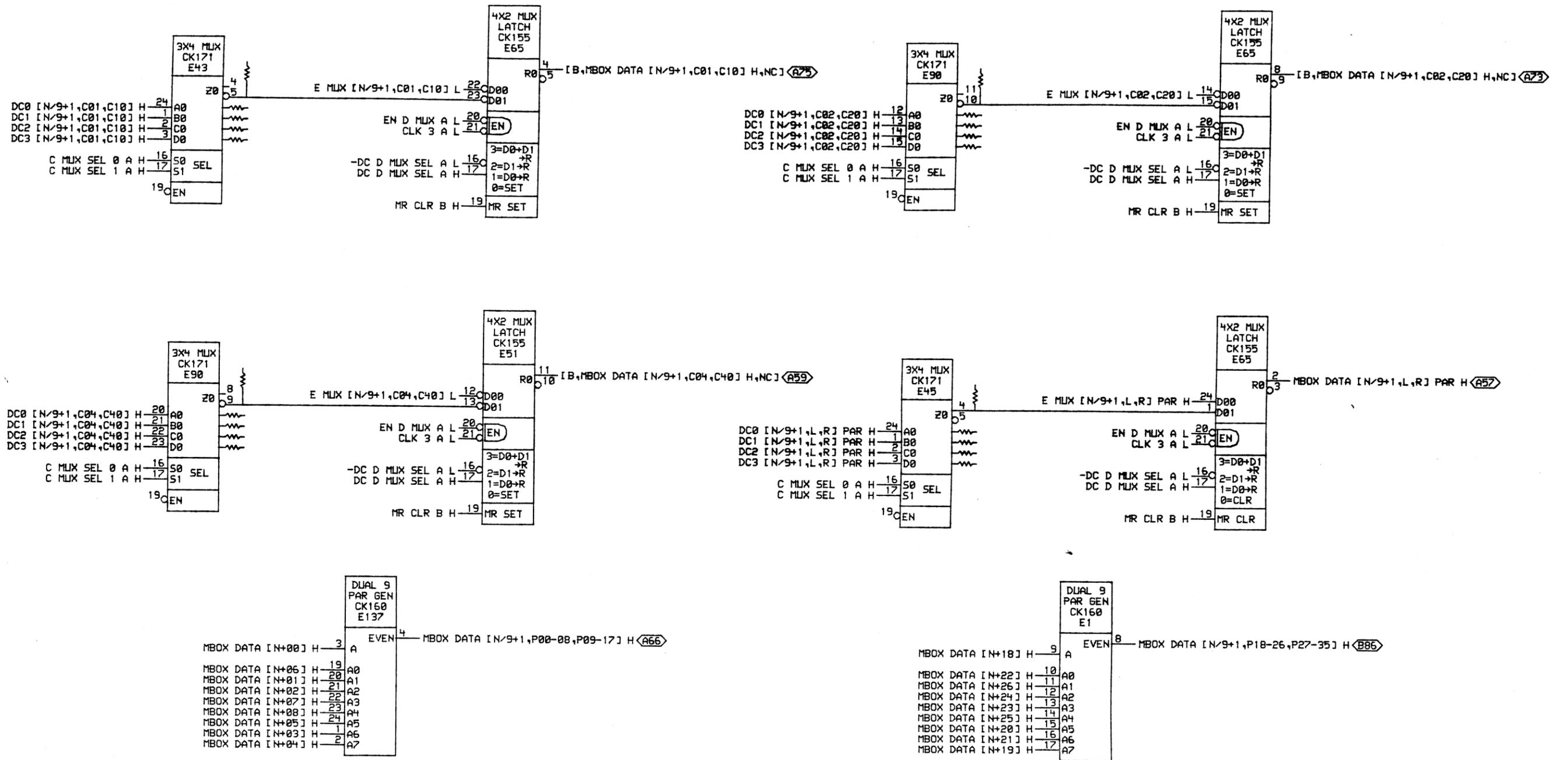
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	29-JAN-82	BOARD LOCATION:		CACHE 2,3 RAM
		SHEET	4 OF 4	
VULCANI <2080.L0060>19MDP4.DRW		29-JAN-82 10:43	NEXT HIGHER ASSEMBLY:	SIZE CODE
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0060-0		NUMBER
				L0060-0-MDP4
				REV. *



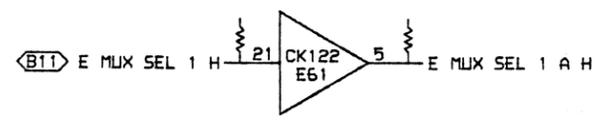
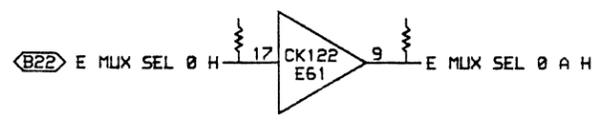
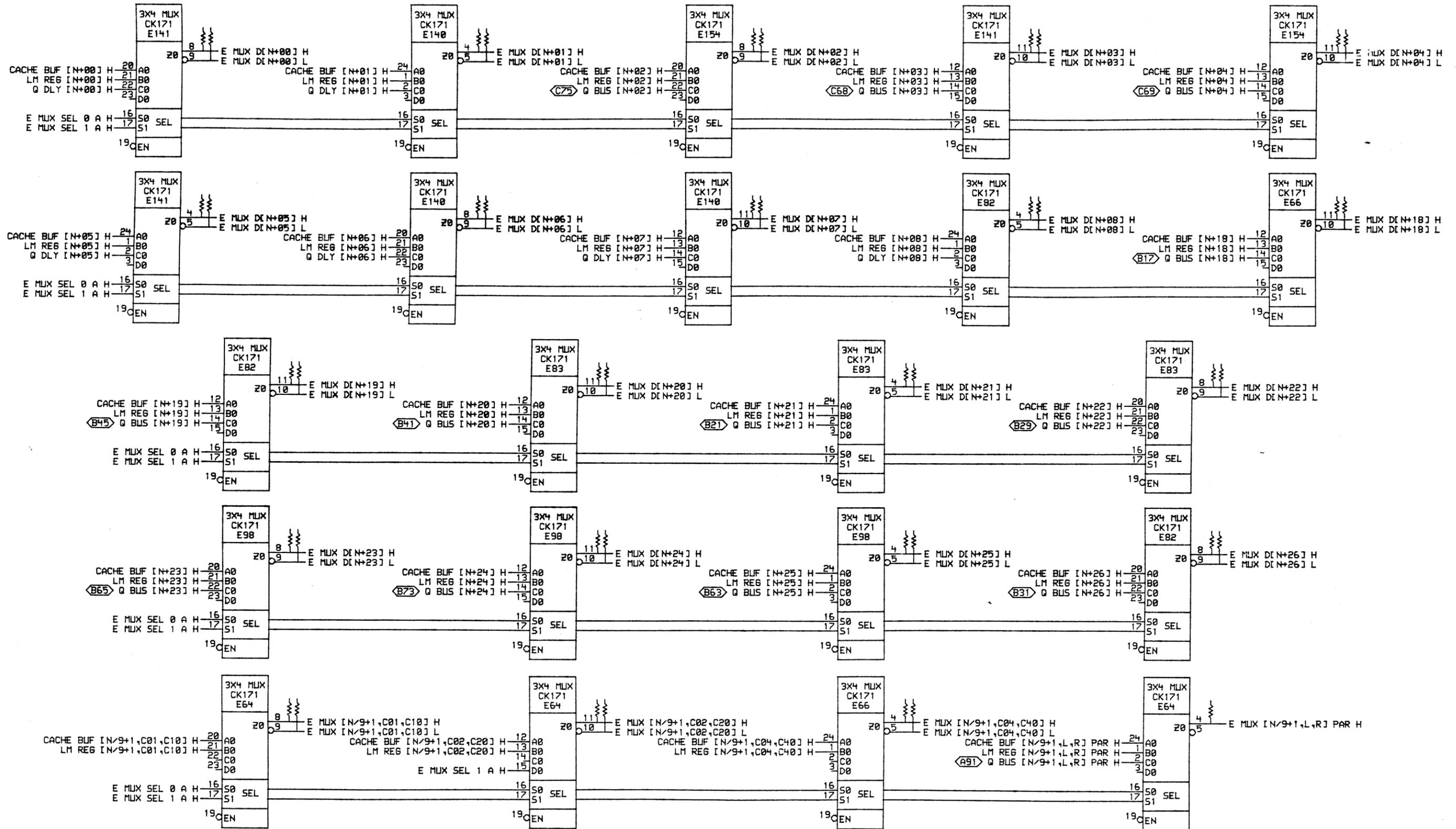
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FIRST USED ON OPTION/MODEL: 2080				00-08 / 09-17
NEXT HIGHER ASSEMBLY: B-DD-L0060-0				REV. *
SHEET 1 OF 3				SIZE CODE NUMBER
D C5 L0060-0-MDP5				



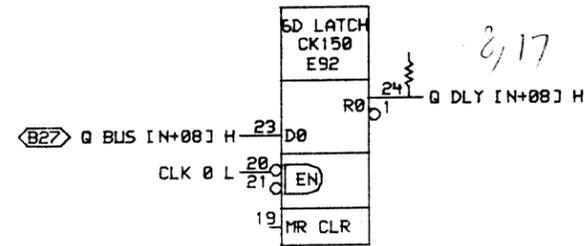
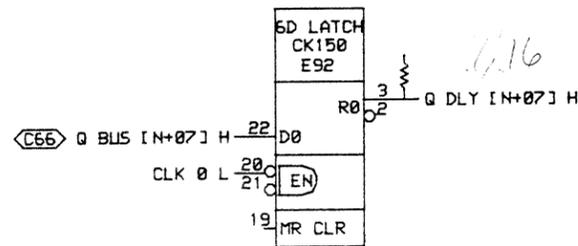
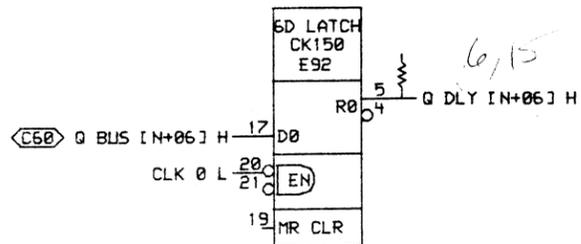
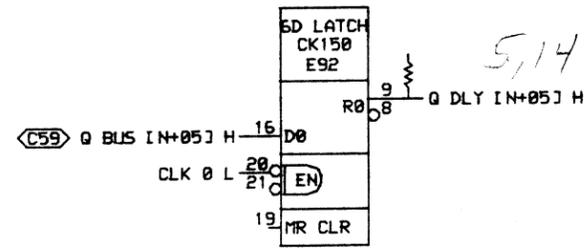
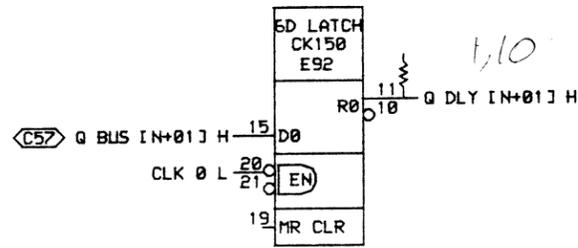
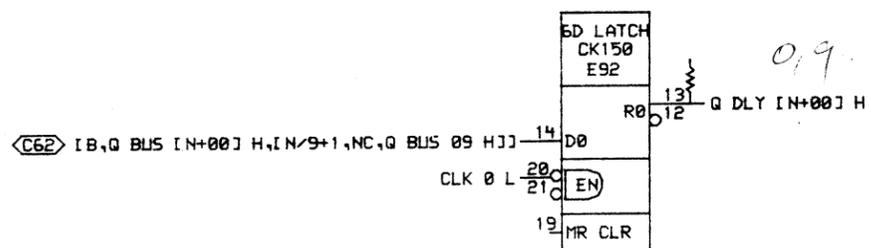
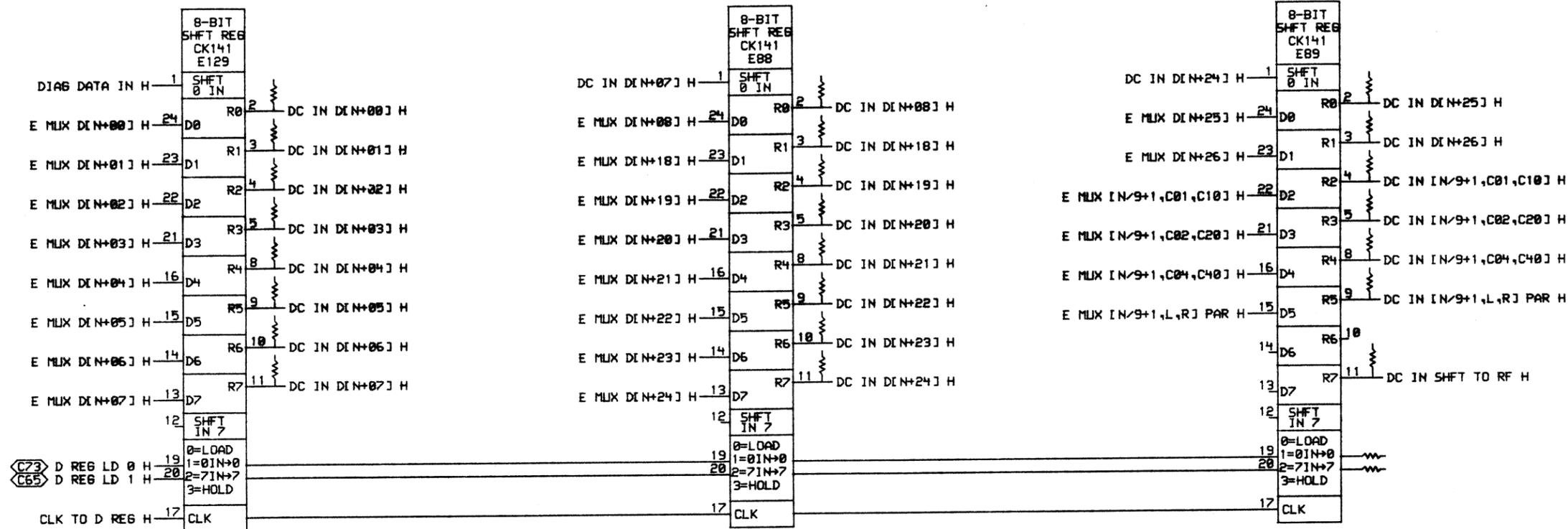
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CHK'D.	DATE	BOARD LOCATION:		MBOX DATA
		SHEET	2 OF 3	18-26 / 27-35
VIL CAN (2000.L0060) 9006.DRW 29-JAN-02 10:43		NEXT HIGHER ASSEMBLY:		SIZE CODE
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0060-0		D CS L0060-0-MDP6
				NUMBER
				REV. *



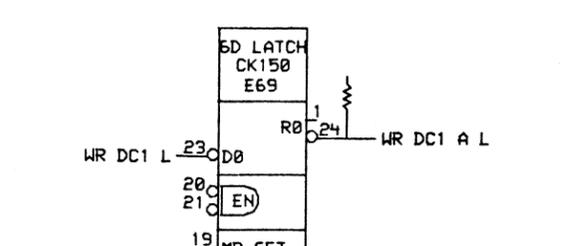
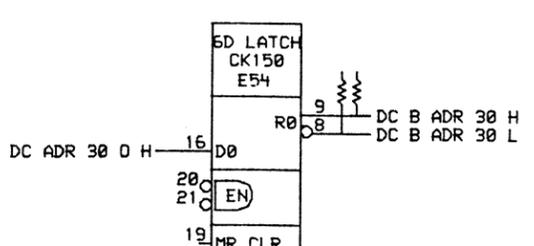
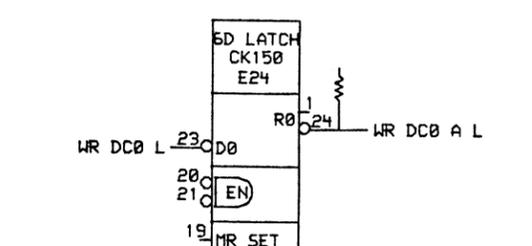
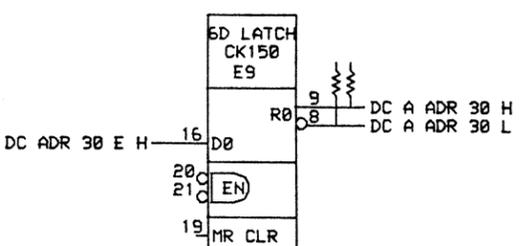
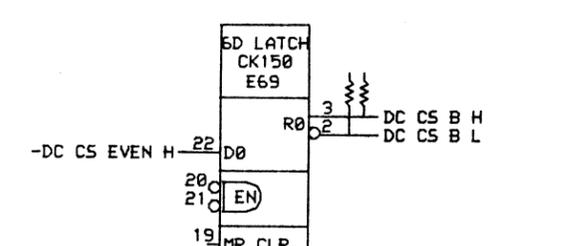
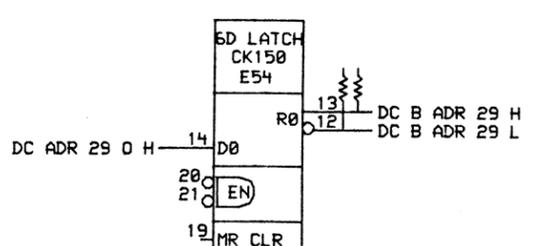
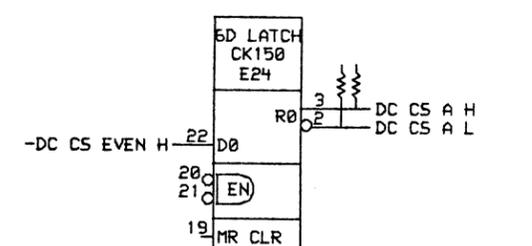
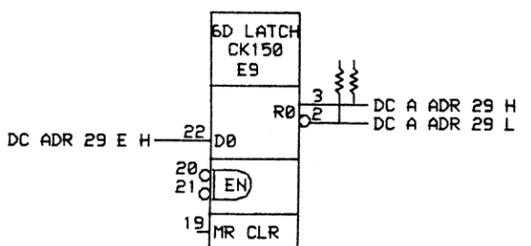
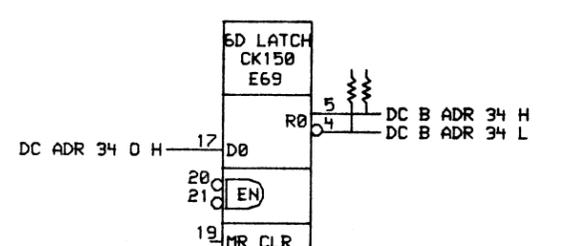
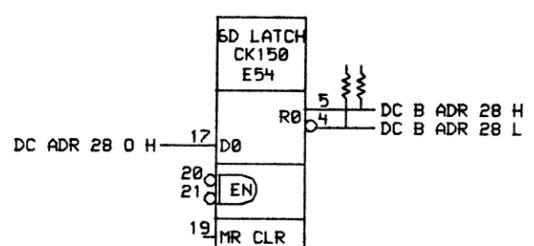
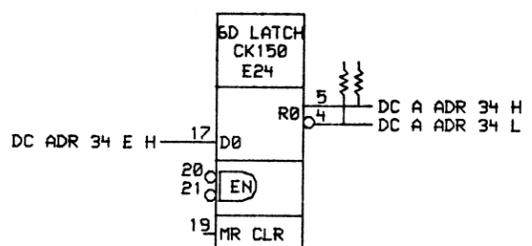
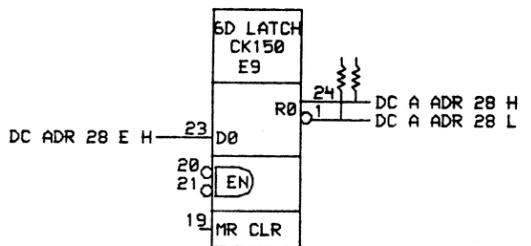
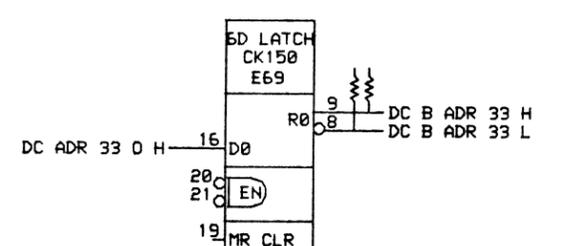
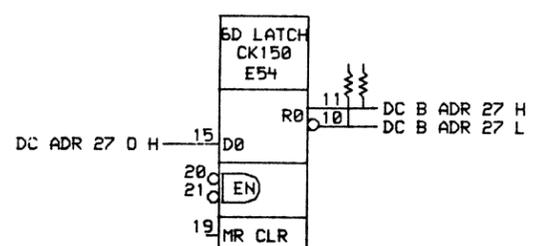
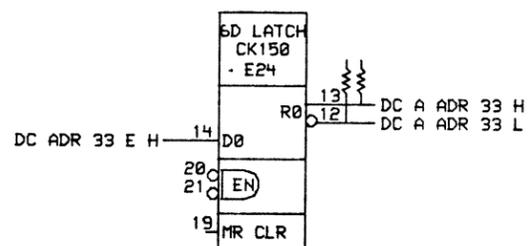
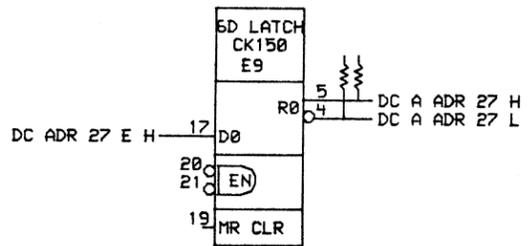
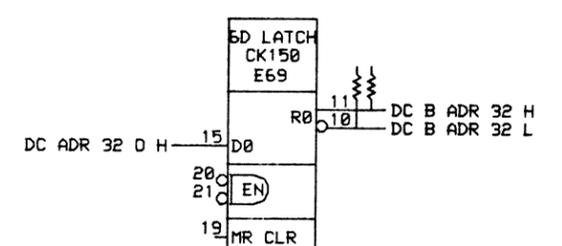
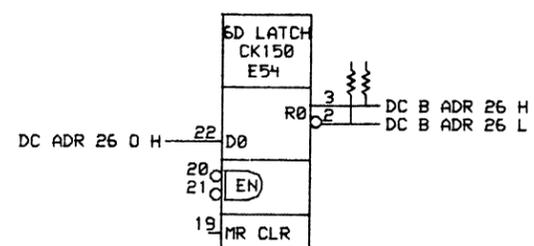
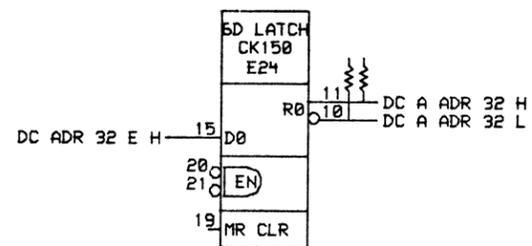
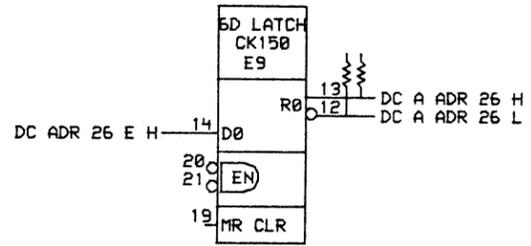
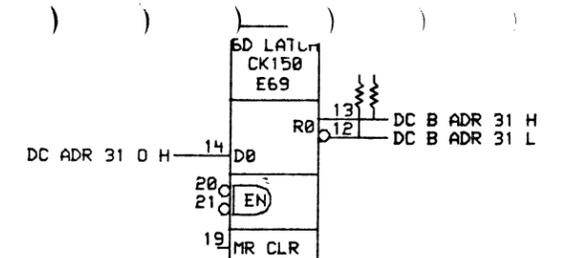
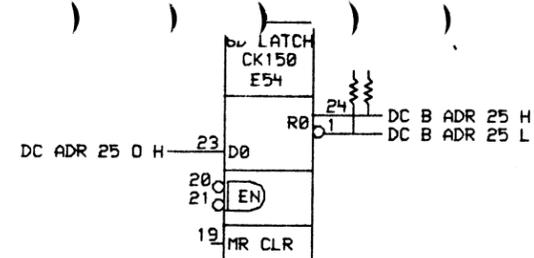
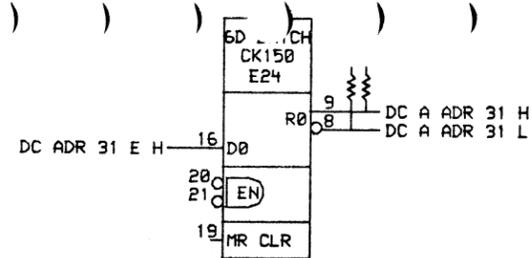
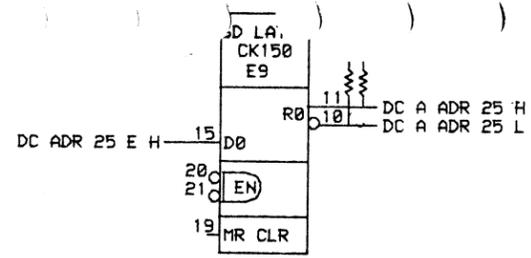
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	29 JAN 82			MBOX DATA
		BOARD LOCATION:		C BITS & PAR
		SHEET 3 OF 3		
FIRST USED ON OPTION/MODEL: 2080				SIZE CODE
NEXT HIGHER ASSEMBLY: B-DD-L0060-0				D CS
				NUMBER
				L0060-0-MDP7
				REV.
				*



DRN.	DATE	ENG.	DATE	TITLE:	E MUX		
CHK'D.	DATE	BOARD LOCATION:					
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER	REV.
2080		B-DD-L0060-0		D	C5	L0060-0-MDP8	*



DRN.	DATE	ENG.	DATE	TITLE:	DC IN		
CHK'D.	DATE	BOARD LOCATION:	SHEET	1	OF	1	
VULCAN: (2000-L0060)26MDP9.DRW [02-AUG-82 11:42				NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL:				B-DD-L0060-0	D	CS	L0060-0-MDP9
							REV.
							*



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REVISIONS
CHK CHANGE NO. REV

<b>digital</b>	DATE	ENG.	DATE	TITLE:
	09-APR-82			DATA CACHE EVEN ADDRESS
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
	07-APR-82 19:18	NEXT HIGHER ASSEMBLY:	B-DD-L0060-0	1
FIRST USED ON OPTION/MODEL:		SIZE	CODE	NUMBER
		D	CS	L0060-0-MDPA
				REV. *

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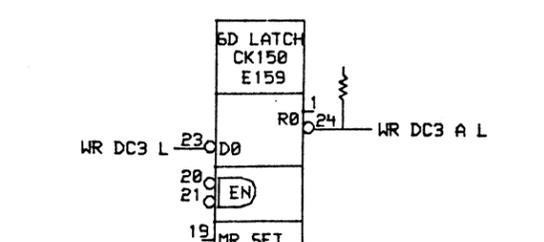
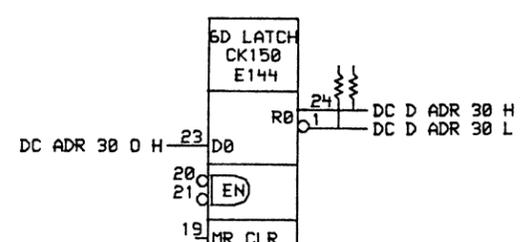
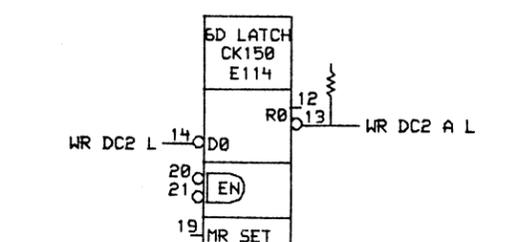
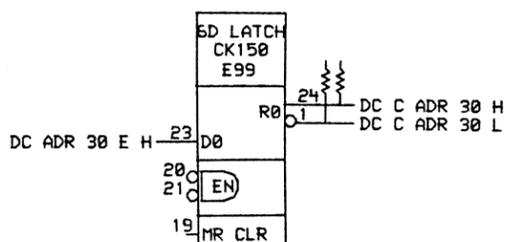
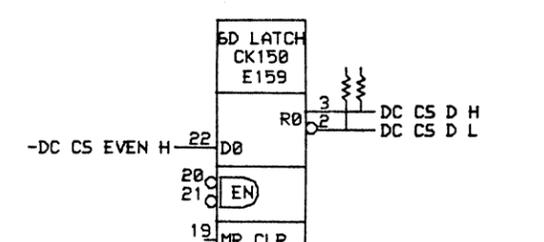
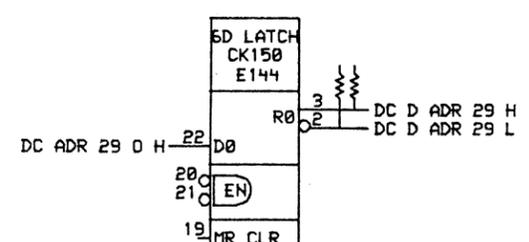
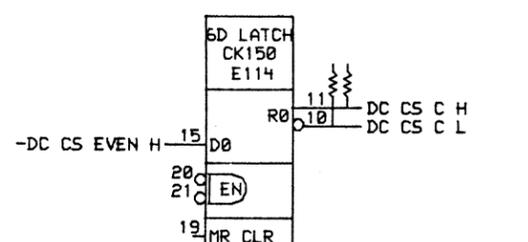
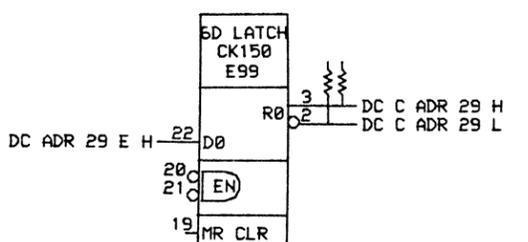
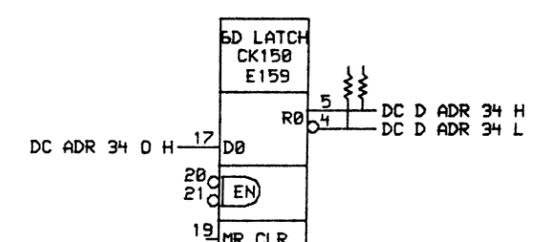
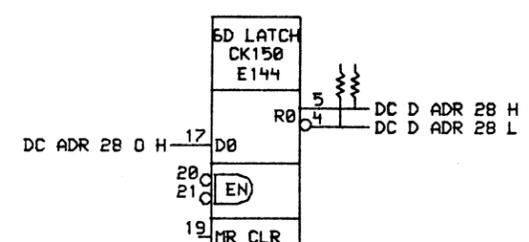
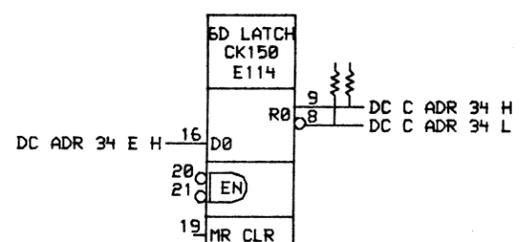
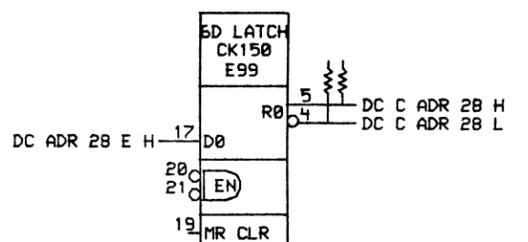
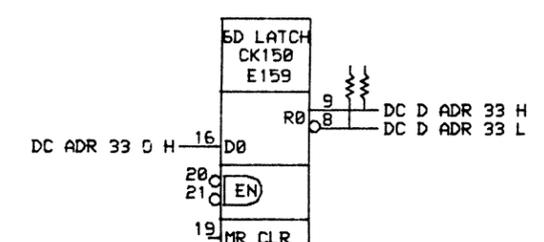
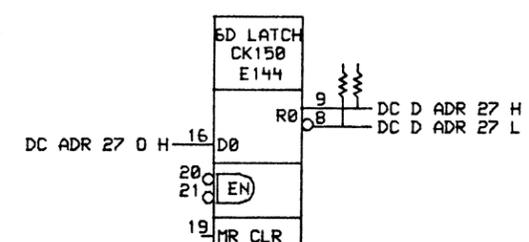
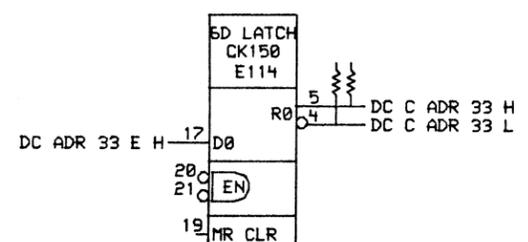
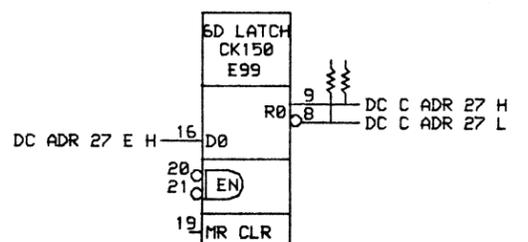
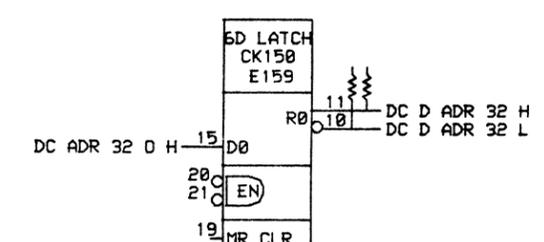
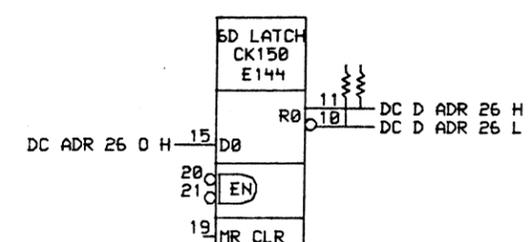
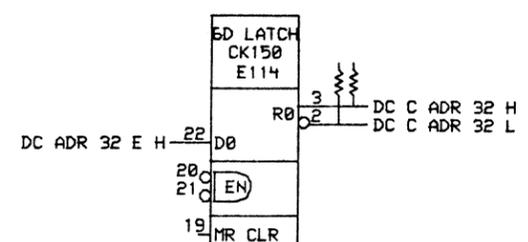
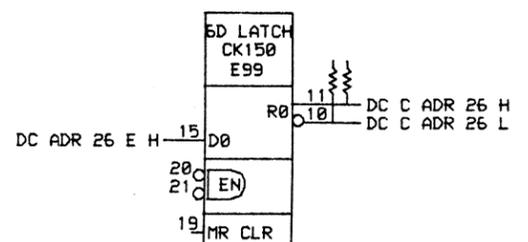
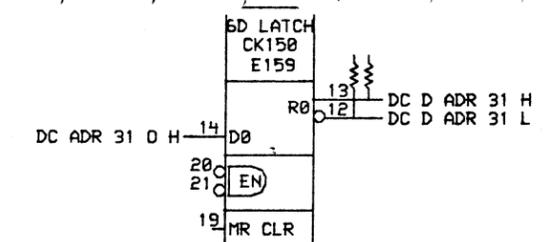
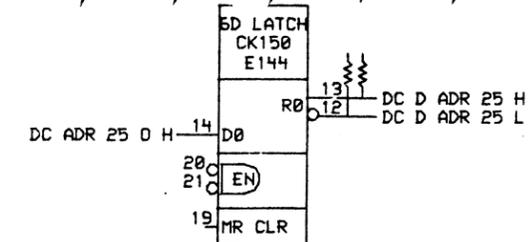
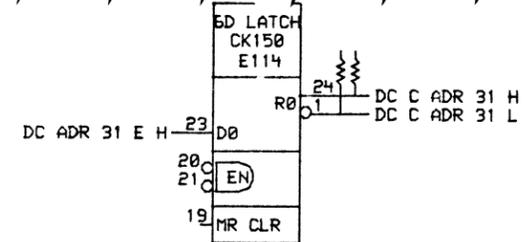
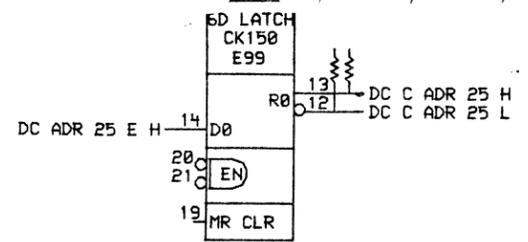
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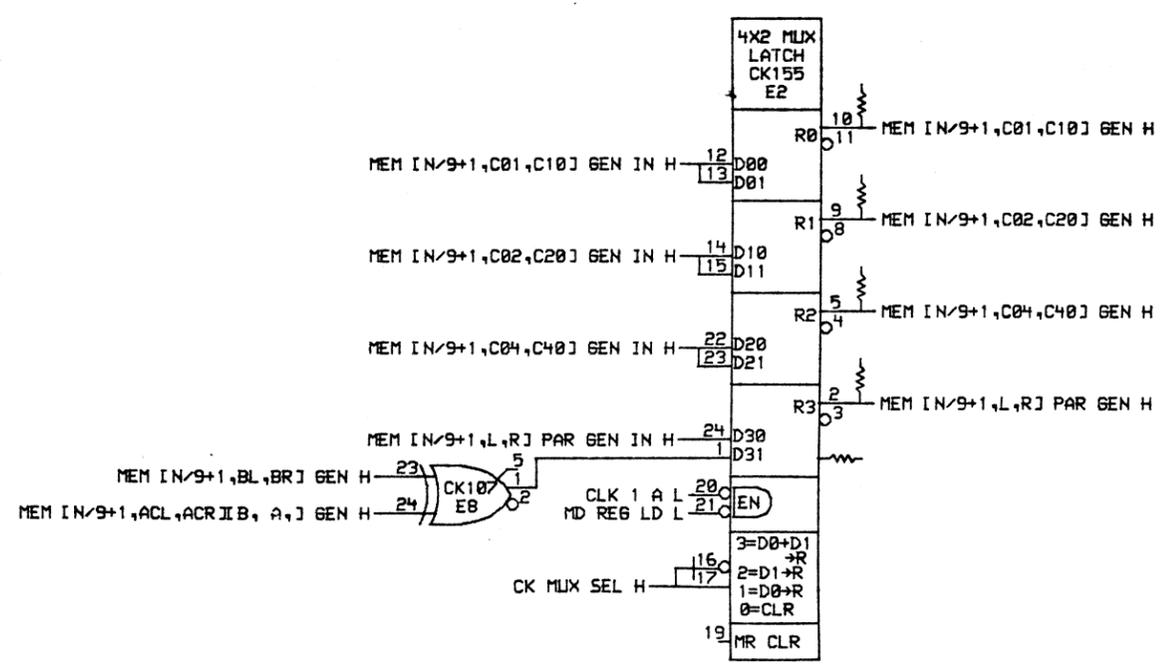
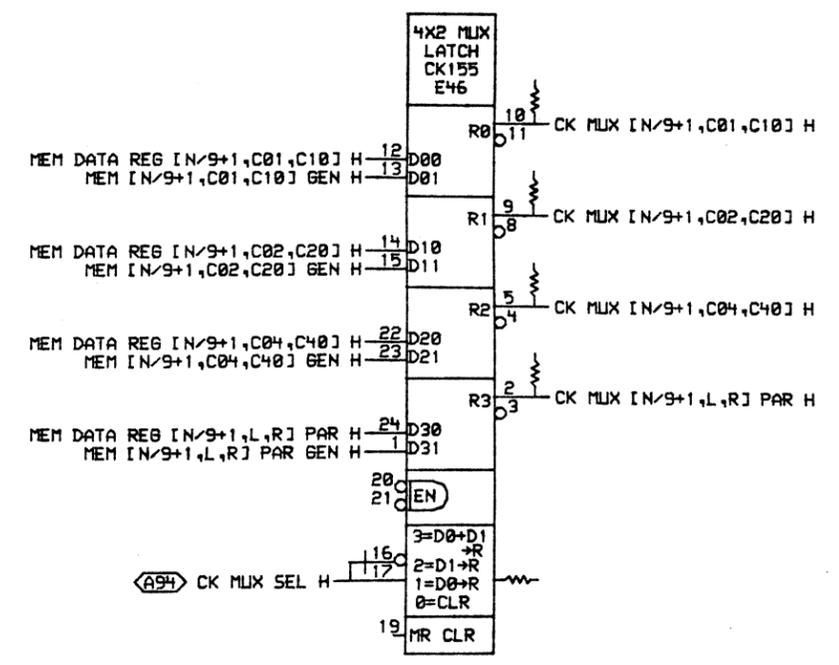
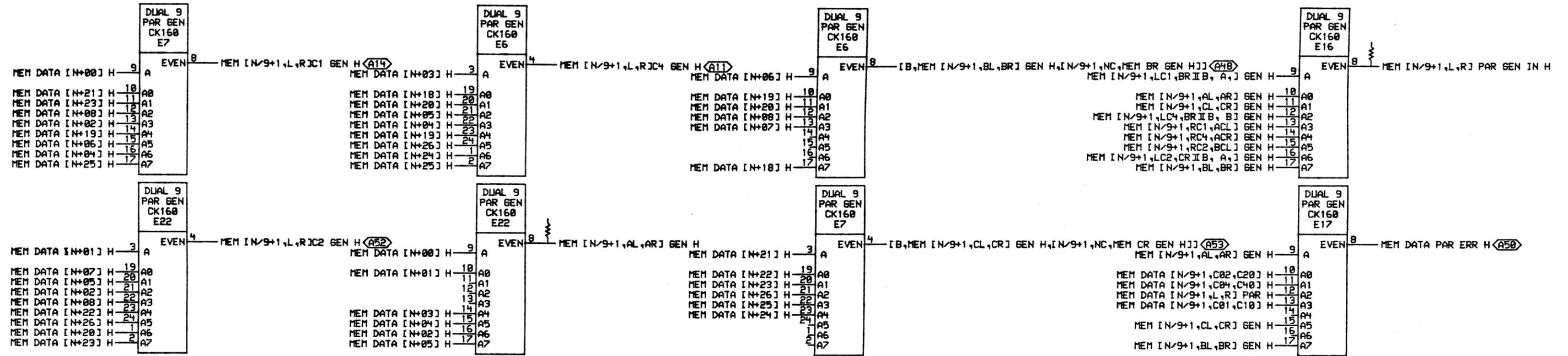
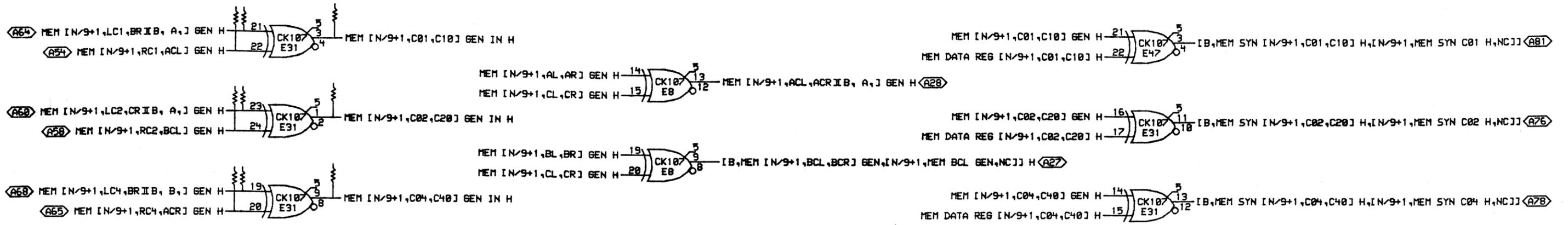
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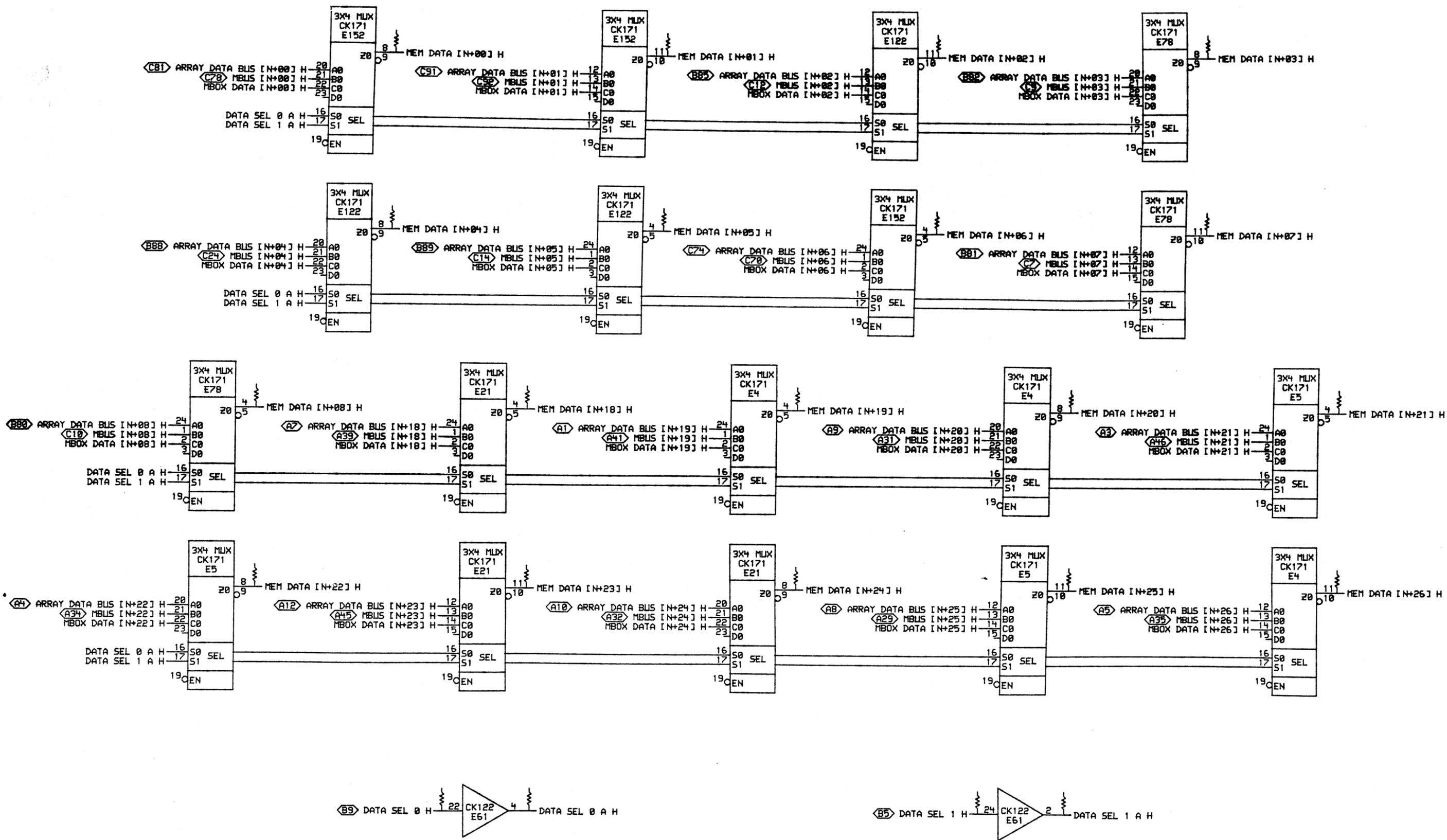
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REVISIONS	
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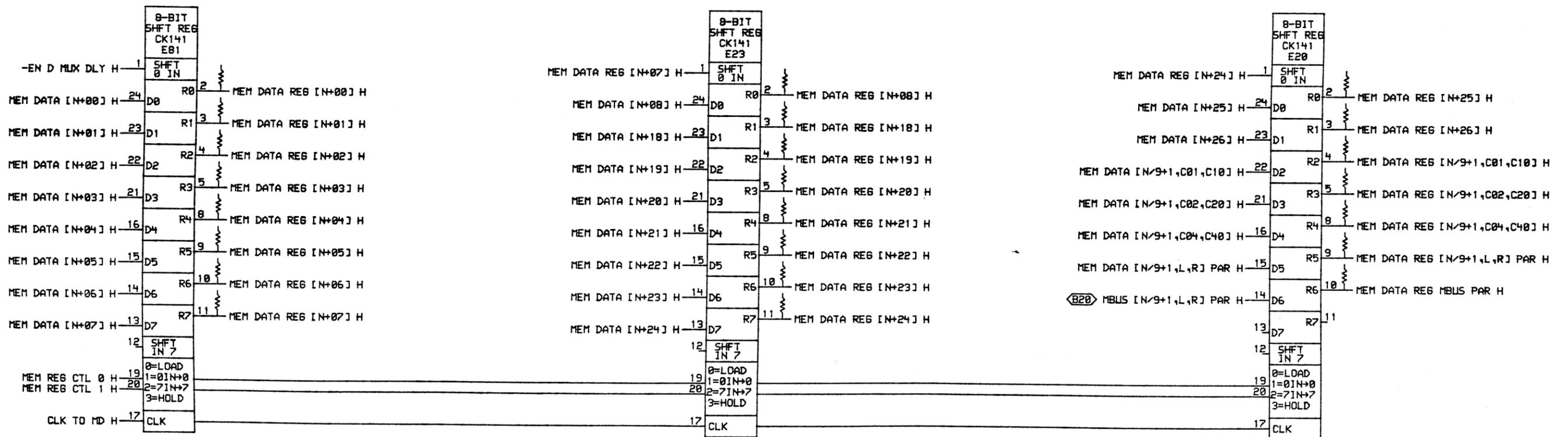
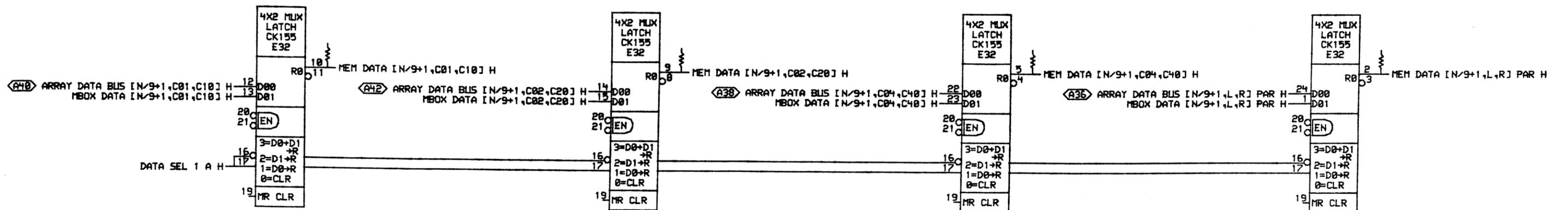
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	DATE: 07-APR-82 19:19	BOARD LOCATION: SHEET 1 OF 1	NEXT HIGHER ASSEMBLY: B-DD-L0060-0	SIZE CODE: D CS L0060-0-MDPB
FIRST USED ON OPTION/MODEL:		NUMBER: L0060-0-MDPB		REV. *



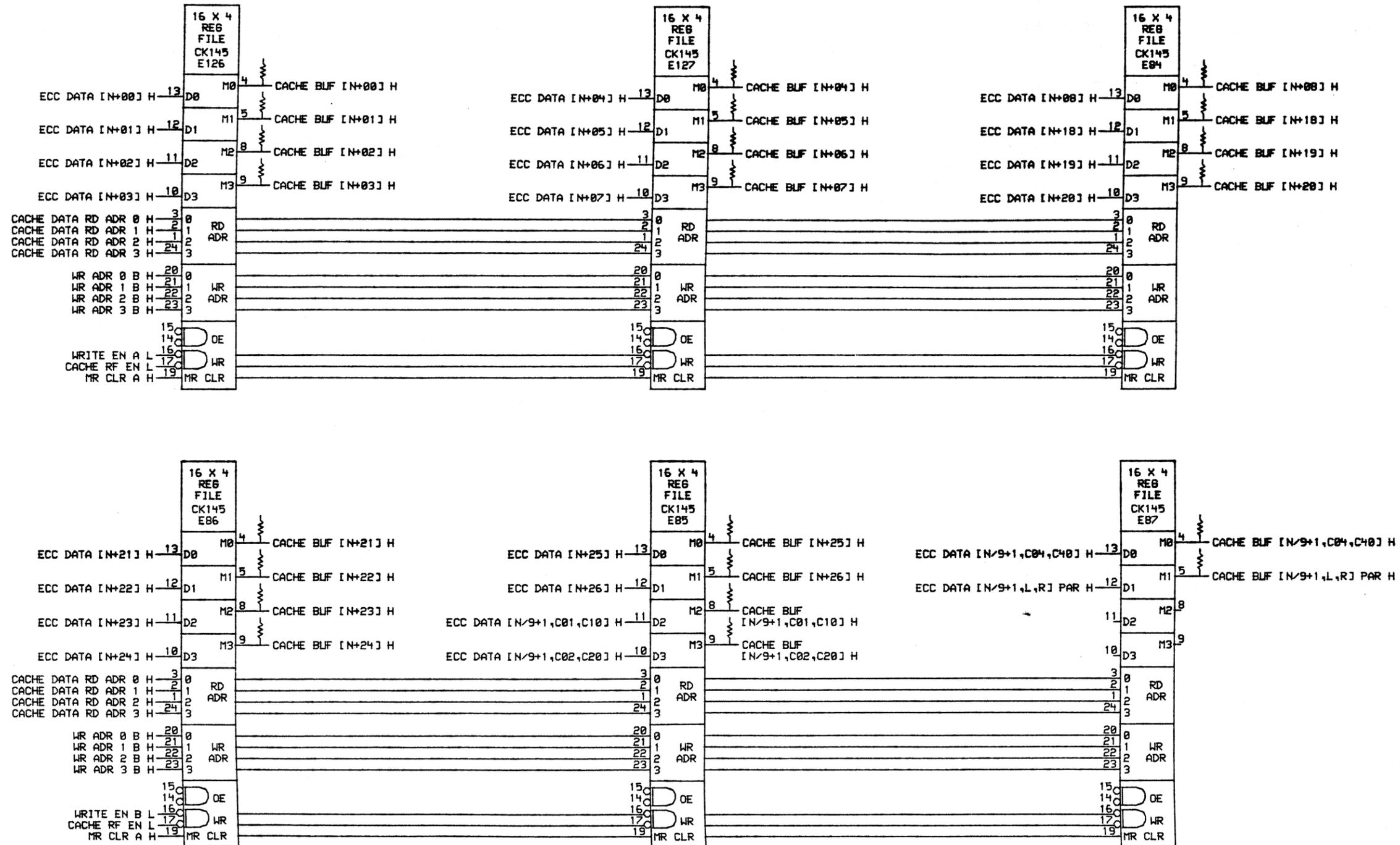
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CHK'D.	DATE	BOARD LOCATION:	SHEET	MEM DATA ECC GEN
FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0060-0		REV. *



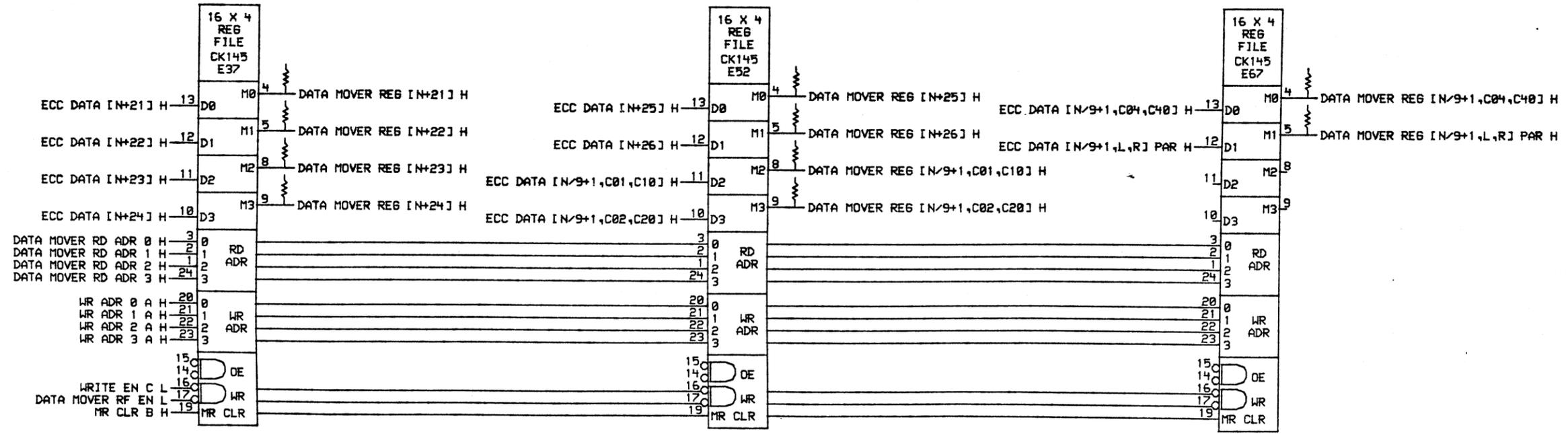
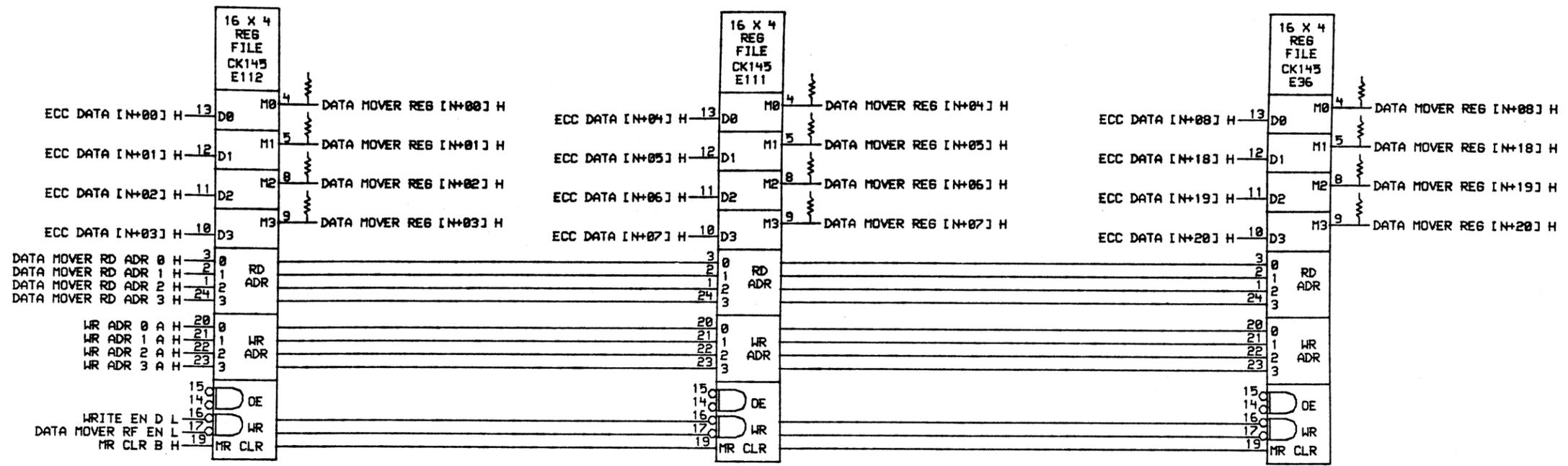
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FIRST USED ON OPTION/MODEL: 2000 B-DD-L0060-0				D CS
				NUMBER
				L0060-0-MDPD
				REV.
				*



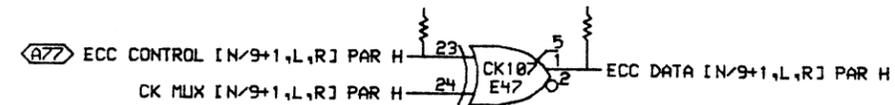
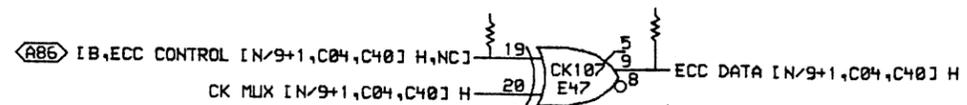
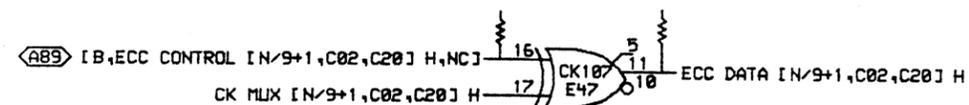
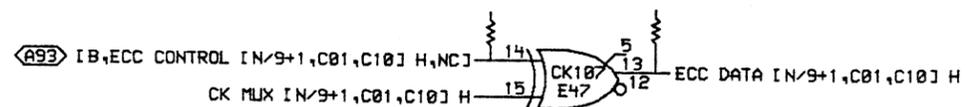
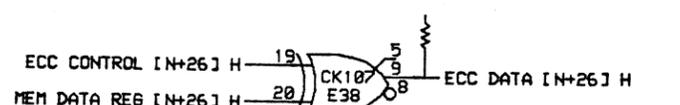
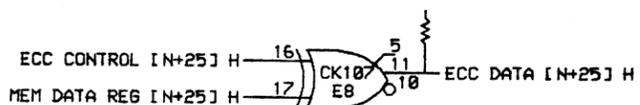
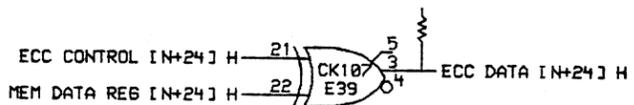
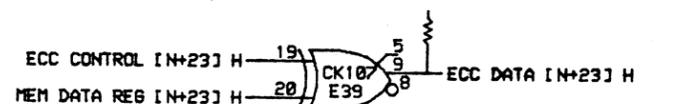
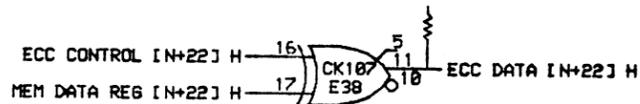
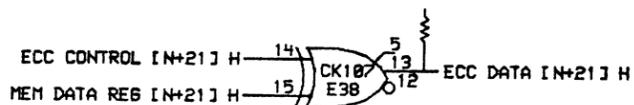
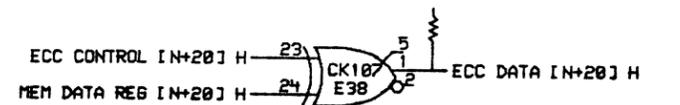
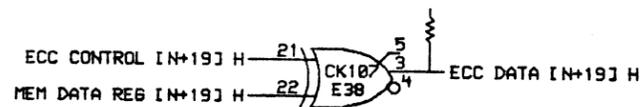
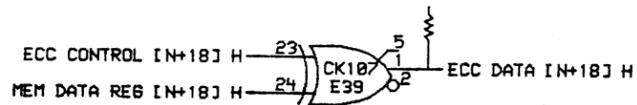
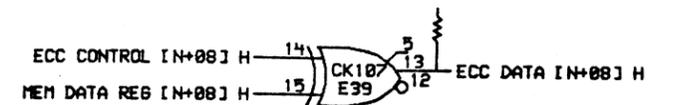
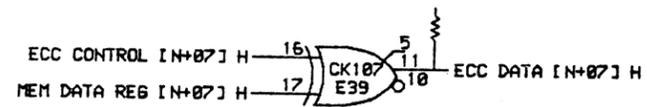
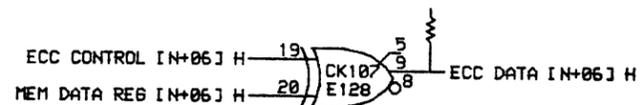
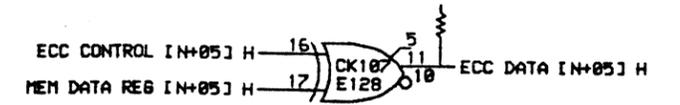
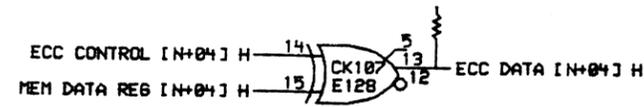
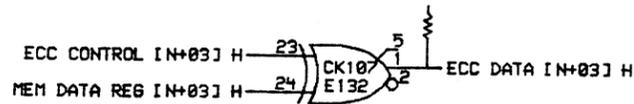
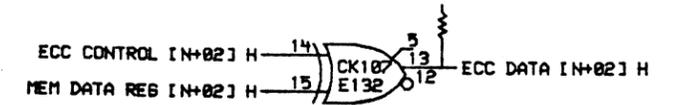
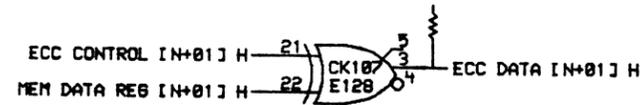
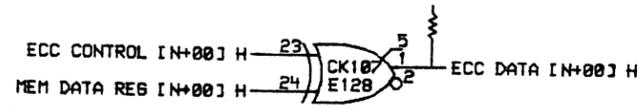
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VIL.CAN(2000.L0060)190PE.DRW 29-JAN-82 10:47				NEXT HIGHER ASSEMBLY:	D	CS	L0060-0-MDPE
FIRST USED ON OPTION/MODEL: 2080				B-DD-L0060-0	REV.	*	



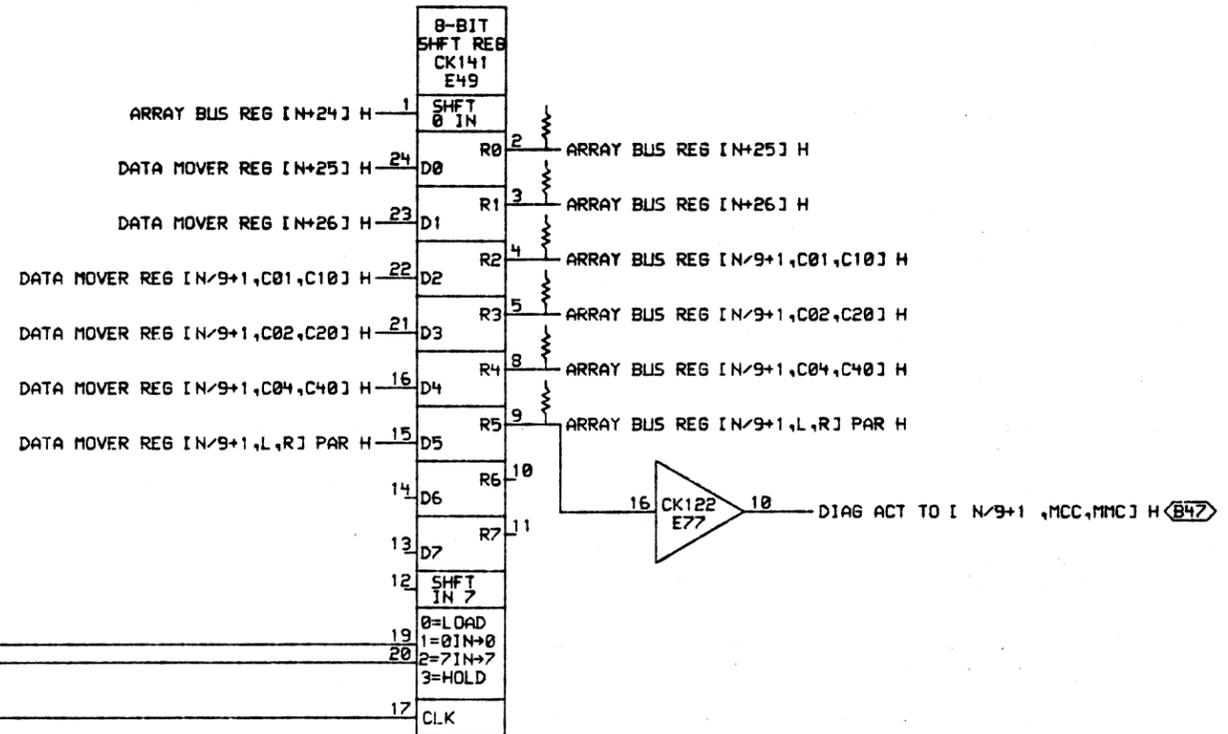
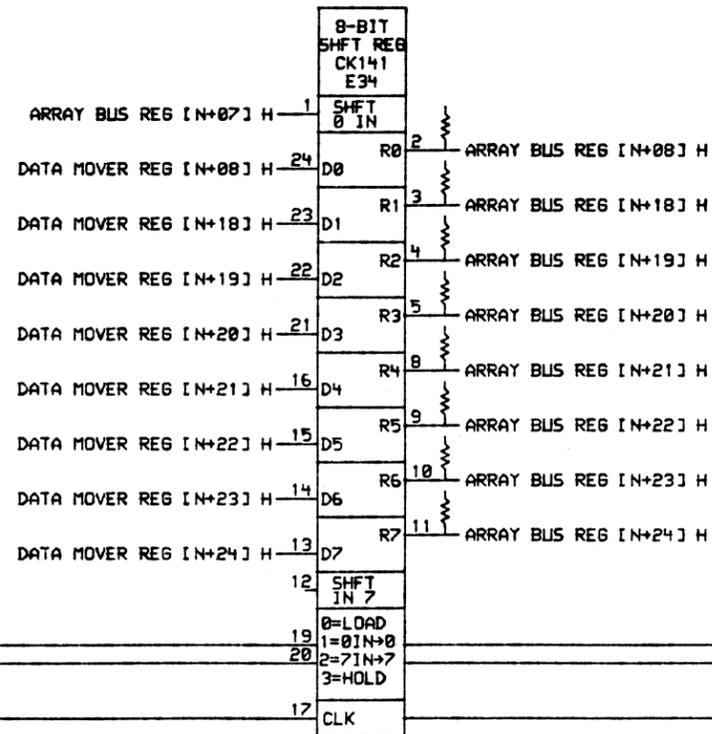
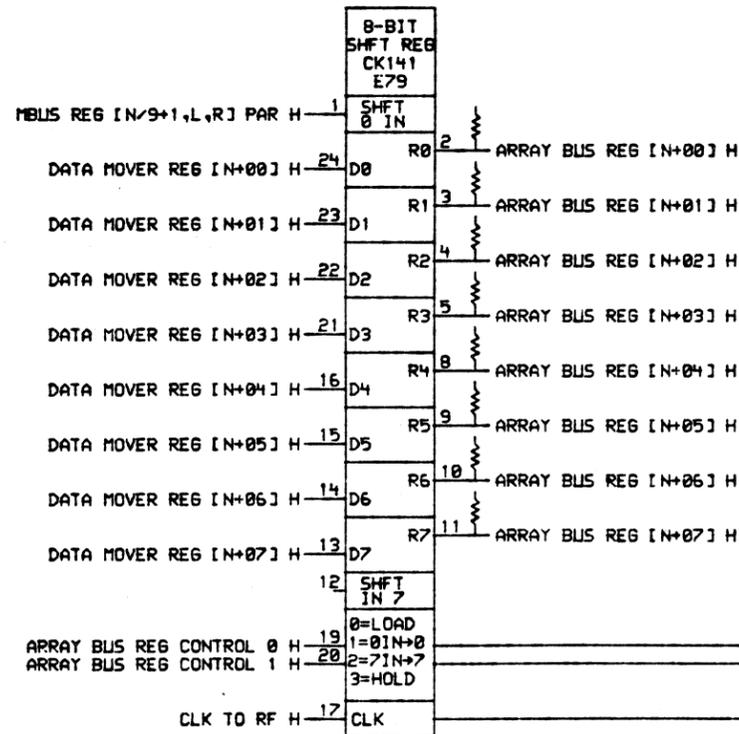
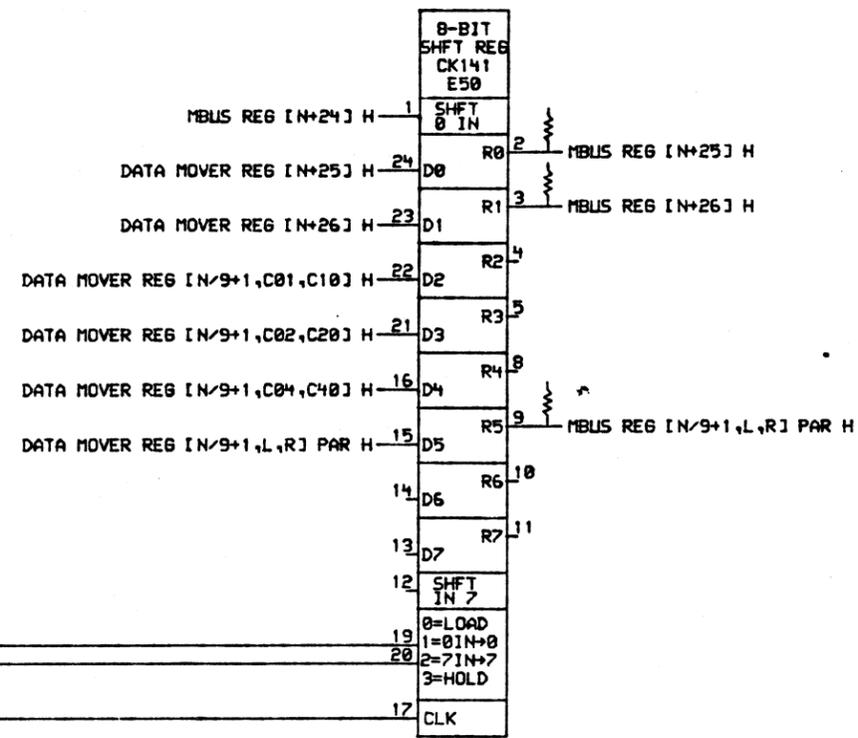
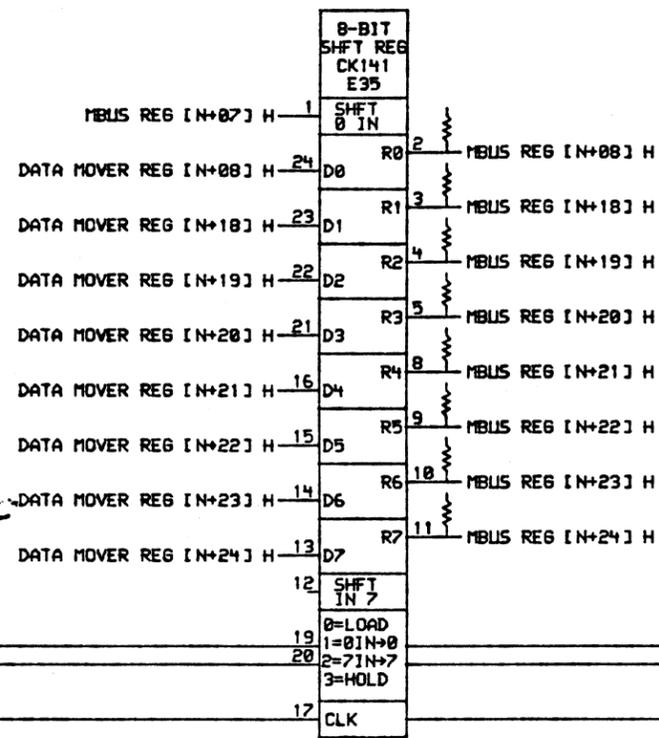
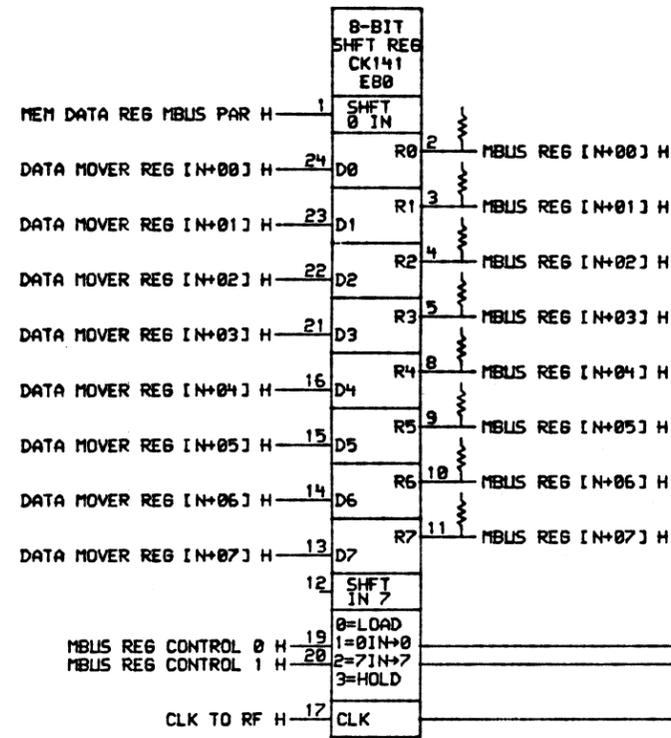
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CHK'D.	DATE	BOARD LOCATION:	CACHE DATA REG FILE	
VILCAN (2000-L0060) MDPF.DRW		29-JAN-02 10:47	NEXT HIGHER ASSEMBLY:	SIZE CODE NUMBER
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0060-0	D C5	L0060-0-MDPF
				REV. *



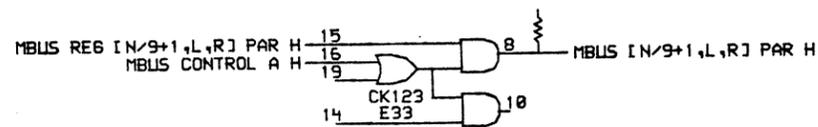
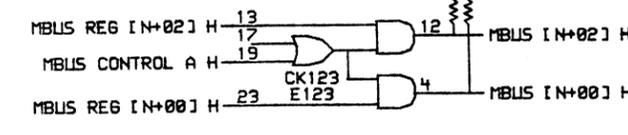
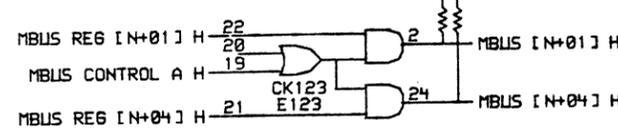
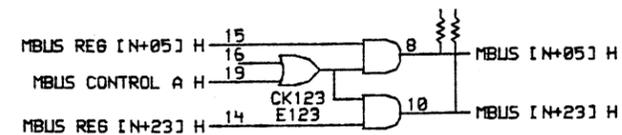
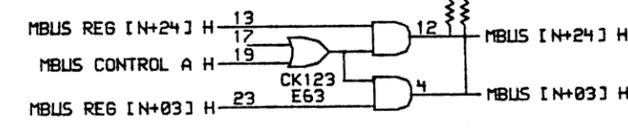
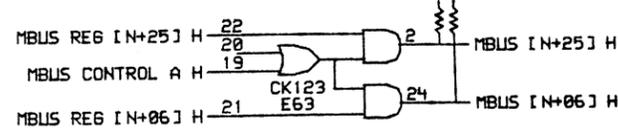
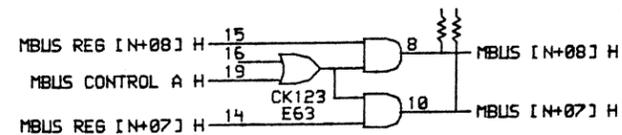
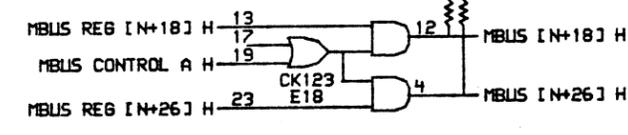
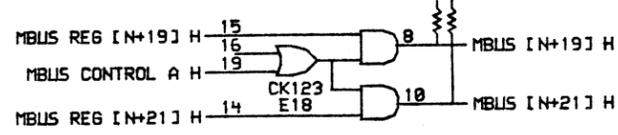
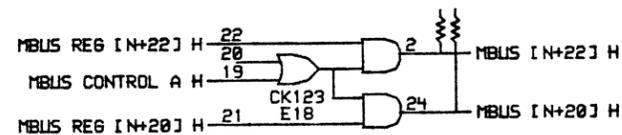
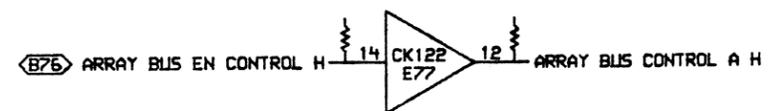
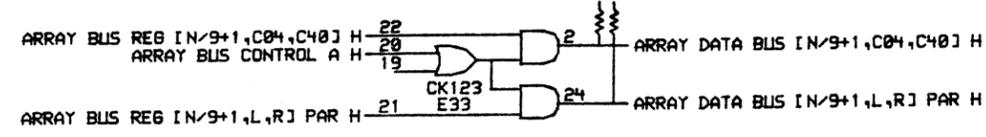
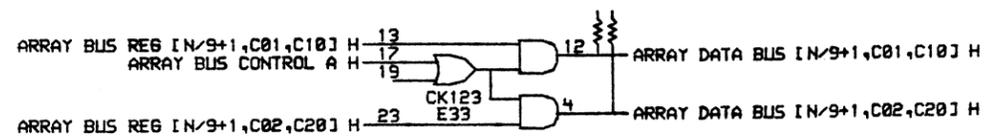
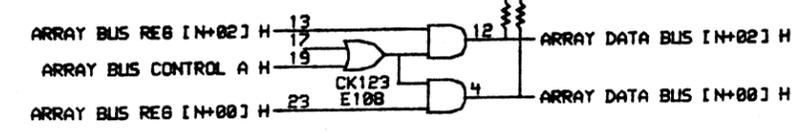
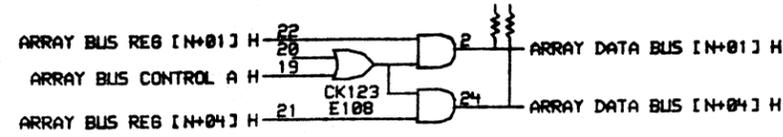
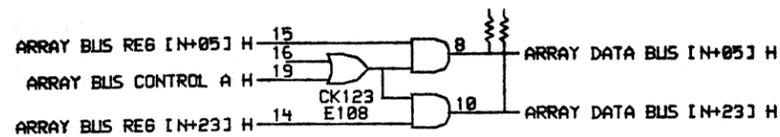
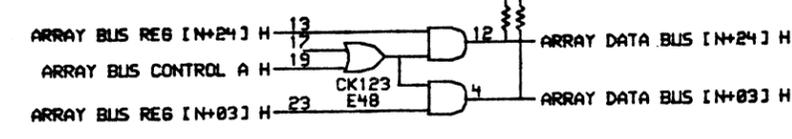
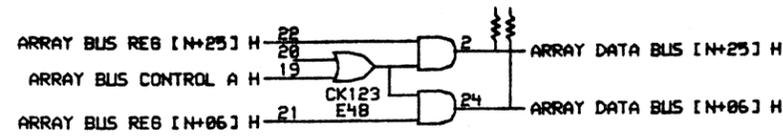
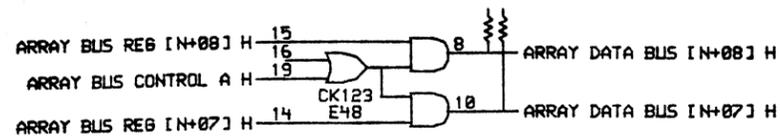
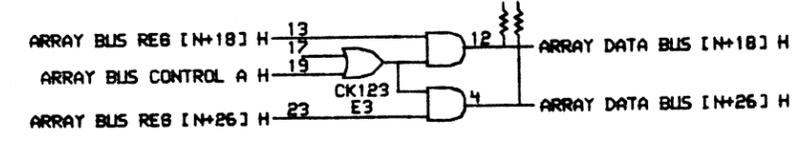
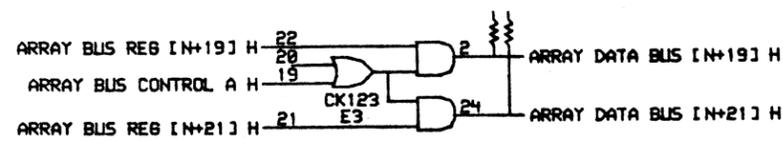
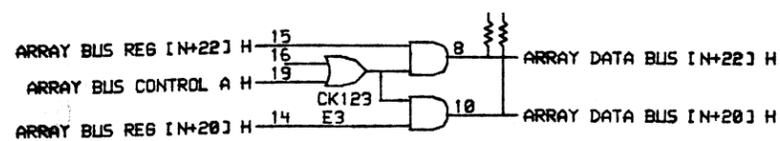
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CHK'D.	DATE	BOARD LOCATION:	SHEET 1 OF 1	DATA MOVER REG FILE
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FIRST USED ON OPTION/MODEL: 2080	B-DD-L0060-0		D C5	L0060-0-MDPG
				REV. *



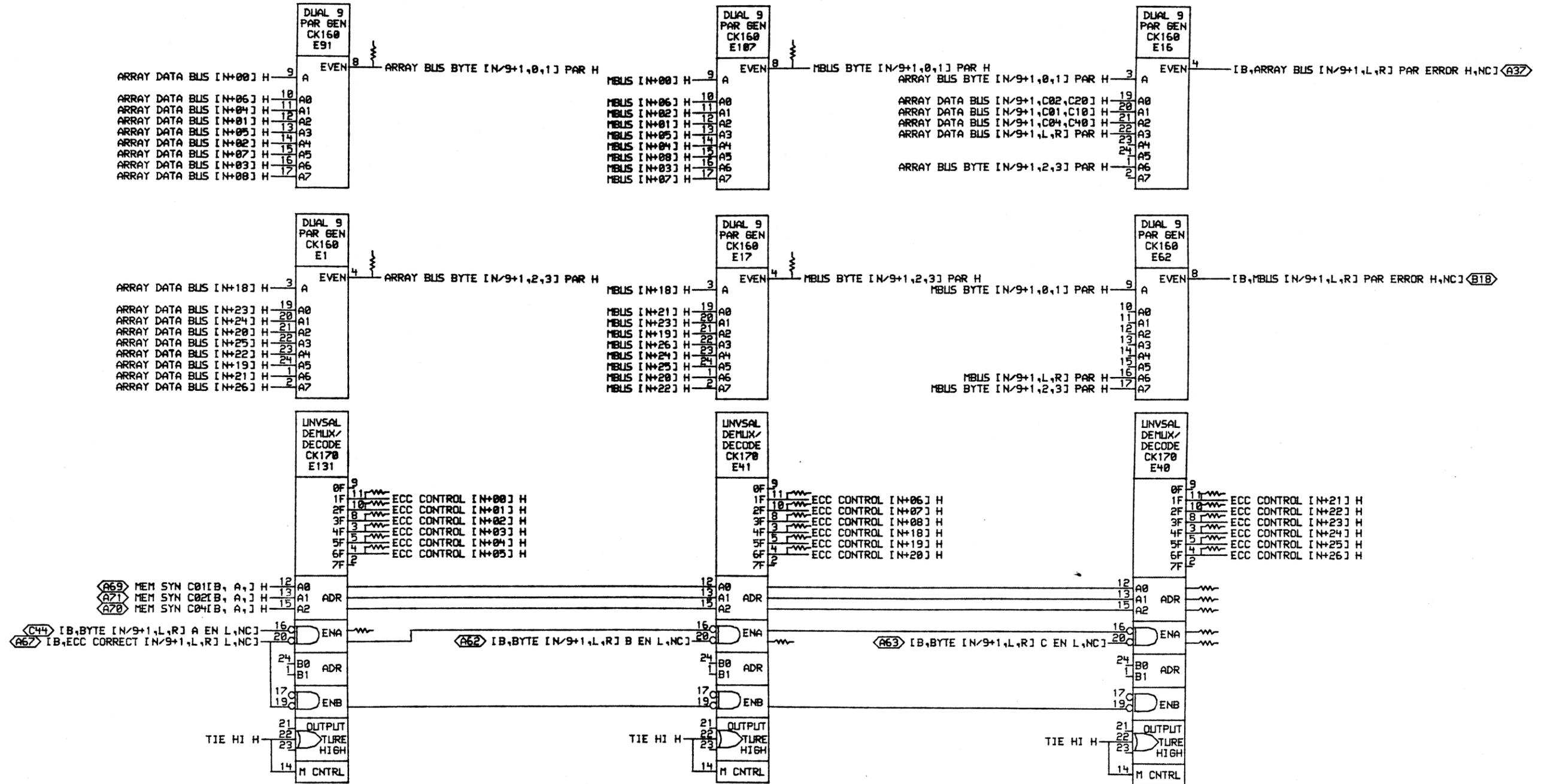
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CHK'D.	29-JAN-82			
VULCAN: (2080-L0060) 19/DPH.DRW		DATE		BOARD LOCATION:
29-JAN-82 10:48		NEXT HIGHER ASSEMBLY:		SHEET 1 OF 1
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0060-0		SIZE CODE
				NUMBER
				D CS L0060-0-MDPH
				REV. *



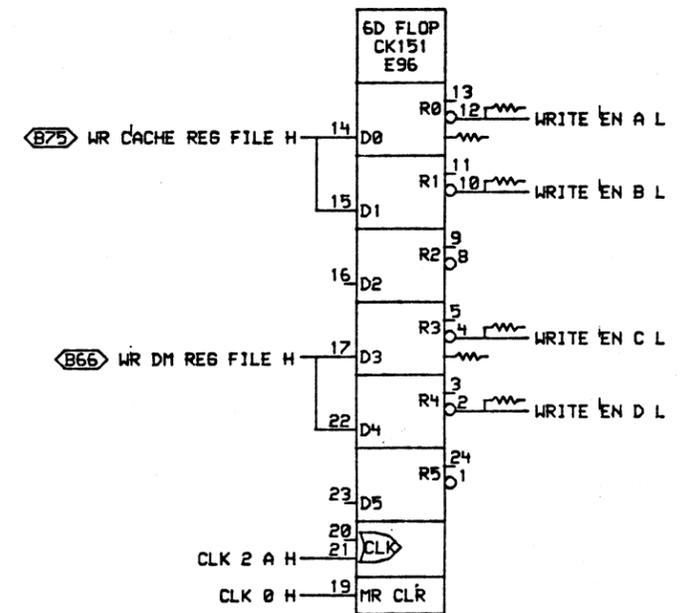
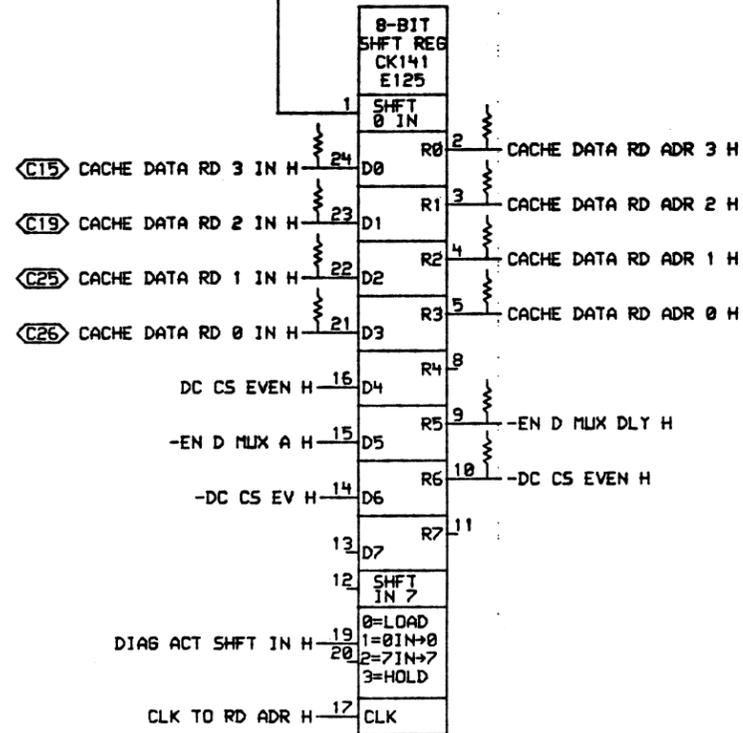
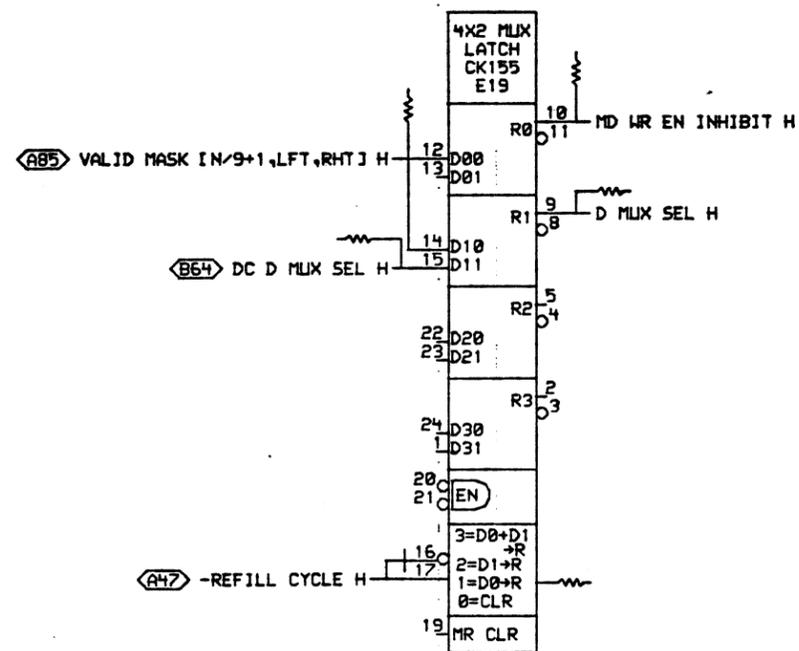
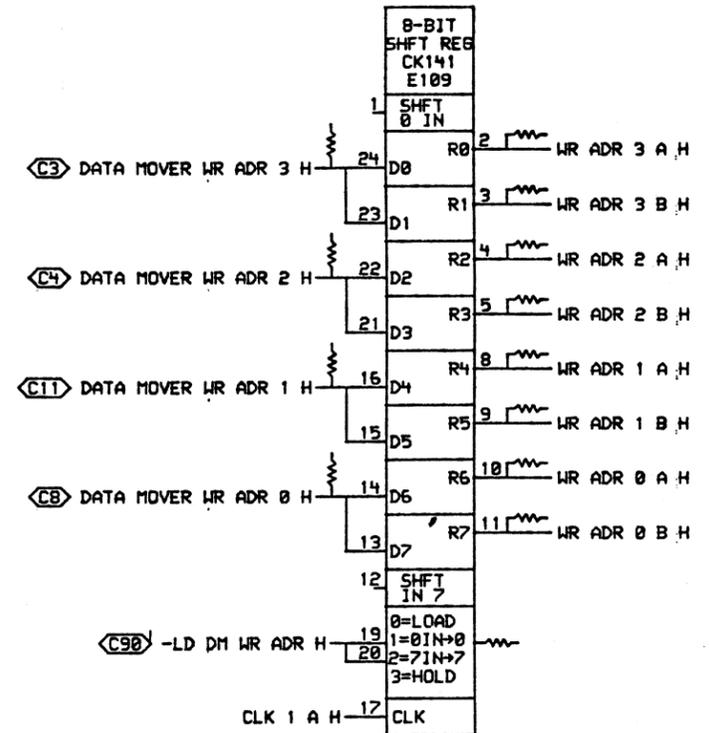
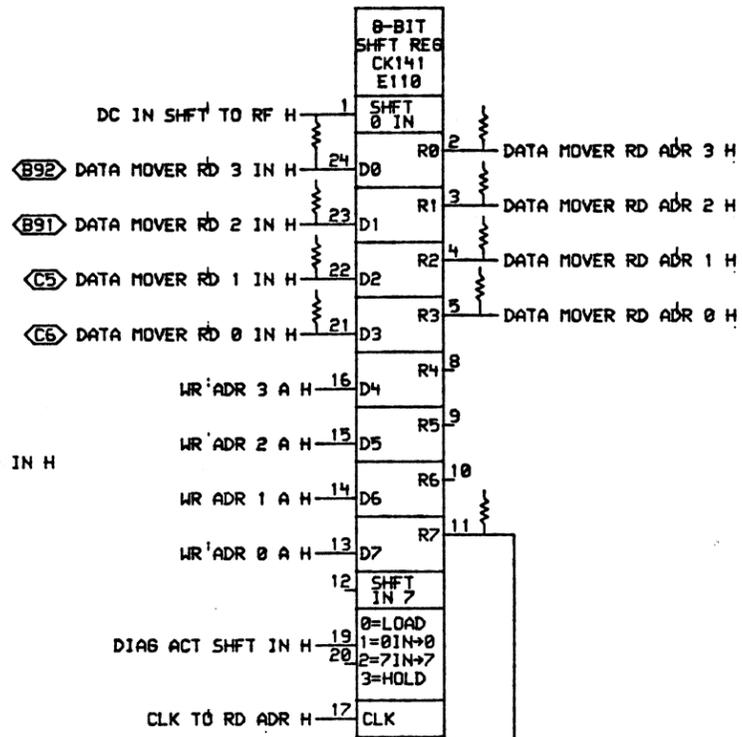
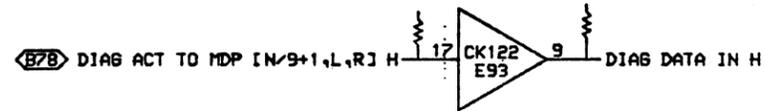
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CHK'D.	DATE	BOARD LOCATION:		MBLUS REG ARRAY BUS REG
VULCAN: (2080.J.0060)26MDPI.DRW 102-AL6-82 12:23				SIZE CODE
FIRST USED ON OPTION/MODEL: 2080				NUMBER
NEXT HIGHER ASSEMBLY: B-DD-L0060-0				REV. *



DRN.	DATE	ENG.	DATE	TITLE:	ARRAY BUS DATA	REV.
CHK'D.	DATE	BOARD LOCATION:	SHEET	NUMBER	L0060-0-MDPJ	*
FIRST USED ON OPTION/MODEL: 2080				NEXT HIGHER ASSEMBLY:	B-DD-L0060-0	



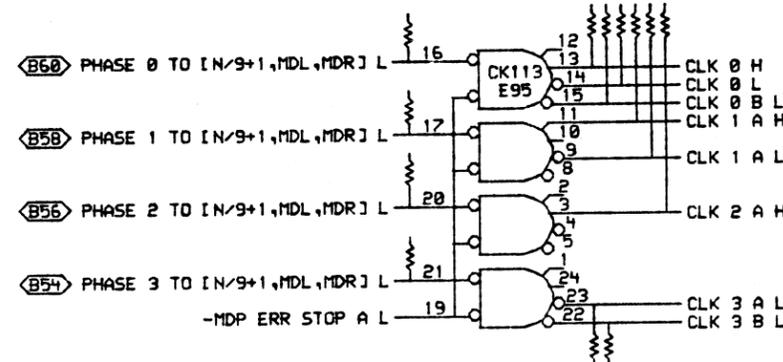
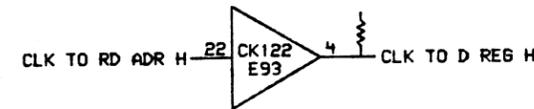
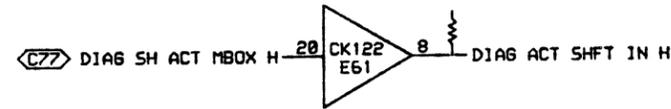
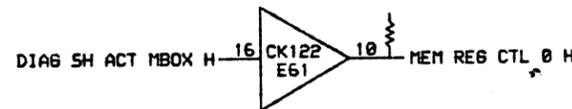
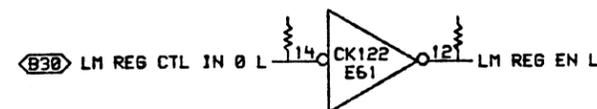
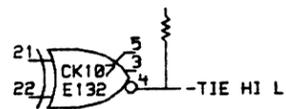
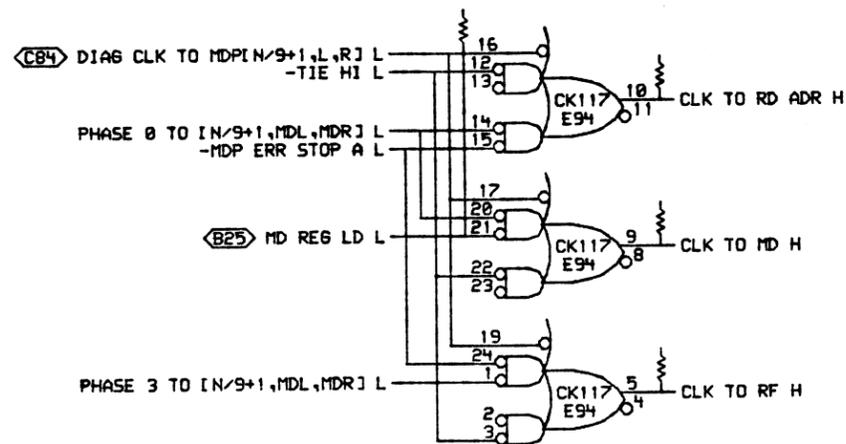
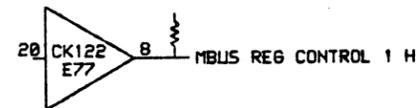
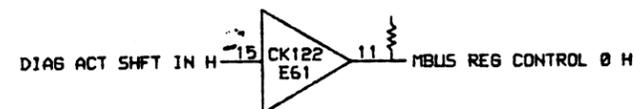
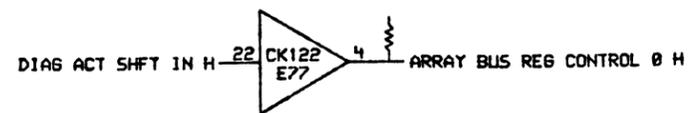
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		ARRAY BUS & MBUS
		SHEET	OF	PAR, ECC CONTR.
VILCAM (2000.L0060)19MDPK.DRW	29-JAN-82 10:48	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0060-0	D CS	L0060-0-MDPK
			REV.	*



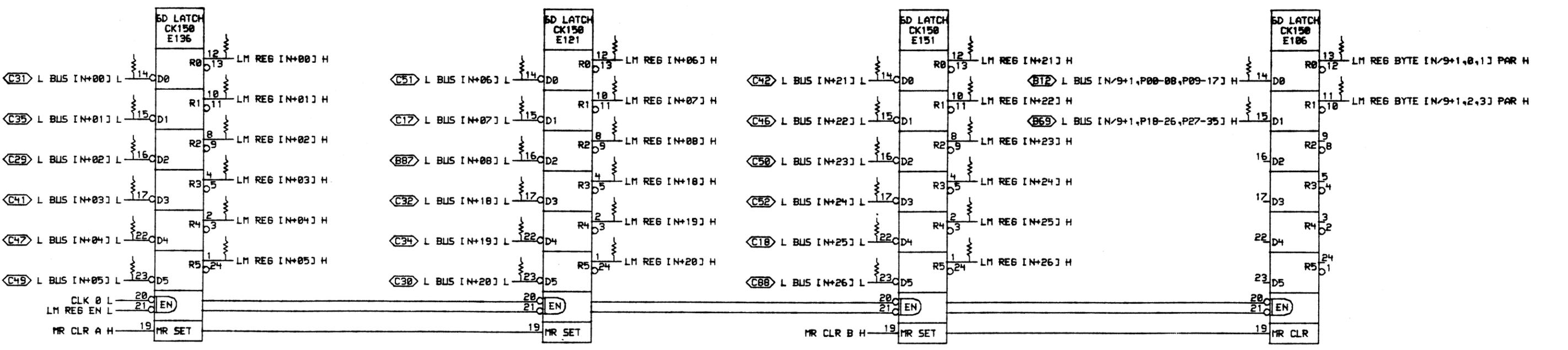
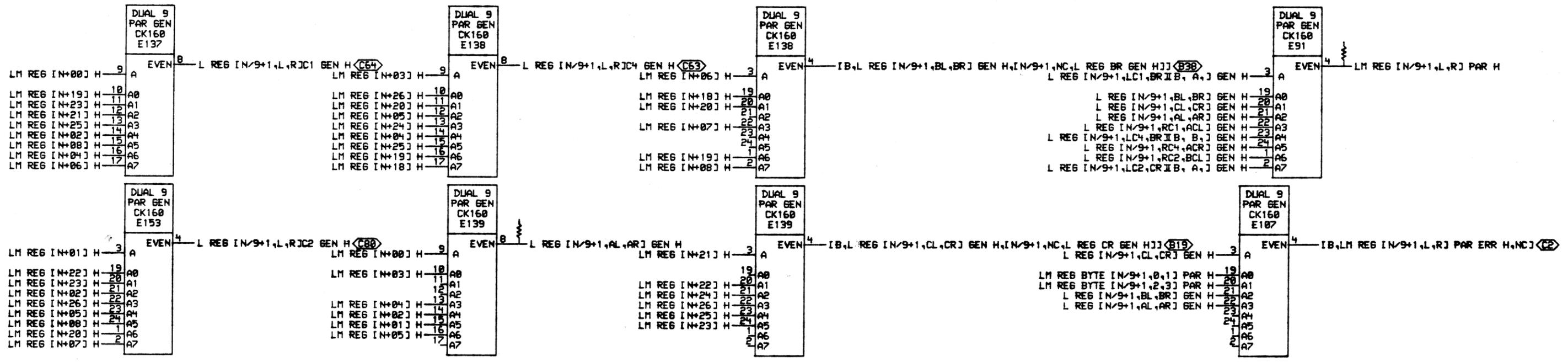
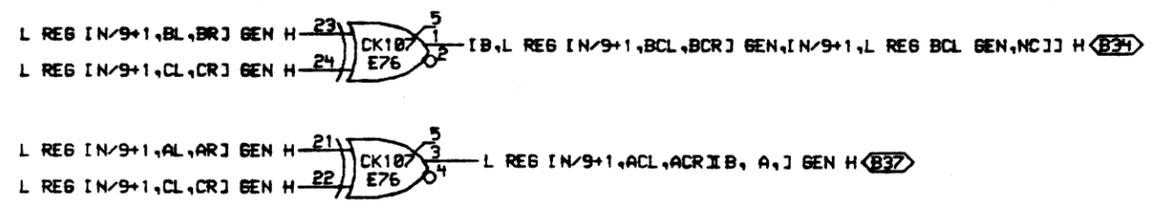
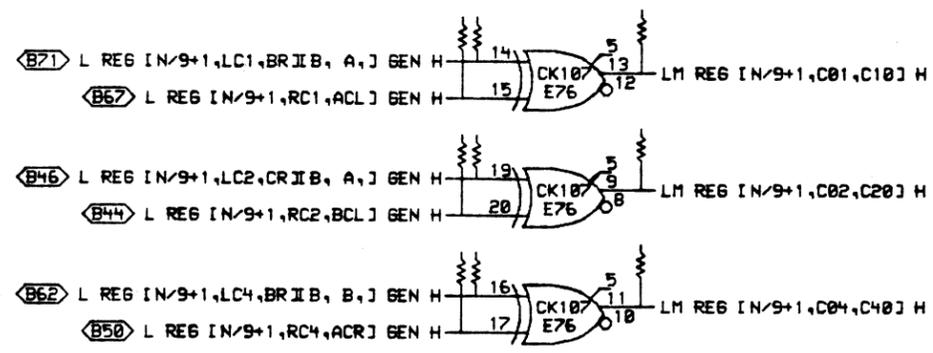
D  
C  
B  
A

REV. #  
NUMBER L0060-0-MDPL  
SIZE CODE CS  
D  
B  
A

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FIRST USED ON OPTION/MODEL: 2080				DATE BOARD LOCATION: SHEET 1 OF 1		NEXT HIGHER ASSEMBLY: B-DD-L0060-0		SIZE CODE D CS	NUMBER L0060-0-MDPL	REV. *



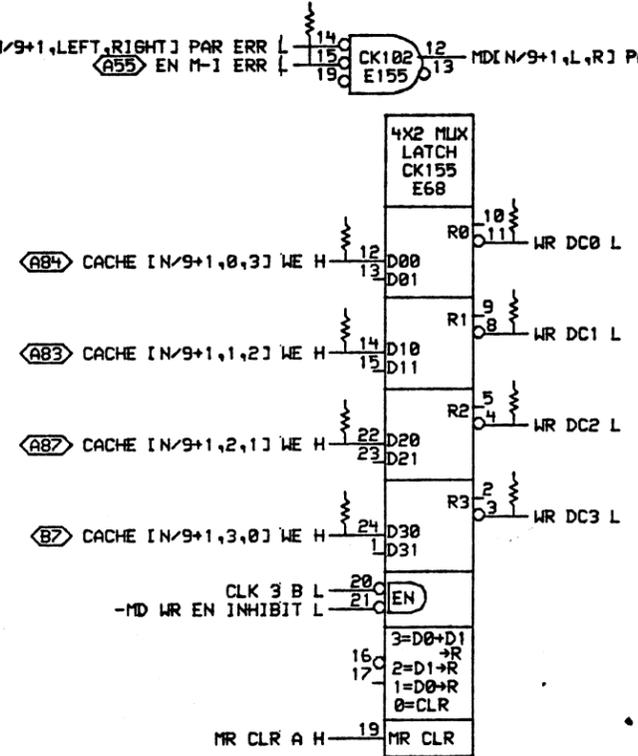
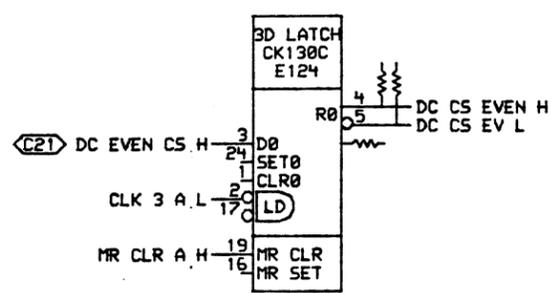
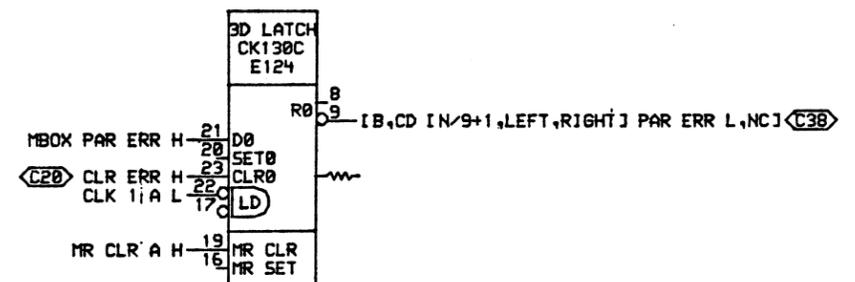
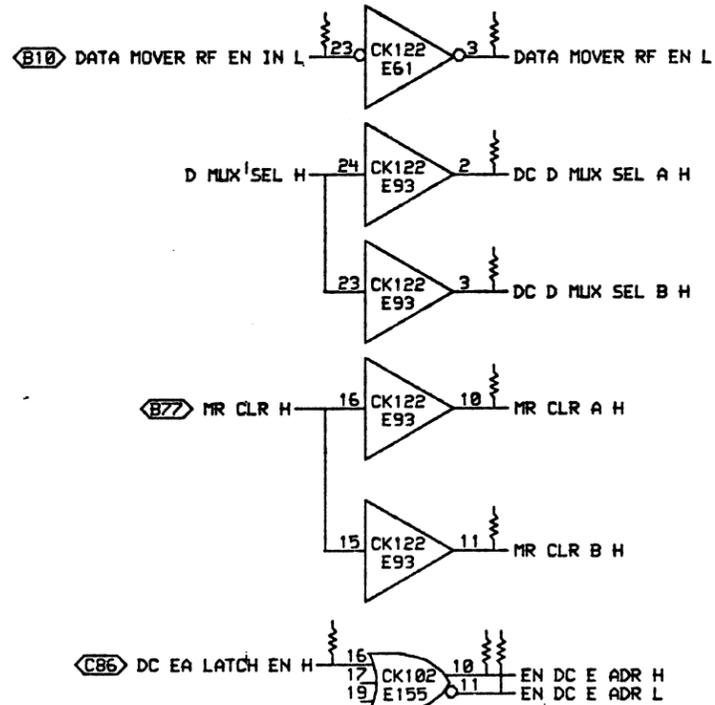
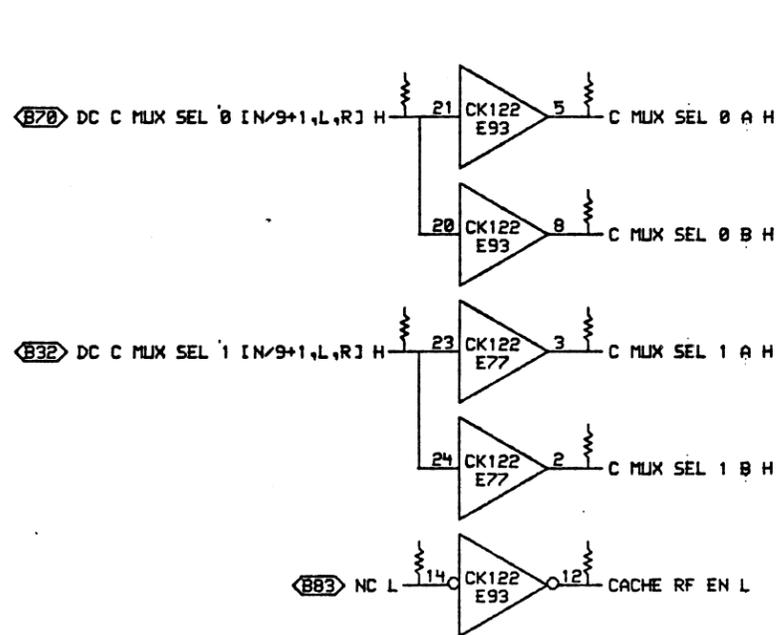
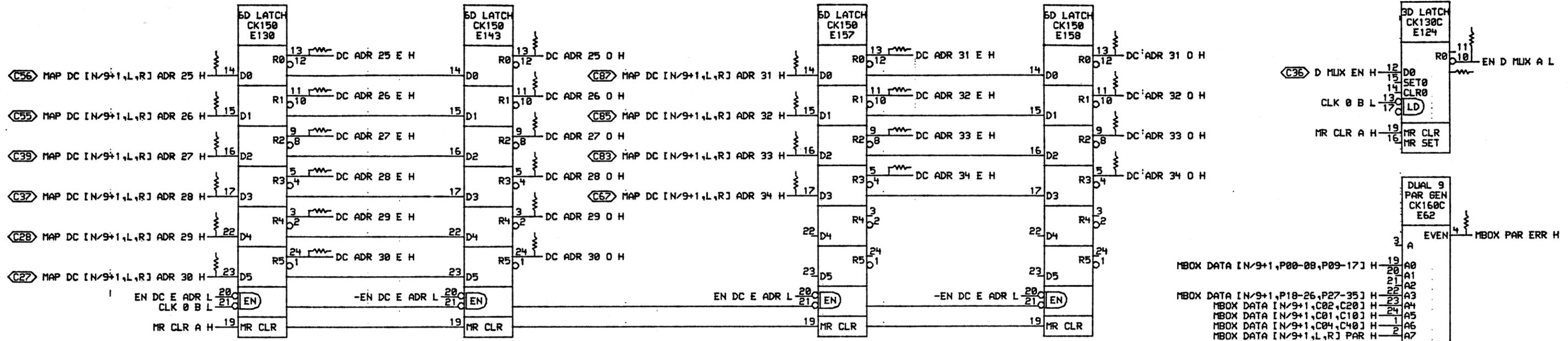
DRN.	DATE	ENG.	DATE	TITLE:	REG CONTR.
CHK'D.	02-AUG-82			BOARD LOCATION:	
				SHEET 1 OF 1	
VULCAN (2080-L0060)26MDPM.DRW	02-AUG-82 10:30	NEXT HIGHER ASSEMBLY:		SIZE	CODE
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0060-0		D	CS
				NUMBER	REV.
				L0060-0-MDPM	*



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REVISIONS		
CHK	CHANGE NO.	REV

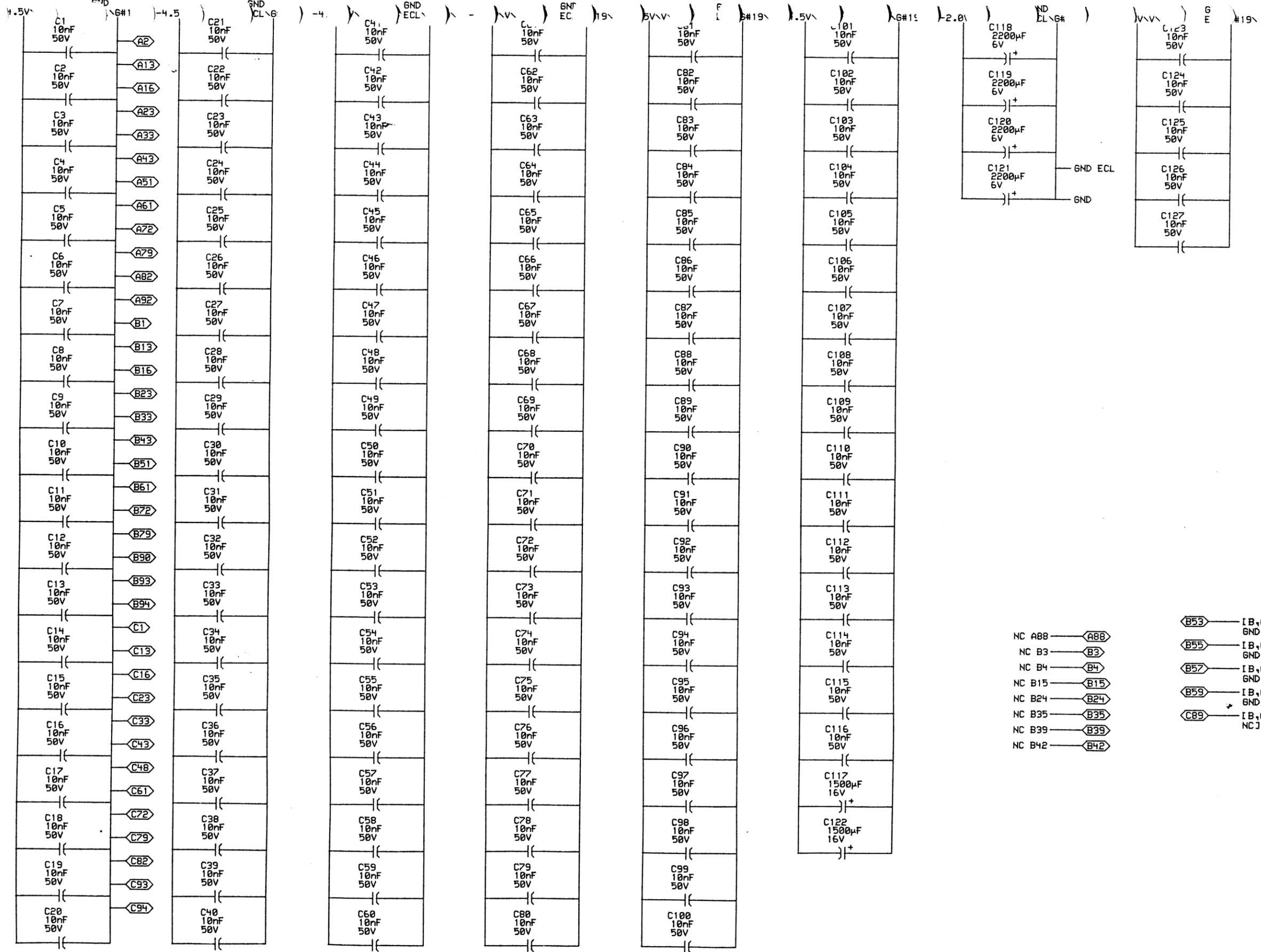
	DATE: 28-MAY-82	ENG.:	DATE:	TITLE: LBUS CHECK BITS GEN
	CHK'D: J. J. J.	DATE: 27-MAY-82	BOARD LOCATION: 1	SIZE CODE: D C5
VALDAM (2000-1000) 27-MAY-82 21:46 NEXT HIGHER ASSEMBLY: B-DD-L0060-0				NUMBER: L0060-0-MDPN REV: *



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE: INPUT BUFFERS
	29-AUG-82			
VULCAN: (2080-L0060) 27MDPP.DRW		DATE		BOARD LOCATION:
128-AUG-82 09:28		SHEET		OF
FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY:		SIZE CODE
		B-DD-L0060-0		NUMBER
				REV. *



- NC A88 — A88
- NC B3 — B3
- NC B4 — B4
- NC B15 — B15
- NC B24 — B24
- NC B35 — B35
- NC B39 — B39
- NC B42 — B42
- B53 — IB, GND ECL, GND CLK\G#20\
- B55 — IB, GND ECL, GND CLK\G#20\
- B57 — IB, GND ECL, GND CLK\G#20\
- B59 — IB, GND ECL, GND CLK\G#20\
- C89 — IB, GND ECL, NC

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRW. J. J. J.	DATE 29-APR-82	ENG.	DATE	TITLE: MDP POWER & GROUND
	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1		
VULCAN (2080-L0060) MDP.DRW 128-APR-82 22:02 NEXT HIGHER ASSEMBLY:		FIRST USED ON OPTION/MODEL: 2080		B-DD-L0060-0	SIZE CODE D C5
				NUMBER L0060-0-MDPR	REV. #

8

7

6

5

4

3

2

1

SIZE CODE NUMBER 05 0300 3215

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D

C

B

A

D

C

REV.

NUMBER

SIZE

CODE

SP

D

B

REV.

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRW. <i>S. J. J.</i>	DATE 10-FEB-83	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
ARCH: <LHLLER.PLOTS>LCAM.DPL.1 10-FEB-83 03:05 NEXT HIGHER ASSEMBLY:					SIZE CODE
FIRST USED ON OPTION/MODEL:					D SP
				NUMBER	REV.

8

7

6

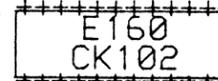
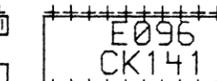
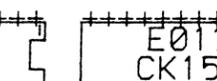
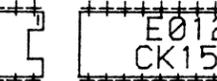
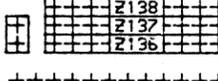
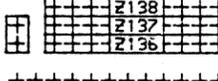
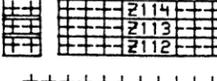
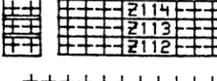
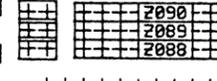
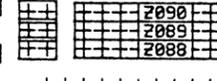
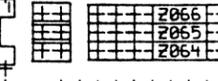
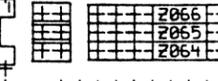
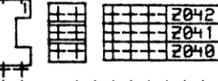
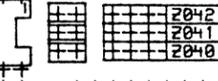
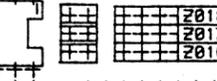
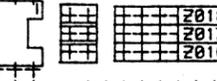
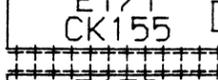
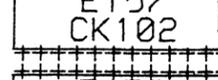
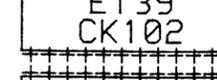
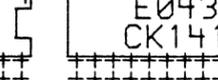
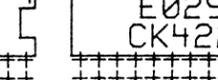
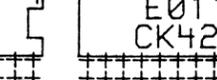
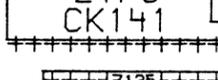
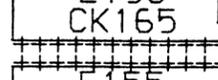
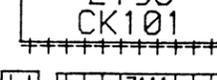
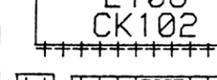
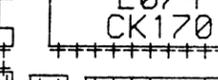
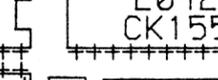
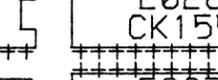
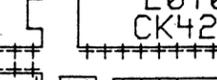
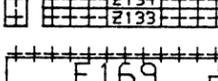
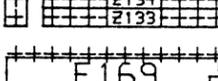
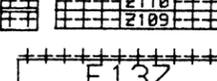
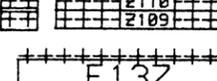
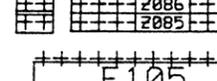
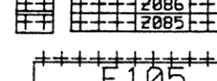
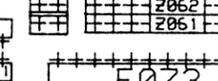
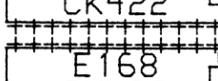
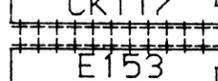
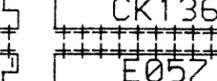
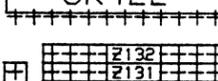
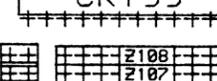
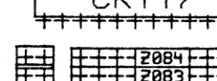
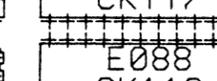
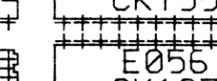
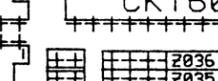
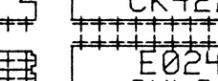
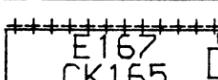
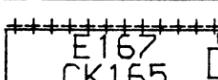
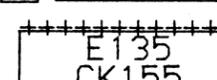
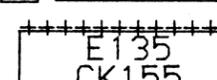
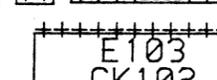
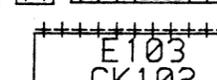
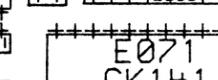
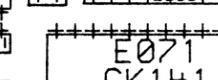
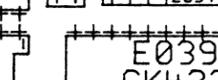
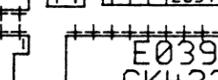
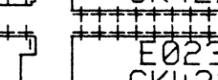
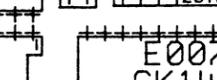
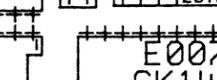
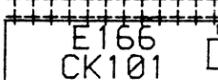
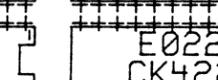
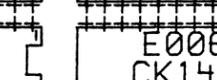
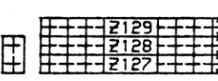
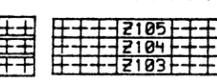
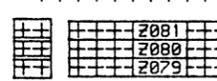
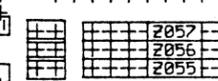
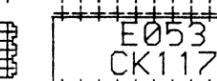
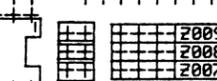
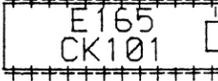
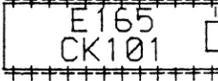
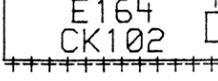
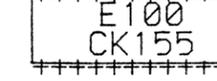
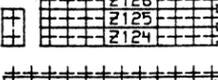
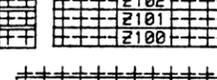
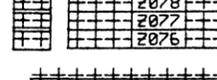
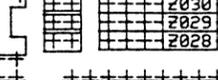
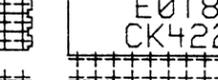
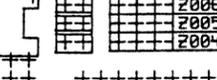
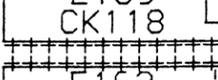
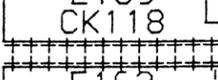
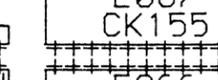
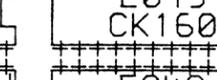
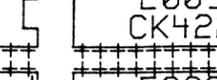
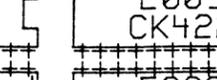
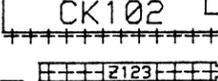
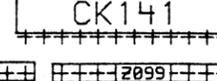
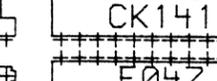
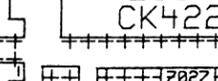
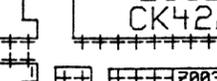
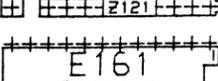
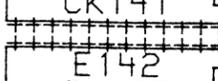
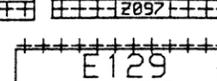
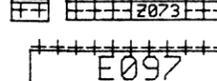
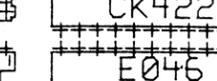
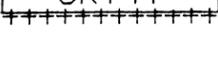
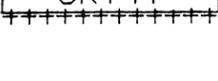
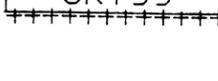
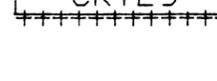
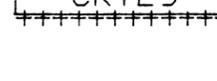
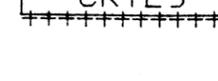
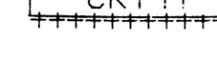
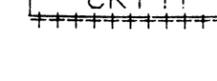
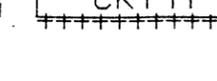
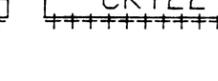
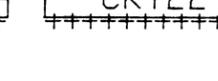
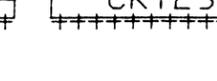
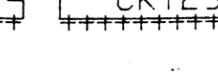
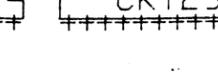
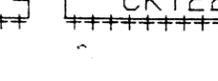
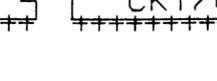
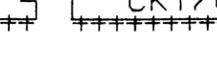
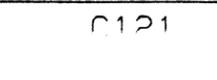
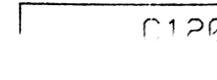
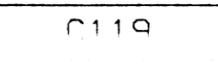
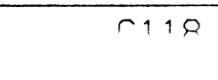
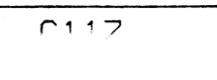
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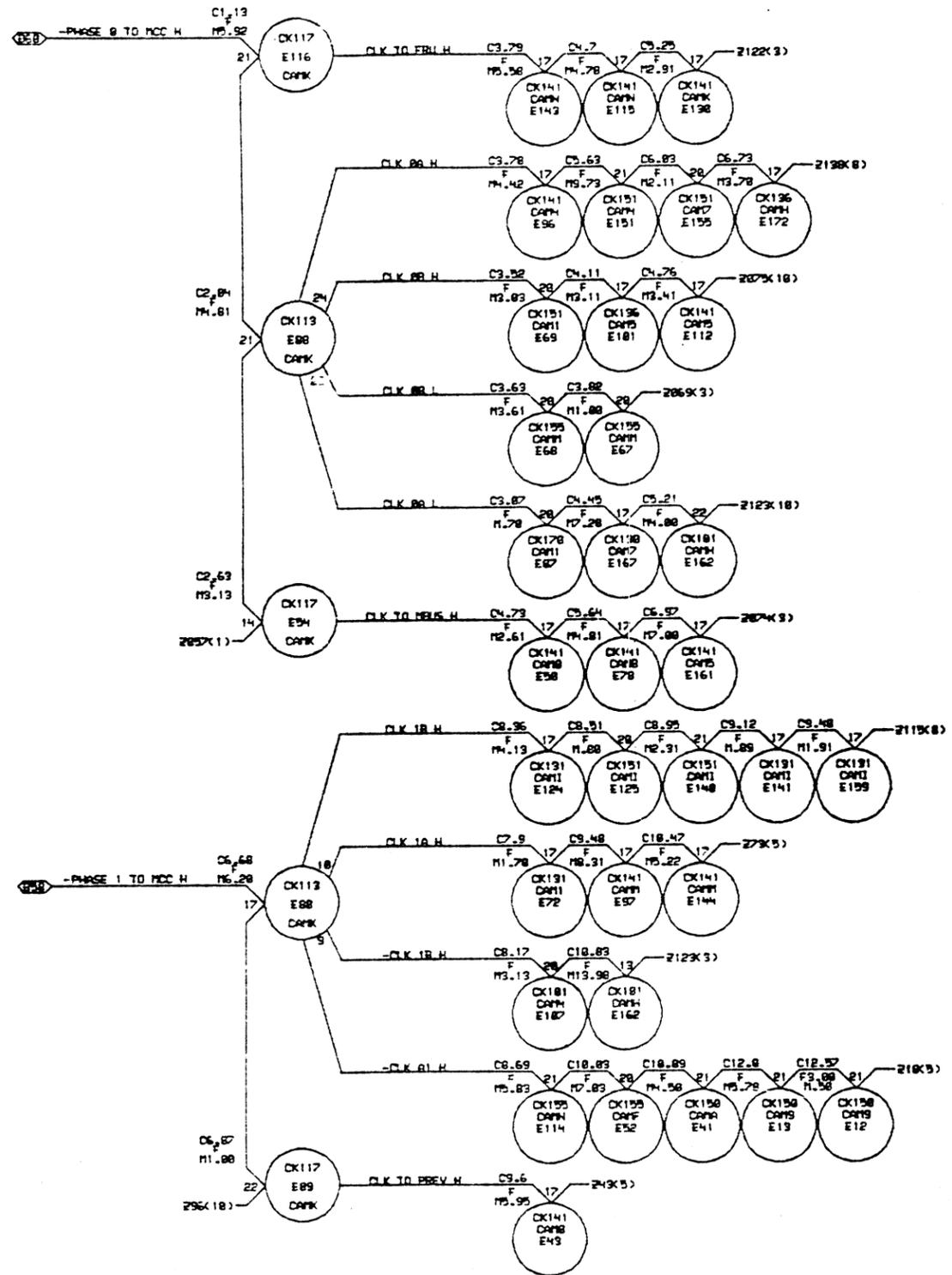
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Cam

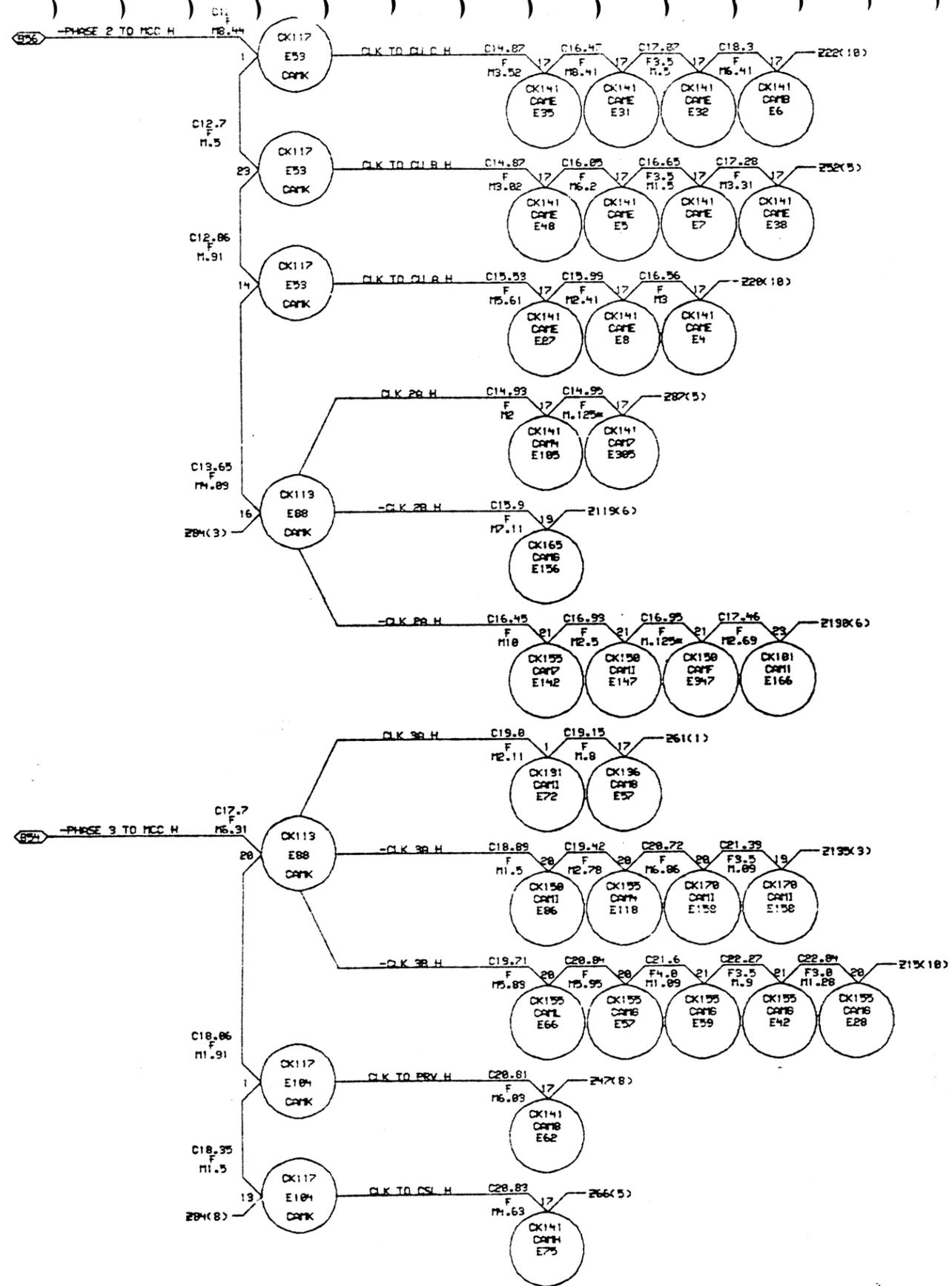


C=CALCULATED TIME  
 D=PROPAGATION DISTANCE  
 F=FIXED LENGTH

CAMI

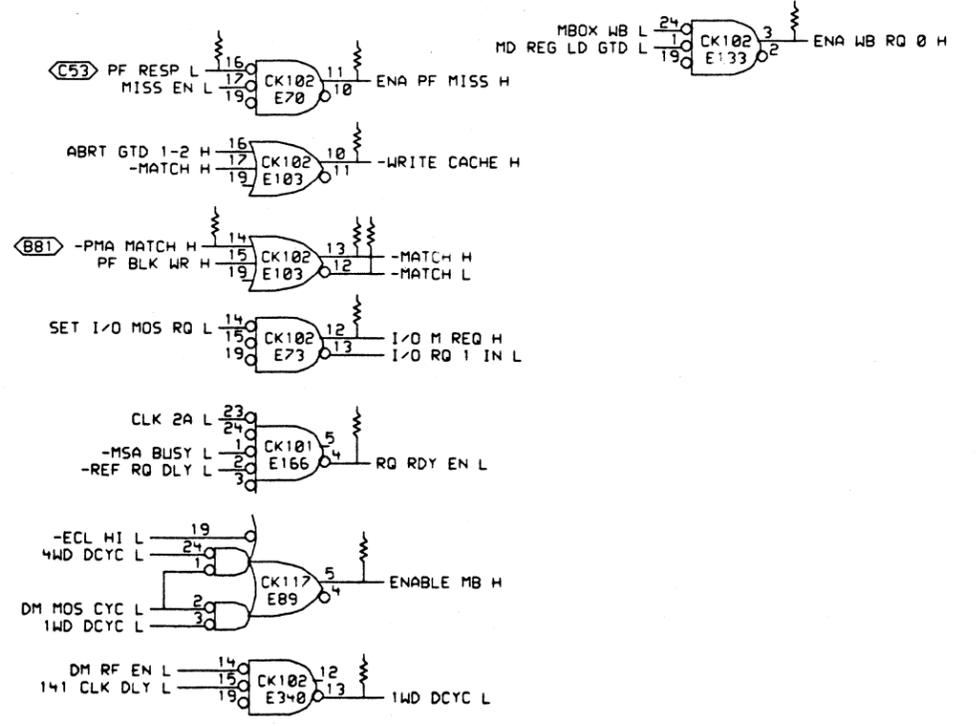
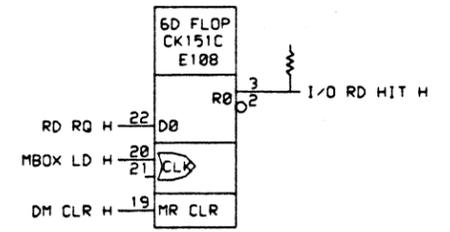
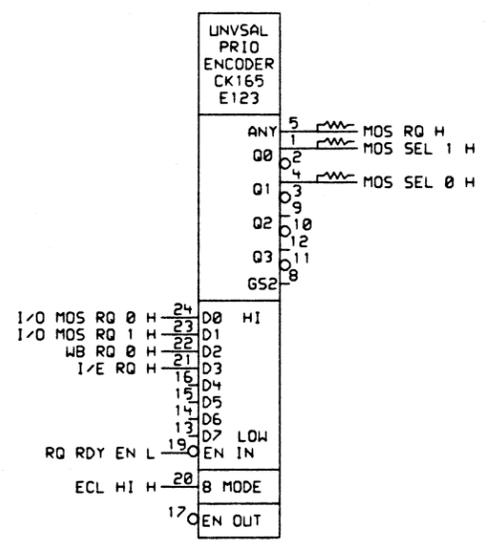
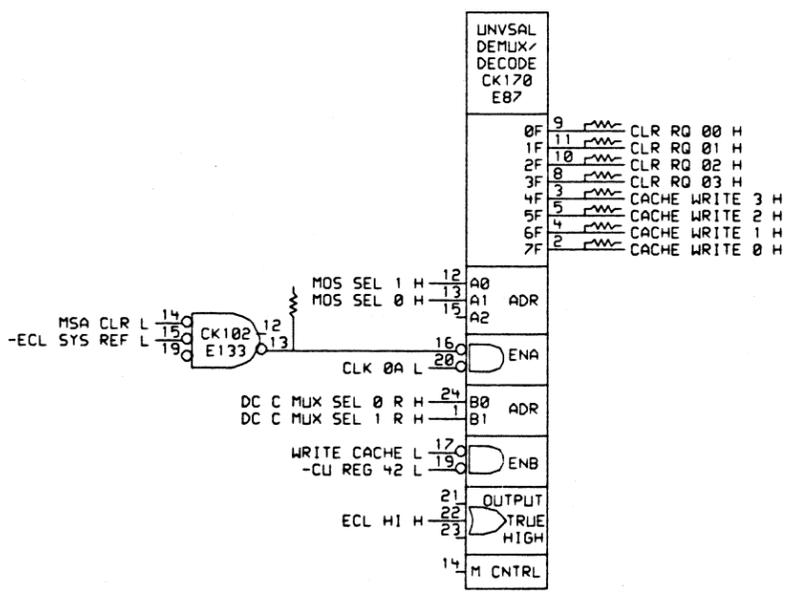
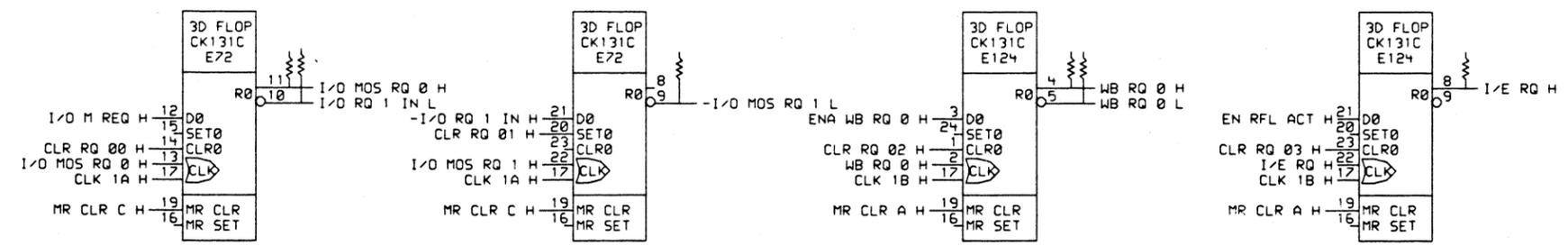
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CHK'D.	DATE	BOARD LOCATION:		
				SIZE CODE
				NUMBER
				REV.

VTARI CROCK BLOCKS FROM TC1.DRW [09-AUG-82 13:07] NEW HIGHER ASSEMBLY  
 FIRST USED ON OPTION/MODEL: 0 05



CAMZ

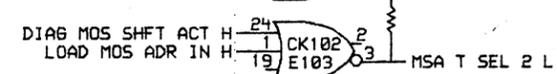
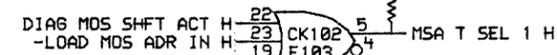
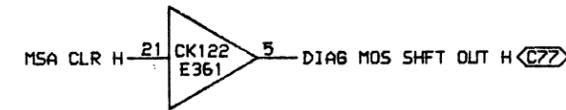
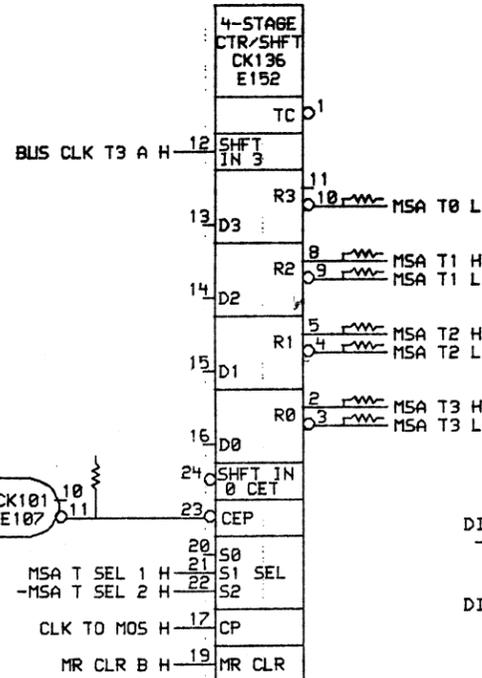
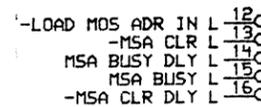
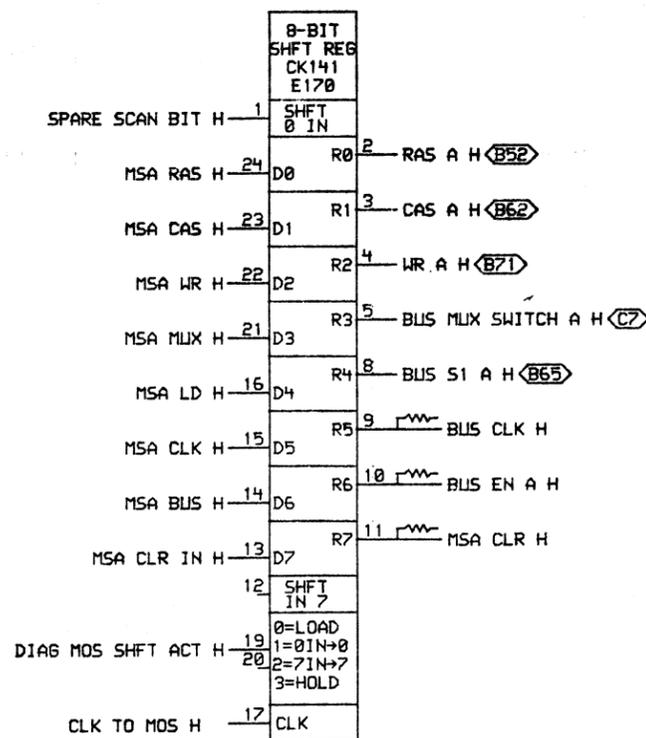
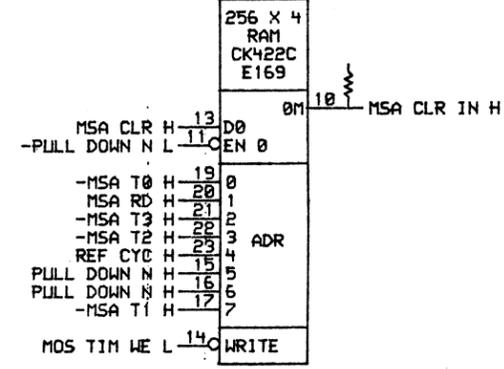
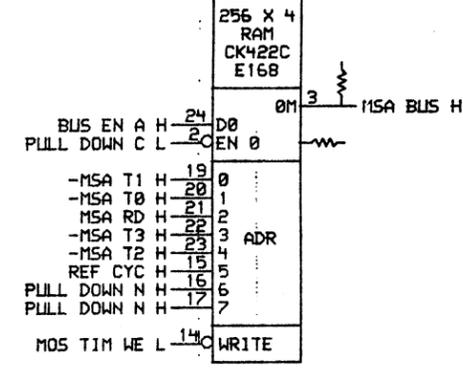
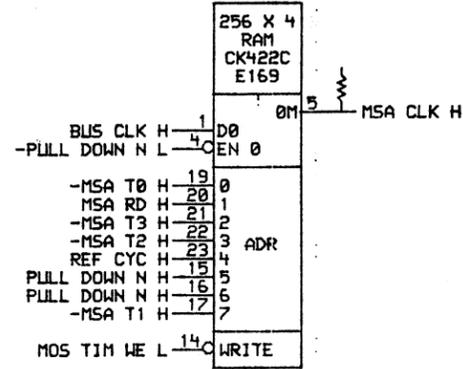
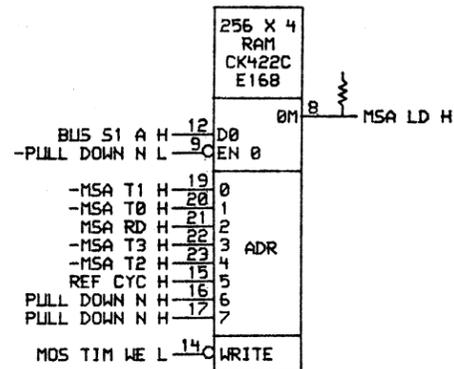
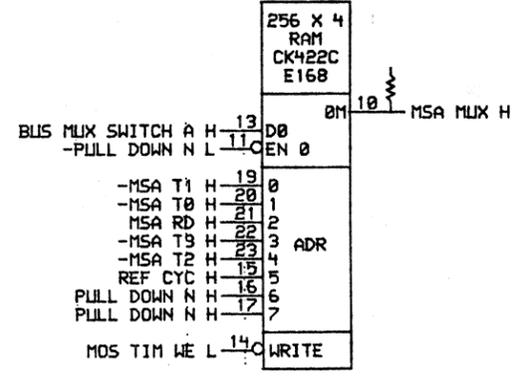
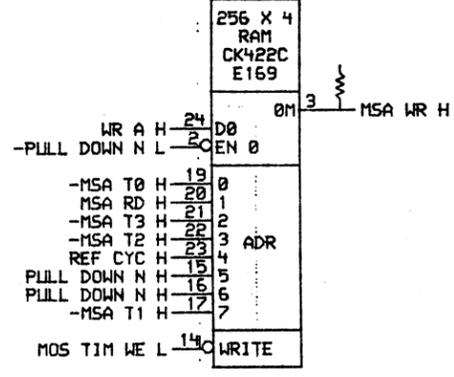
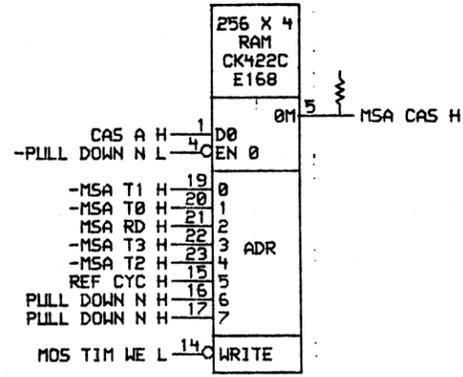
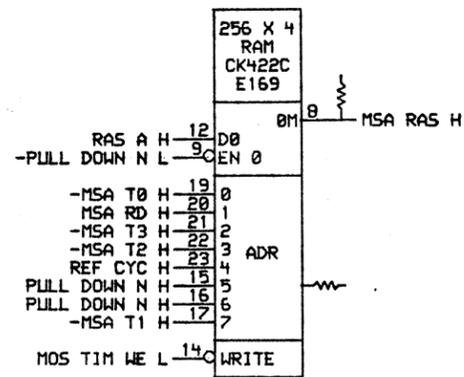
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CHK'D.	DATE	BOARD LOCATION:		
XTRAI CLOCK BLOCKS FROM T2 DRU 09-AUG-82 13:18 NEX HIGHER ASSEMBLY:				SIZE CODE
FIRST USED ON OPTION/MODEL:				NUMBER
				REV.



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REVISIONS	
CHK	CHANGE NO. REV

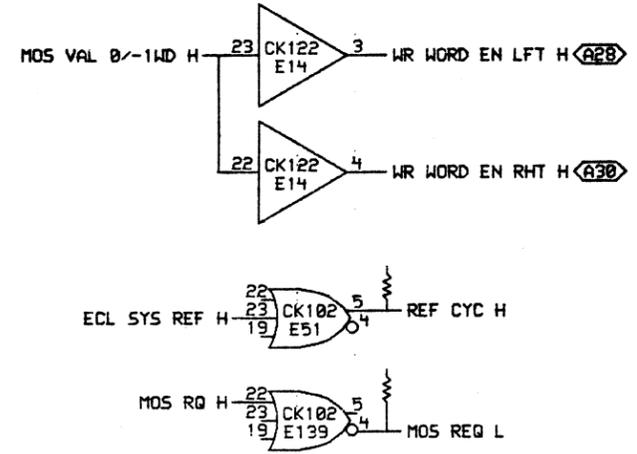
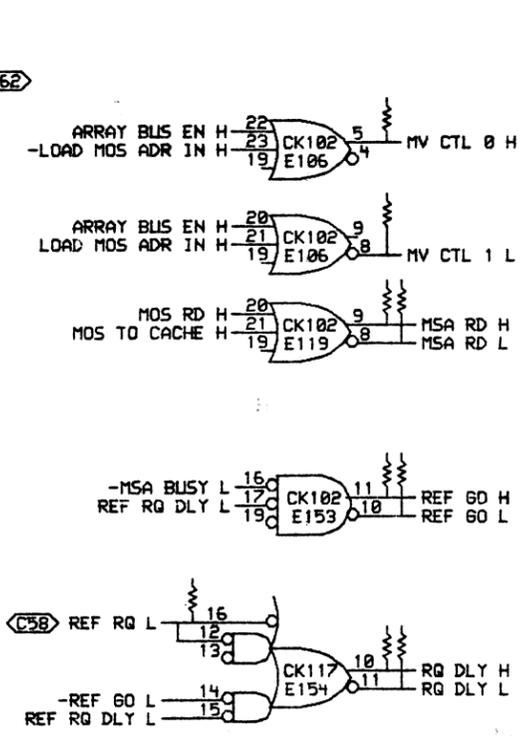
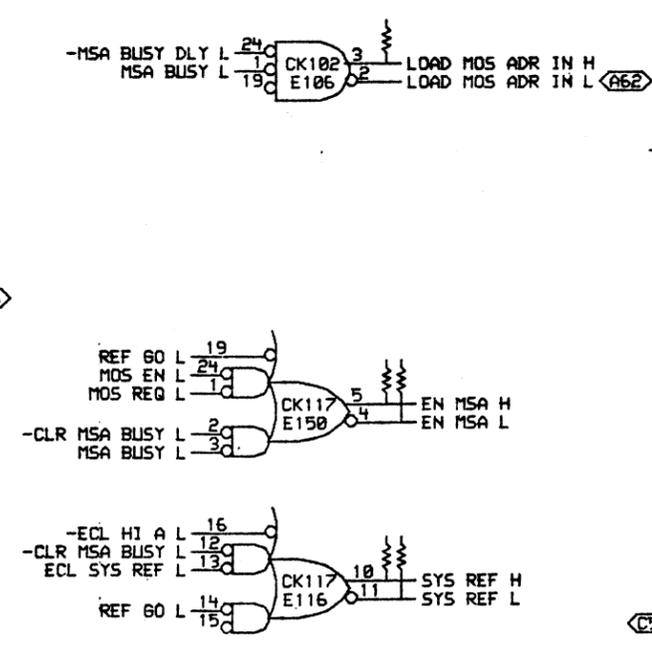
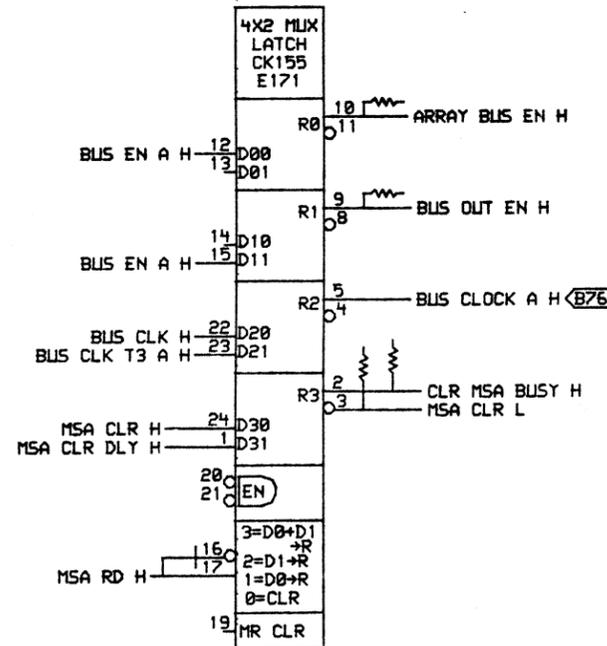
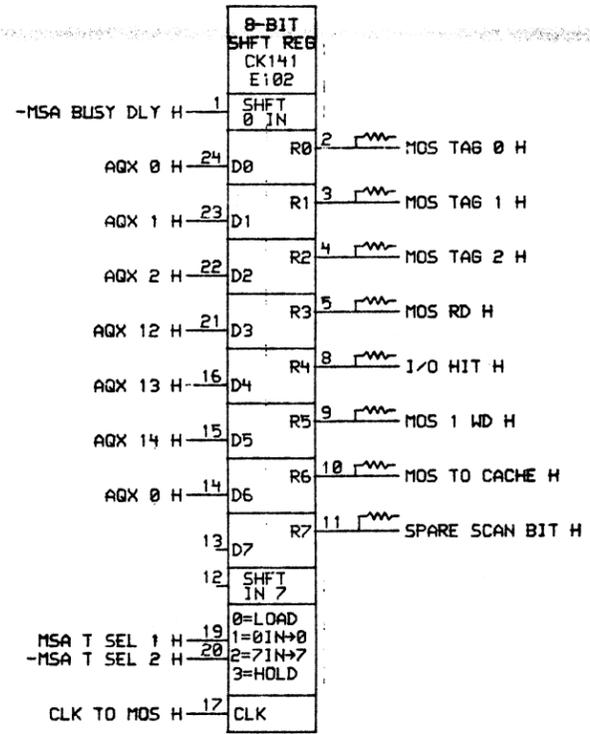
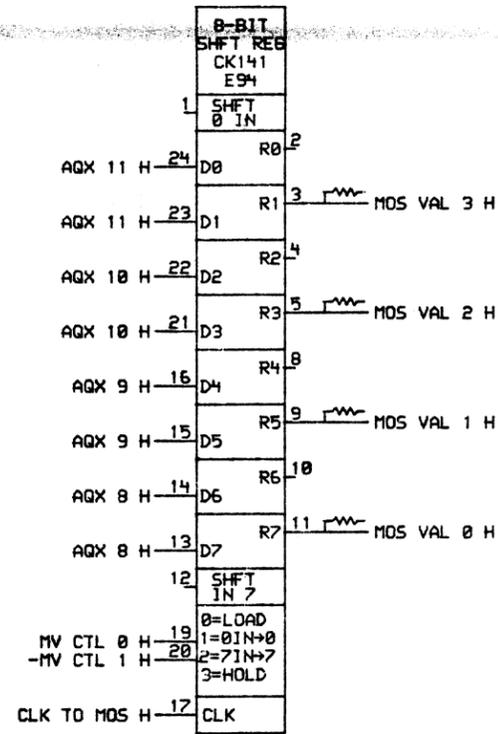
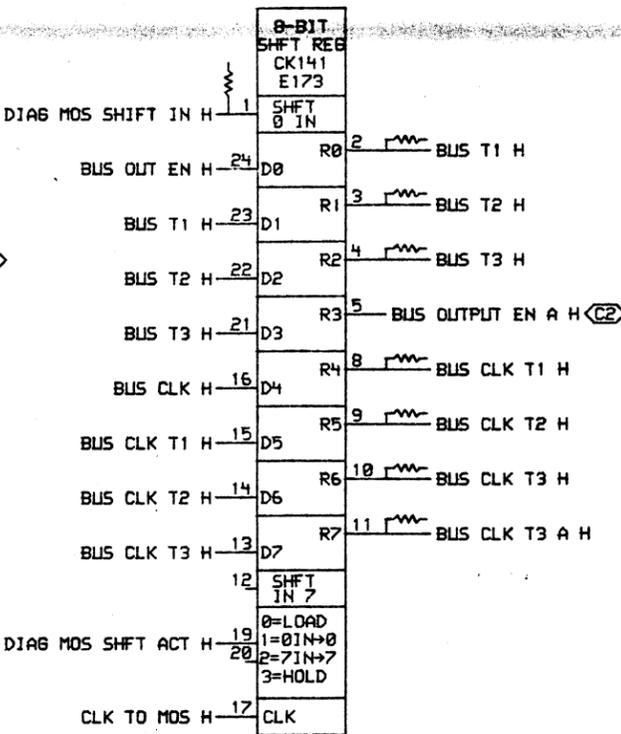
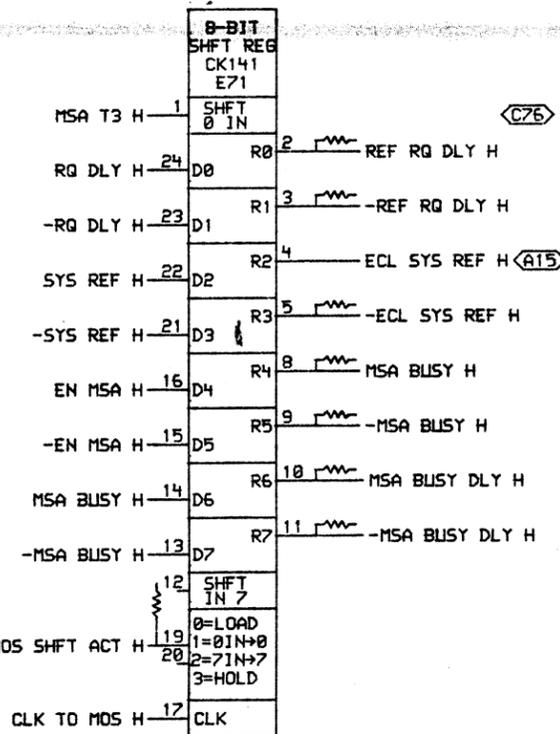
	DATE	ENG.	DATE	TITLE:
	03-JAN-83	J. J. J.	03-JAN-83	MOS RQ FF MOS PRI EN
VULCANI(2000.CAM)@CAM1.DRW FIRST USED ON OPTION/MODEL: 2000	DATE	BOARD LOCATION:	SHEET	OF
03-JAN-83 1315	1	B-DD-L0059-0	1	1
	SIZE CODE	NUMBER	REV.	
	D CS	L0059-0-CAM1	*	



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	07-SEP-82	J. Jambly		MOS SEQUENCER CTL A
	DATE	BOARD LOCATION:	SHEET	OF
			1	1
VULCAN: (2080-CAM) 61CAM2.DRW	06-SEP-82 09:22	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0059-0	D CS	L0059-0-CAM2
				REV. *



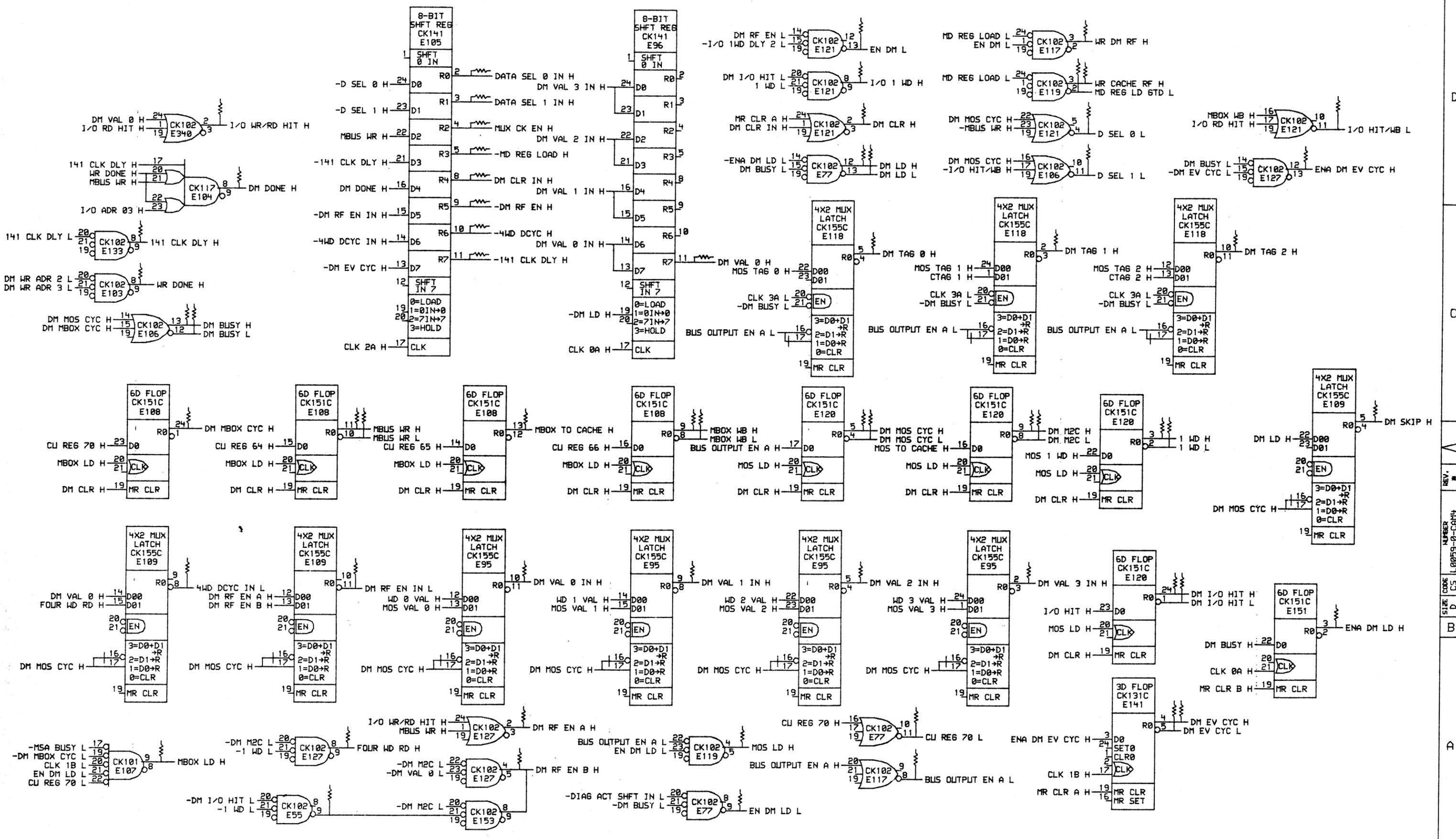
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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE 23-SEP-82	ENG.	DATE	TITLE: MOS START CTL
CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1		
VULCAN: (2000.CAM)62CAP3.DRW	22-SEP-82 14:16	NEXT HIGHER ASSEMBLY: B-DD-L0059-0		
FIRST USED ON OPTION/MODEL: 2080				

SIZE D	CODE CS	NUMBER L0059-0-CAM3	REV. *
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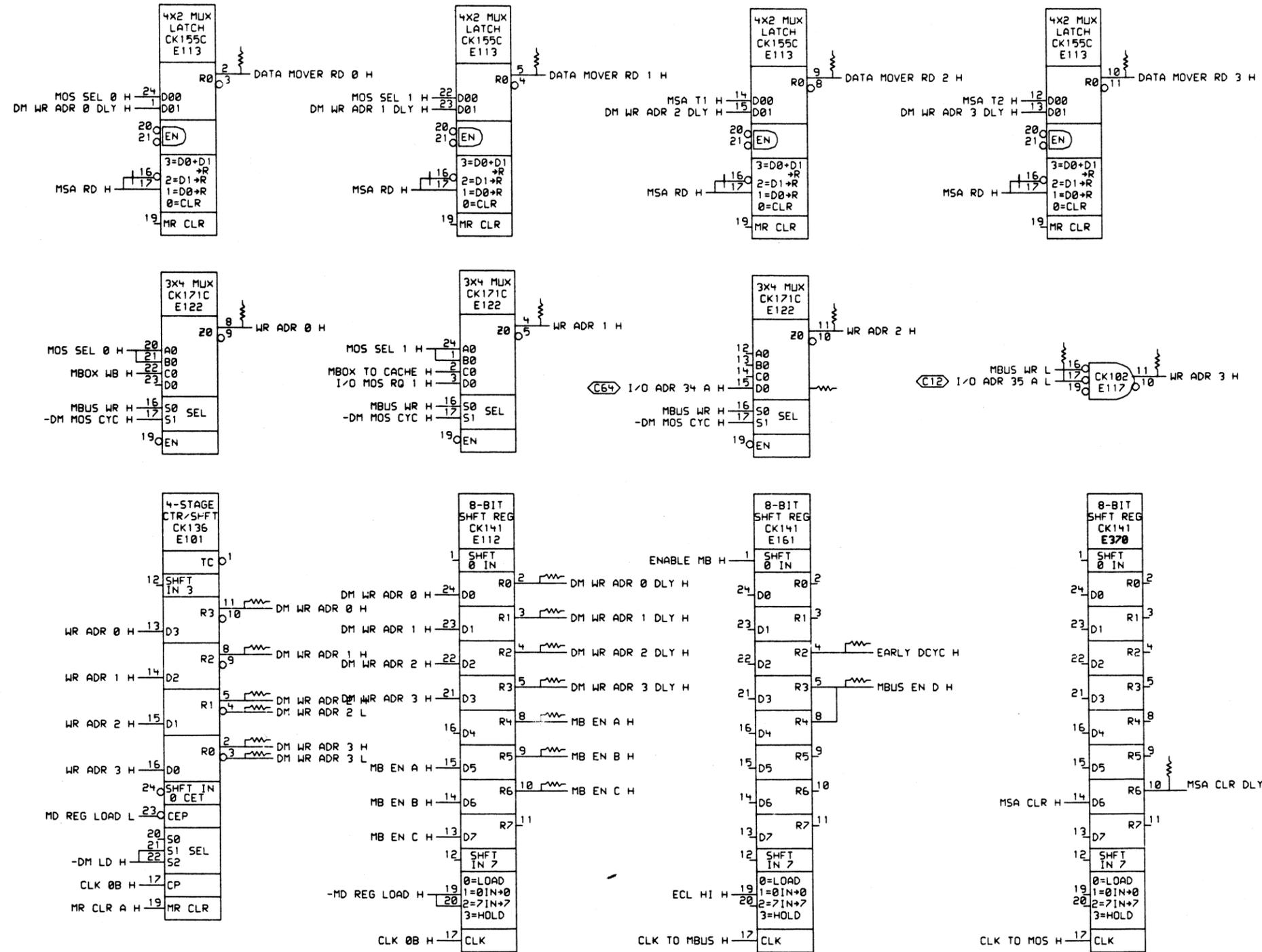
8 7 6 5 4 3 2 1



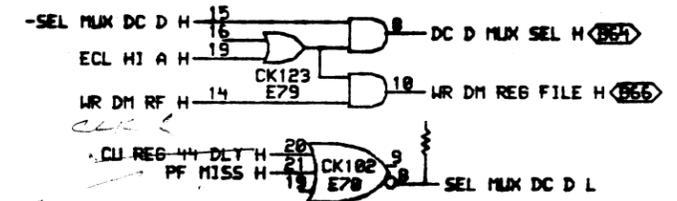
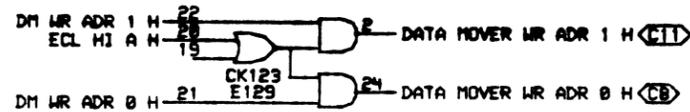
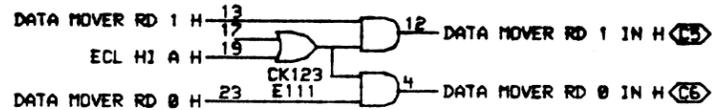
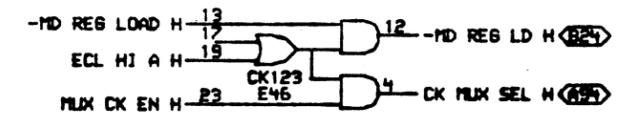
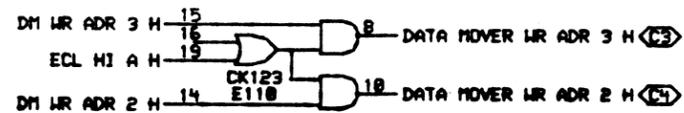
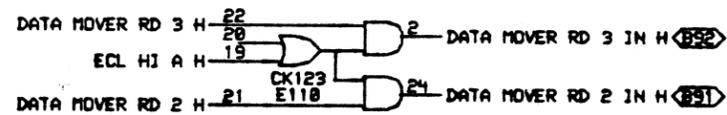
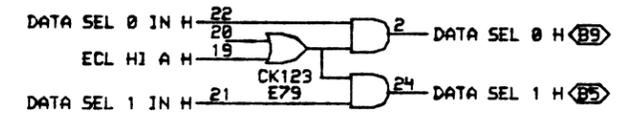
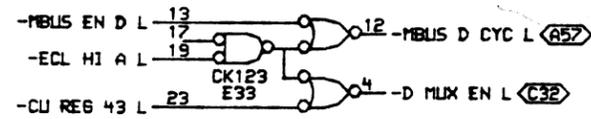
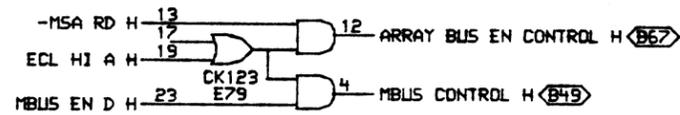
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REVISIONS	
CHK	CHANGE NO. REV

digital	DRW. J. J. J.	DATE 15-OCT-82	ENG.	DATE	TITLE: DATA MOVER CTL
	CHK'D.	DATE	BOARD LOCATION:	SHEET 1 OF 1	
VLLCAN: (2088.CAM)67CAM.DRW 14-OCT-82 15:10		NEXT HIGHER ASSEMBLY:		SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0059-0		D CS	L0059-0-CAM4
					REV. *



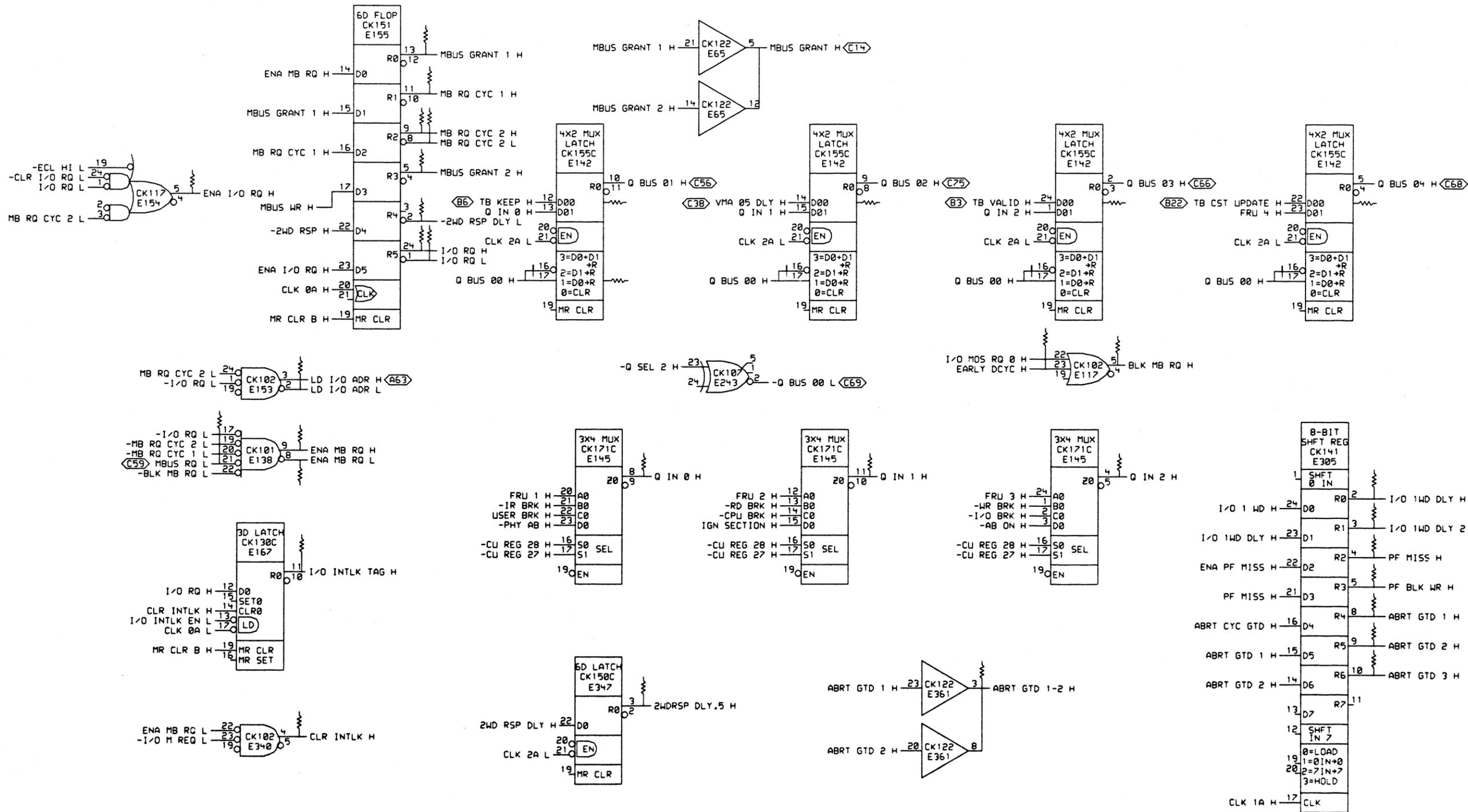
DRN.	DATE	ENG.	DATE	TITLE:	DM RD/WR ADR CTL		
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	SIZE	CODE	NUMBER
ARCH: CUMLER.PLOTS\27CAM5.DRW 21-NOV-82 11:27				NEXT HIGHER ASSEMBLY:	D	CS	L0059-0-CAM5
FIRST USED ON OPTION/MODEL: 2080				B-DD-L0059-0			REV. *



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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE:
	20-MAY-82	J. Family		MDP DMU CONTROL LOGIC
VULCANI (2080, CAM) SCALE: DRW	DATE	BOARD LOCATION:	SIZE	CODE
27-MAY-82 19:01			D	CS
FIRST USED ON OPTION MODEL: 2080	SHEET	OF	NUMBER	REV.
			L0059-0-CAM6	*



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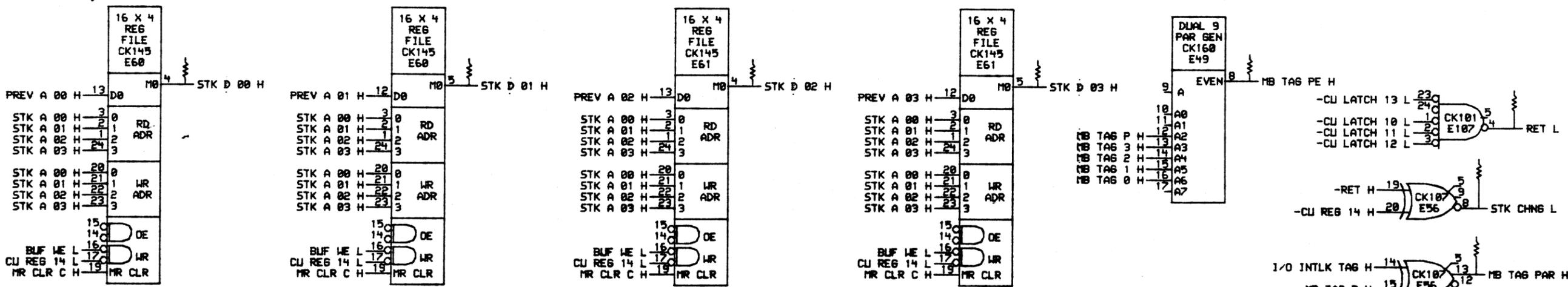
REVISIONS	
CHK	CHANGE NO. REV

**digital** DR. *J. J. J.* DATE *01-JAN-87* ENG. DATE TITLE:  
 VULCANI (2000.CAM) (CAM7). DRW 103-JAN-83 11:13 NEXT HIGHER ASSEMBLY: B-DD-L0059-0  
 FIRST USED ON OPTION/MODEL: 2080

SIZE	CODE	NUMBER	REV.
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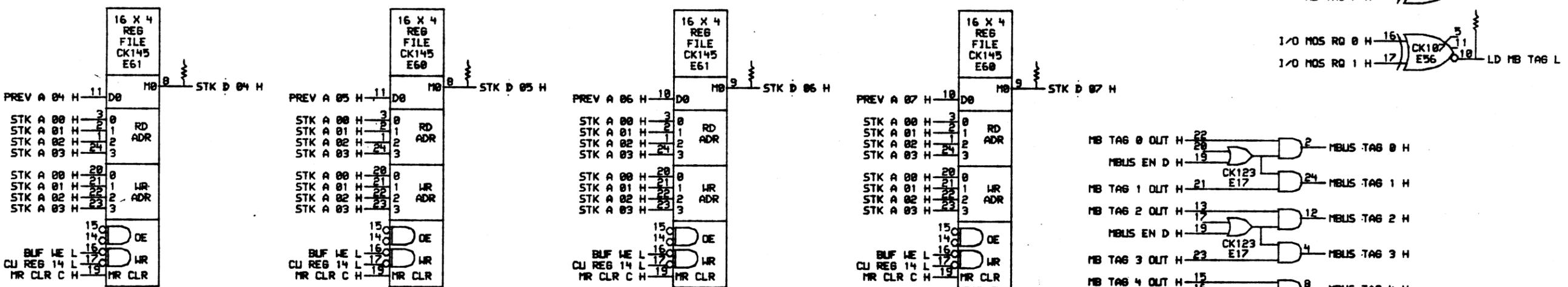
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D



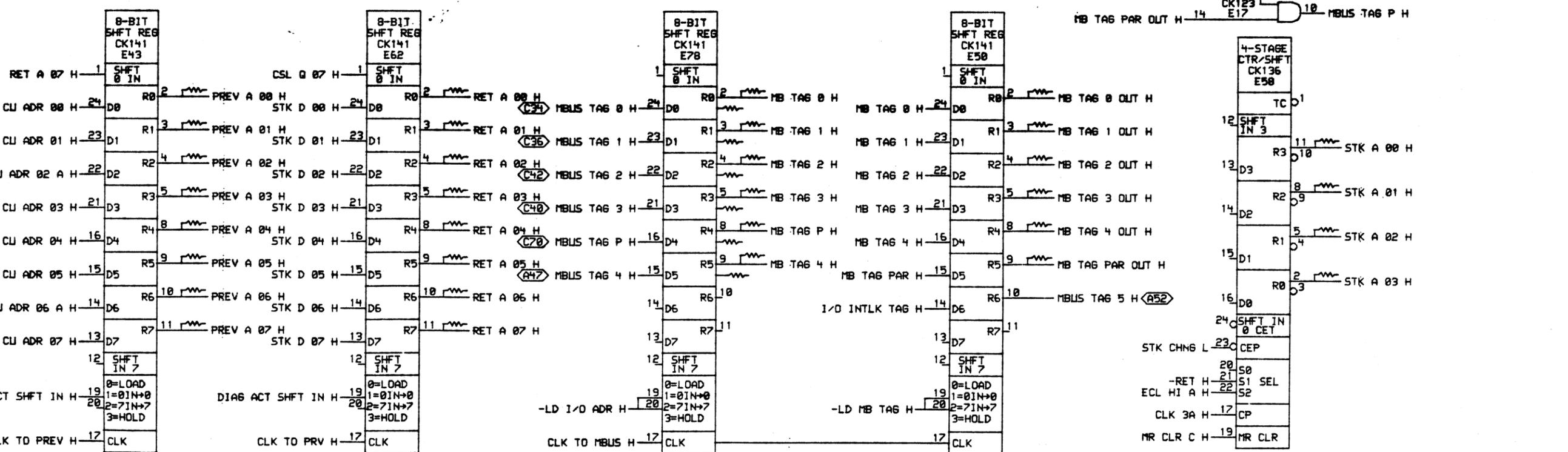
C

C



B

B



A

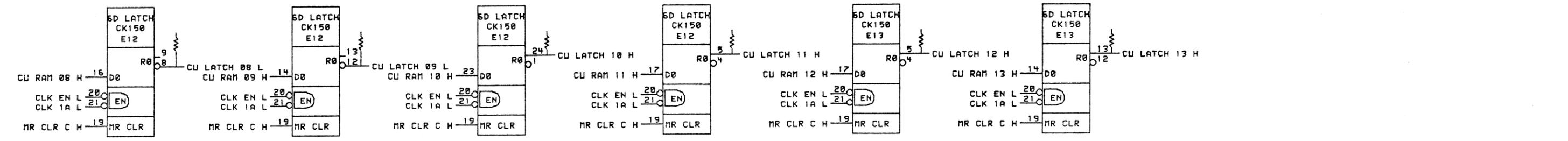
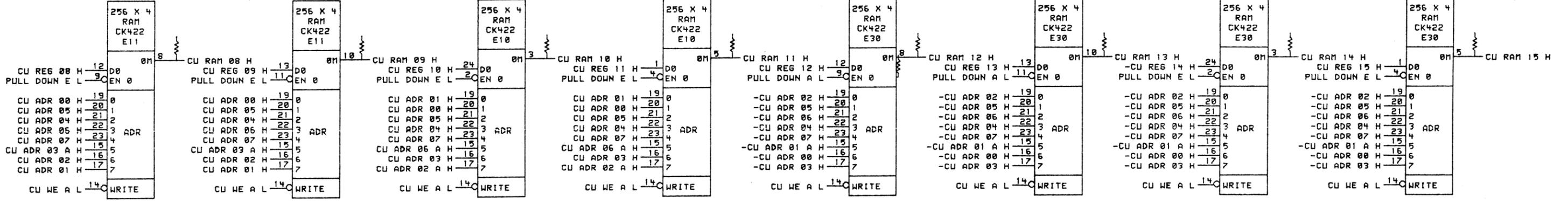
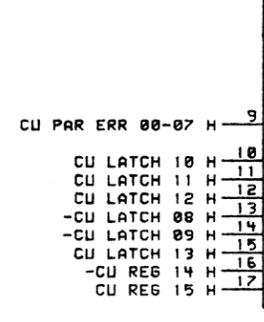
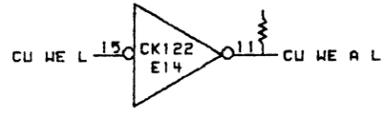
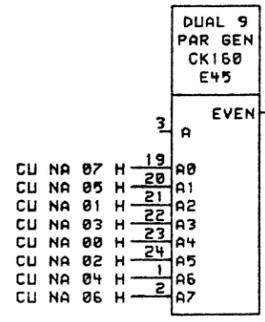
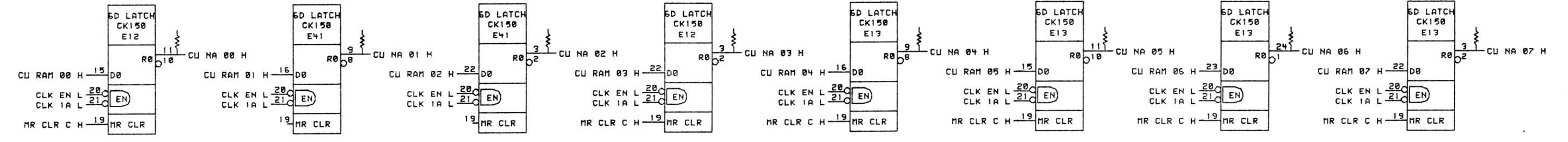
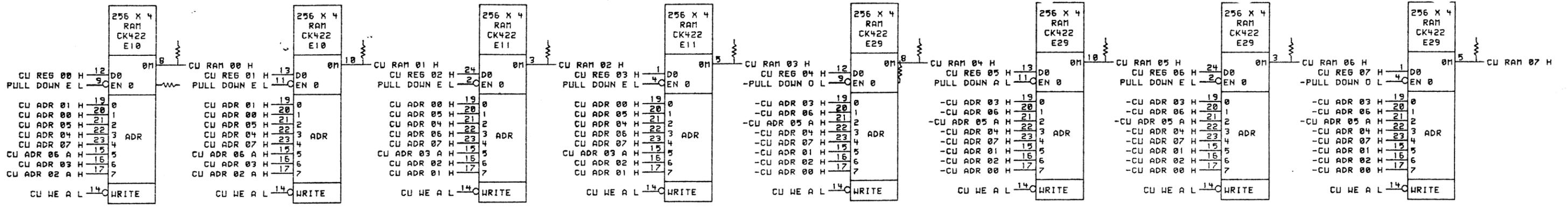
A

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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE:
	12-JUL-82	J. J. J.		CU STACK
	DATE	BOARD LOCATION:		
		SHEET		
VLLCAN: (2000,CAM)250CAM.DRW	11-JUL-82 11:41	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: 2000		B-DD-L0059-0	D	CS

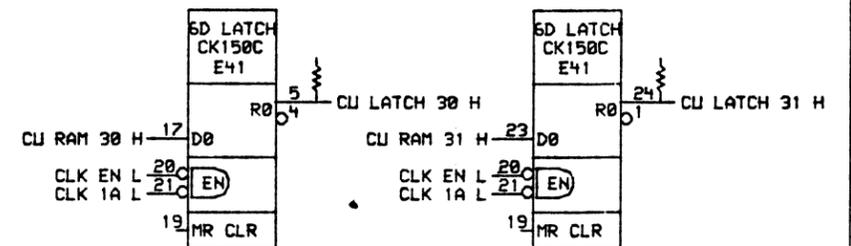
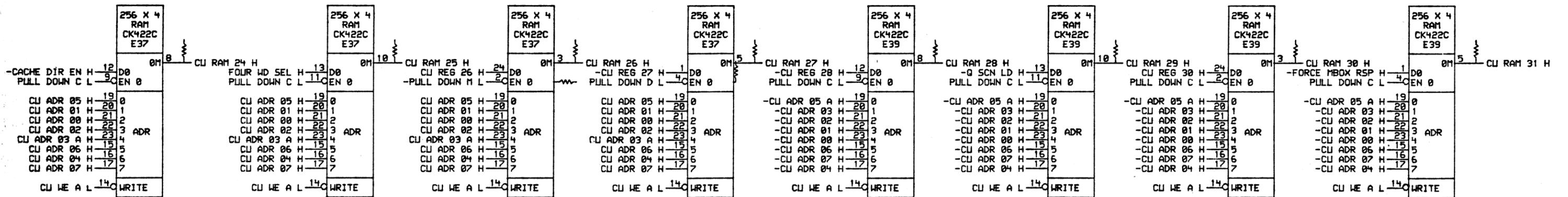
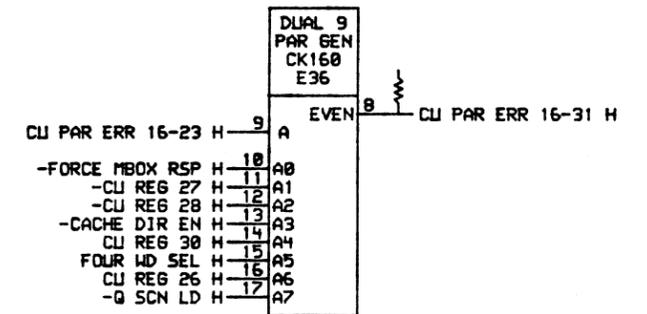
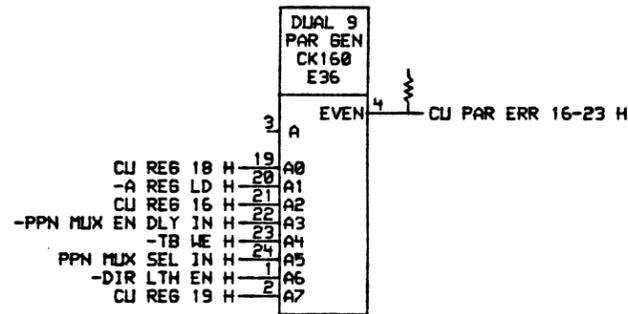
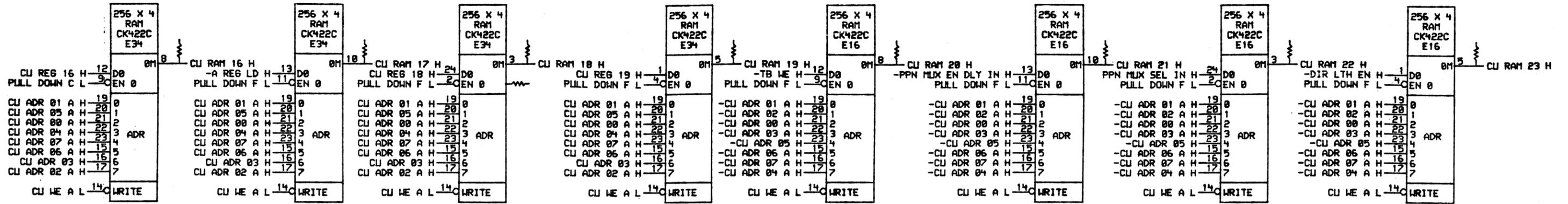
NUMBER	REV.
L0059-0-CAM8	*



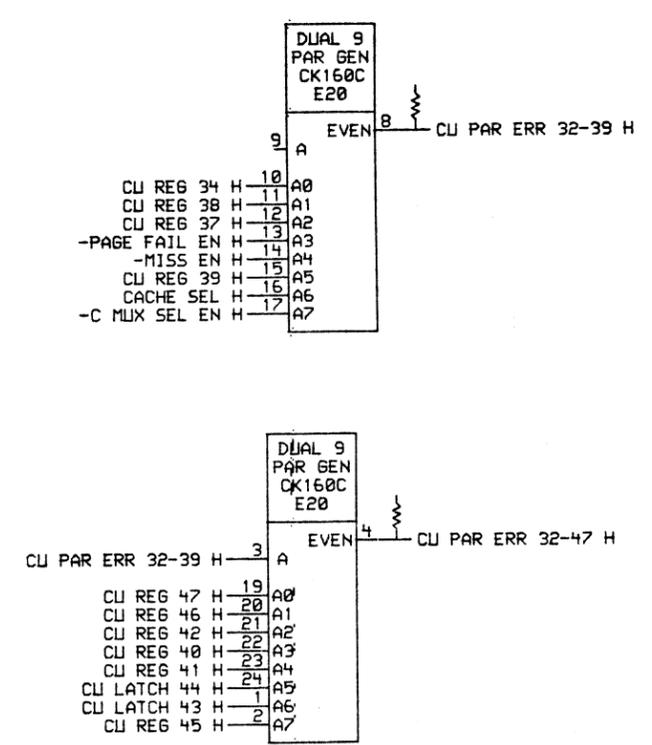
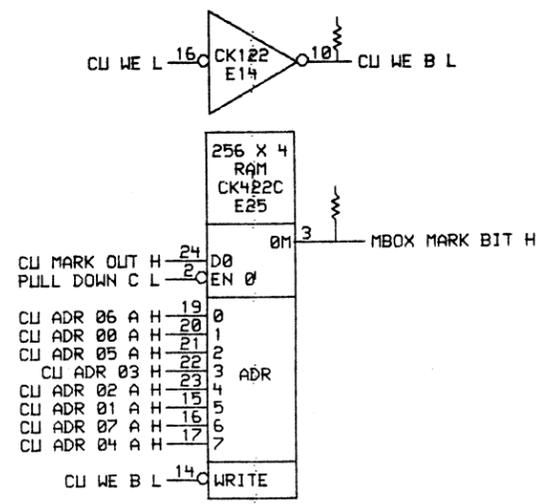
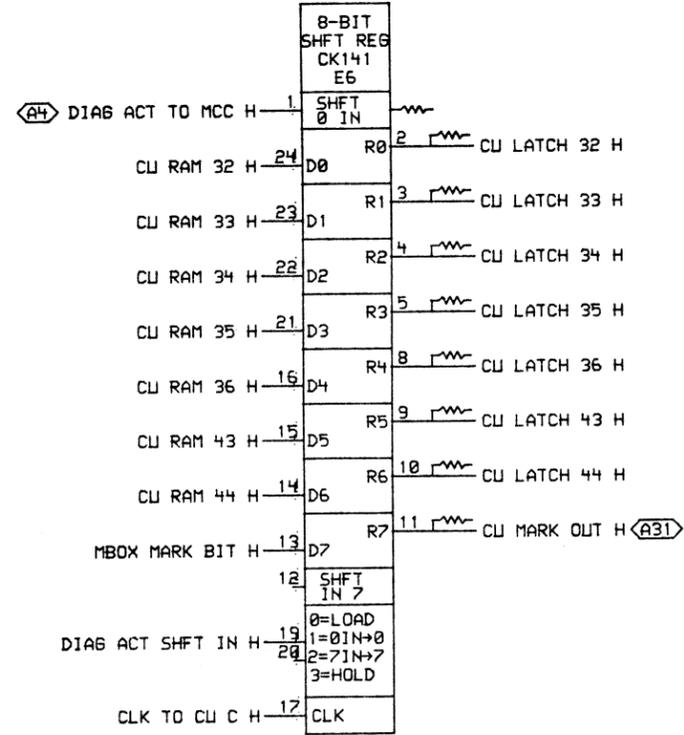
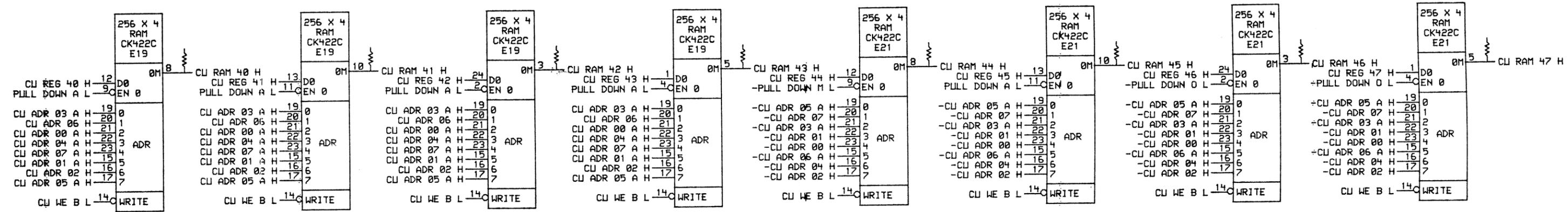
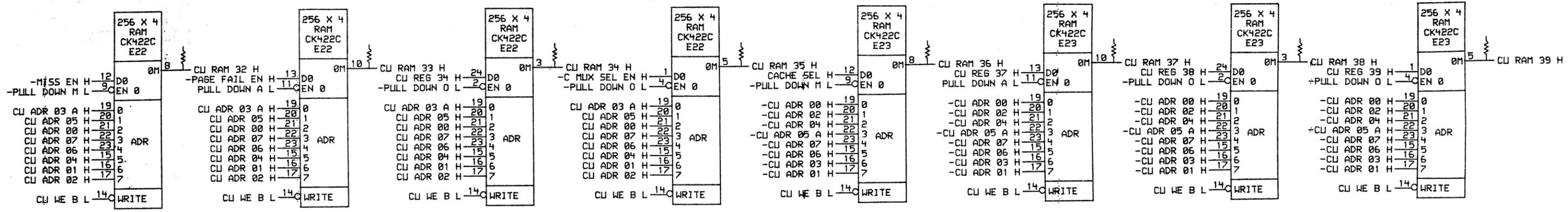
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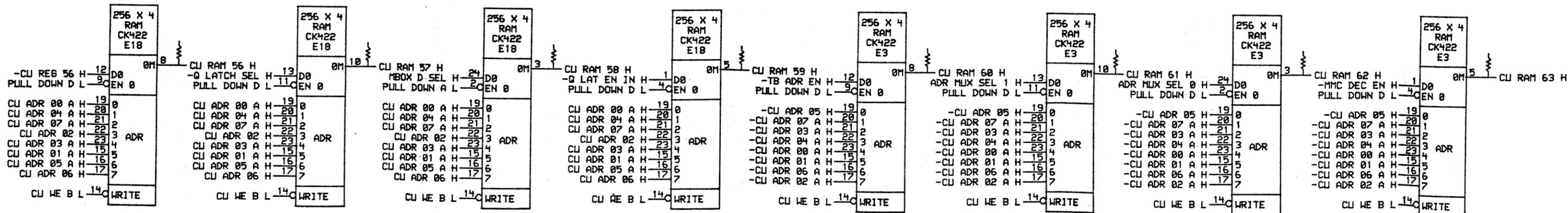
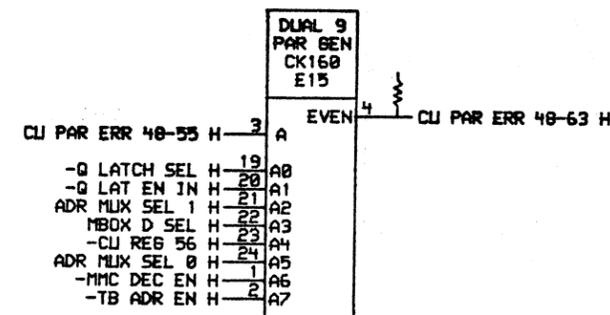
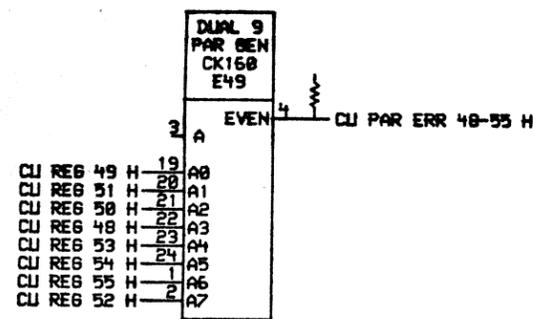
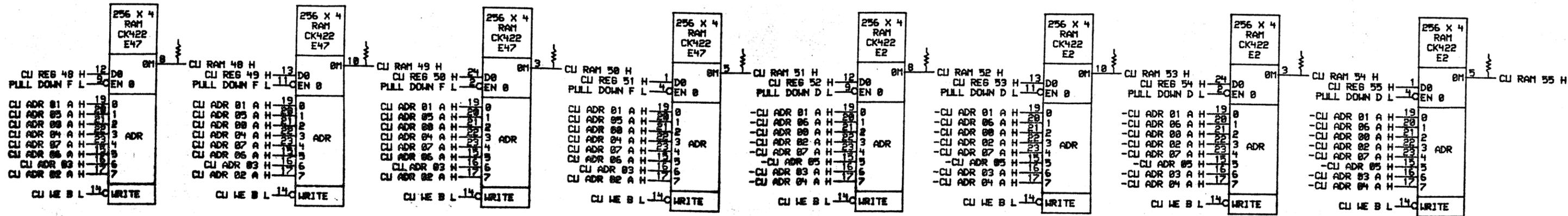
REVISIONS		
CHK	CHANGE NO.	REV

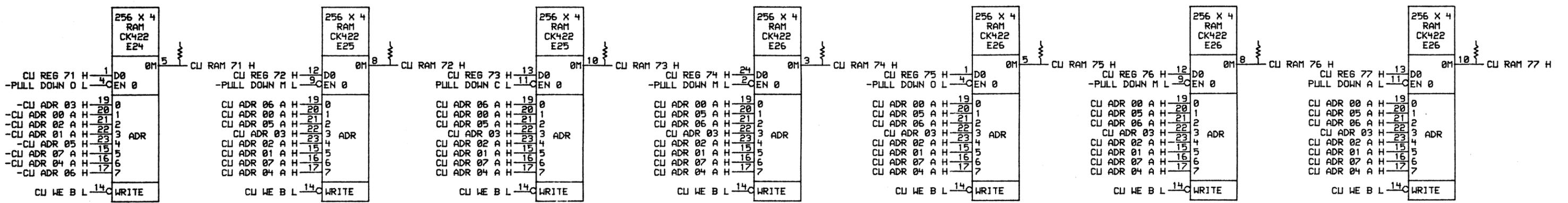
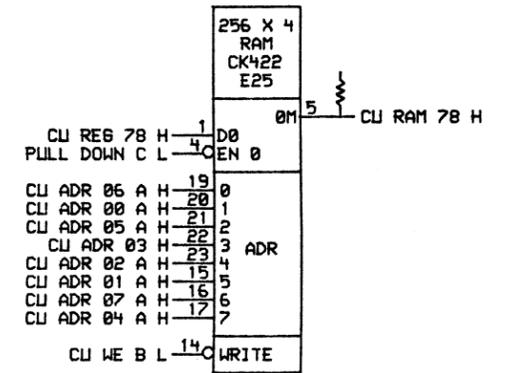
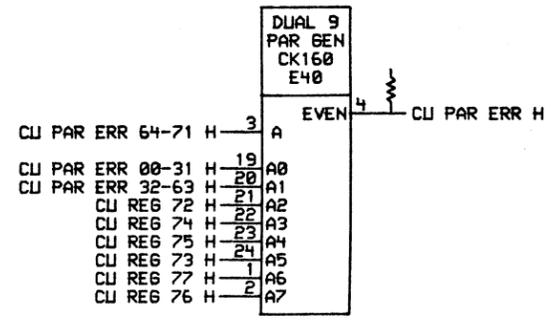
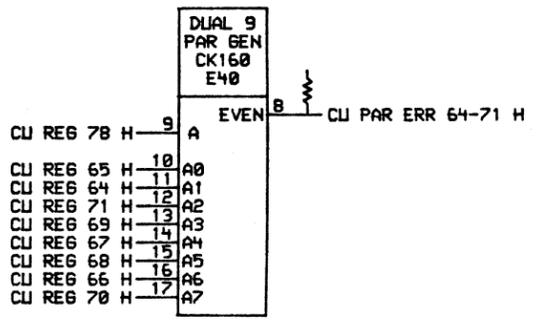
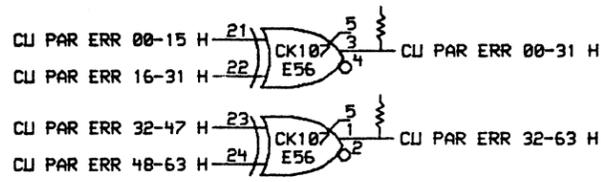
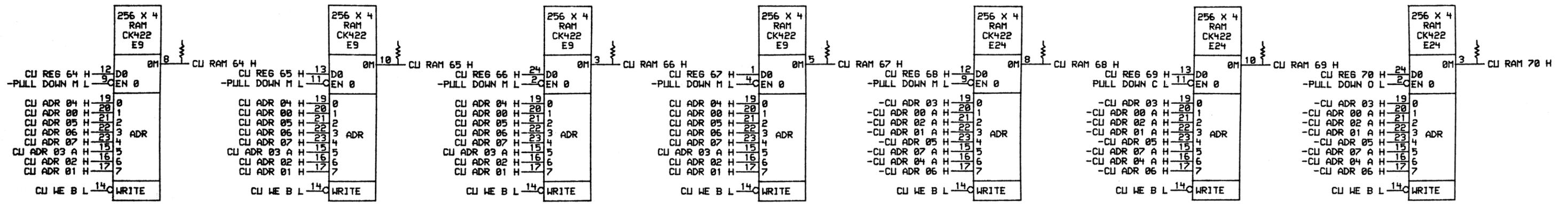
<b>digital</b>	DATE	ENG.	DATE	TITLE:
	11-MAR-82	J. J. J.		CU RAM 00-15, CU LATCH 00-15 + P
	DATE	BOARD LOCATION:		
		SHEET	OF	
VULCANI (2000.CAM) 14CAM9.DRW		10-MAR-82 21:54	NEXT HIGHER ASSEMBLY:	SIZE CODE
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0059-0	D C5
				NUMBER
				L0059-0-CAM9
				REV.
				*



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	24-AUG-82	BOARD LOCATION:		CU RAM 16-31,
		SHEET	2 OF 5	CU LATCH 16-31 + P
VILCAN: <2080.CAM>99CAMA.DRW	17-AUG-82 09:44	NEXT HIGHER ASSEMBLY:		SIZE CODE
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0059-0		D CS L0059-0-CAMA
				NUMBER
				REV.
				*





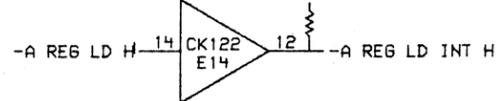
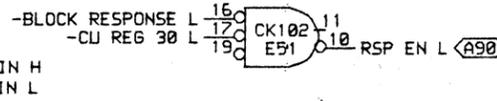
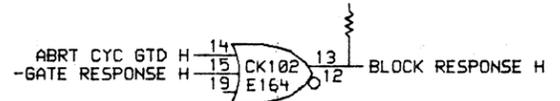
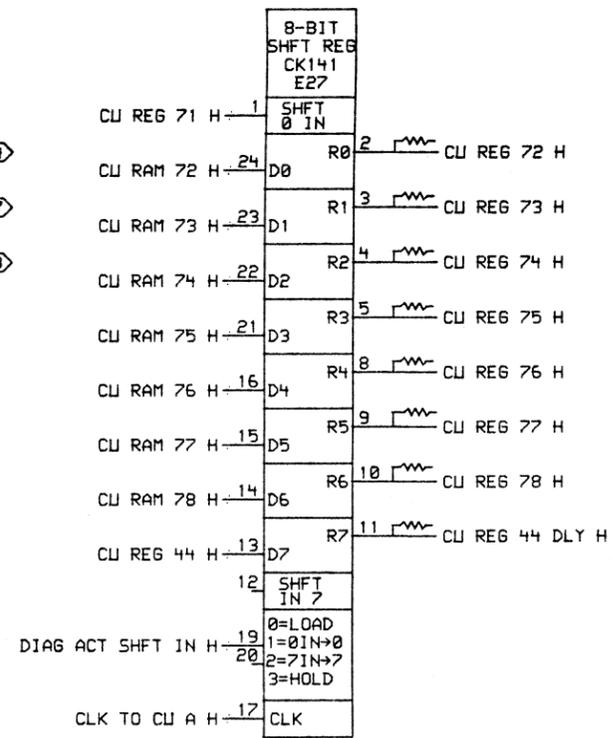
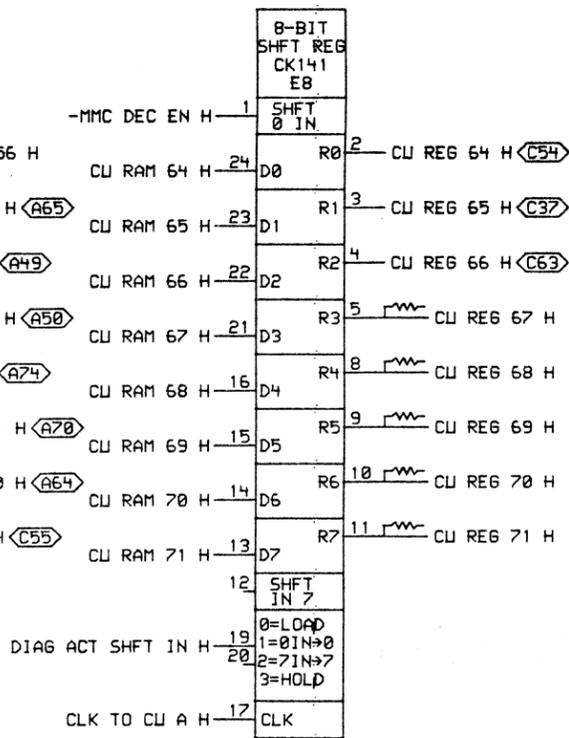
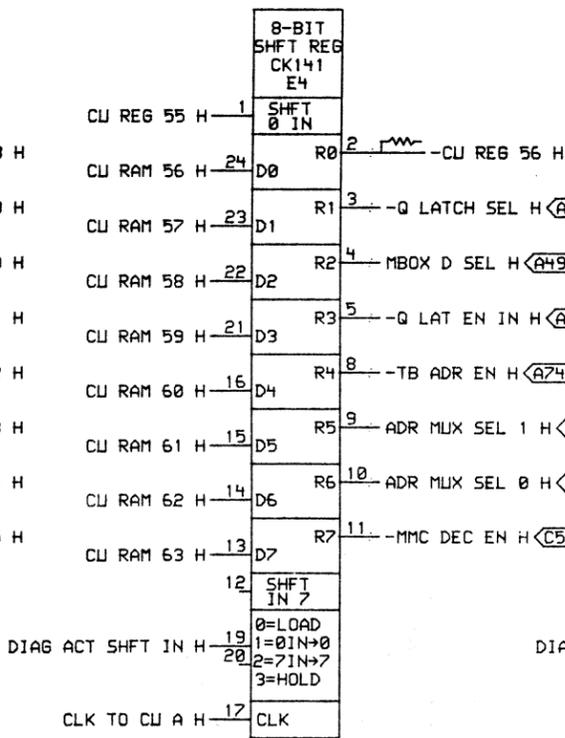
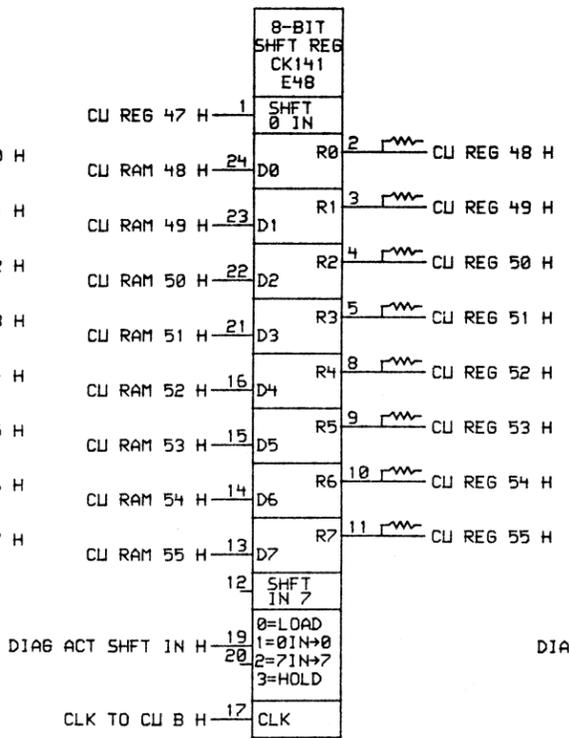
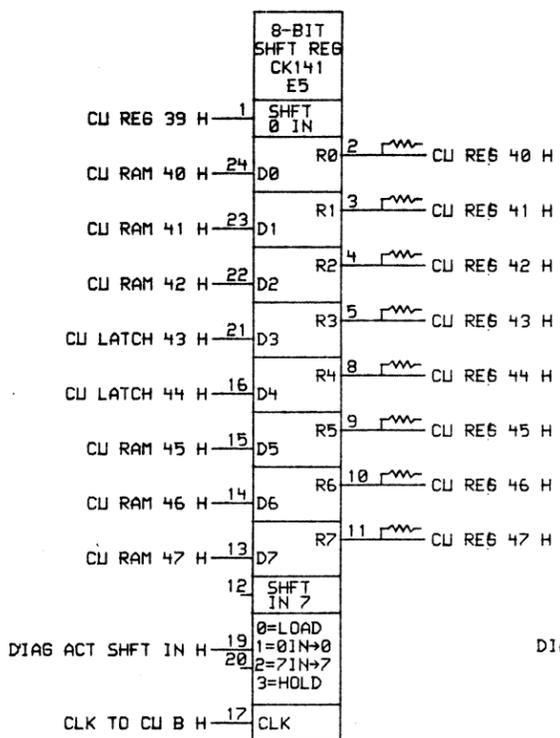
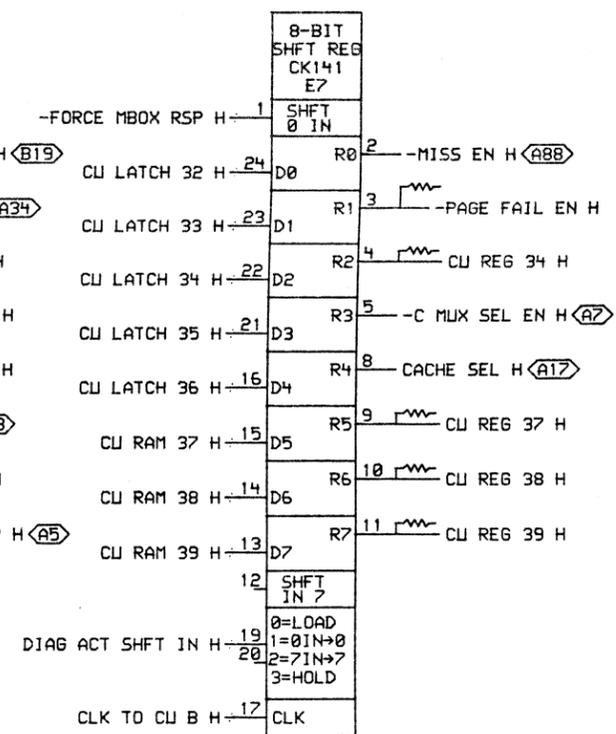
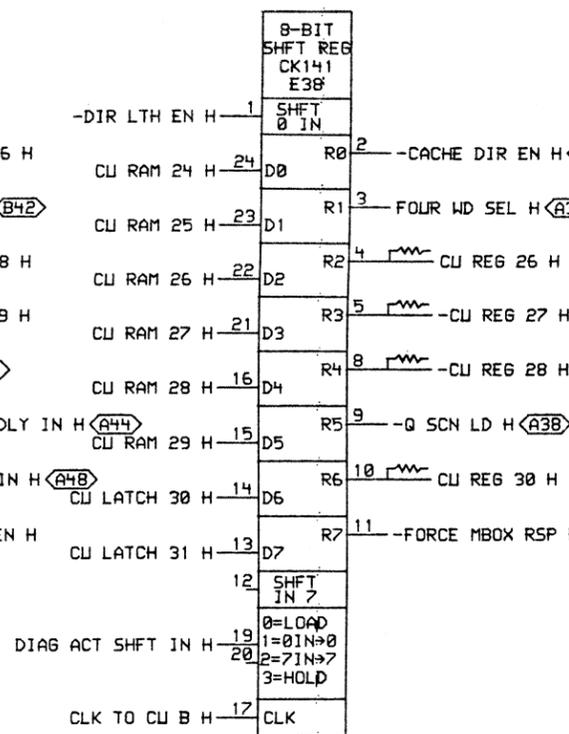
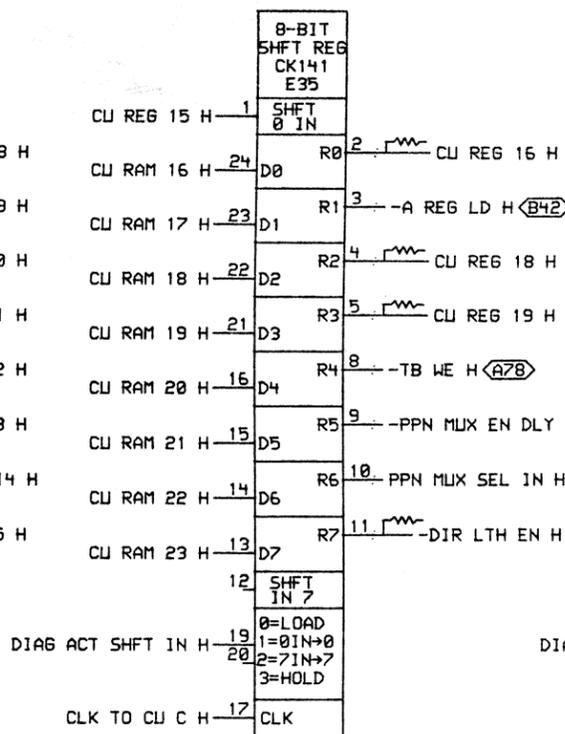
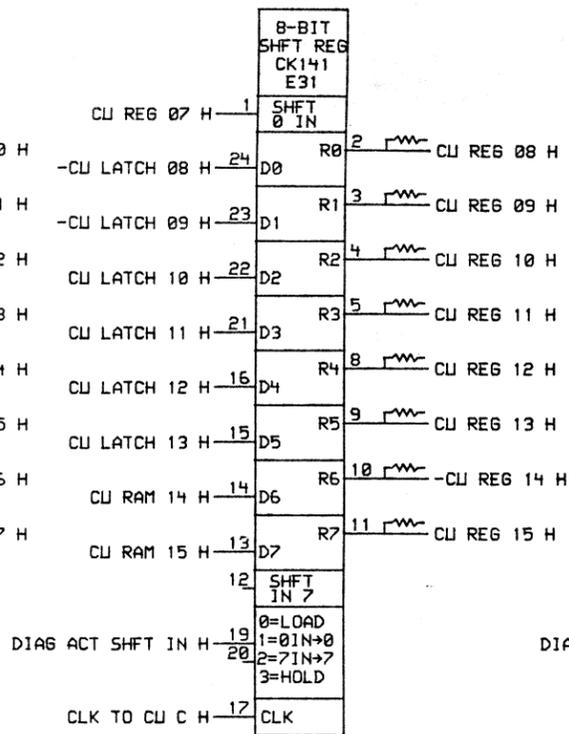
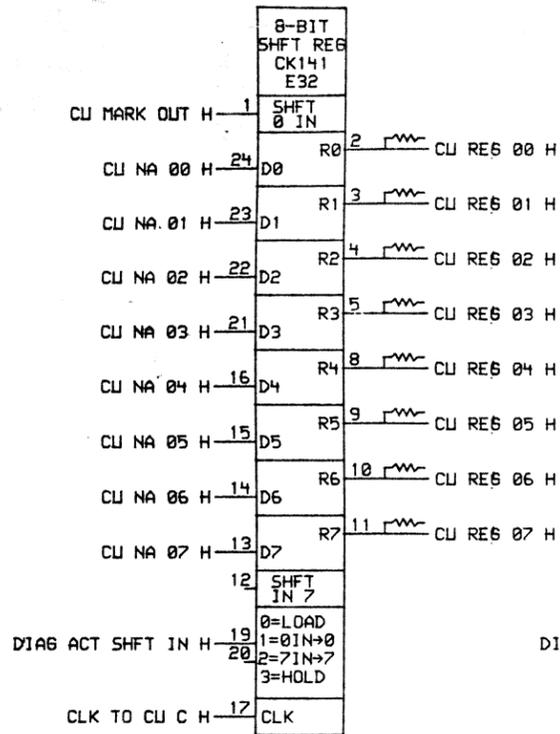


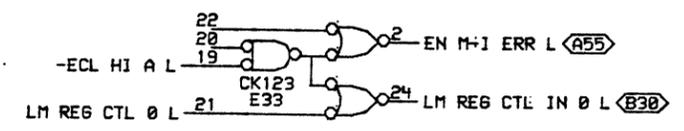
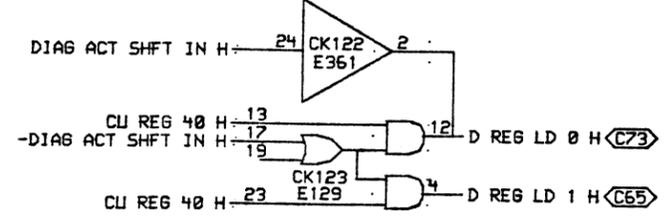
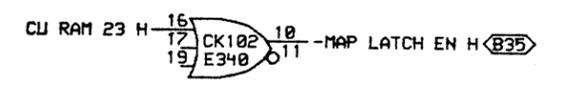
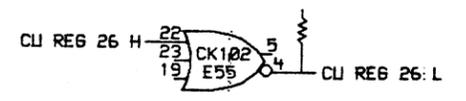
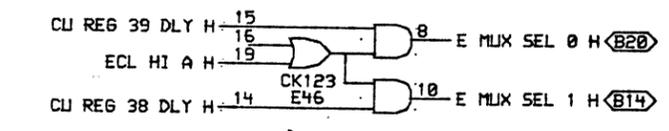
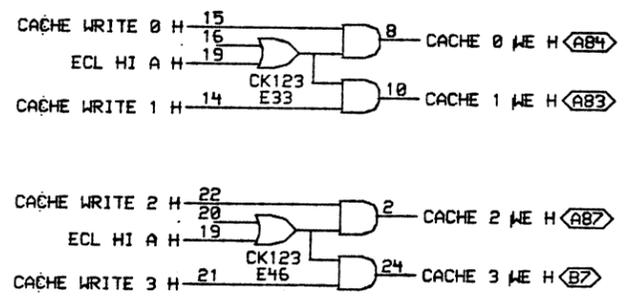
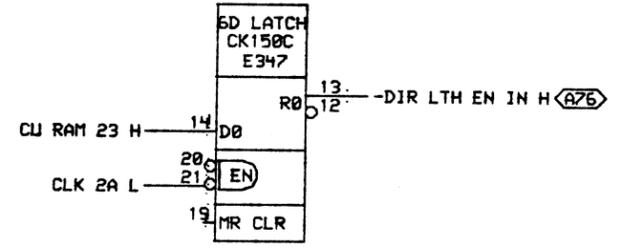
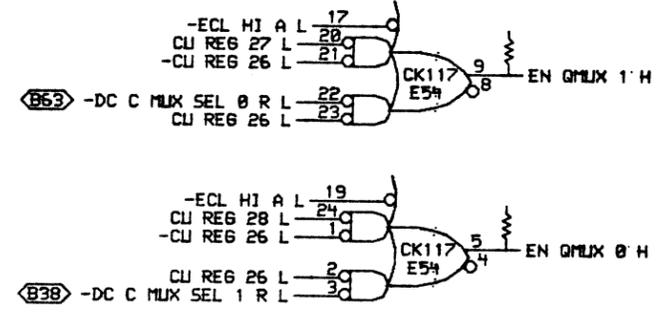
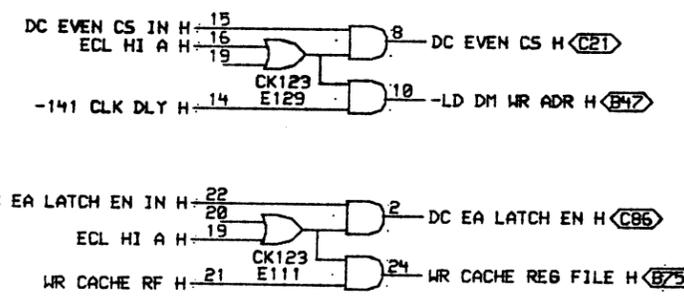
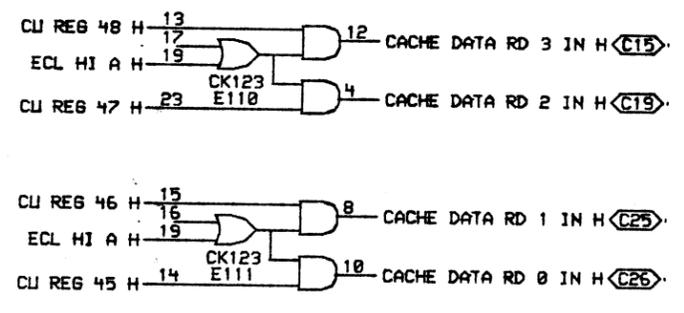
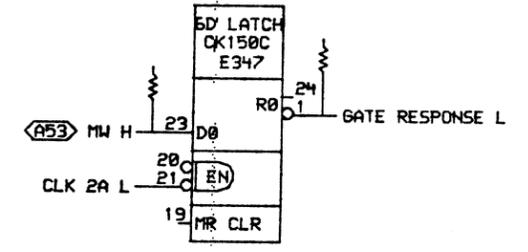
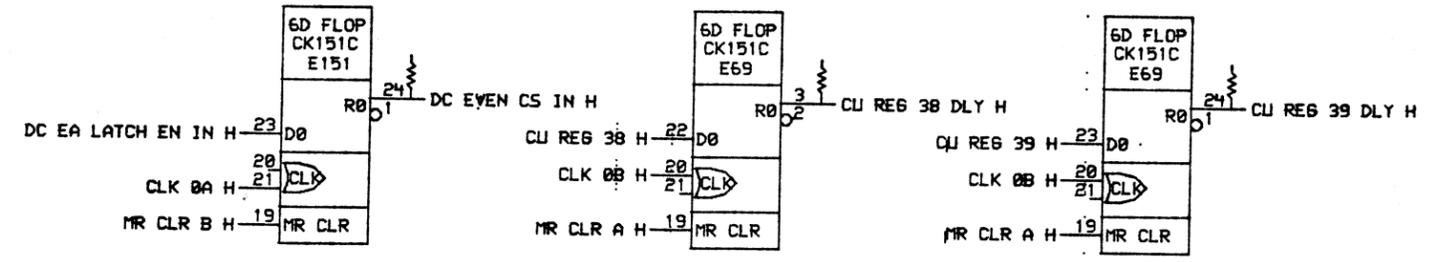
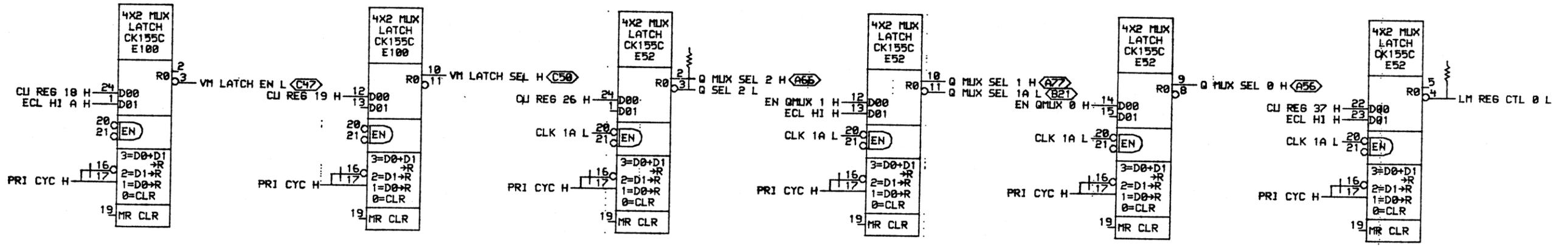
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REVISIONS	
CHK	CHANGE NO. REV

DATE	ENG.	DATE	TITLE
06-MAR-82	J. J. J.		CU RAM 64-73 CU LATCH 64-73 + P

digital	DATE	ENG.	DATE	TITLE
	06-MAR-82	J. J. J.		CU RAM 64-73 CU LATCH 64-73 + P
VULCAN (2000.CAM) \DCAMD.DRW 105-MAR-82 10:10	DATE	BOARD LOCATION:	SHEET	OF
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0059-0	4	5

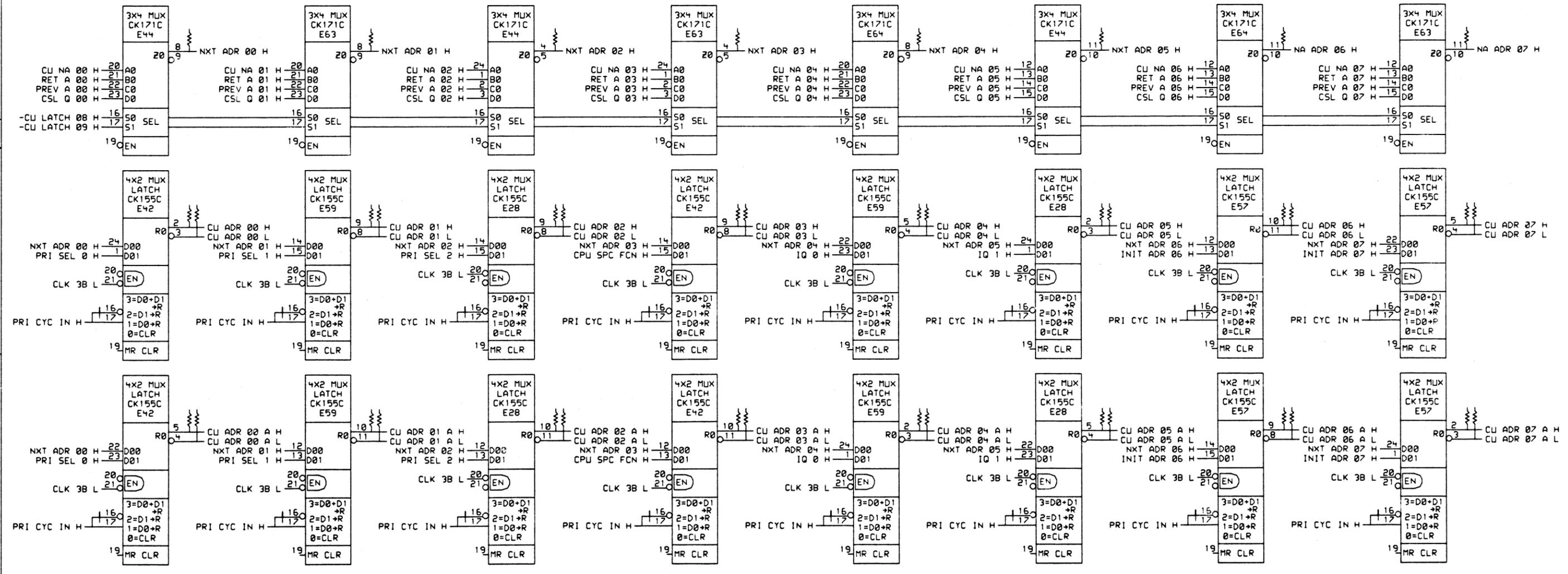
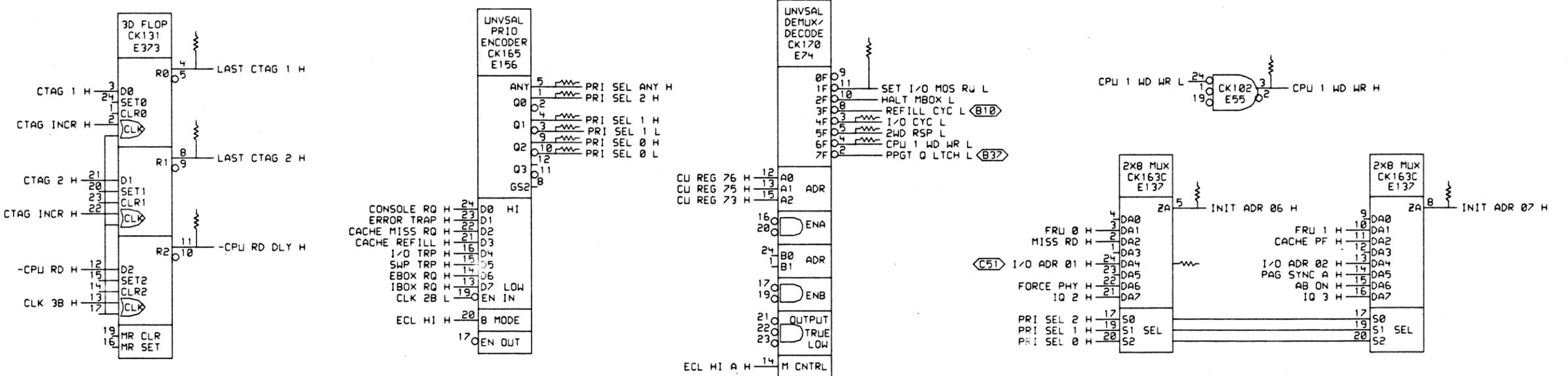




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REVISIONS	
CHK	CHANGE NO. REV.

digital	DATE	11-NOV-82	ENG.		DATE		TITLE:	MDP CU CONTROL LOGIC
	CHK'D.							
VULCAN: C2880.CAM175.CAMF.DRW		10-NOV-82 20:28	NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL: 2080:		B-DD-L0059-0		D	CS	L0059-0-CAMF	*	

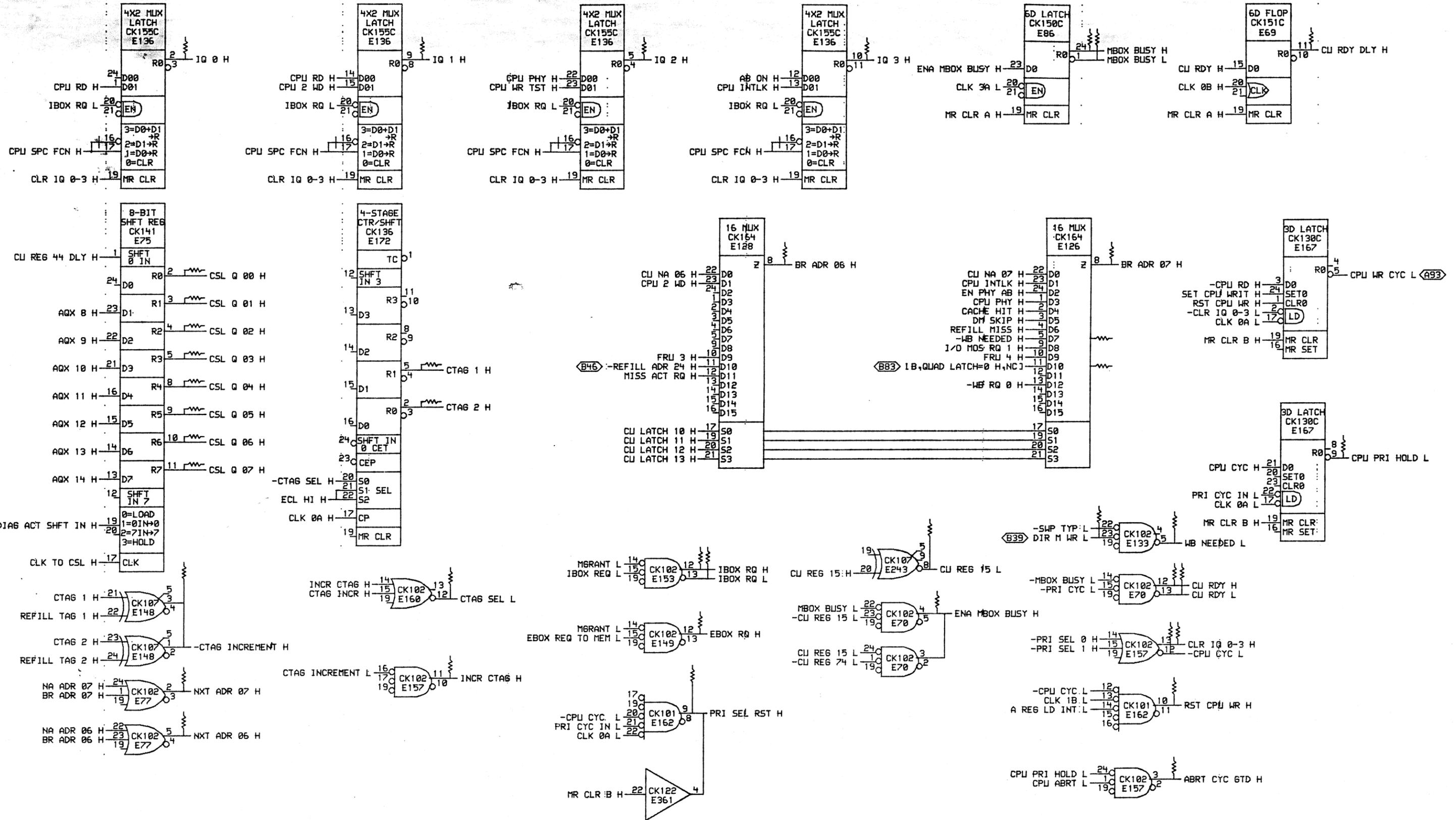


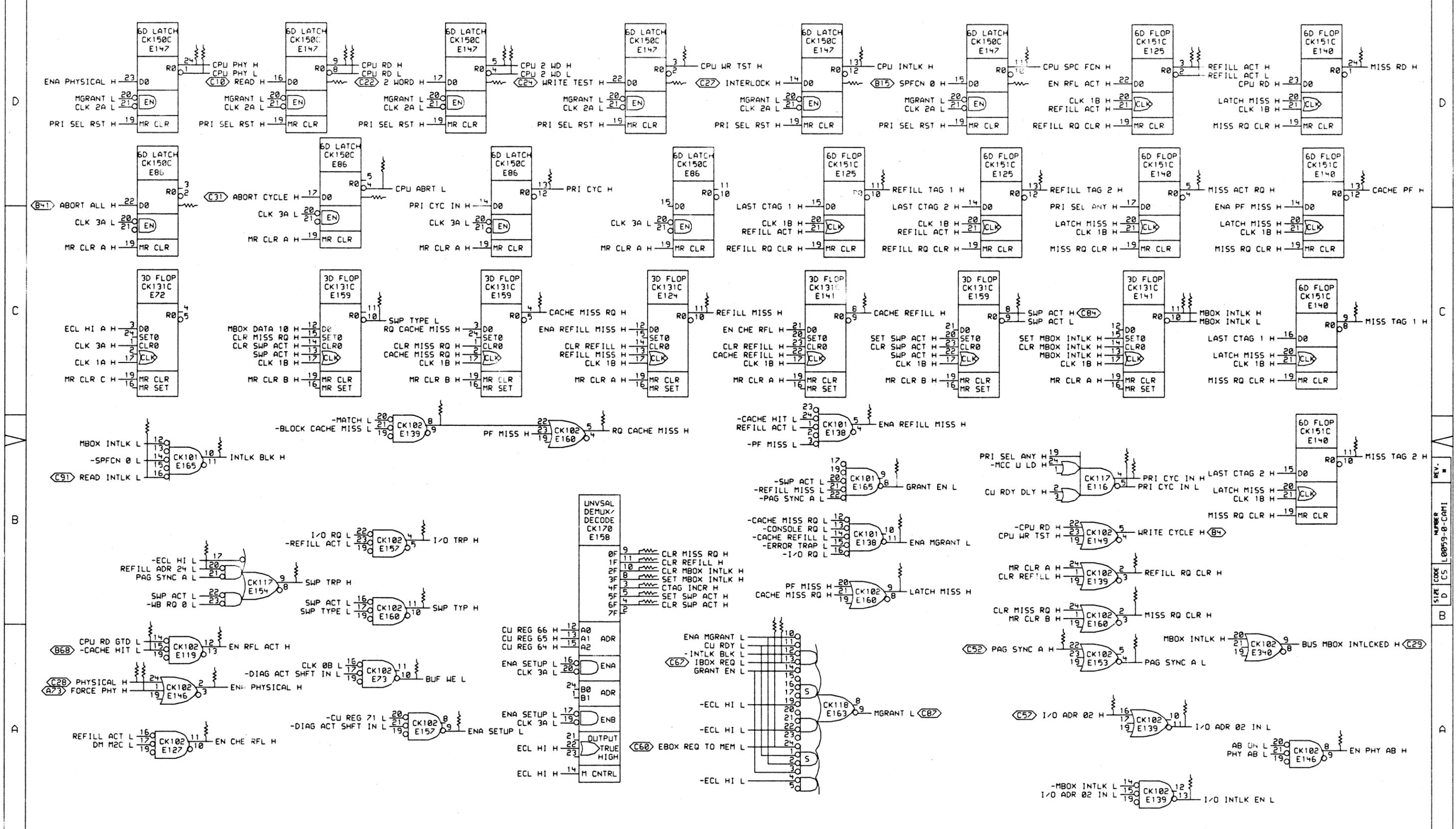
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REVISIONS		
CHK	CHANGE NO.	REV

digit@l  
 DATE 22-DEC-82  
 DATE 21-DEC-82 14:09  
 NEXT HIGHER ASSEMBLY: B-DD-L0059-0  
 TITLE: NXT ADR MUX CU ADR LATCH  
 SIZE CODE D CS  
 NUMBER L0059-0-CAMG  
 REV. \*

REV. 1  
 NUMBER L0059-0-CAMG  
 SIZE CODE D CS

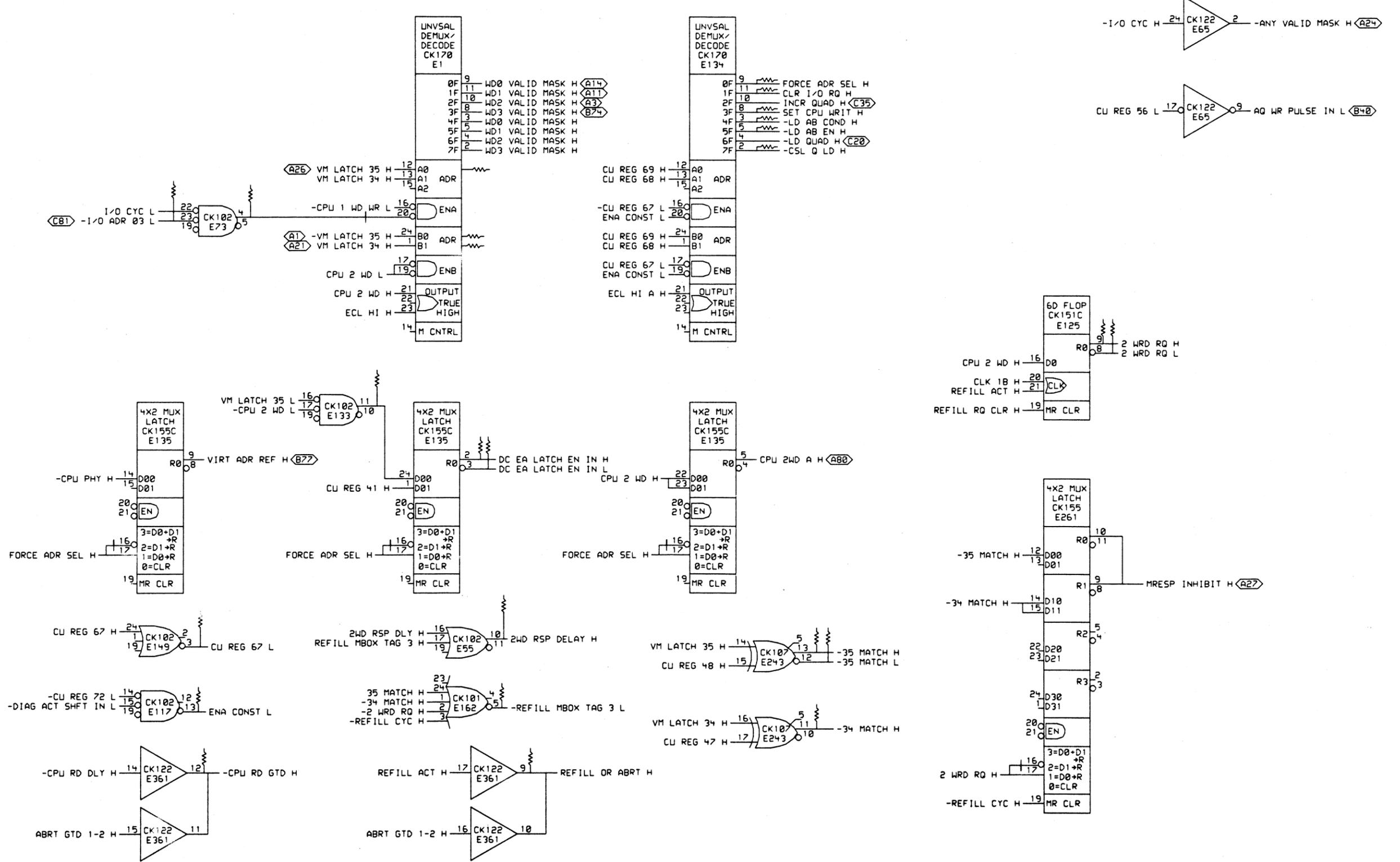




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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:	CPU CYC QUAL
	21-JAN-83				
VULCAN: (2080.CAM)8-CAMI.DRW	DATE	BOARD LOCATION:	SHEET 1 OF 1		SIZE CODE
	20-JAN-83 14:59	B-DD-L0059-0	NEXT HIGHER ASSEMBLY:		D CS
FIRST USED ON OPTION/MODEL: 2080	NUMBER			REV.	*
L0059-0-CAMI					

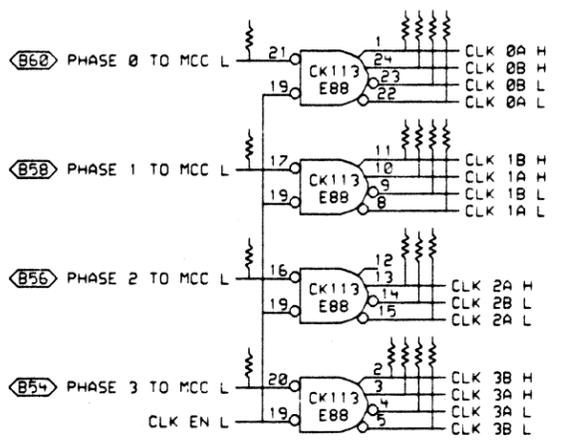
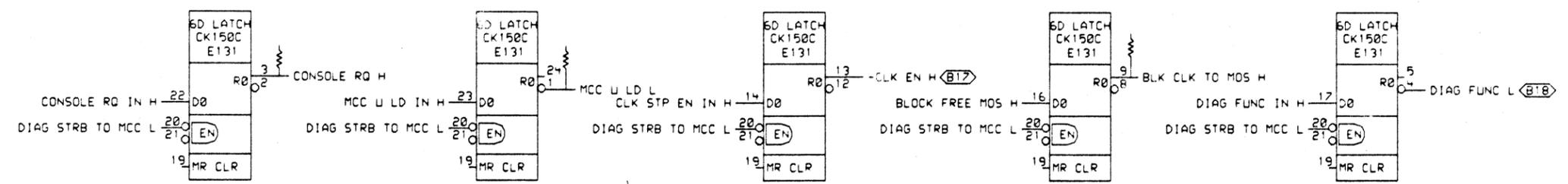


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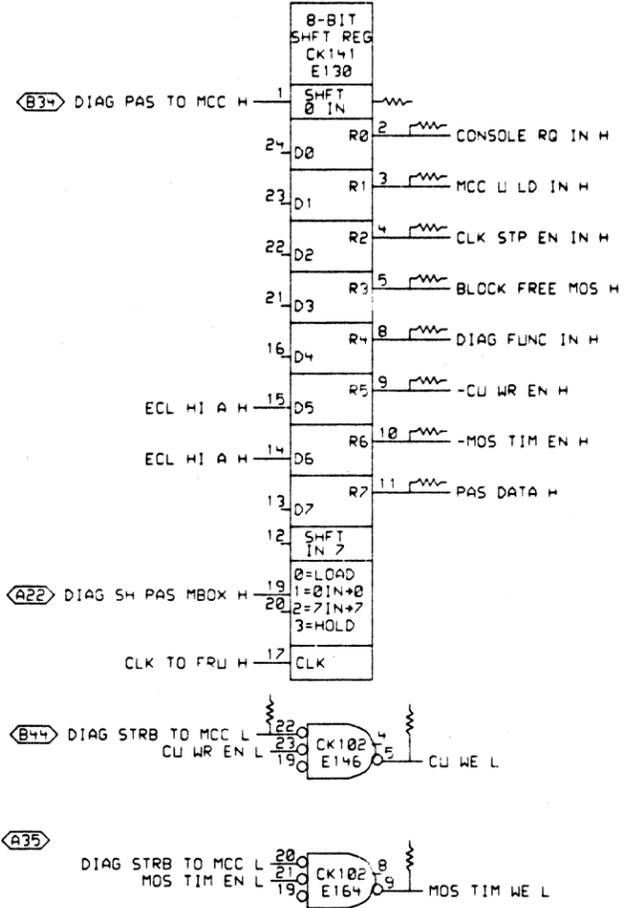
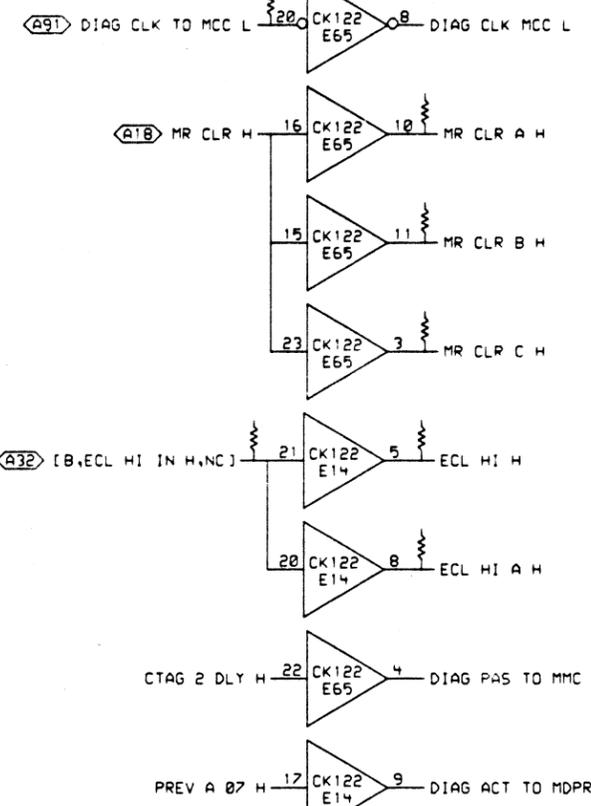
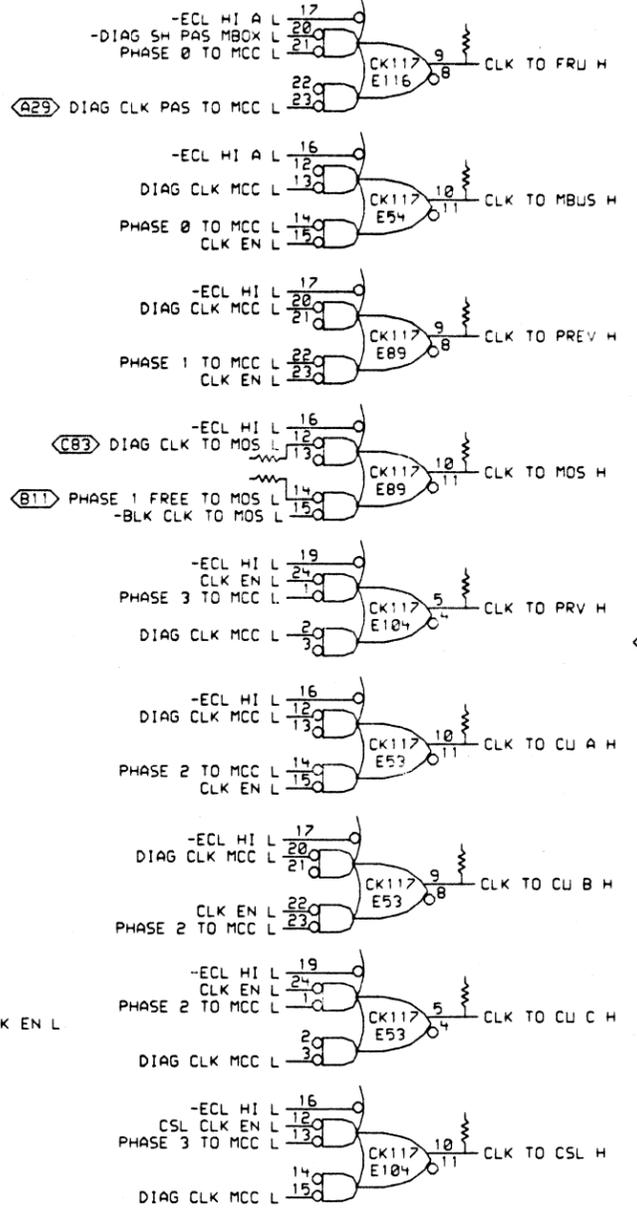
REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE: MAP CONTROL LOGIC
	22-DEC-82			
VULCANI(2080.CAM)BICAMJ.DRW		DATE		BOARD LOCATION:
21-DEC-82 13:24		NEXT HIGHER ASSEMBLY:		SHEET 1 OF 1
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0059-0		SIZE CODE
				D CS
				NUMBER
				L0059-0-CAMJ
				REV.
				*

REV. #  
 NUMBER L0059-0-CAMJ  
 CODE CS  
 D

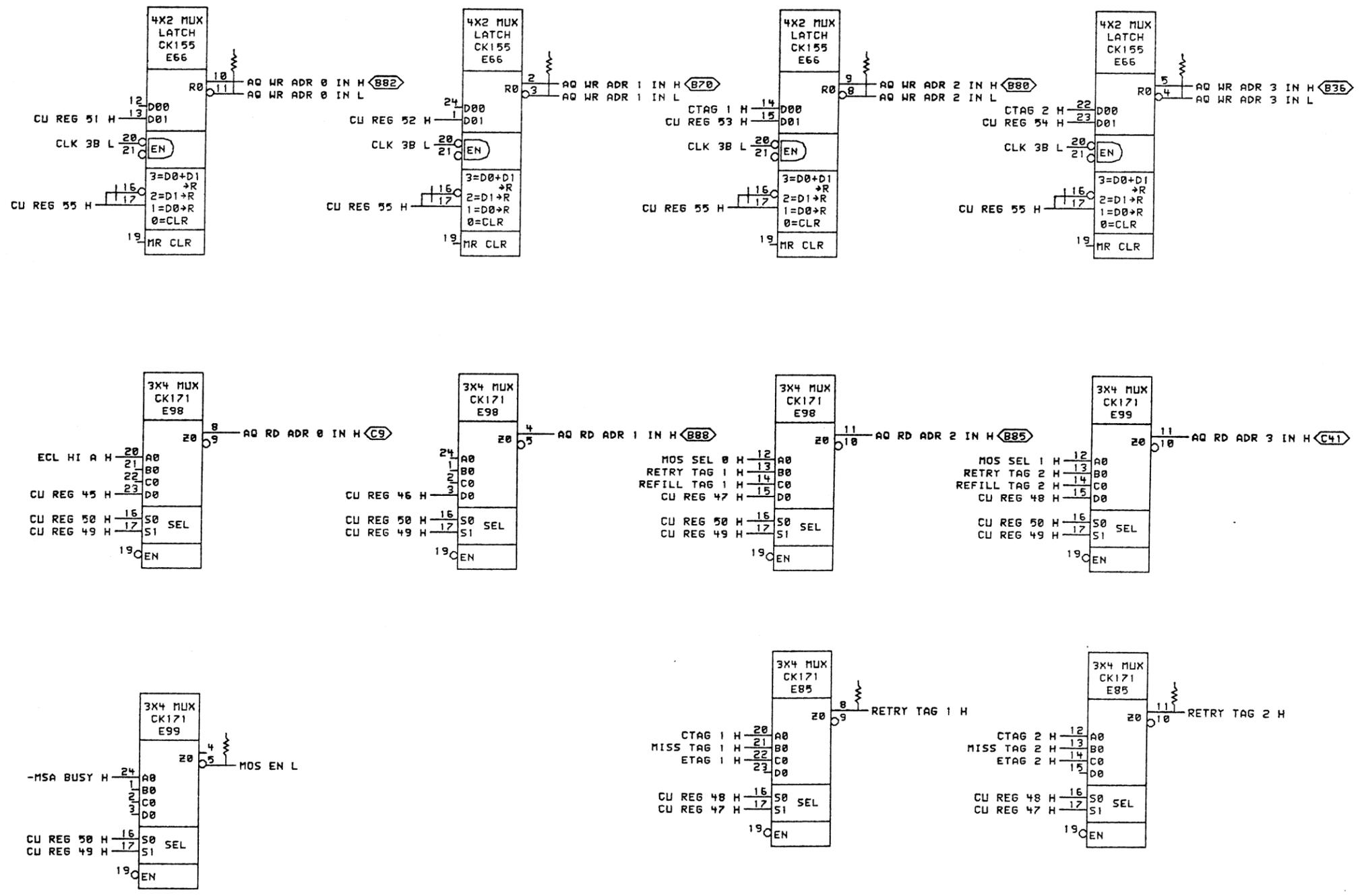


(A10) [B,DIAG CLK MCC L,DIAG CLK TO MDPL L]  
 (A12) [B,DIAG CLK PAS TO MCC L,DIAG CLK PAS TO MAP L]



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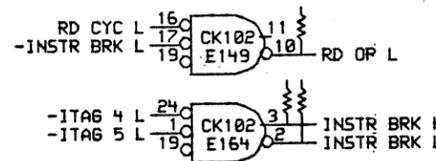
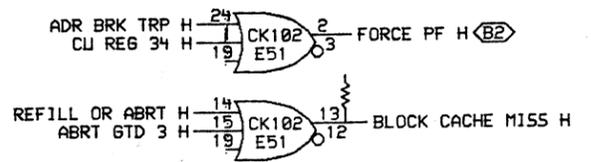
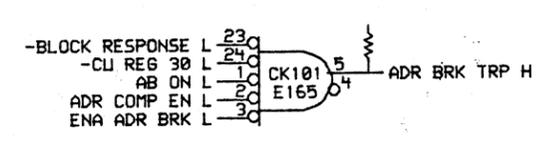
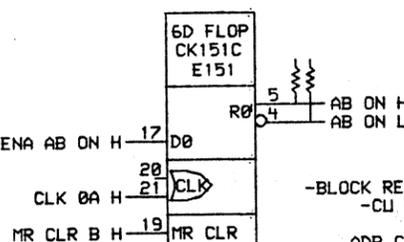
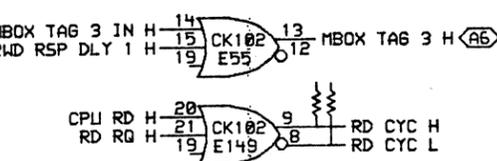
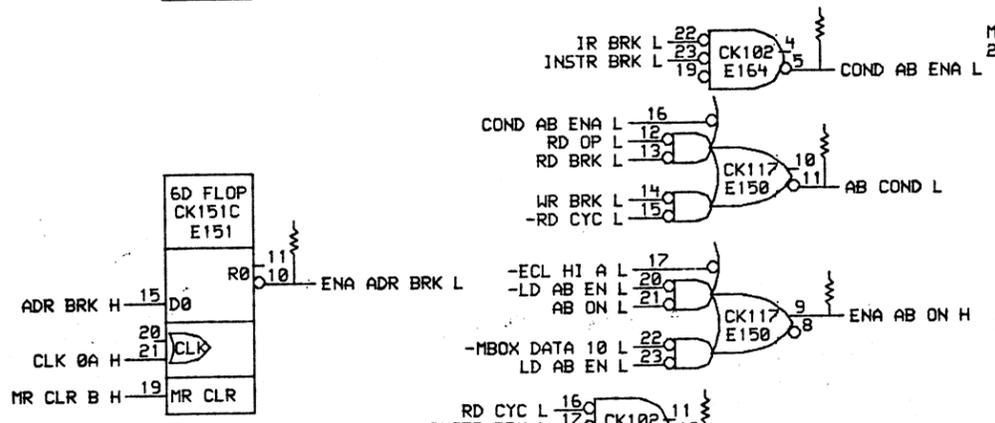
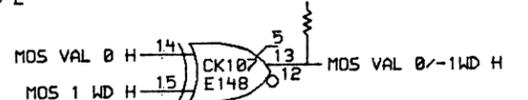
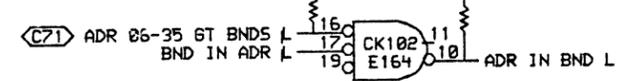
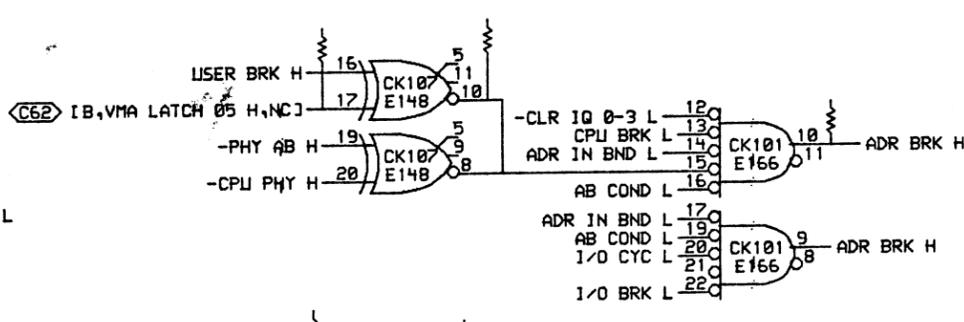
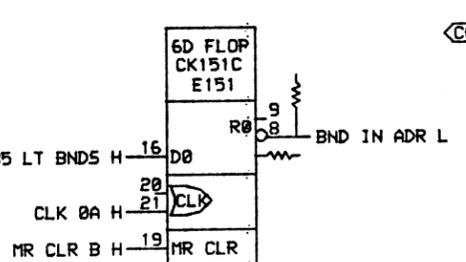
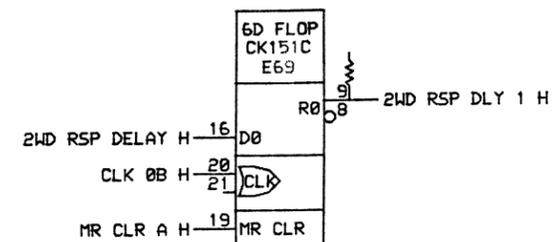
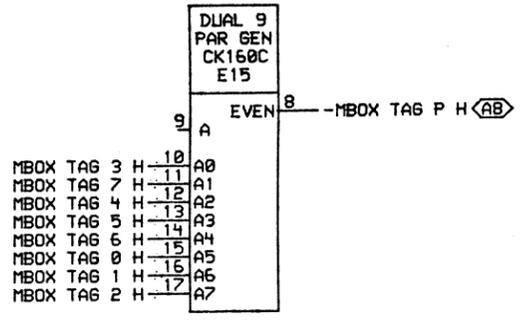
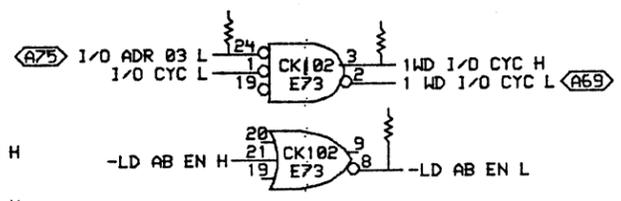
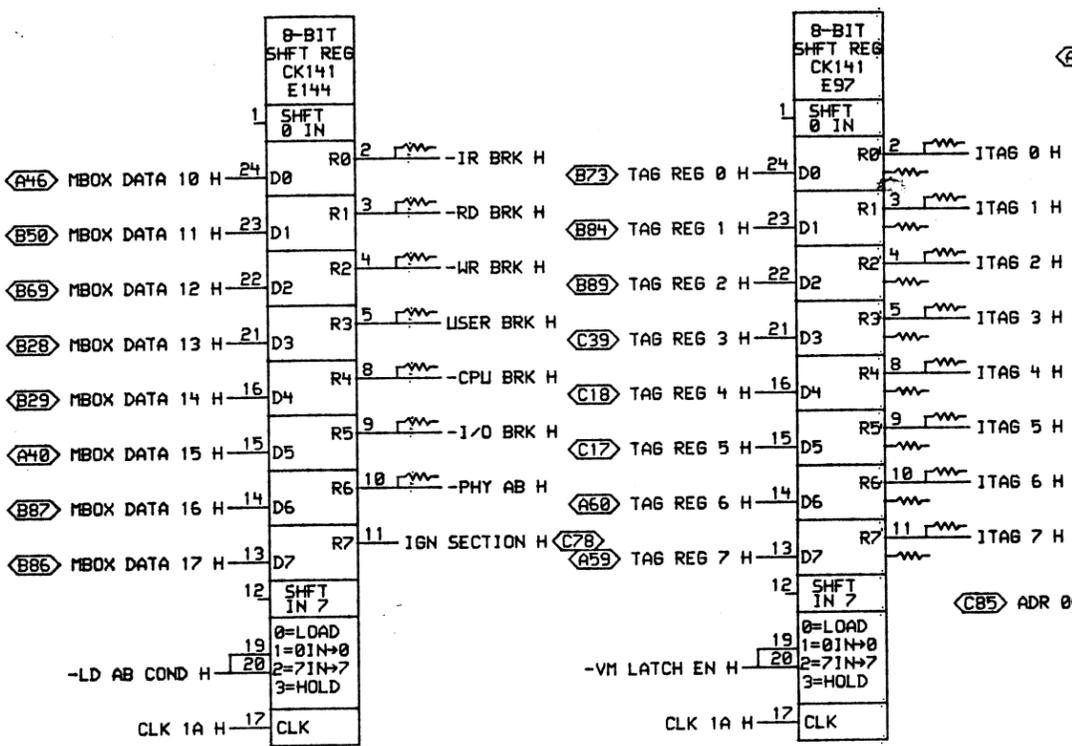
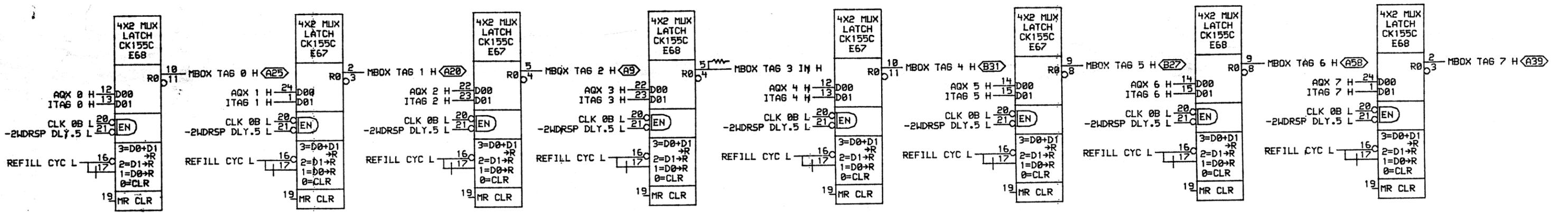
REV.	CHG	CHANGE NO.	REV

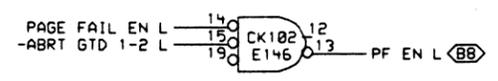
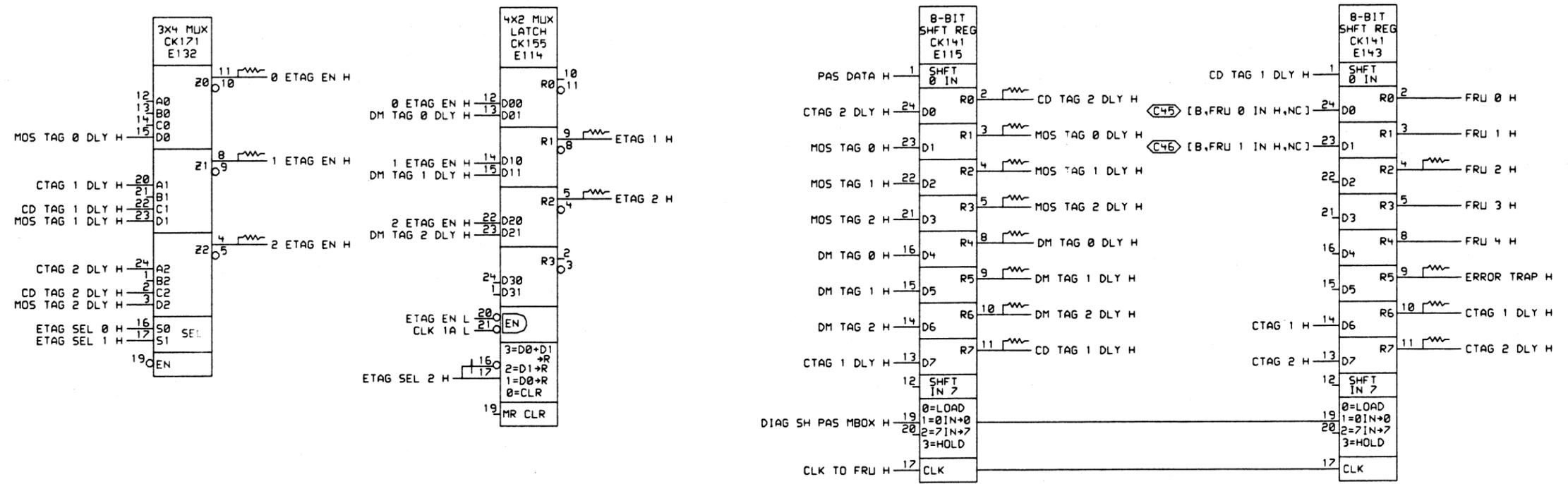


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REVISIONS		
CHK	CHANGE NO.	REV

	DATE 11-MAR-82 ENG. <i>J. J. J.</i> DATE <i> </i> BOARD LOCATION: <i> </i> SHEET <i> </i> OF <i> </i>	TITLE:
	VULCAN: (2080.CAM) 12CAML.DRW 10-MAR-82 09:54 NEXT HIGHER ASSEMBLY: FIRST USED ON OPTION/MODEL: 2080 B-DD-L0059-0	SIZE CODE D CS NUMBER L0059-0-CAML REV. *

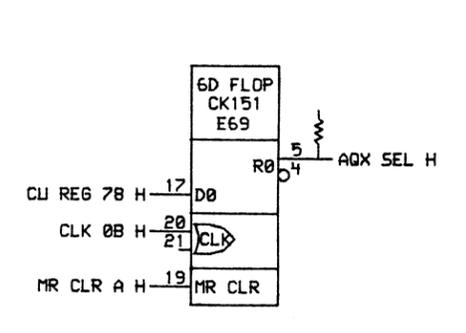
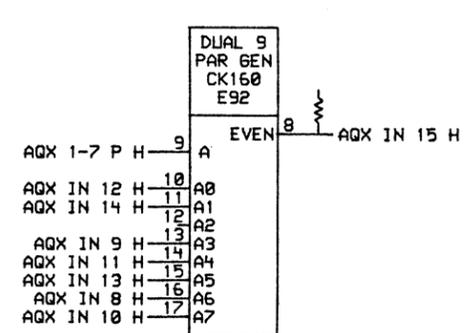
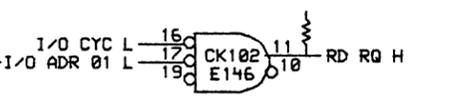
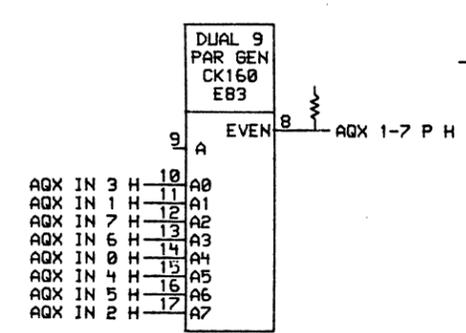
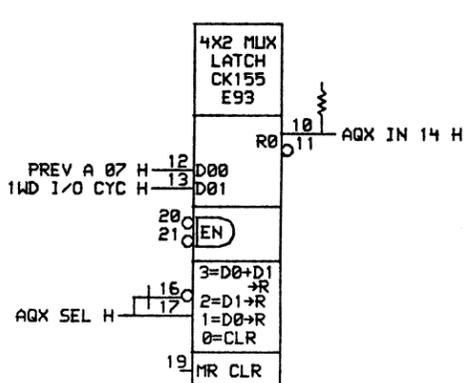
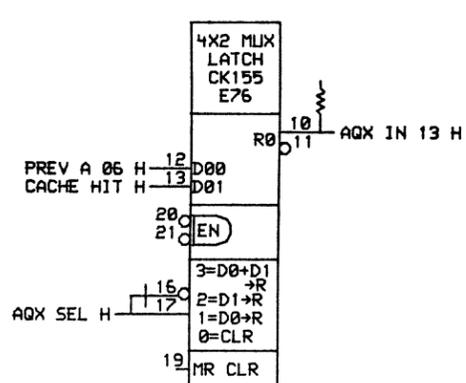
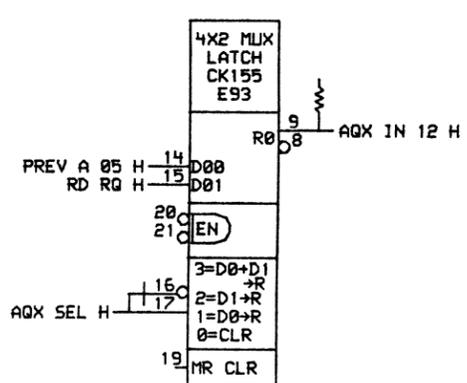
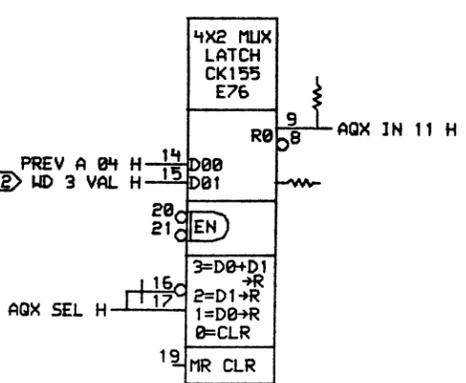
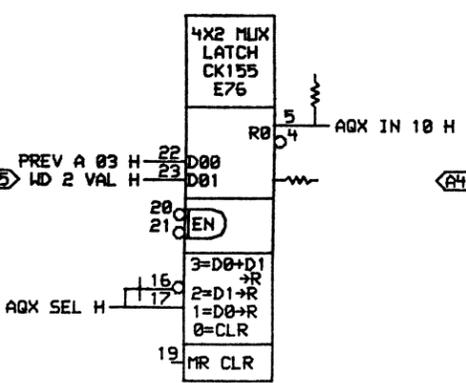
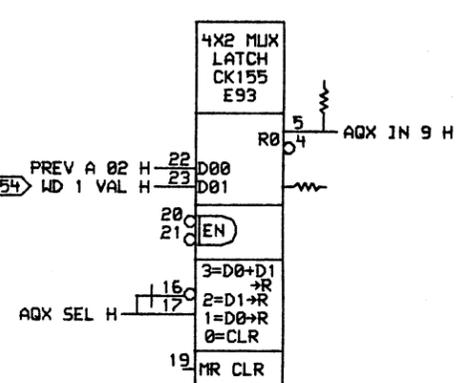
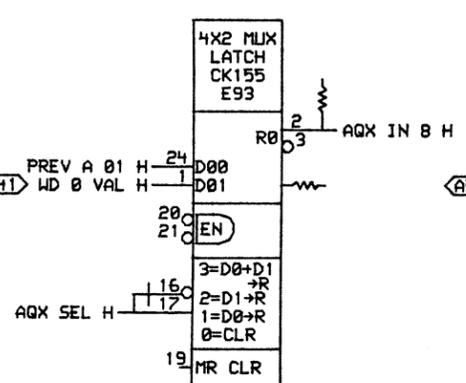
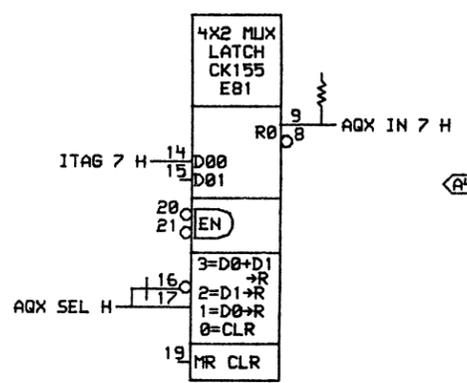
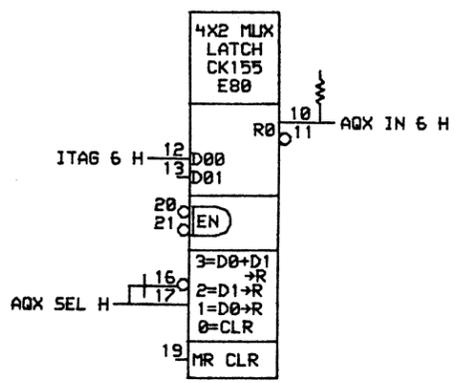
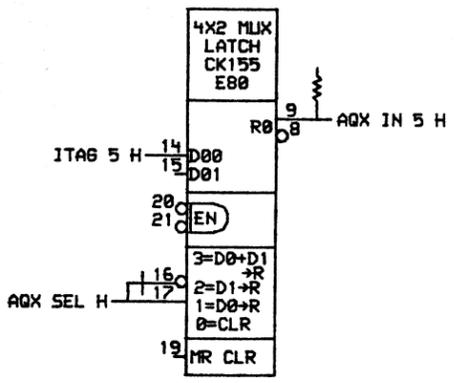
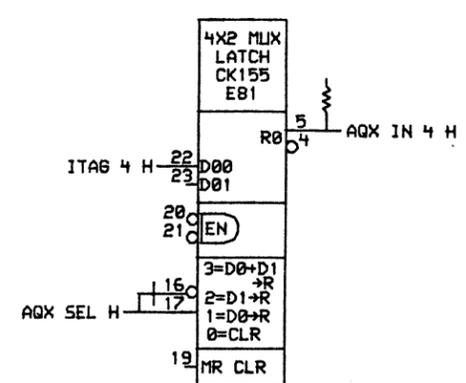
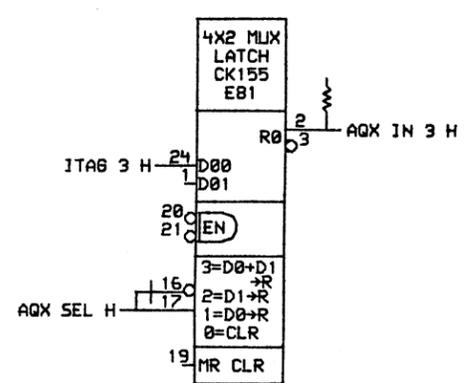
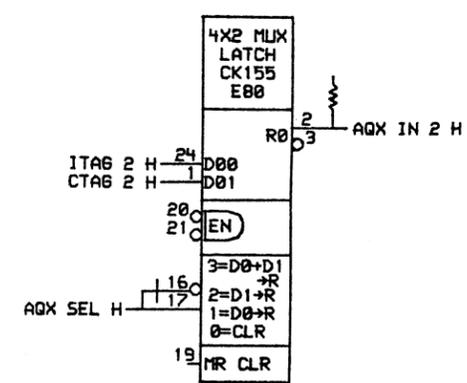
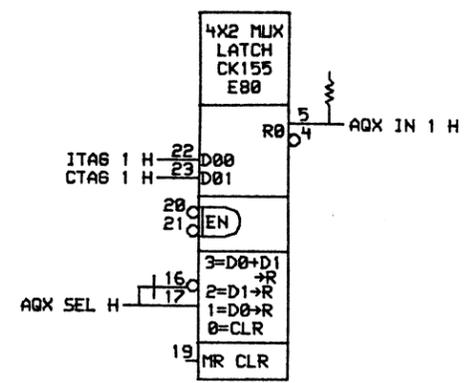
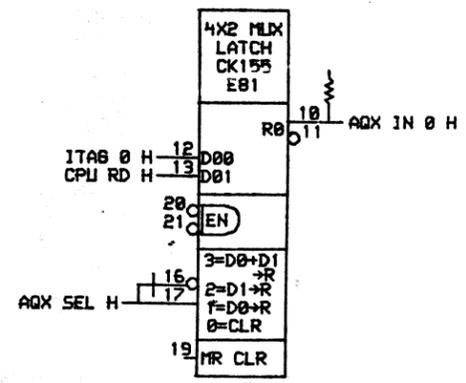


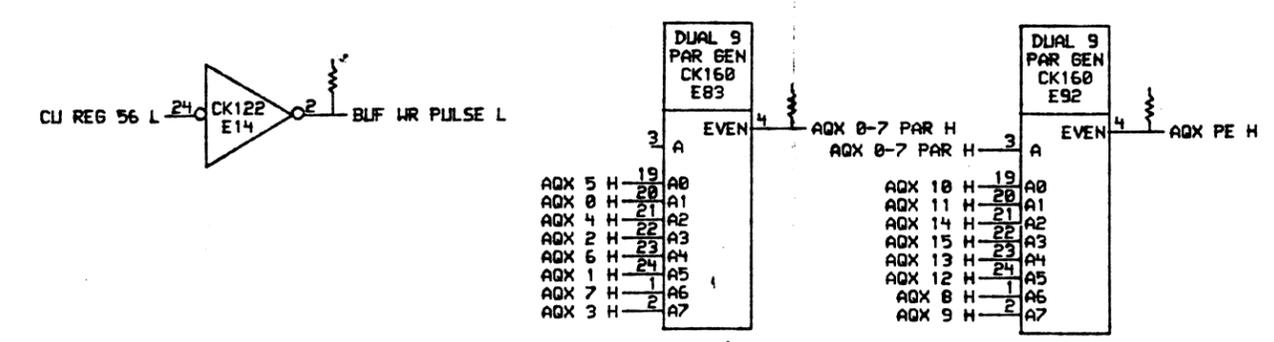
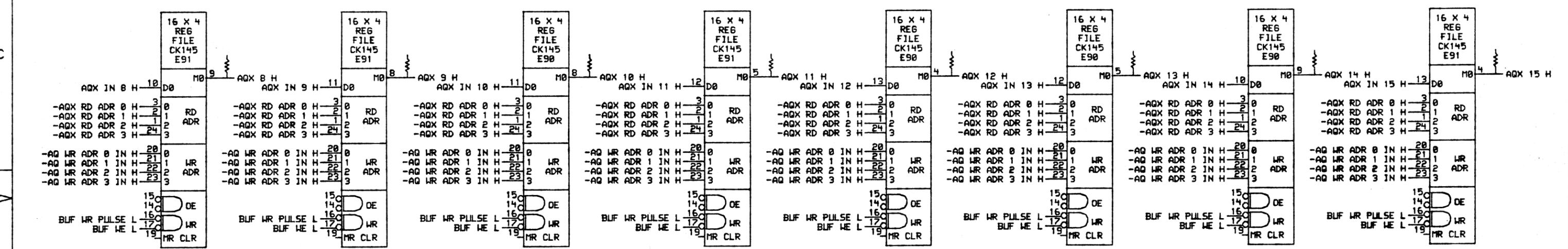
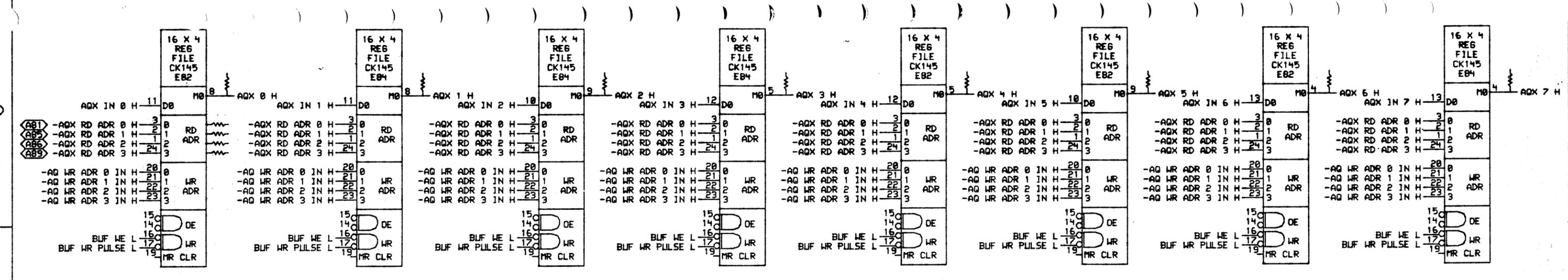


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REVISIONS		
CHK	CHANGE NO.	REV

	DATE 22-DEC-82	ENG.	DATE	TITLE: MCC FRU
	DATE	BOARD LOCATION:	SHEET 1 OF 1	
VULCANI<2000.CAM>BICAMN.DRW 21-DEC-82 13:33 NEXT HIGHER ASSEMBLY:				SIZE CODE D CS
FIRST USED ON OPTION/MODEL: 2000 B-DD-L0059-0				NUMBER L0059-0-CAMN
				REV. *

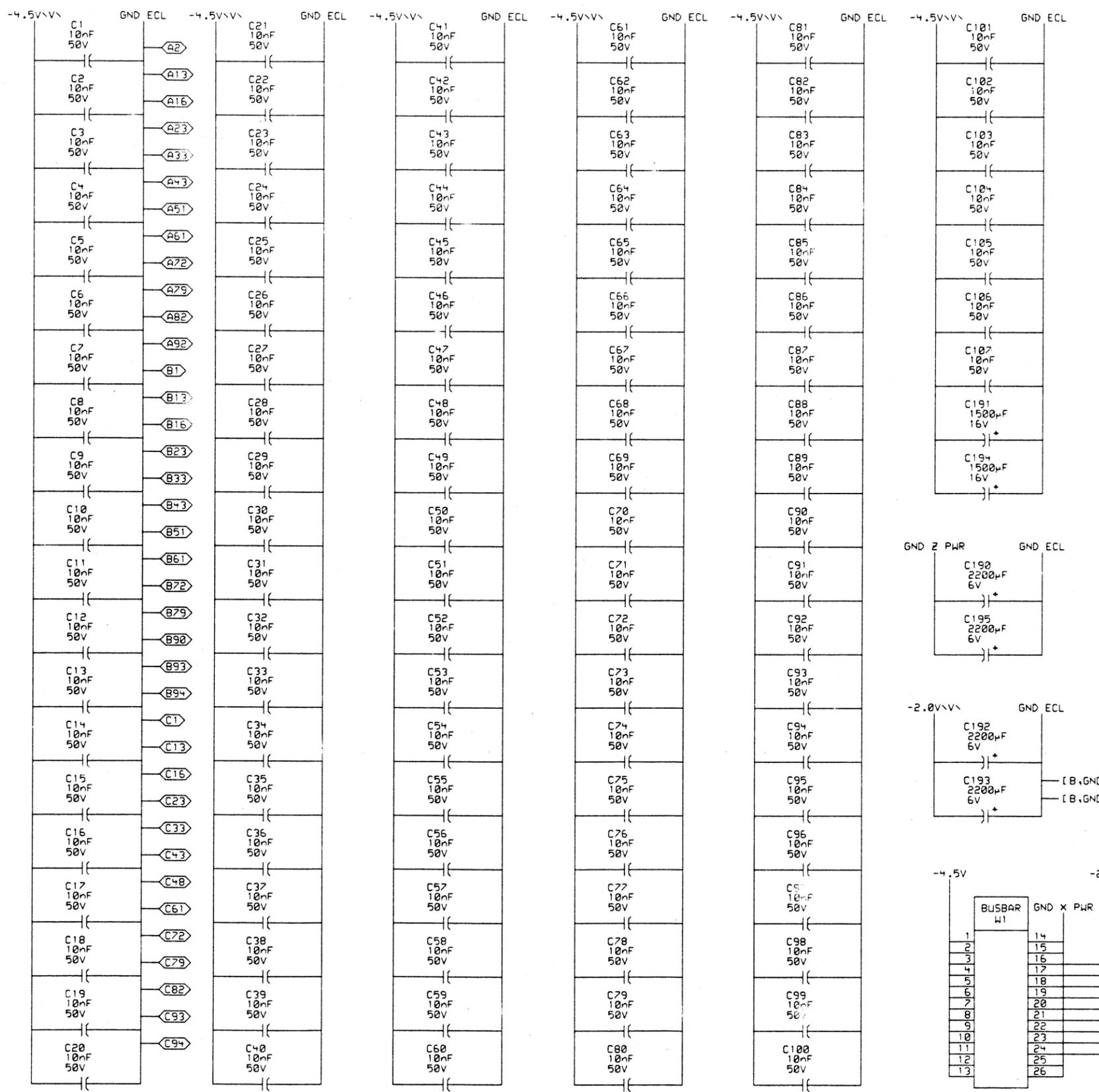




REVISIONS			DATE		TITLE	
CHK	CHANGE NO.	REV	DATE	ENG.	DATE	NUMBER

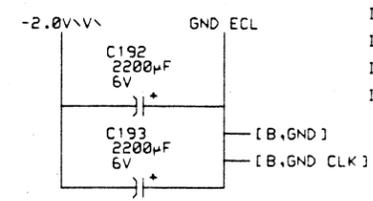
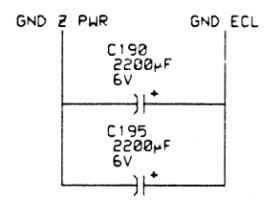
  

		DATE: 2/20/68 ENG.: [Signature]		TITLE: ADR QUE EXTENSION AQX 00-15	
FIRST USED ON OPTION MODEL: 2080		B-DD-L0059-0		D CS L0059-0-CAMR	

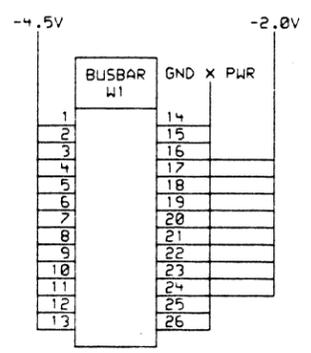
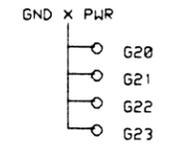
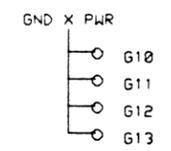


- ⓅB53 - GND CLK
- ⓅB55 - GND CLK
- ⓅB57 - GND CLK
- ⓅB59 - GND CLK

- ⓅA37 - NC A37
- ⓅA67 - NC A67
- ⓅA68 - NC A68
- ⓅA71 - NC A71
- ⓅB12 - NC B12
- ⓅB25 - NC B25
- ⓅB26 - NC B26
- ⓅB32 - NC B32
- ⓅB45 - NC B45
- ⓅB48 - NC B48
- ⓅB78 - NC B78
- ⓅC30 - NC C30
- ⓅC44 - NC C44
- ⓅC74 - NC C74
- ⓅC80 - NC C80
- ⓅC88 - NC C88
- ⓅC90 - NC C90
- ⓅC92 - NC C92
- ⓅC89 - [B.GND ECL,NC]



- I100 - [D,IMP1,NC]
- I110 - [D,IMP1,NC]
- I200 - [D,IMP2,NC]
- I210 - [D,IMP2,NC]



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REVISIONS	
CHK	CHANGE NO. REV

digital DRN. J. J. J. DATE 10-FEB-83 ENG. DATE TITLE: CAM POWER AND GROUND

VULCAN: 2080.CAM185CAMS.DRW 09-FEB-83 18:36 NEXT HIGHER ASSEMBLY: SHEET 1 OF 1

FIRST USED ON OPTION/MODEL: 2080 B-DD-L0058-0

SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAMS	*

<2080.L0058>59MCC1 ✓  
<2080.L0058>47MCC2 ✓  
<2080.L0058>54MCC4 ✓  
<2080.L0058>57MCC5 ✓  
<2080.L0058>53MCC6 ✓  
<2080.L0058>35MCC7 ✓  
<2080.L0058>56MCC8 ✓  
<2080.L0058>53MCC9 ✓  
<2080.L0058>12MCCA ✓  
<2080.L0058>13MCCB ✓  
<2080.L0058>12MCCC ✓  
<2080.L0058>MCCD ✓  
<2080.L0058>MCC E ✓  
<2080.L0058>40MCCF ✓  
<2080.L0058>54MCCG ✓  
<2080.L0058>38MCC H ✓  
<2080.L0058>55MCCI ✓  
<2080.L0058>51MCCJ ✓  
<2080.L0058>60MCCK ✓  
<2080.L0058>47MCC L ✓  
<2080.L0058>59MCC M ✓  
<2080.L0058>39MCC N ✓  
<2080.L0058>58MCC P ✓  
<2080.L0058>53MCC R ✓  
<2080.L0058>40MCC S ✓  
<2080.L0058>58MCC V ✓

E165 CK136	E150 CK102	E135 CK141	E120 CK102	E105 CK141	E090 CK131	E075 CK136	E060 CK171	E045 CK160	E030 CK141	E015 CK150
E164 CK131	E149 CK131	E134 CK141	E119 CK102	E104 CK155	E089 CK164	E074 CK145	E059 CK171	E044 CK141	E029 CK150	E014 CK141
E163 CK155	E148 CK170	E133 CK422	E118 CK102	E103 CK102	E088 CK155	E073 CK171	E058 CK155	E043 CK150	E028 CK422	E013 CK422
E162 CK155	E147 CK165	E132 CK422	E117 CK151	E102 CK151	E087 CK141	E072 CK145	E057 CK141	E042 CK422	E027 CK422	E012 CK422
E161 CK171	E146 CK102	E131 CK136	E116 CK163	E101 CK101	E086 CK151	E071 CK141	E056 CK141	E041 CK422	E026 CK422	E011 CK422
E160 CK141	E145 CK165	E130 CK101	E115 CK141	E100 CK102	E085 CK141	E070 CK102	E055 CK155	E040 CK155	E025 CK155	E010 CK141
E159 CK117	E144 CK171	E129 CK151	E114 CK131	E099 CK155	E084 CK150	E069 CK107	E054 CK422	E039 CK422	E024 CK141	E009 CK422
E158 CK102	E143 CK117	E128 CK102	E113 CK102	E098 CK164	E083 CK102	E068 CK160	E053 CK422	E038 CK141	E023 CK160	E008 CK141
E157 CK151	E142 CK107	E127 CK136	E112 CK117	E097 CK141	E082 CK171	E067 CK155	E052 CK141	E037 CK422	E022 CK422	E007 CK422
E156 CK141	E141 CK160	E126 CK155	E111 CK102	E096 CK102	E081 CK170	E066 CK155	E051 CK145	E036 CK422	E021 CK141	E006 CK160
E155 CK141	E140 CK101	E125 CK141	E110 CK150	E095 CK171	E080 CK113	E065 CK160	E050 CK145	E035 CK155	E020 CK422	E005 CK141
E154 CK123	E139 CK117	E124 CK170	E109 CK123	E094 CK102	E079 CK117	E064 CK141	E049 CK145	E034 CK151	E019 CK102	E004 CK422
E153 CK118	E138 CK102	E123 CK171	E108 CK150	E093 CK155	E078 CK102	E063 CK160	E048 CK145	E033 CK117	E018 CK422	E003 CK422
E152 CK165	E137 CK141	E122 CK155	E107 CK141	E092 CK155	E077 CK155	E062 CK155	E047 CK122	E032 CK160	E017 CK122	E002 CK141
E151 CK102	E136 CK123	E121 CK123	E106 CK123	E091 CK123	E076 CK141	E061 CK155	E046 CK123	E031 CK123	E016 CK122	E001 CK170

mcc

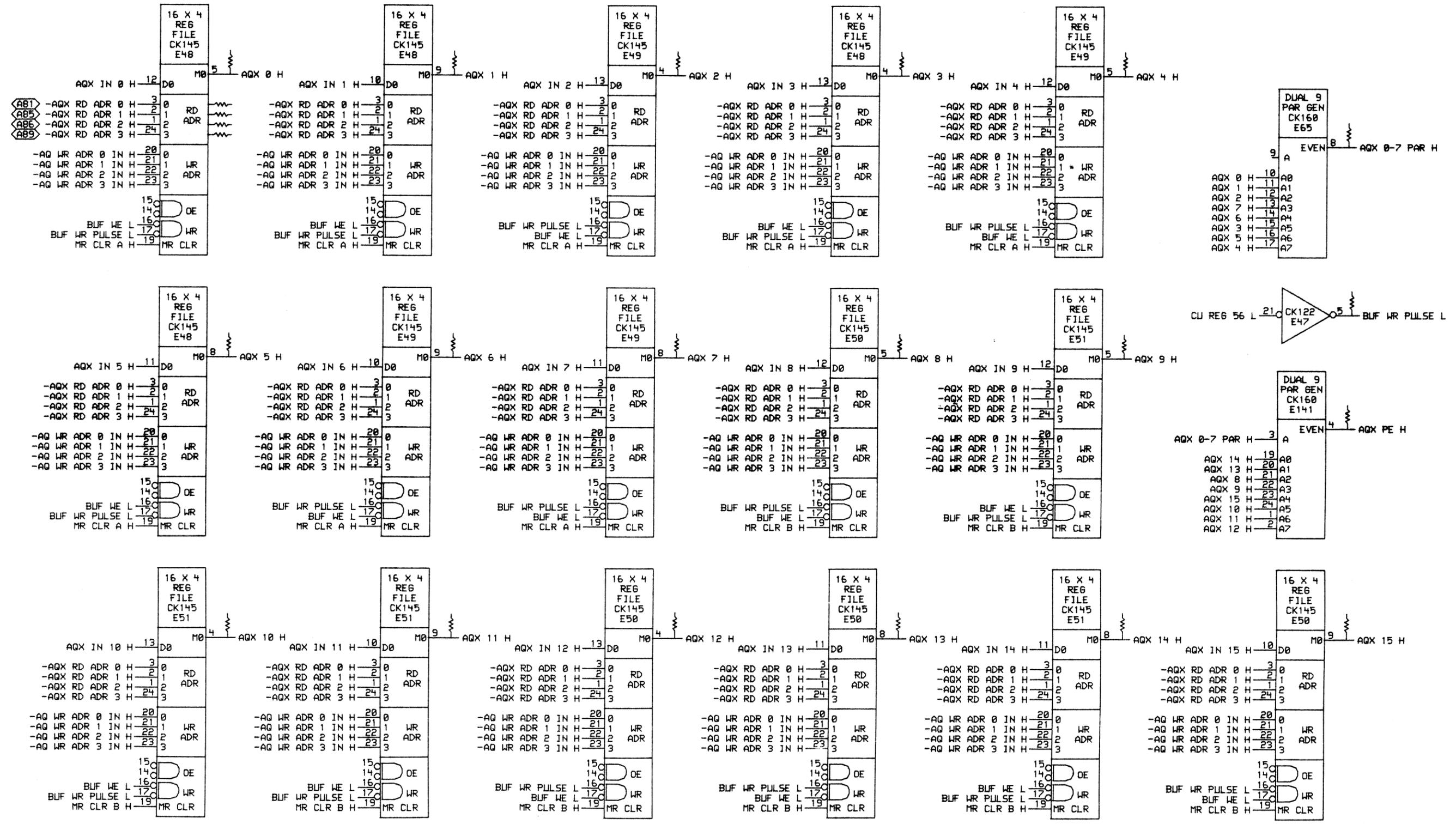
+ C122 + + C121 + + C120 + + C119 + + C118 + + C117 +

D

C

B

A

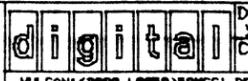


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REVISIONS		
CHK	CHANGE NO.	REV

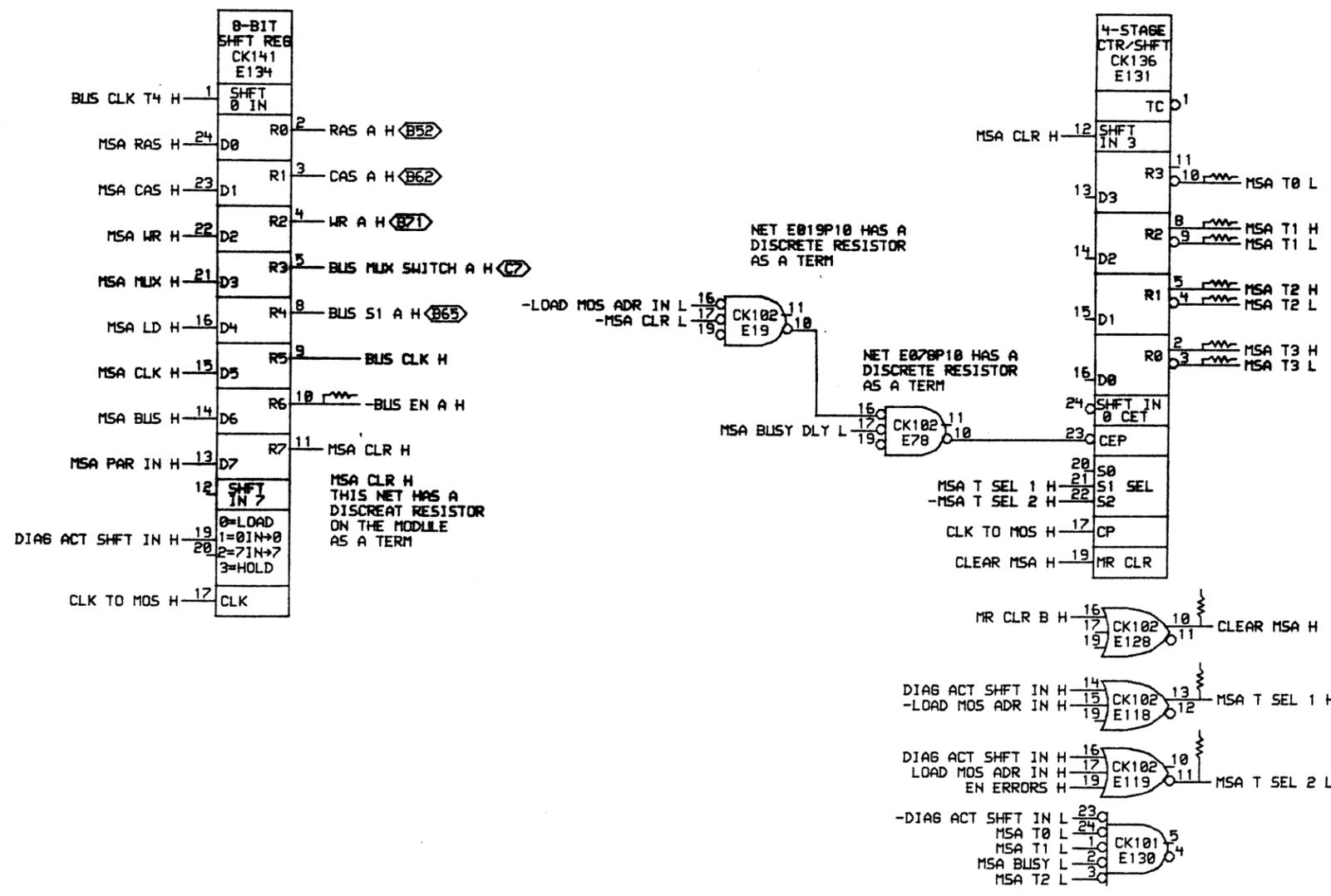
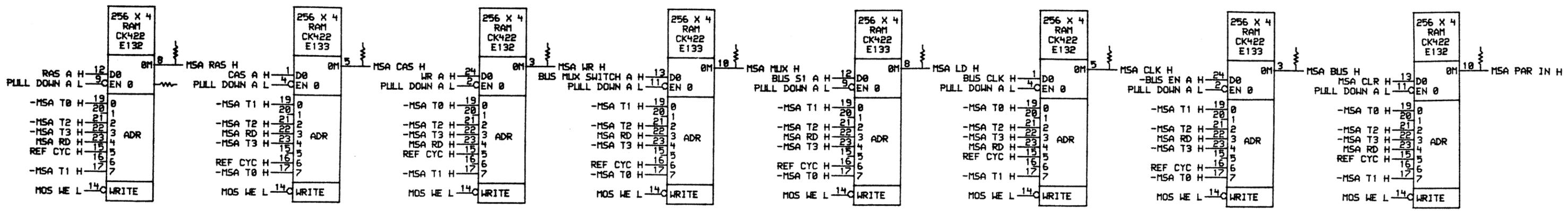
DATE	ENG.	DATE	TITLE
22-FEB-82			ADR QUT EXTENSION
DATE	BOARD LOCATION:	SHEET	OF





*Handwritten signature and initials*

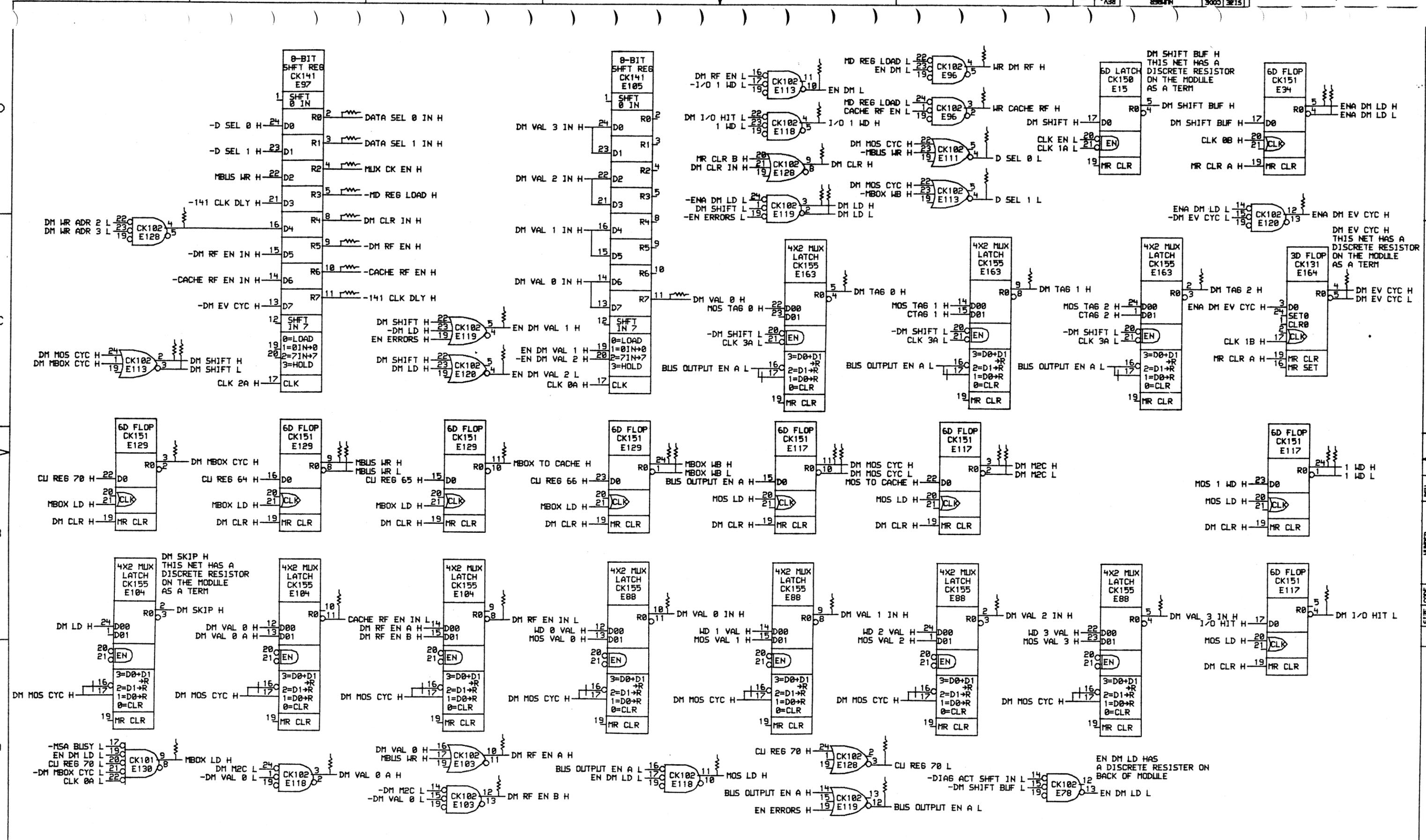
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 FIRST USED ON OPTION/MODEL: 2080  
 SIZE CODE D CS L0058-0-MCC1  
 NUMBER L0058-0-MCC1  
 REV. \*



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REVISIONS	
CHK	CHANGE NO. REV.

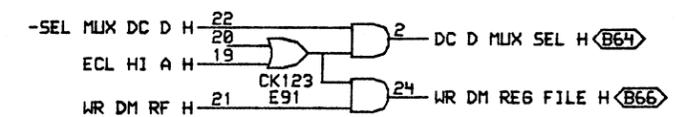
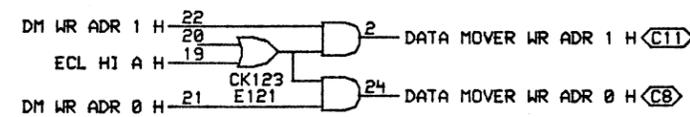
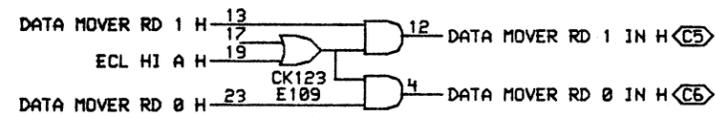
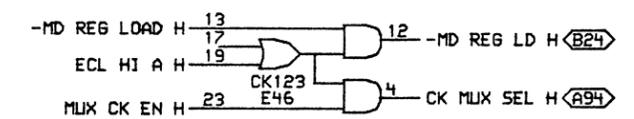
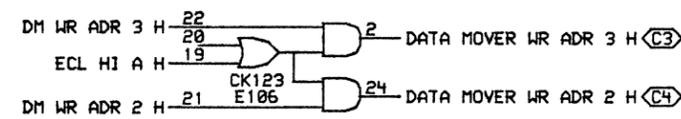
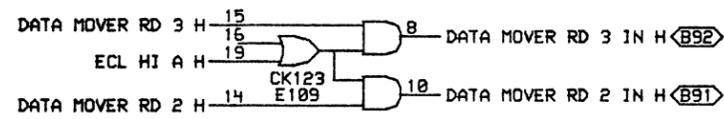
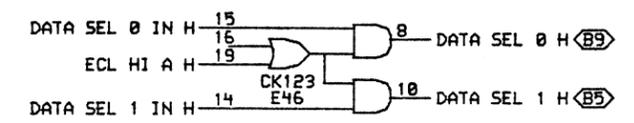
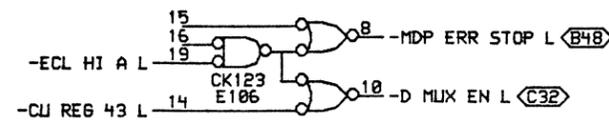
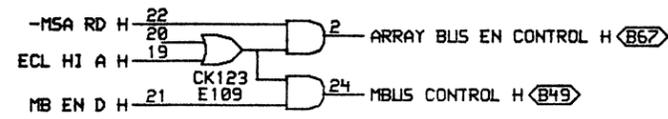
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	27-JAN-82			MOS SEQUENCER CTL A
DATE	BOARD LOCATION:	SHEET	OF	
26-JAN-82 10:01		1	1	
VILCANI (2000-L0058)MCC2.DRN	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL: 2000	B-DD-L0058-0	D	CS	L0058-0-MCC2
				REV. *



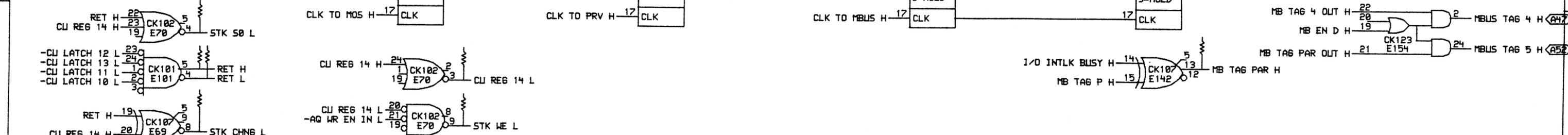
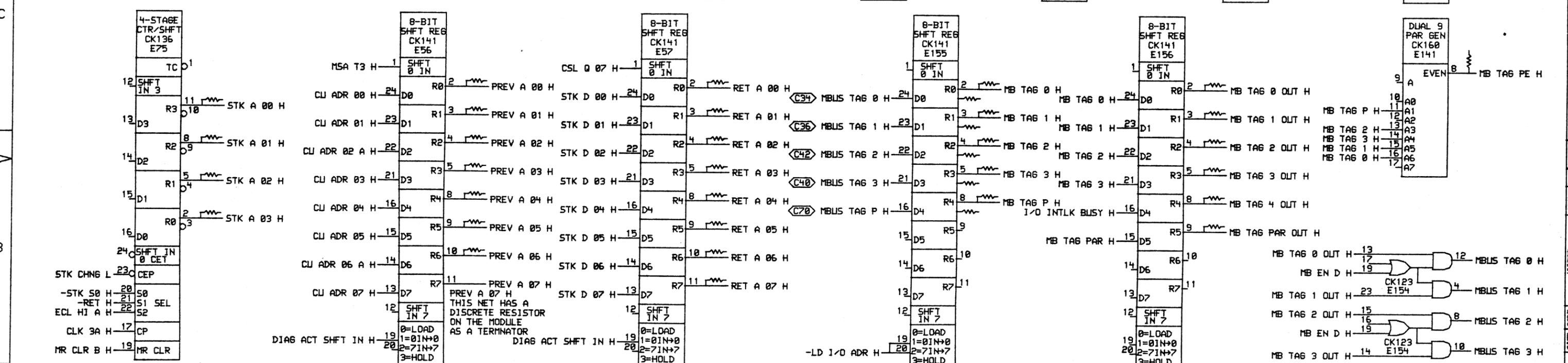
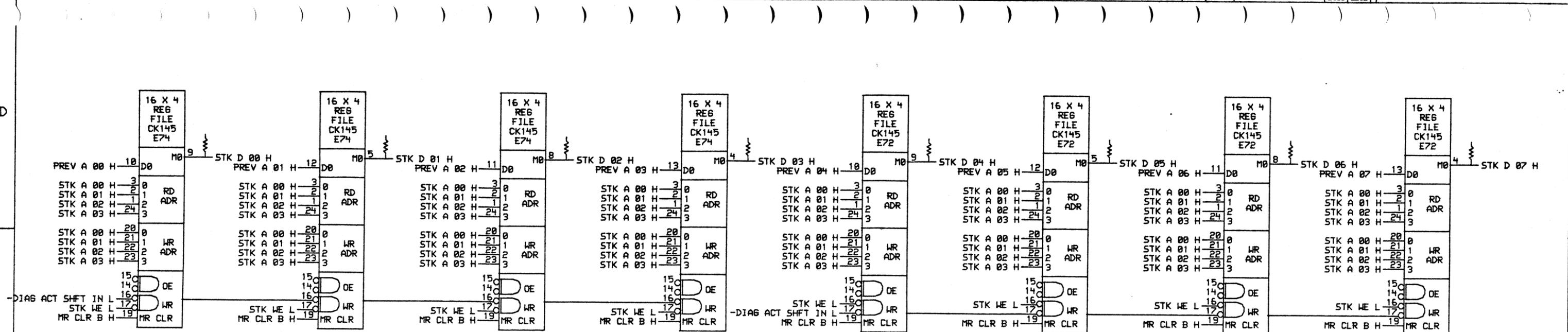
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CHK	CHANGE NO.	REV

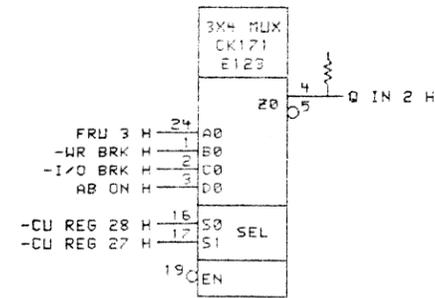
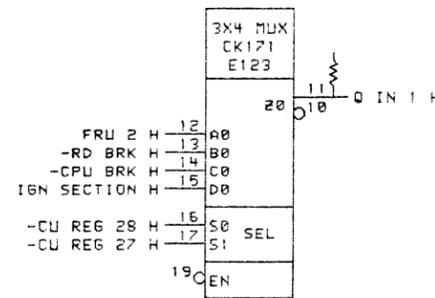
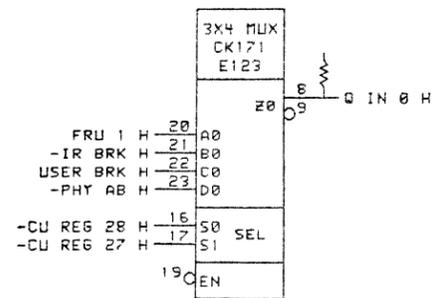
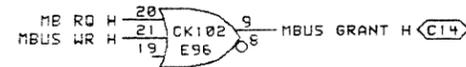
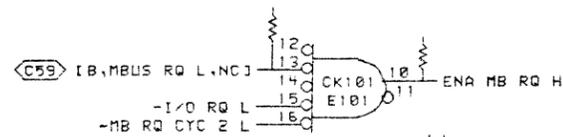
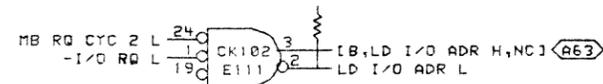
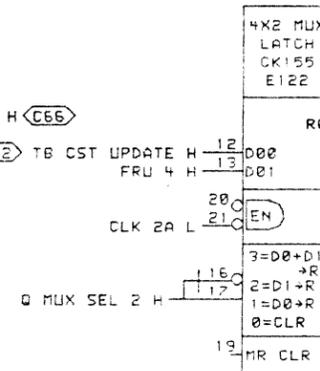
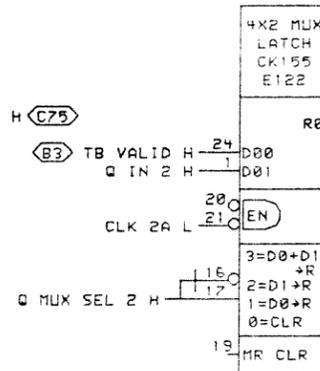
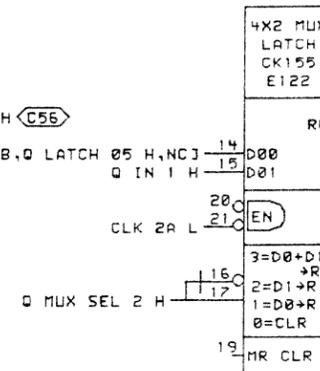
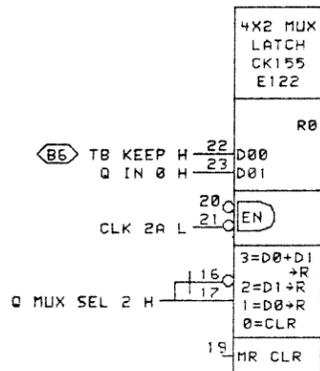
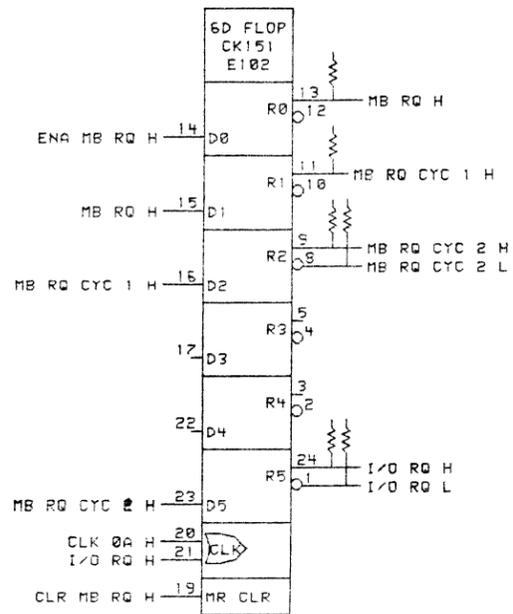
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VLL CAN (2000-L0058) MCCC4 DRW 05-FEB-82 15:30 NEXT HIGHER ASSEMBLY:			BOARD LOCATION:		SIZE: D
FIRST USED ON OPTION/MODEL: 2000			SHEET: 1 OF 1		NUMBER: L0058-0-MCC4
					REV: *

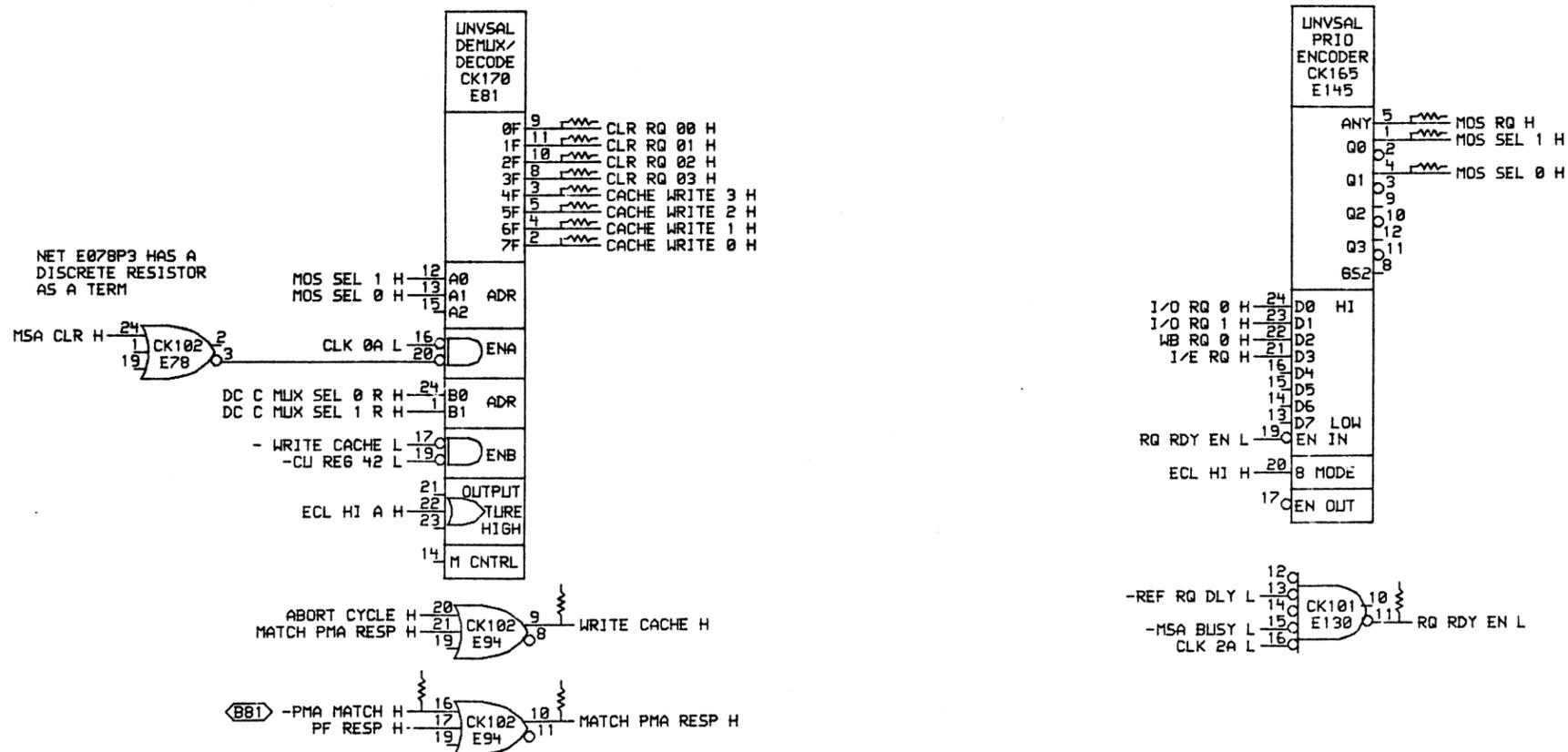
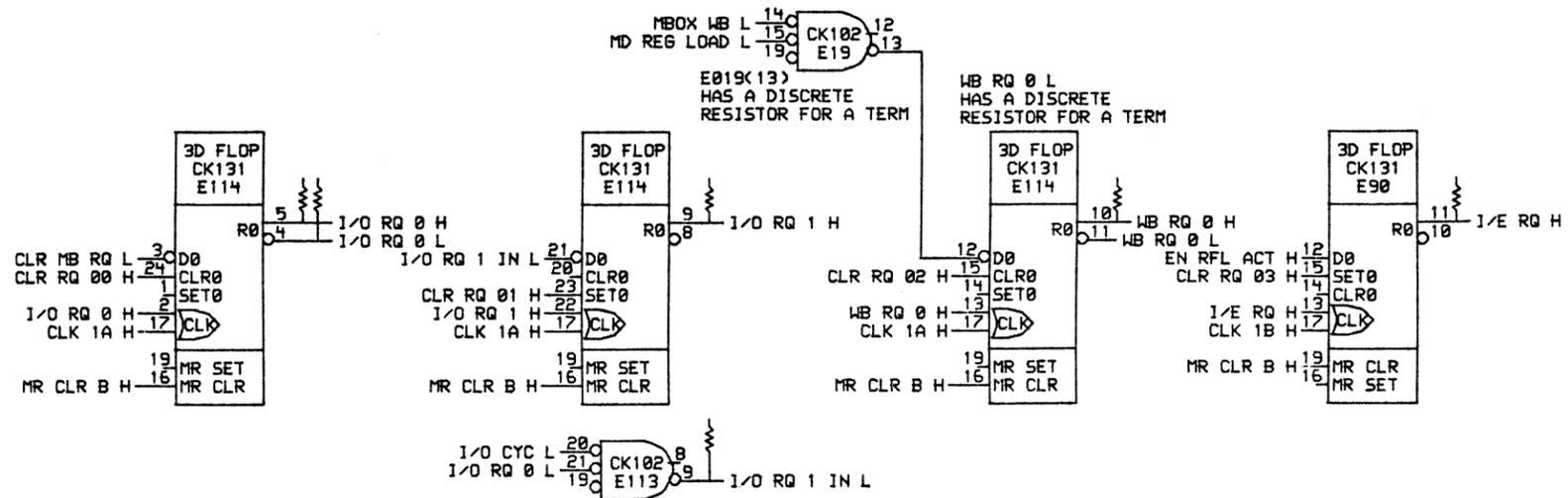


DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		MDP DMU CONTROL LOGIC
VILCANI (2080.L0058)57MCC5.DRW	11-FEB-82	SHEET 1 OF 1		NUMBER L0058-0-MCC5
FIRST USED ON OPTION/MODEL: 2080	10-FEB-82 13:53	NEXT HIGHER ASSEMBLY: B-DD-L0058-0	SIZE D	CODE C5
				REV. *



REVISIONS		TITLE: CU STACK	
CHK	CHANGE NO. REV	DATE	ENG.
		02-FEB-82	
		DATE	BOARD LOCATION:
		SHEET	OF
		NEXT HIGHER ASSEMBLY:	
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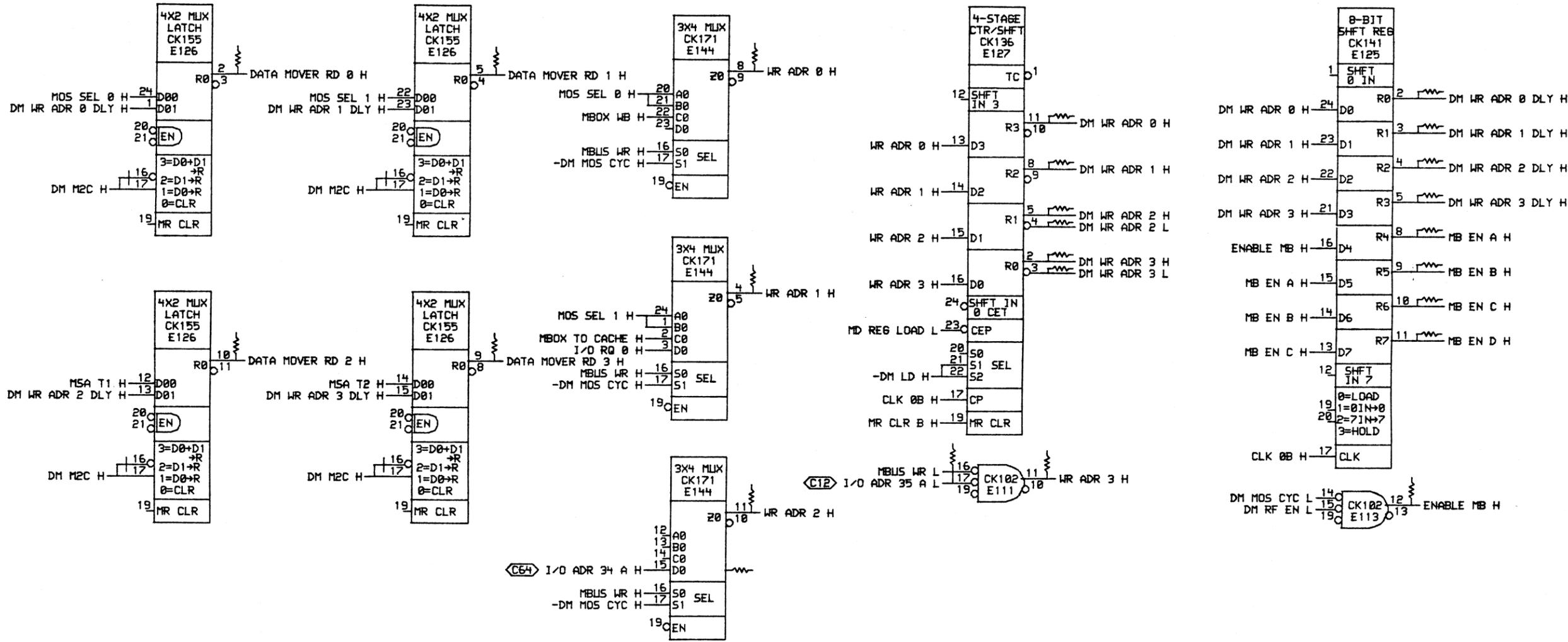




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REVISIONS		
CHK	CHANGE NO.	REV

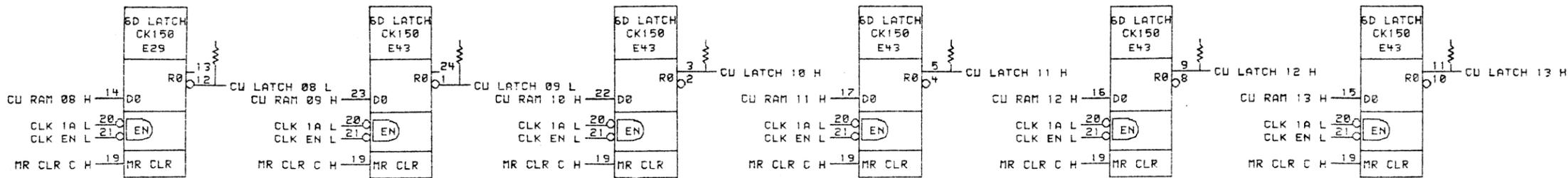
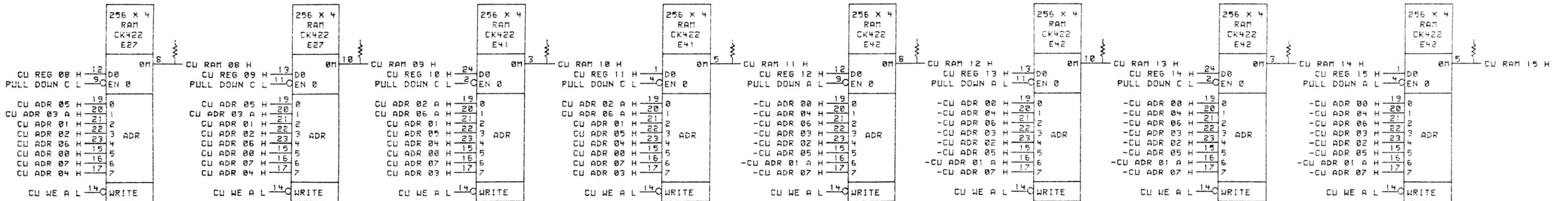
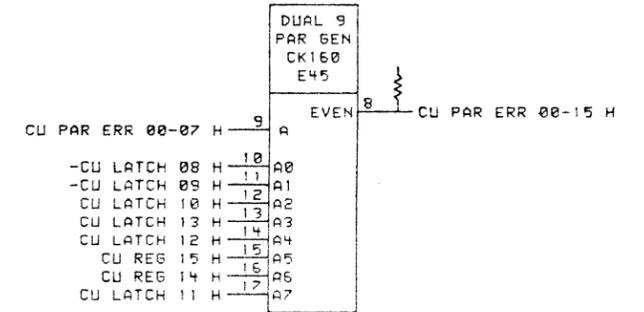
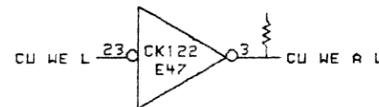
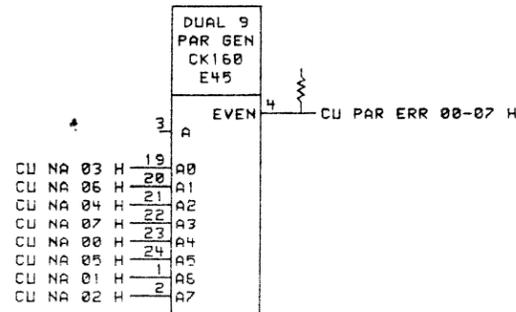
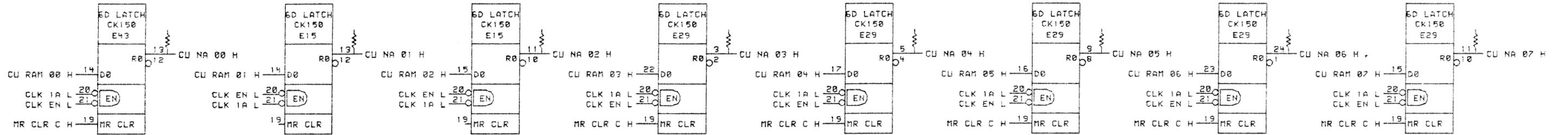
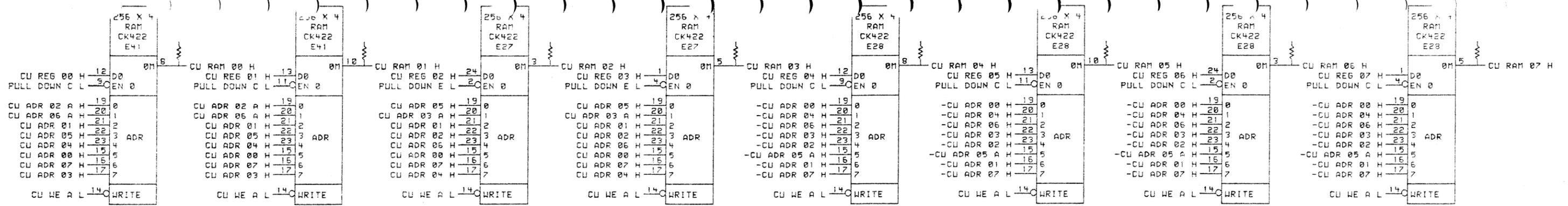
digital	DATE	ENG.	DATE	TITLE:
	18-FEB-82	J. J. J.	18-FEB-82	MOS RQ FF MOS PRI EN
VILCAN: <2000.L0058>56MCCB.DRW		DATE	BOARD LOCATION:	
09-FEB-82 09:19			SHEET	1 OF 1
FIRST USED ON OPTION/MODEL: 2000		NEXT HIGHER ASSEMBLY:		
	B-DD-L0058-0	SIZE	CODE	NUMBER
		D	CS	L0058-0-MCC8
				REV. *



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REVISIONS		
CHK	CHANGE NO.	REV

<b>digital</b>	DATE: 05-FEB-82	ENG.:	DATE:	TITLE: DM RD/WR ADR CTL
	CHK'D: <i>J. J. J.</i>	BOARD LOCATION: SHEET 1 OF 1		
VLL CAN: (2080-L0058)53MC:9-DRW 04-FEB-82 12:29				SIZE CODE: D CS
FIRST USED ON OPTION/MODEL: 2080				NUMBER: L0058-0-MCC9
				REV. #: *

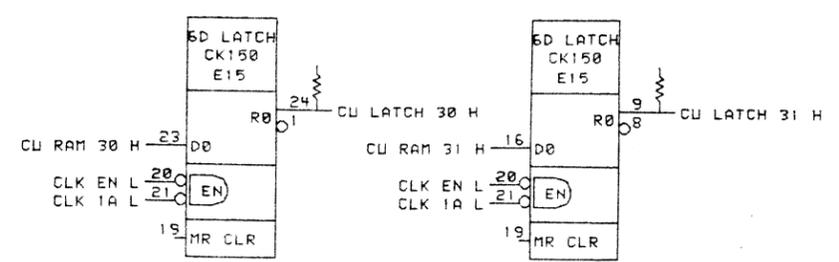
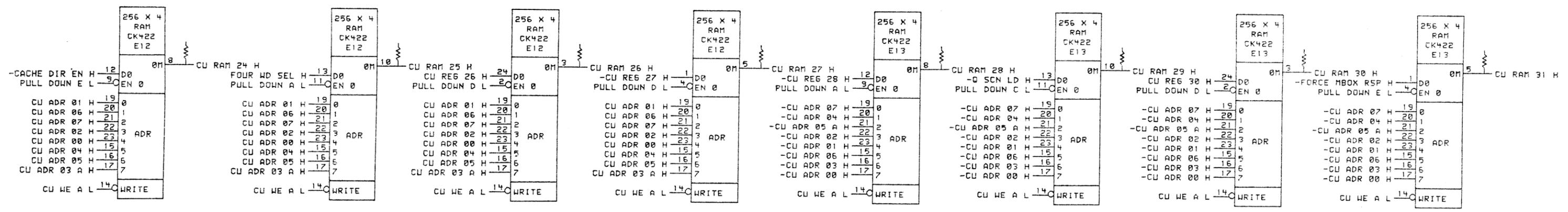
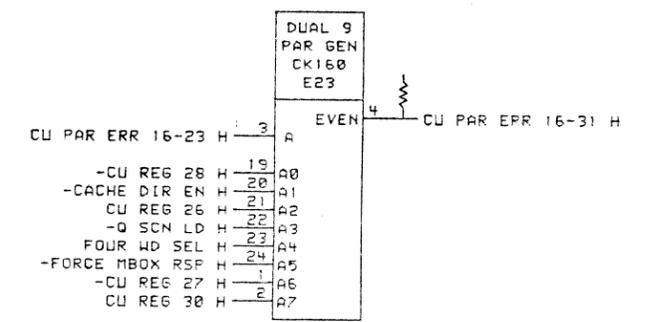
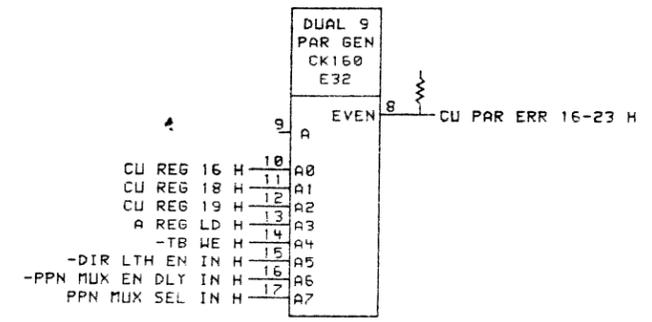
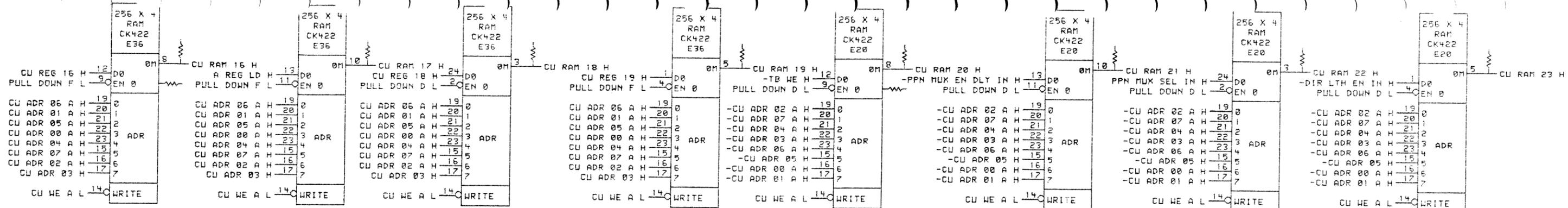


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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	04-JAN-88			CU RAM 00-15, CU LATCH 00-15 + P
	DATE	BOARD LOCATION:	SHEET	OF
	23-OCT-81 11:45	NEXT HIGHER ASSEMBLY:	1	5
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0058-0	SIZE	CODE
			D	CS

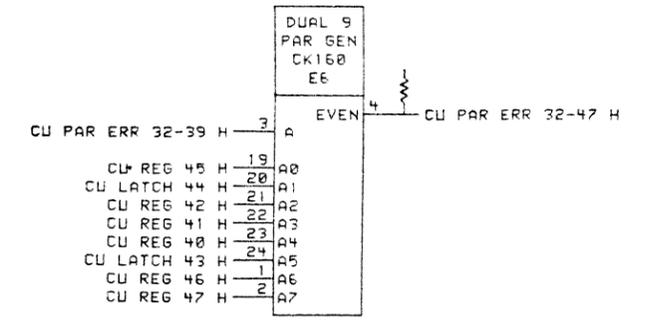
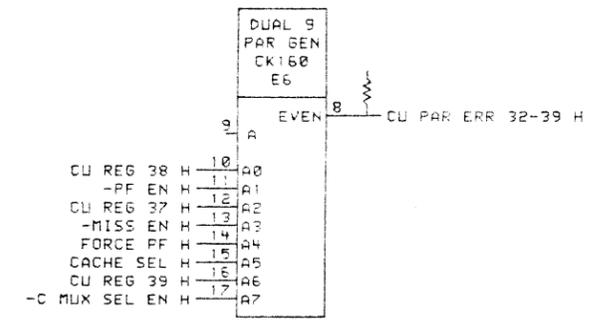
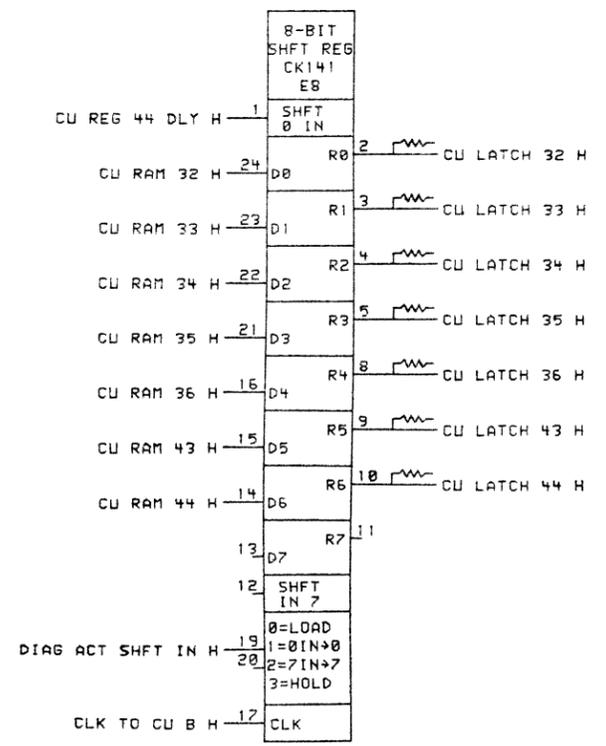
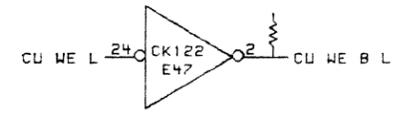
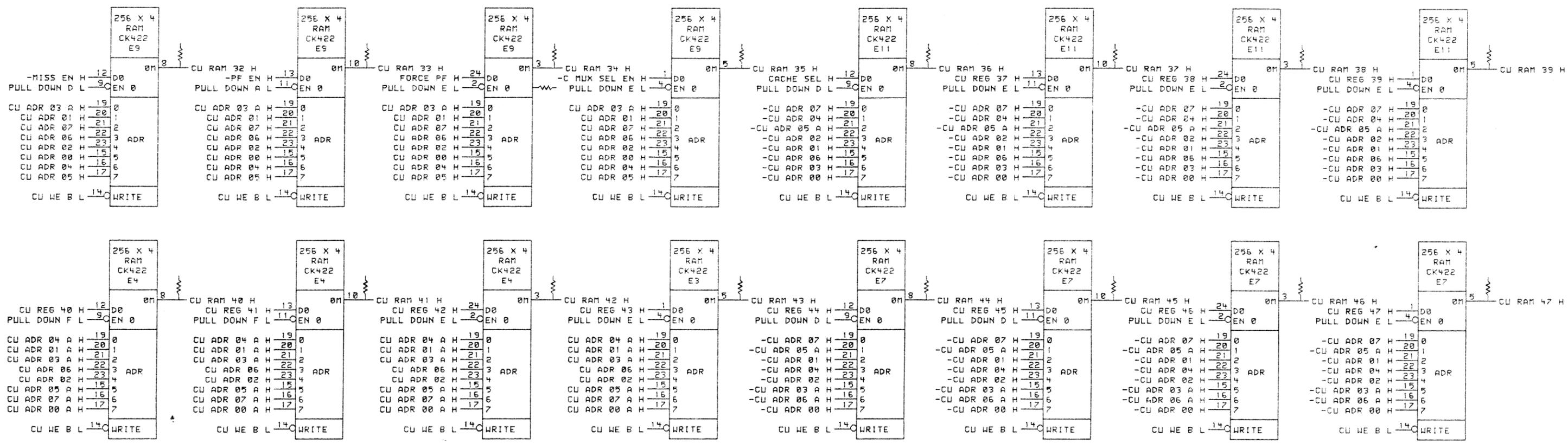
NUMBER	REV.
L0058-0-MCCA	*



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REVISIONS	
CHK	CHANGE NO. REV

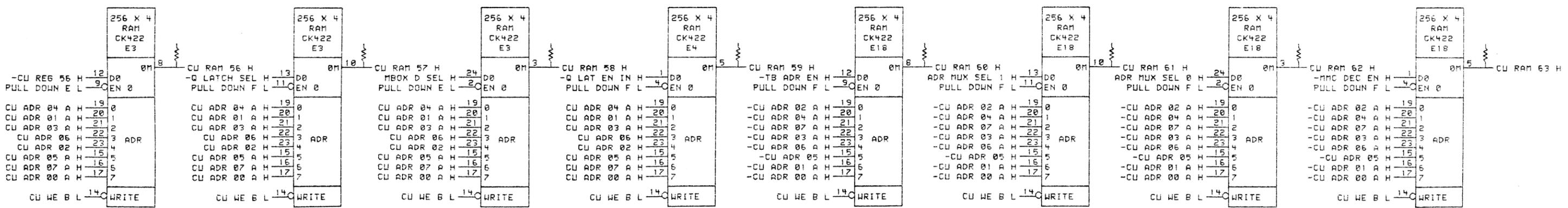
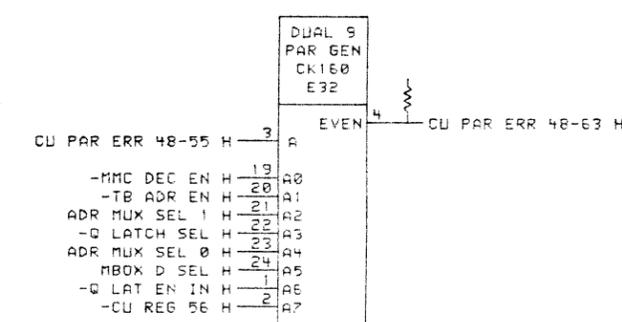
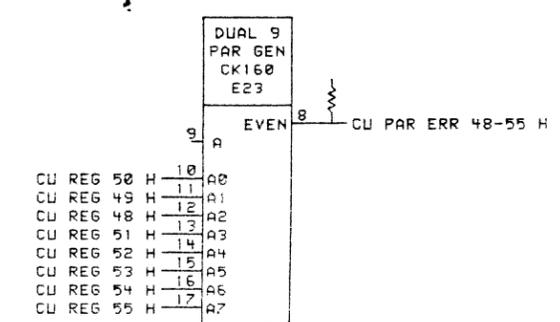
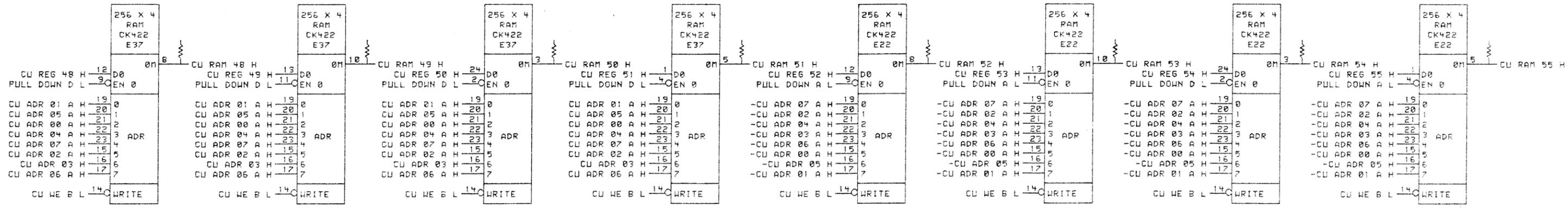
digital	DRW: J. J. J.	DATE: 04-JAN-82	ENG.	DATE	TITLE: CU RAM 16-31, CU LATCH 16-31 + P
	CHK: D.	DATE	BOARD LOCATION: SHEET 2 OF 5		
VULCAN: C668.L0058-13MCCB.DRW		30-OCT-81 10:56	NEXT HIGHER ASSEMBLY:	SIZE: D	CODE: CS
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0058-0		NUMBER: L0058-0-MCCB	REV: *

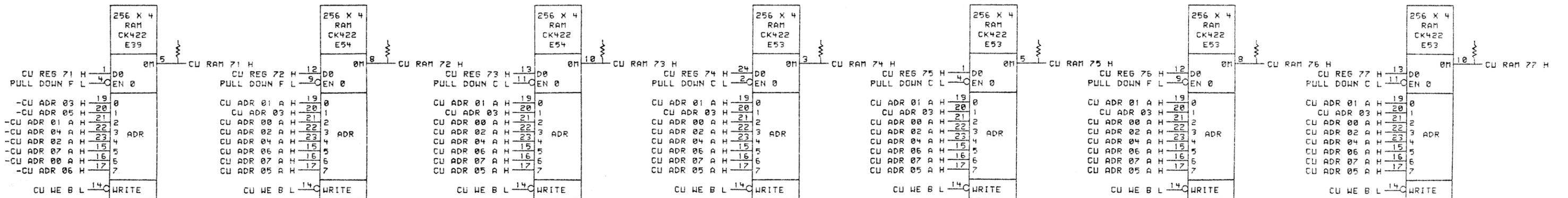
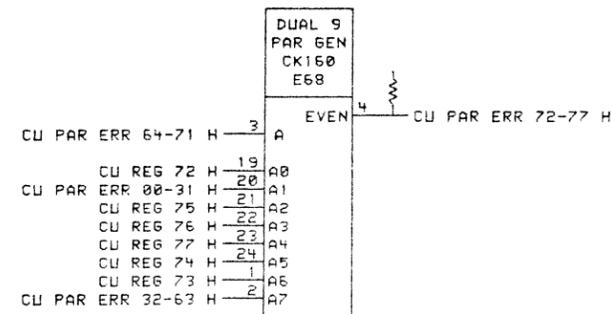
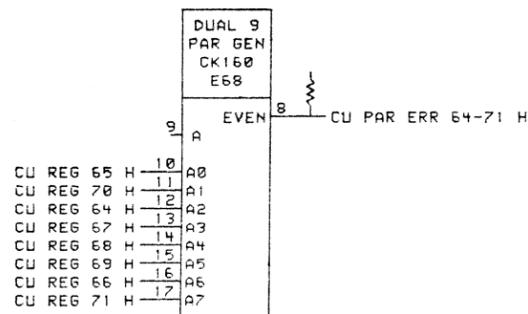
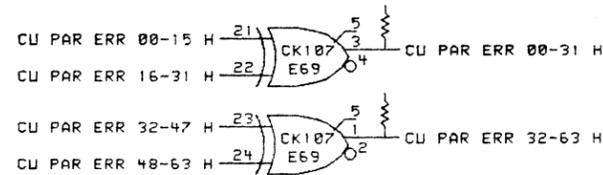
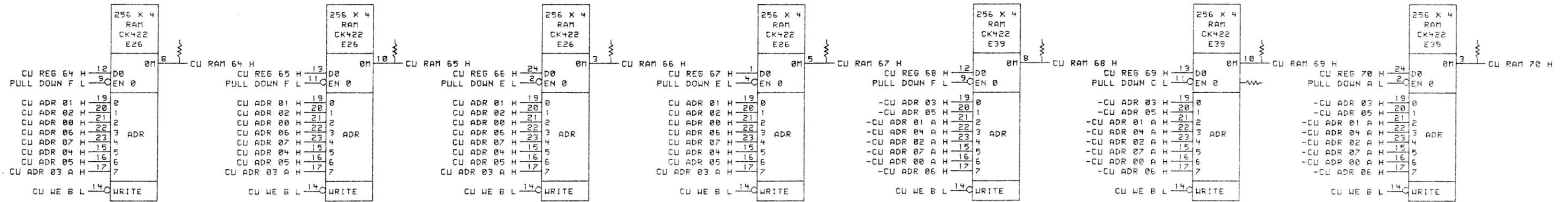


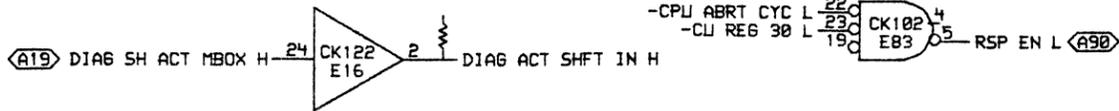
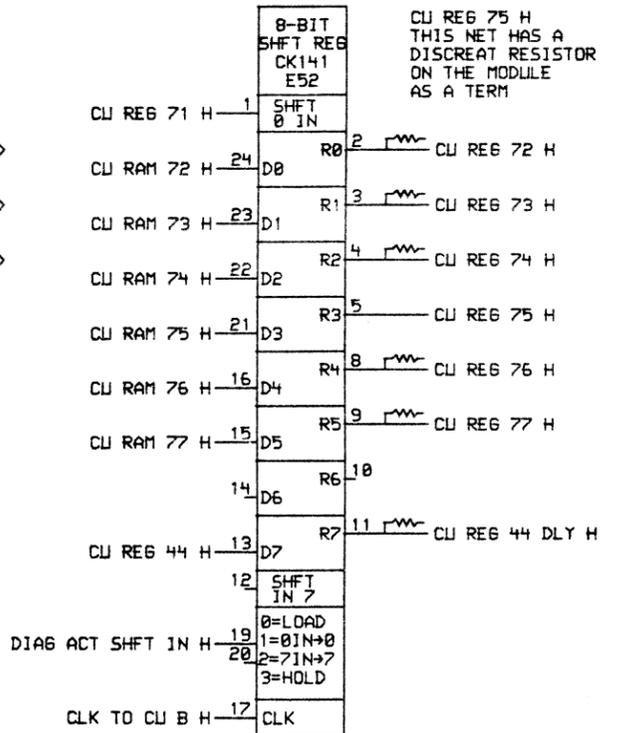
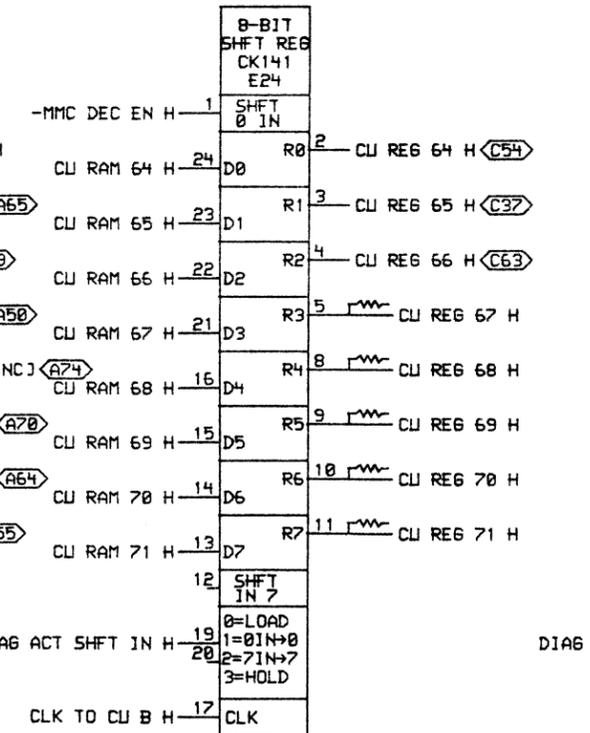
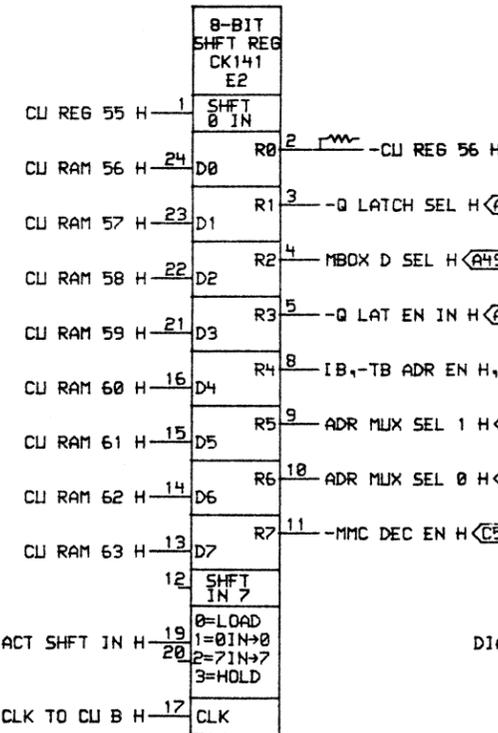
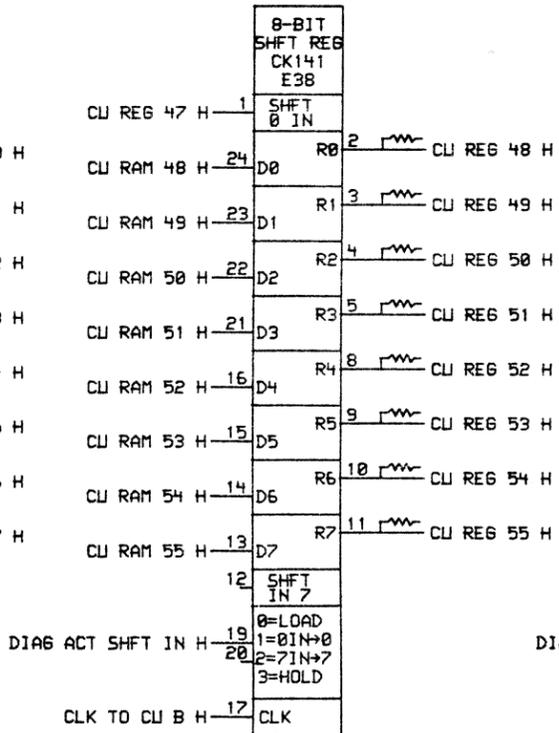
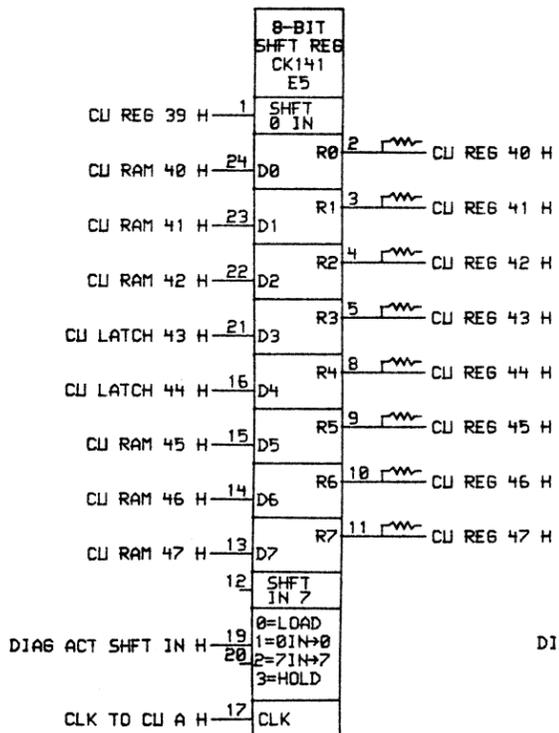
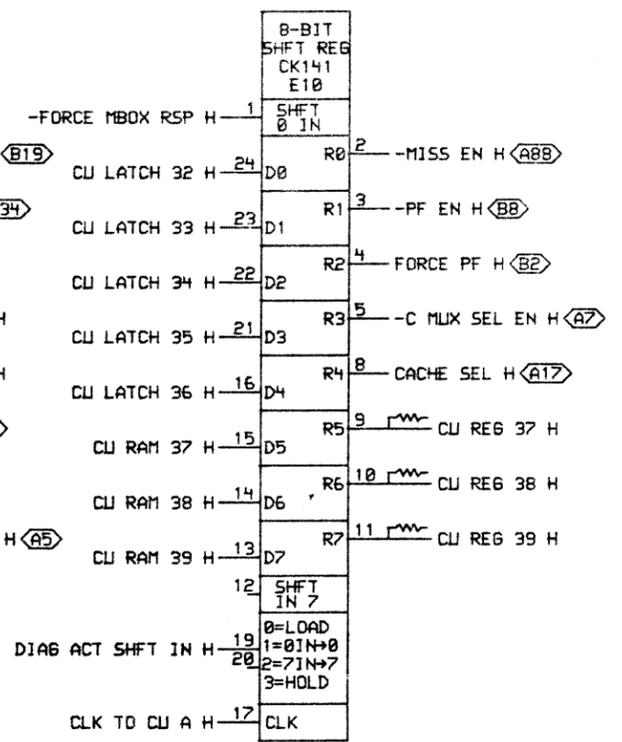
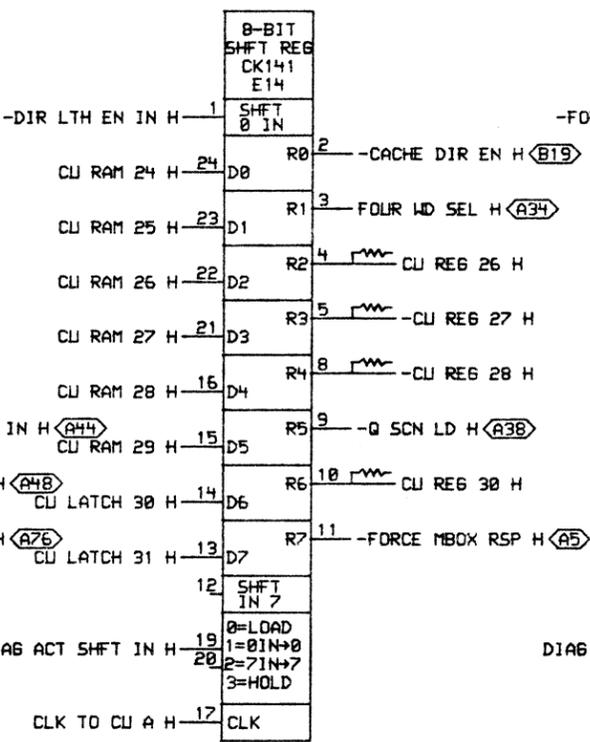
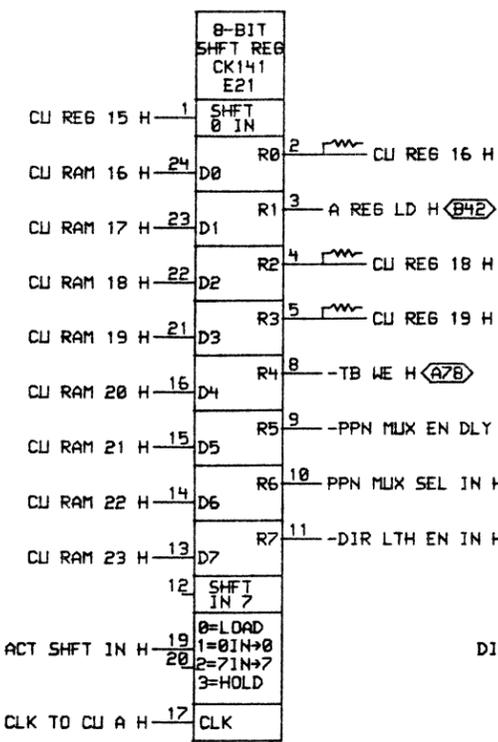
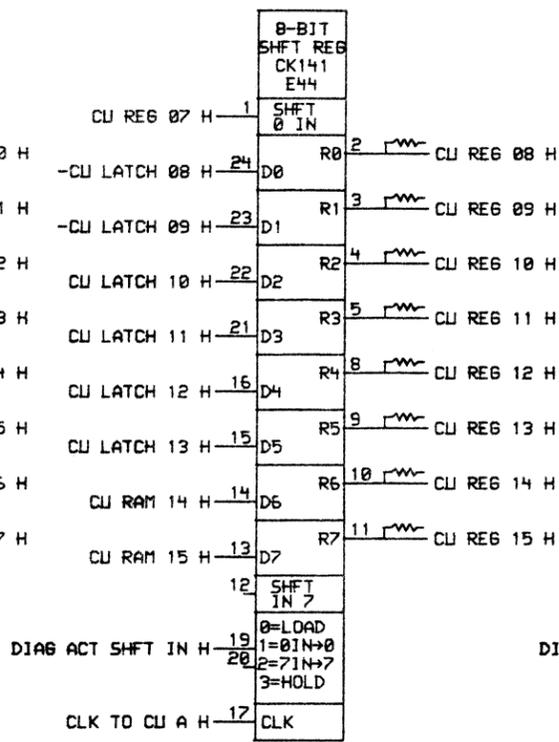
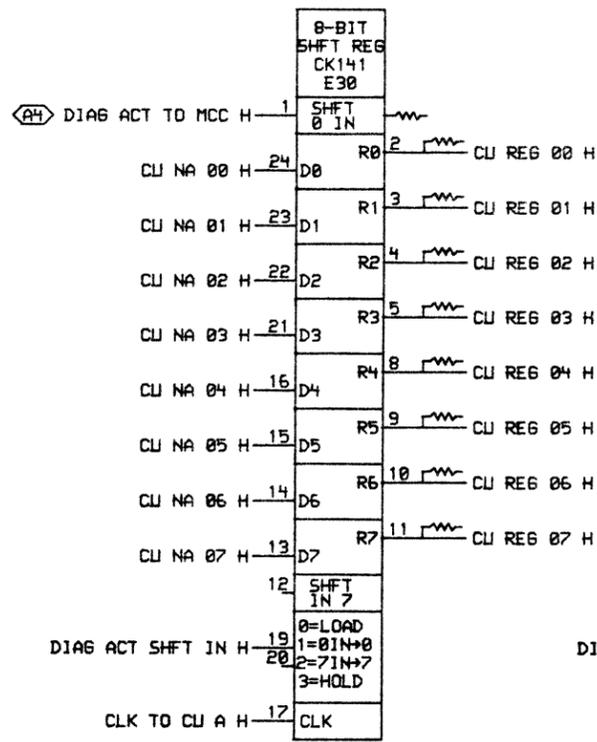
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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	04-JAN-82	ENG.	DATE		TITLE:	CU RAM 32-47,
	CHK'D.			DATE		BOARD LOCATION:	CU LATCH 32-47 + P
VULCAN-0000-10050-10MCC-004		07-NOV-81 09:45	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION MODEL:		2000	P-00-10050-0	3	OF	5	*

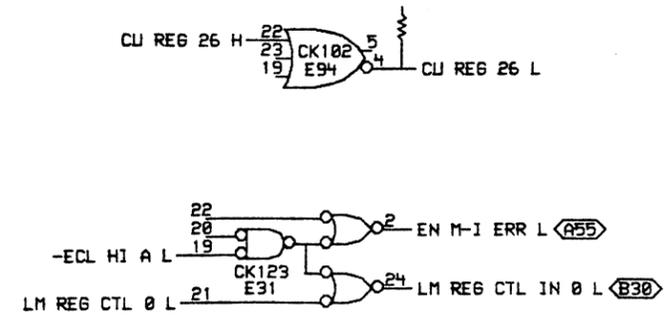
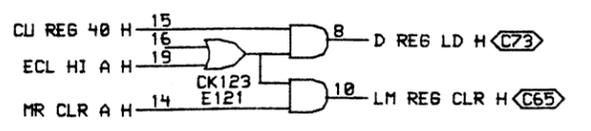
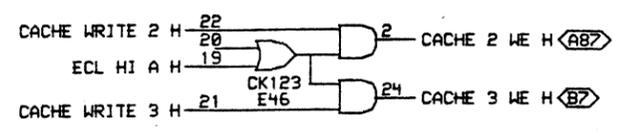
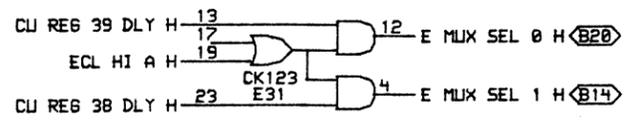
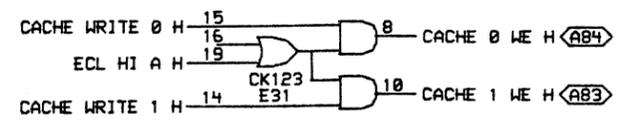
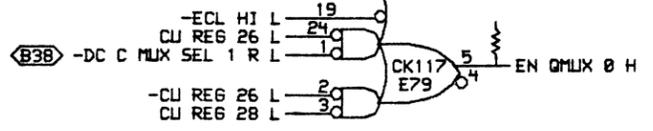
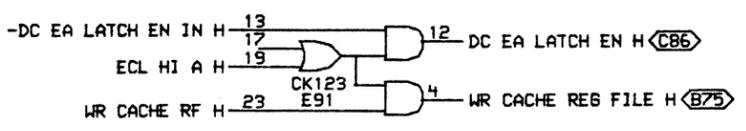
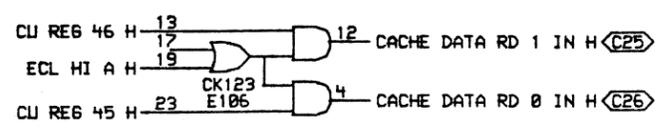
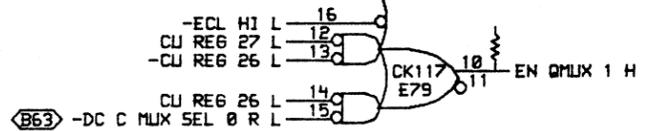
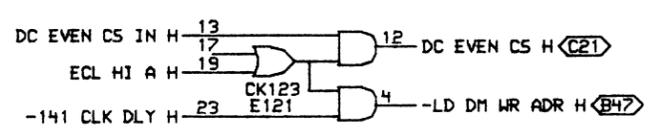
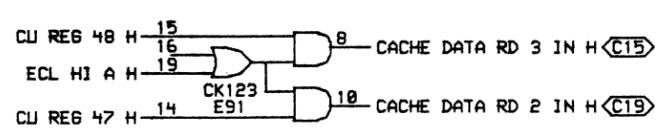
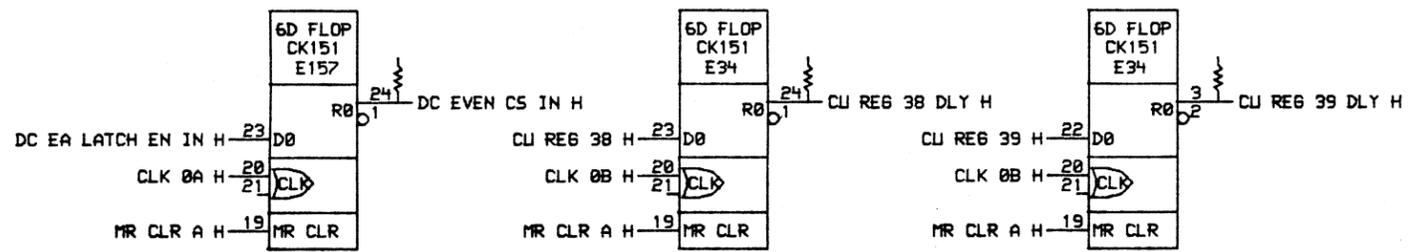
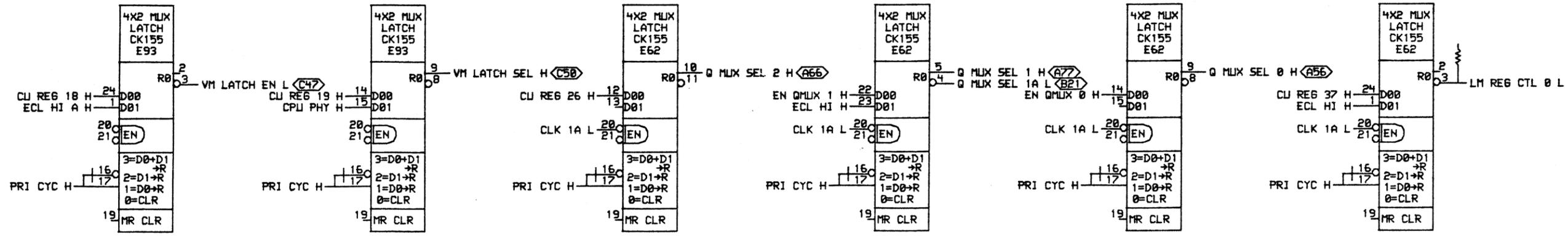






CU REG 75 H  
THIS NET HAS A  
DISCREAT RESISTOR  
ON THE MODULE  
AS A TERM

DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
VILCANI C2000-L0058-MCCF.DRW 06-JAN-02 15:59				NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: 2000				B-DD-L0058-0
SIZE	CODE	NUMBER	REV.	
D	C5	L0058-0-MCCF	*	

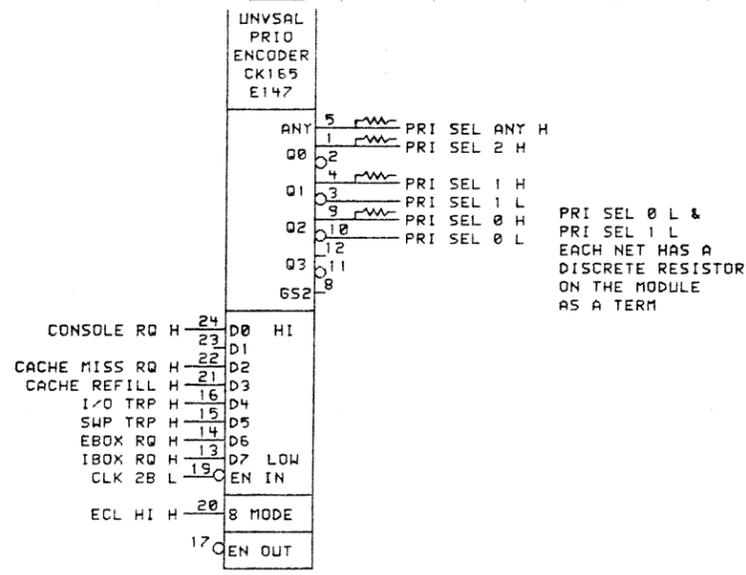


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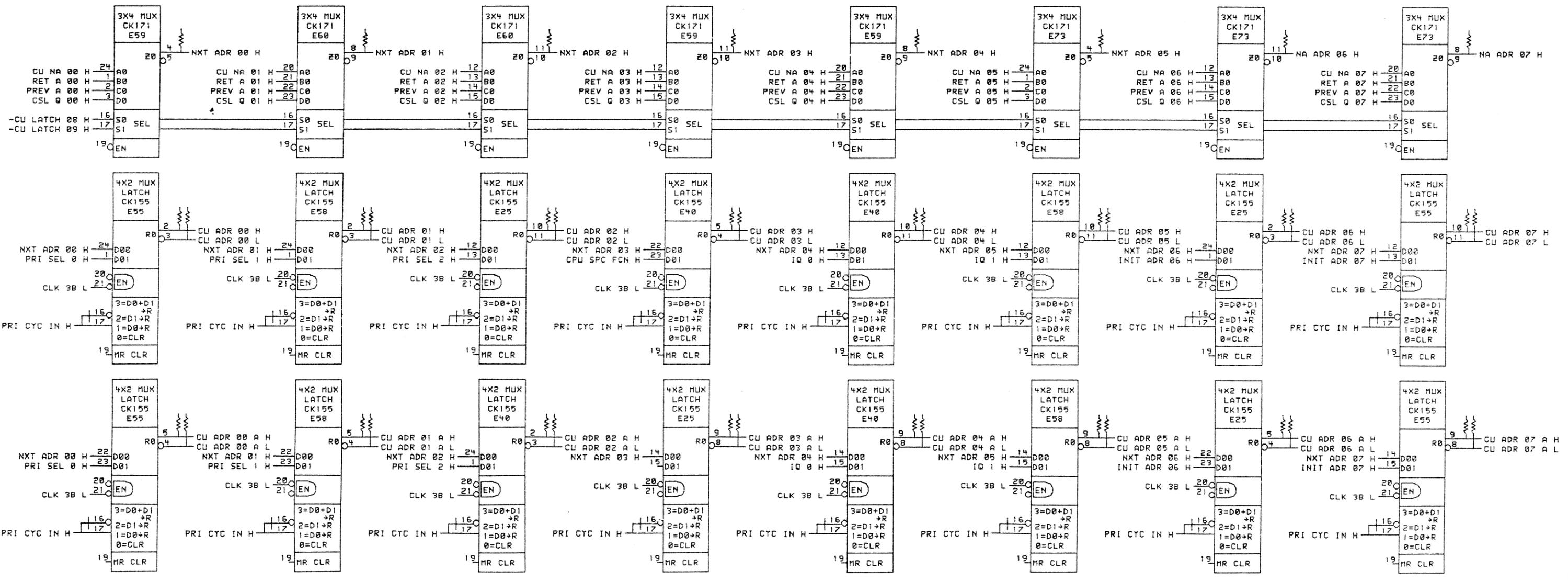
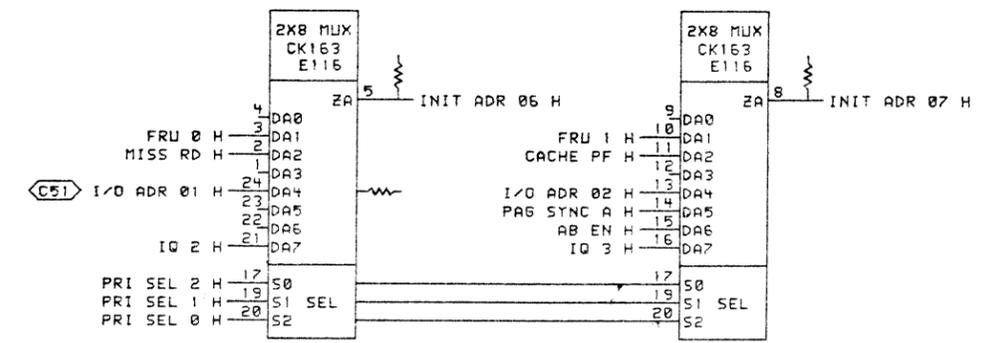
REVISIONS	
CHK	CHANGE NO. REV

digital	DATE ENG.	DATE	TITLE:
	DATE	BOARD LOCATION:	MDP CU CONTROL LOGIC
FIRST USED ON OPTION/MODEL: 2080		SHEET 1 OF 1	SIZE CODE NUMBER REV.
			D CS L0058-0-MCCG *

DATE	ENG.	DATE	TITLE:
06-FEB-82			MDP CU CONTROL LOGIC



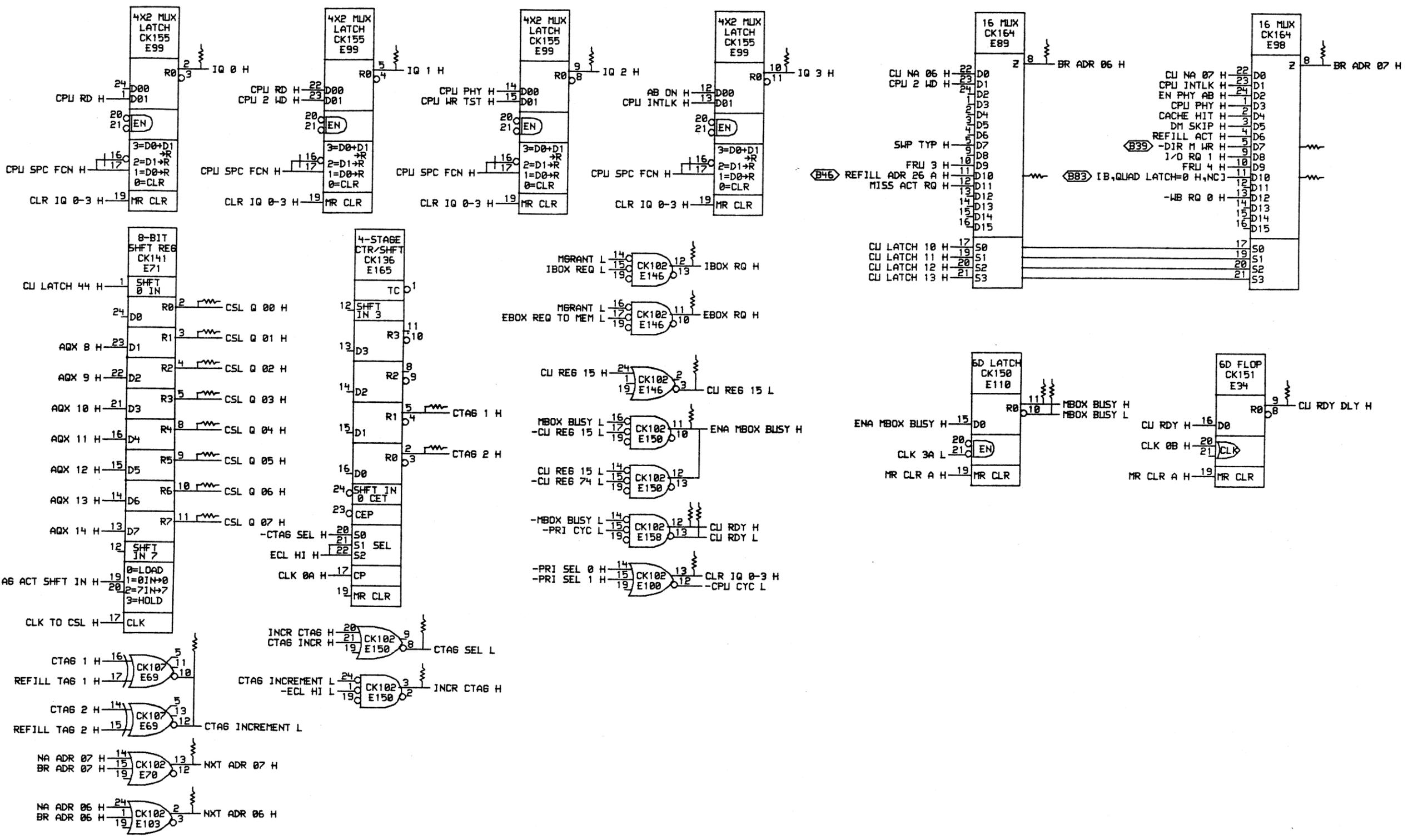
PRI SEL 0 L &  
PRI SEL 1 L  
EACH NET HAS A  
DISCRETE RESISTOR  
ON THE MODULE  
AS A TERM



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REVISIONS		
CHK	CHANGE NO.	REV

	DATE	ENG.	DATE	TITLE:
	04-JAN-82			
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
			1	1
VULCAN+ (2080.L0058) 98CCH.DRW	04-JAN-82 14:26	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0058-0	D	CS
			NUMBER	REV.
			L0058-0-MCCH	*

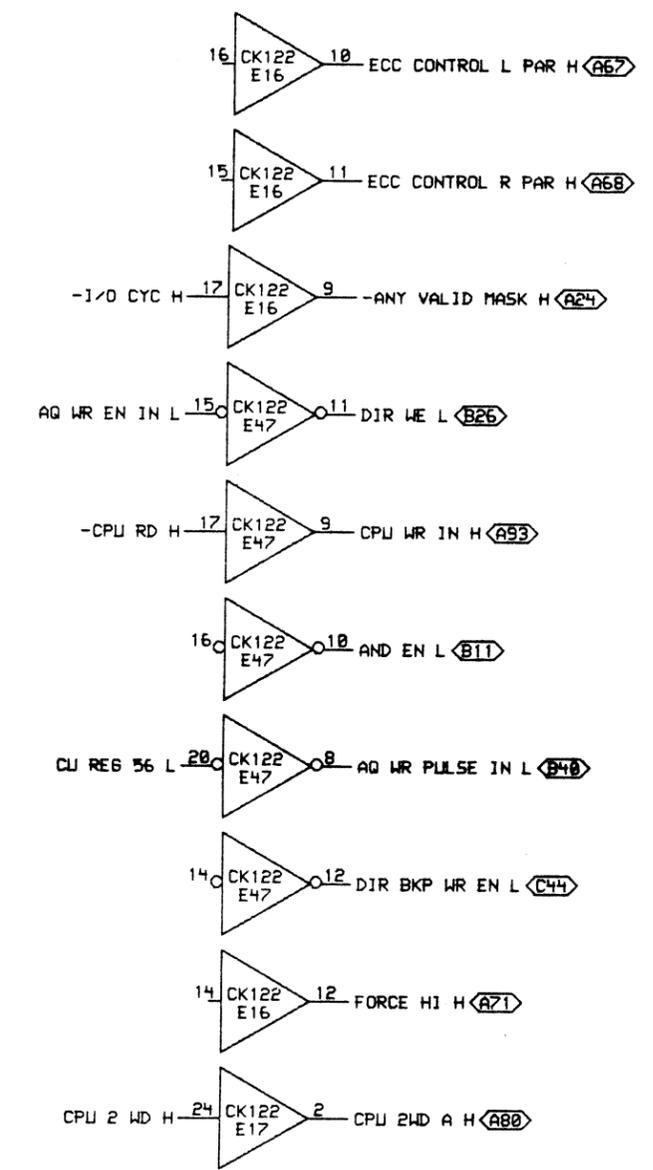
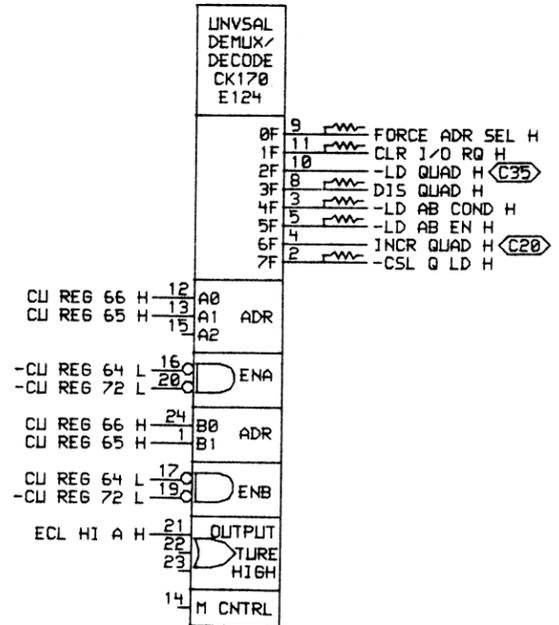
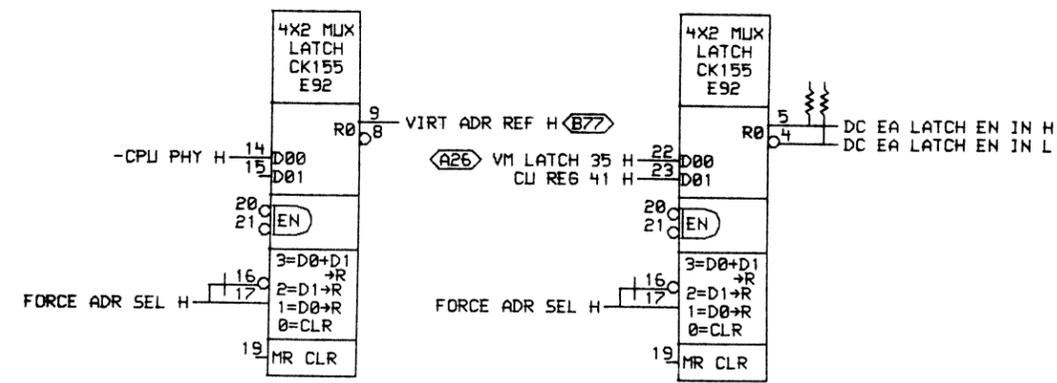
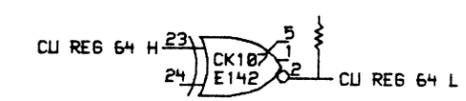
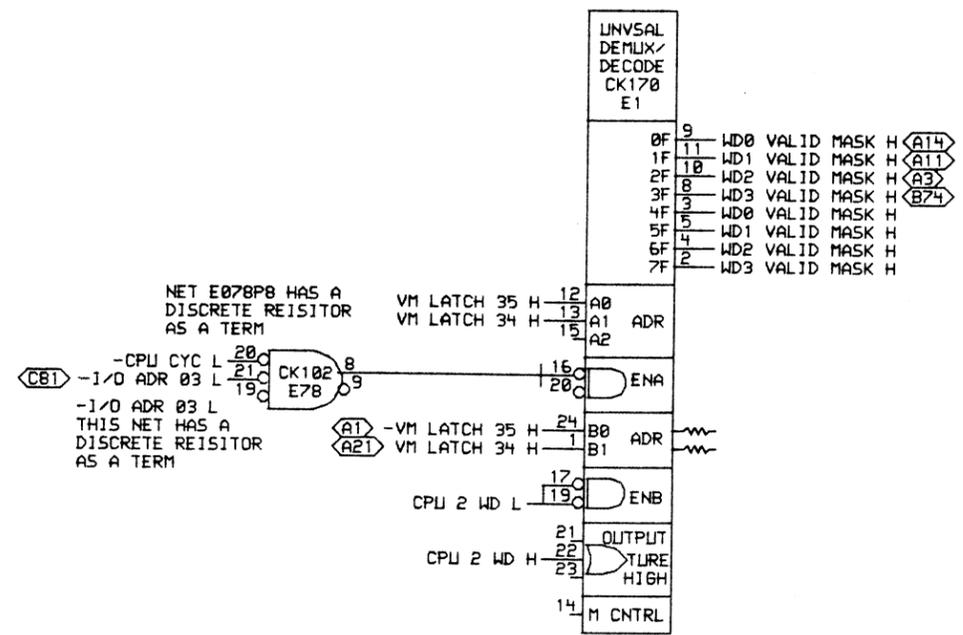


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REVISIONS	
CHK	CHANGE NO. REV

	DRN. <i>J. J. J.</i> CHK'D.	DATE 06-FEB-82 ENG.	DATE TITLE:
	VILCANI <2080-L0058> MCCI.DRW 05-FEB-82 17:56 FIRST USED ON OPTION MODEL: 2080	DATE BOARD LOCATION:	DATE SHEET 1 OF 1
SIZE D CODE CS	NUMBER L0058-0-MCCI	REV. *	TITLE:

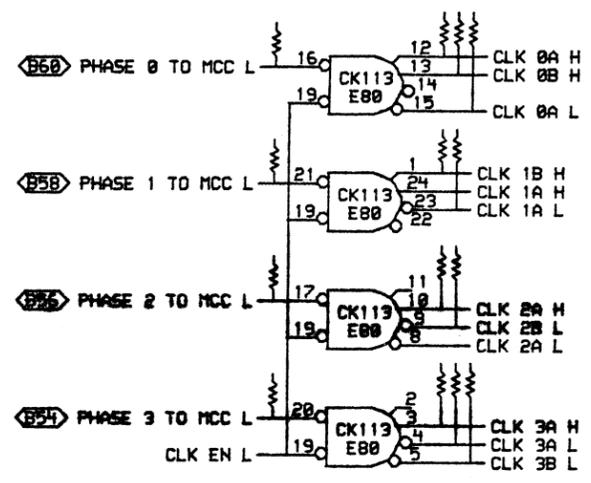
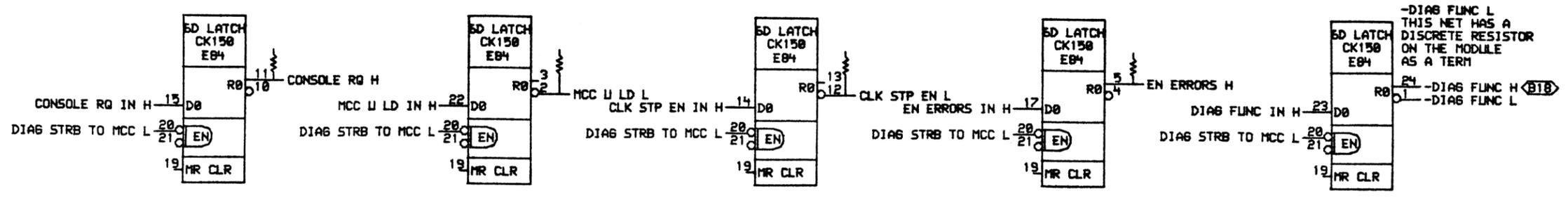




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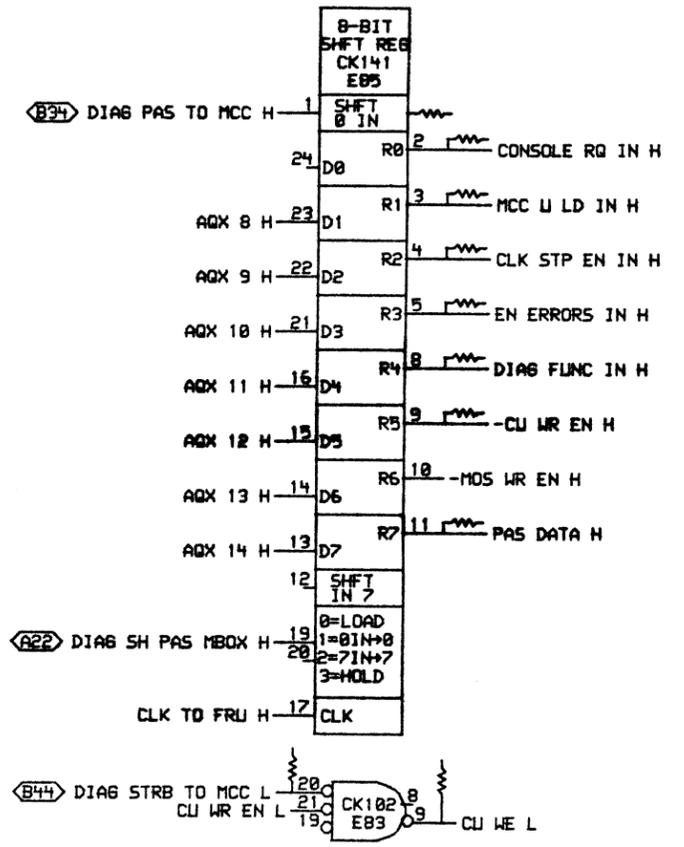
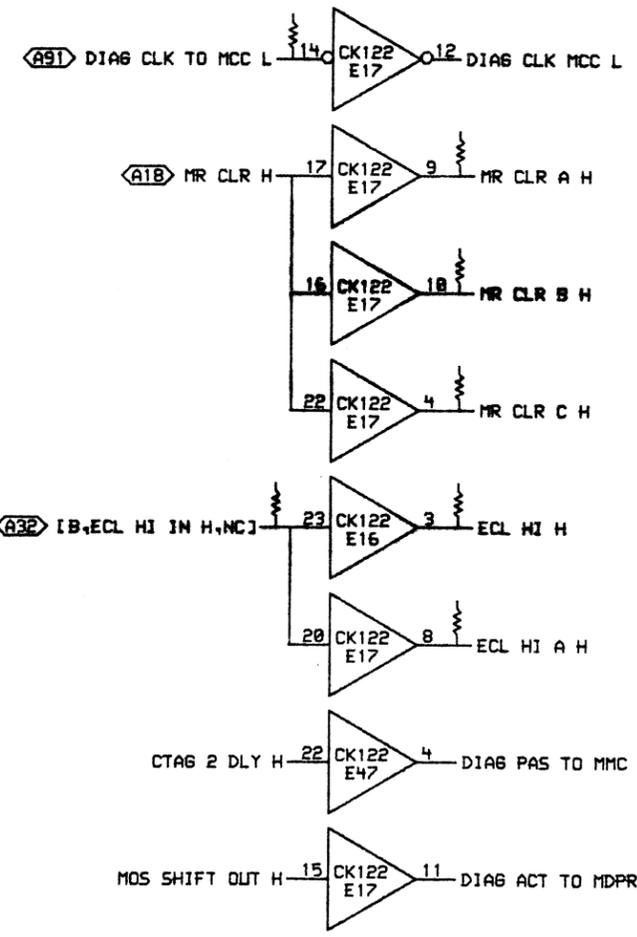
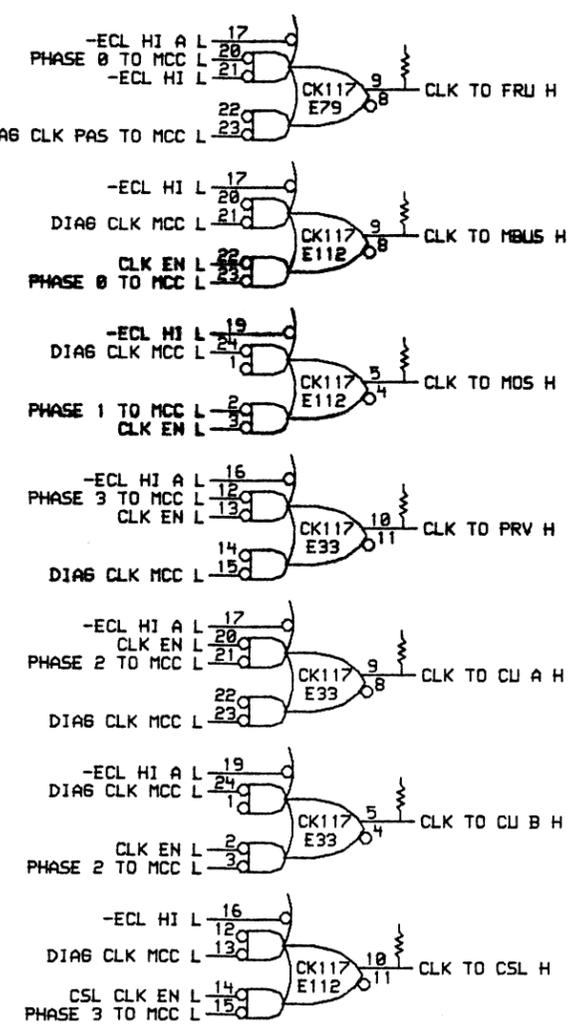
REVISIONS		
CHK	CHANGE NO.	REV.

digital	DATE	ENG.	DATE	TITLE: MAP CONTROL LOGIC
	25-FEB-82			
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
24-FEB-82			1	1
VILLANI (P000) L0058-0-MCCK.DRW	24-FEB-82 22:56	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: 2080	B-DD-L0058-0		D	CS
		NUMBER	L0058-0-MCCK	REV.
				*



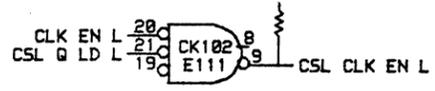
CLK 1A H  
THIS NET HAS A  
DISCRETE RESISTOR  
AS A TERM

CLK 2A L  
THIS NET HAS A  
DISCRETE RESISTOR  
AS A TERM



DIAG STRB TO MCC L  
MOS WR EN L

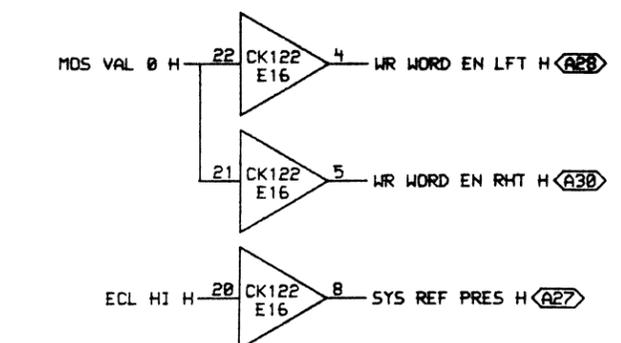
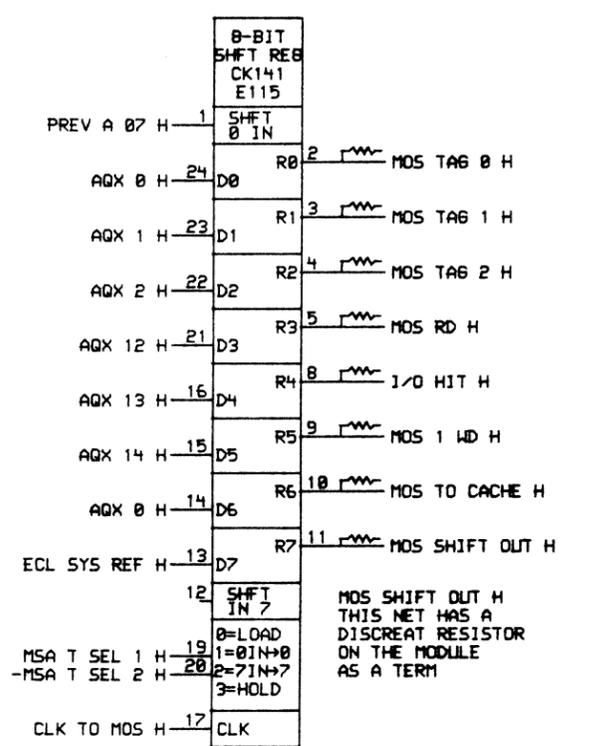
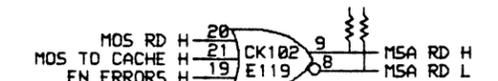
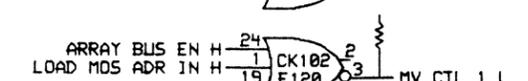
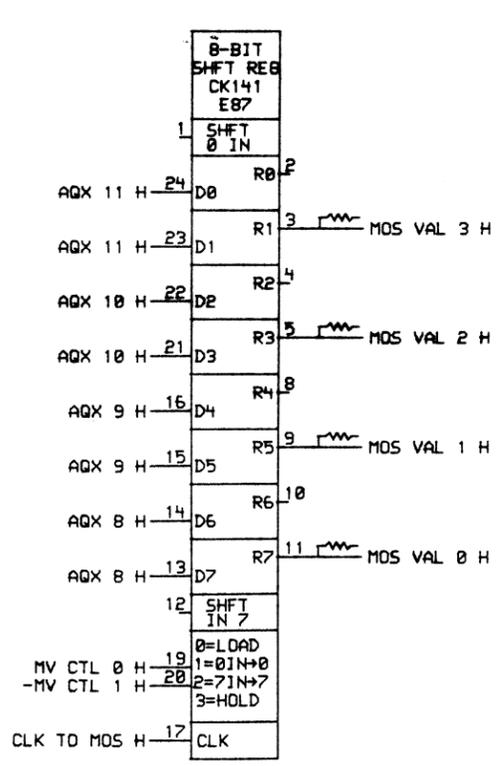
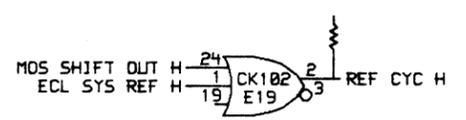
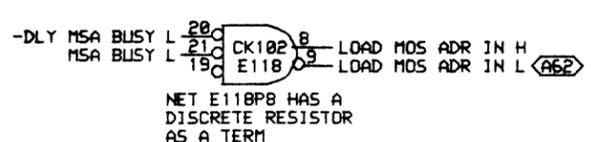
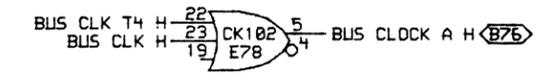
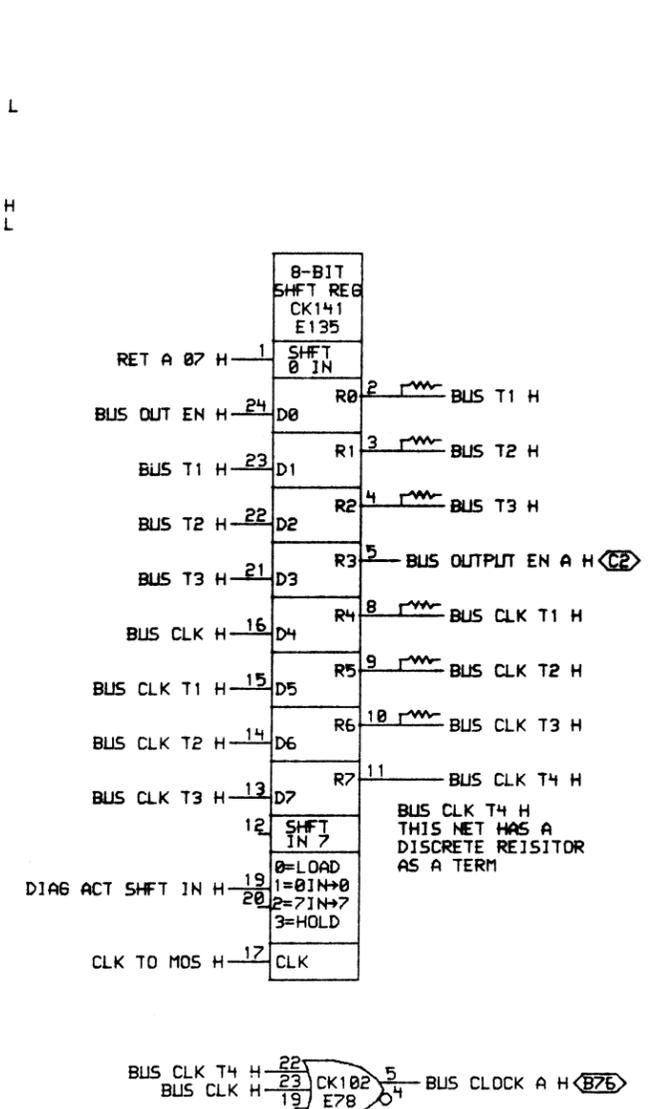
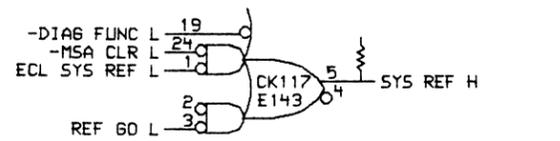
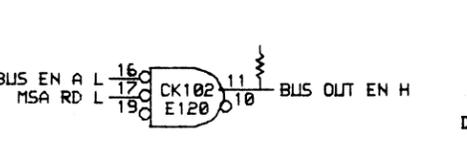
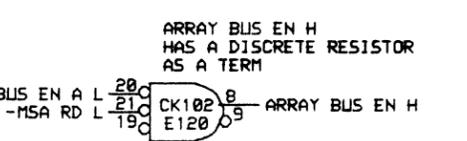
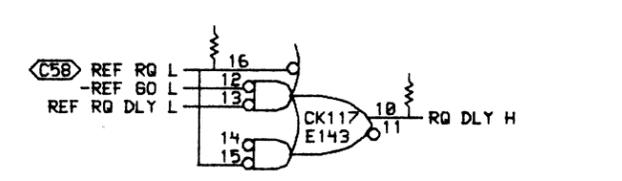
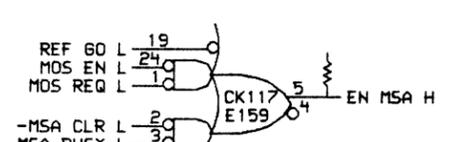
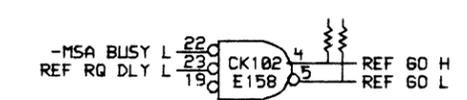
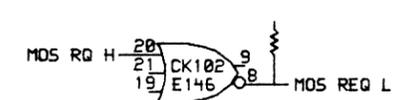
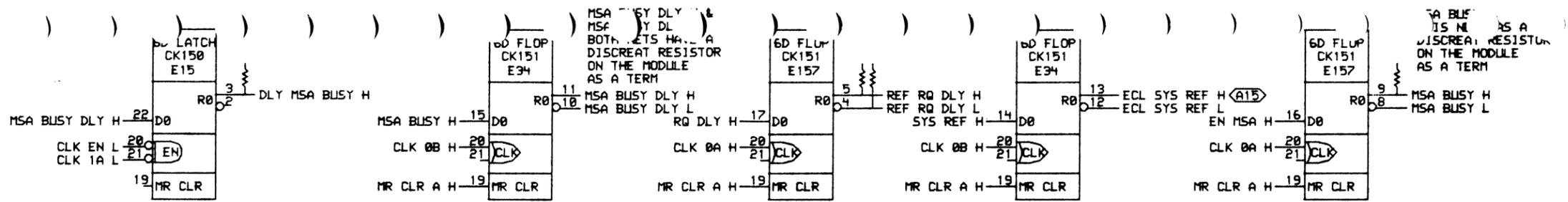
MOS WR EN L  
HAS A DISCRETE  
RESISTOR FOR A TERM



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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE:	CLK BUFFER
	77-JAN-82				
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	
			1	1	
FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0058-0		SIZE	CODE
				D	CS
				NUMBER	REV.
				L0058-0-MCCL	*

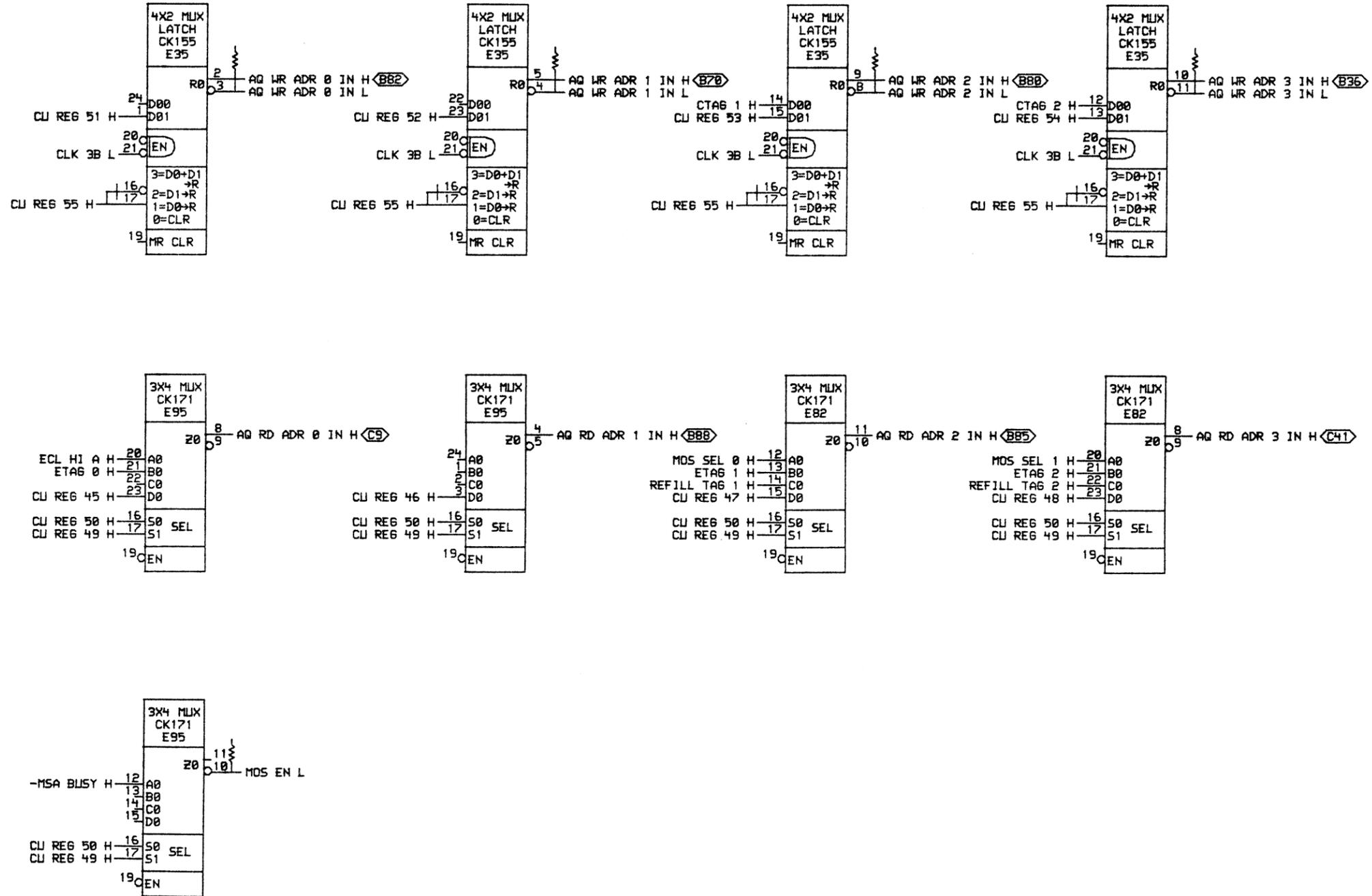


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REVISIONS		
CHK	CHANGE NO.	REV

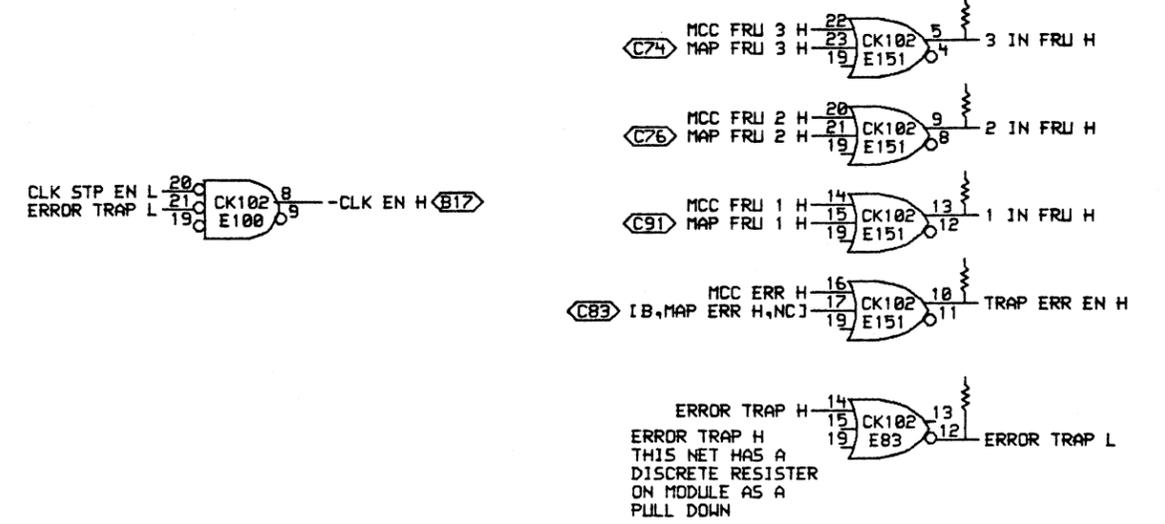
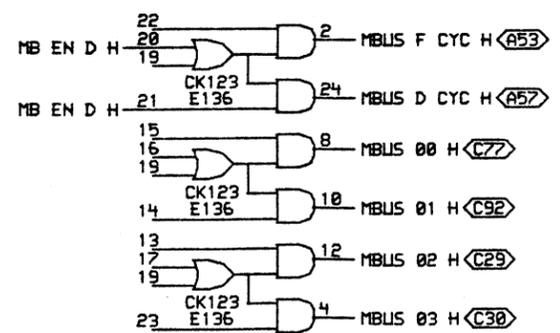
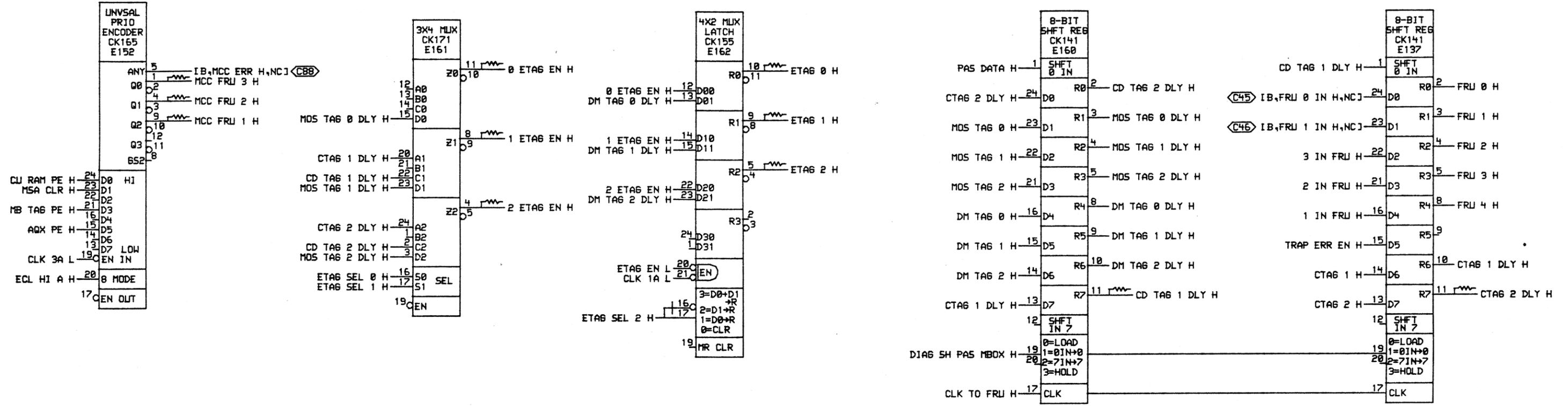
	DATE	ENG.	DATE	TITLE:
	22-FEB-82			MOS START CTL
SHEET 1 OF 1		BOARD LOCATION:		
V.L. CAN (2000.L.0058) 1982CH.DRW		NEXT HIGHER ASSEMBLY:		SIZE CODE
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0058-0		D CS

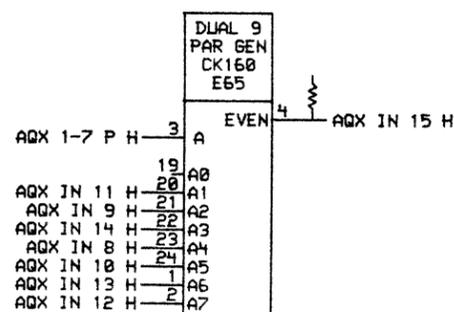
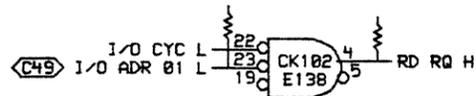
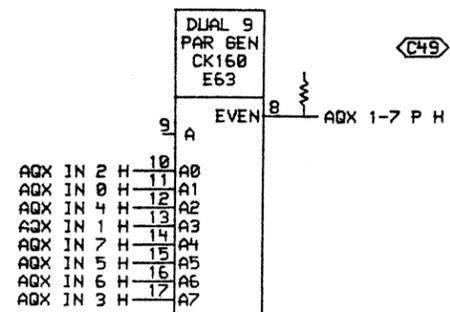
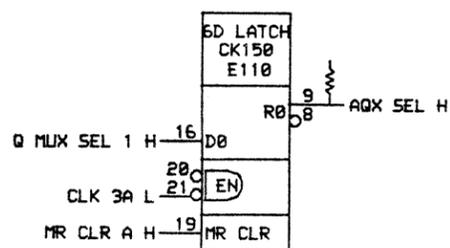
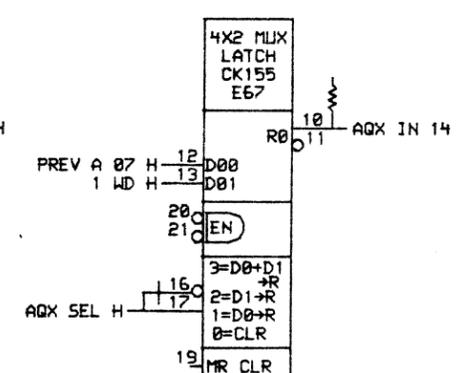
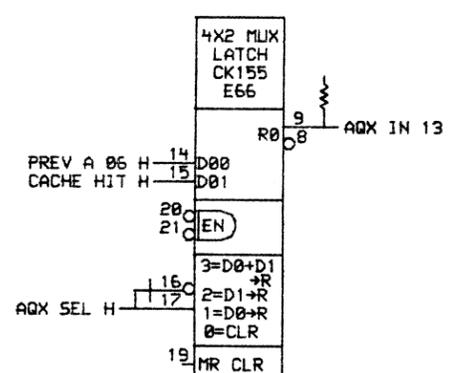
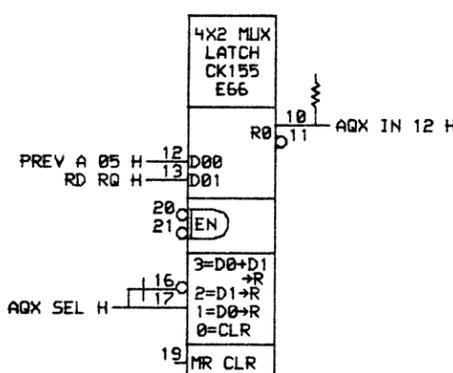
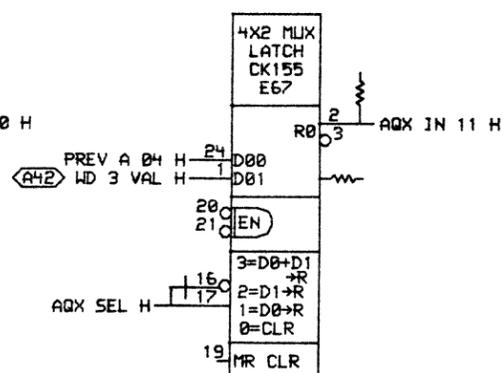
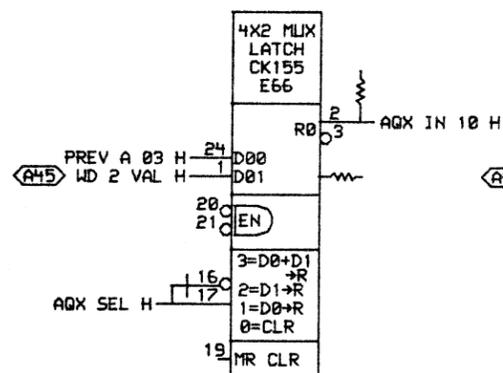
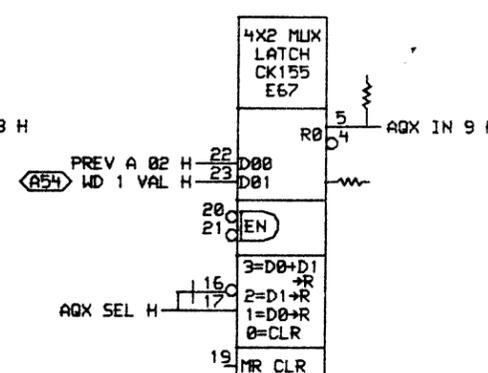
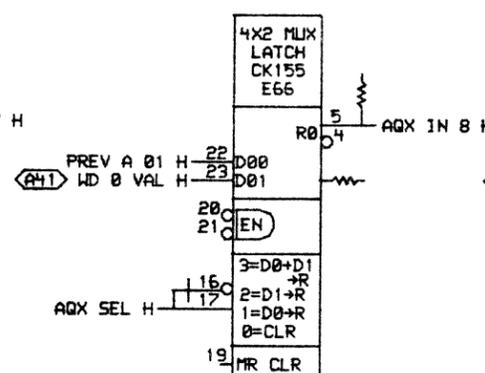
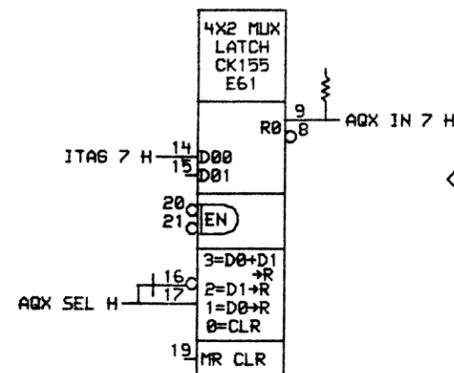
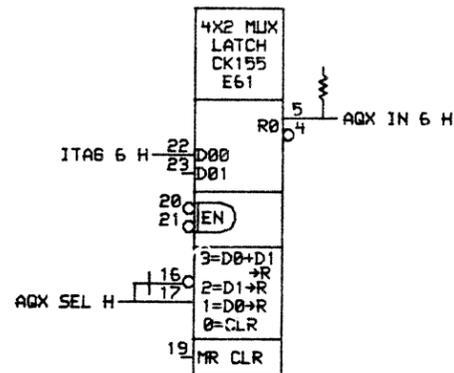
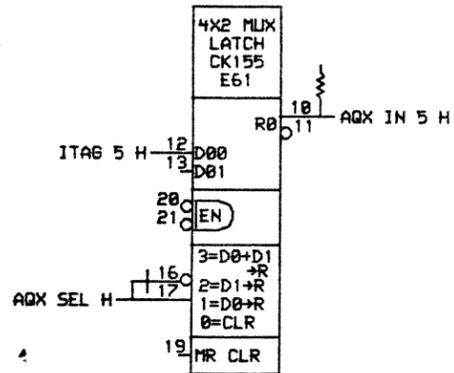
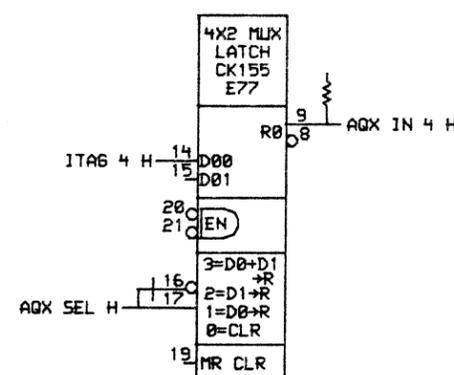
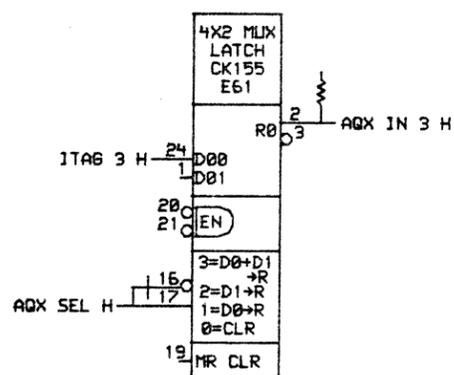
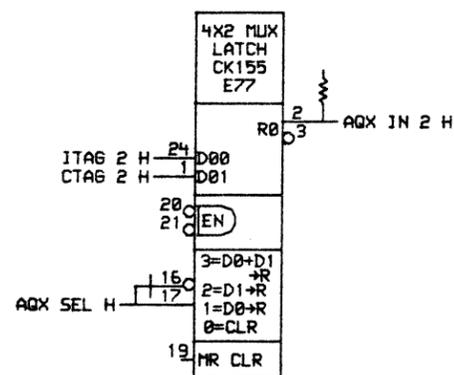
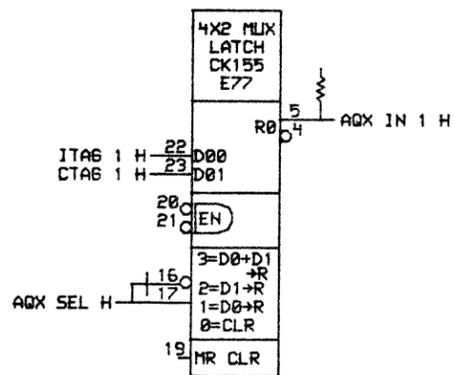
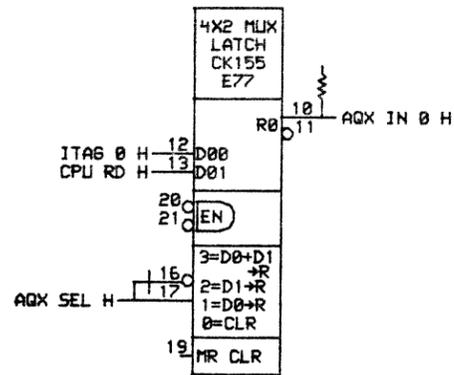
NUMBER	REV.
L0058-0-MMCM	*



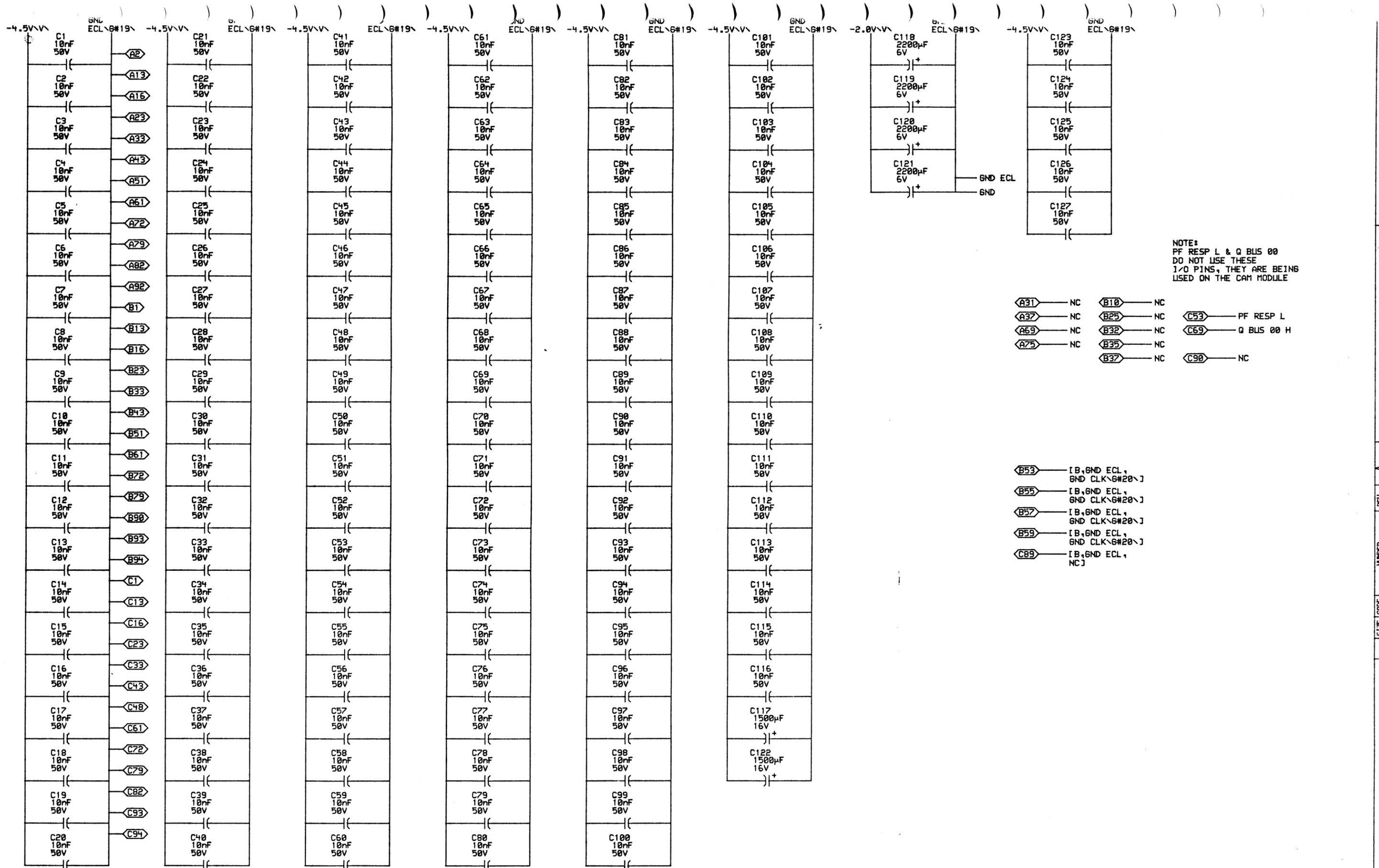
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		
VIL CANI (2000-L0058) 93 MCCN.DRW				SHEET 1 OF 1
FIRST USED ON OPTION/MODEL: 2000				SIZE CODE
NEXT HIGHER ASSEMBLY: B-DD-L0058-0				NUMBER
				L0058-0-MCCN
				REV. *







DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		
VLL DWH <2000.L0058>MCCS.DRW 06-JAN-02 15:56		NEXT HIGHER ASSEMBLY:		SIZE CODE
FIRST USED ON OPTION/MODEL: 2000		B-DD-L0058-0		NUMBER
				REV. *



NOTE:  
 PF RESP L & Q BUS 00  
 DO NOT USE THESE  
 I/O PINS, THEY ARE BEING  
 USED ON THE CAM MODULE

- A31 — NC
- A37 — NC
- A69 — NC
- A75 — NC
- B10 — NC
- B25 — NC
- B32 — NC
- B35 — NC
- B37 — NC
- C53 — PF RESP L
- C59 — Q BUS 00 H
- C90 — NC

- B53 — [B,GND ECL,  
GND CLK\6#20\]
- B55 — [B,GND ECL,  
GND CLK\6#20\]
- B57 — [B,GND ECL,  
GND CLK\6#20\]
- B59 — [B,GND ECL,  
GND CLK\6#20\]
- C89 — [B,GND ECL,  
NC]

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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE:
	20-FEB-82			MCC POWER & GROUND
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
			1	1
FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY:		SIZE CODE
		B-DD-L0058-0		D C5
NUMBER				REV.
L0058-0-MCCV				*

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6

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REV. 1 A3b 838-74 d5 3000 d 3215

VULCAN:<2080.L0061>25MMC1  
 VULCAN:<2080.L0061>25MMC2  
 VULCAN:<2080.L0061>25MMC3  
 VULCAN:<2080.L0061>25MMC4  
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 VULCAN:<2080.L0061>59MMC9  
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D

D

C

C

B

B

A

A

REV. NUMBER SIZE CODE SP

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>J. J. J.</i>	DATE 10-FEB-83	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
ARCH: <UHLER.PLOTS>LMC.DPL.1 10-FEB-83 03:05			NEXT HIGHER ASSEMBLY:		SIZE CODE
FIRST USED ON OPTION/MODEL:			D	SP	NUMBER
					REV.

8

7

6

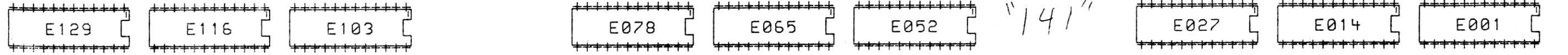
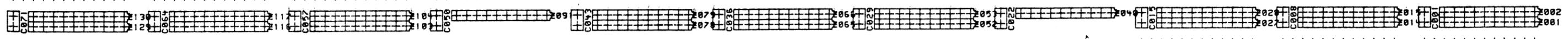
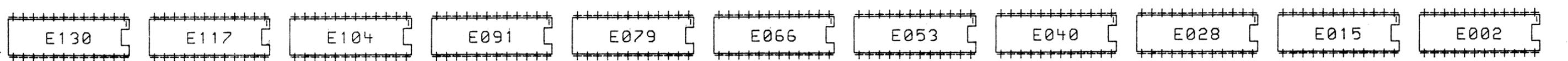
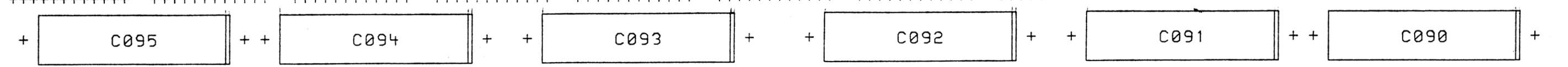
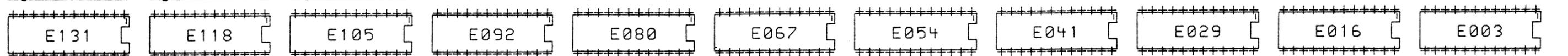
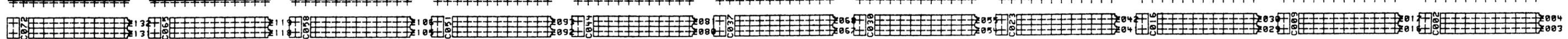
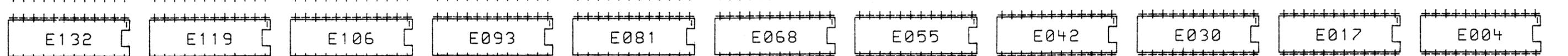
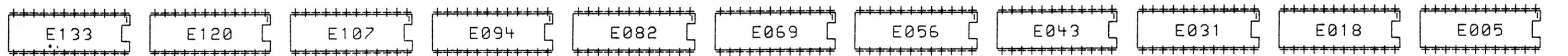
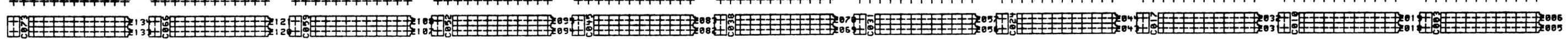
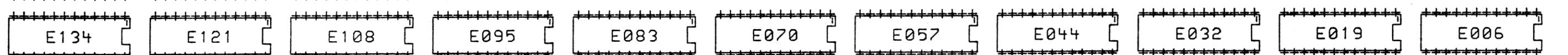
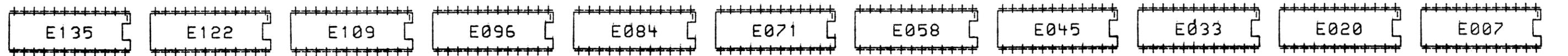
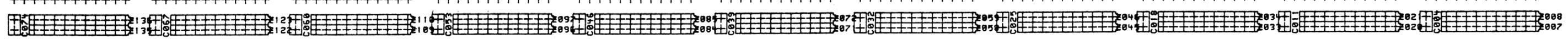
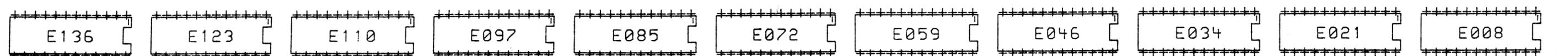
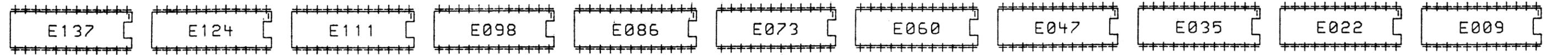
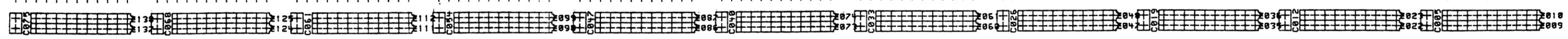
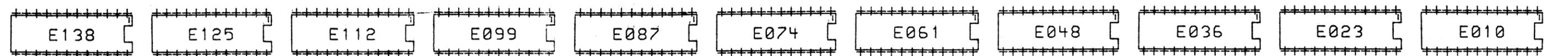
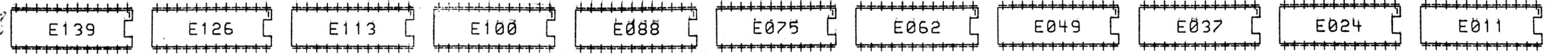
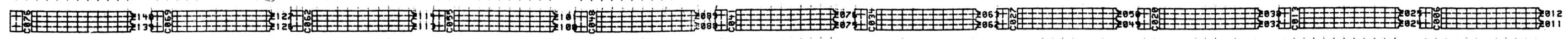
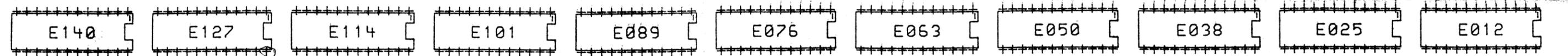
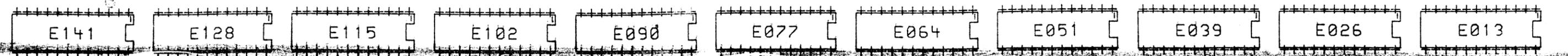
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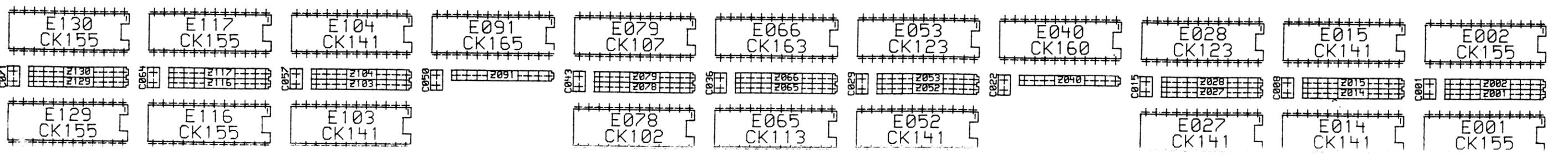
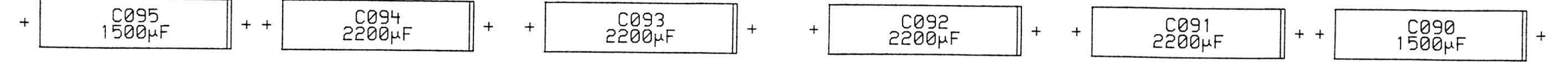
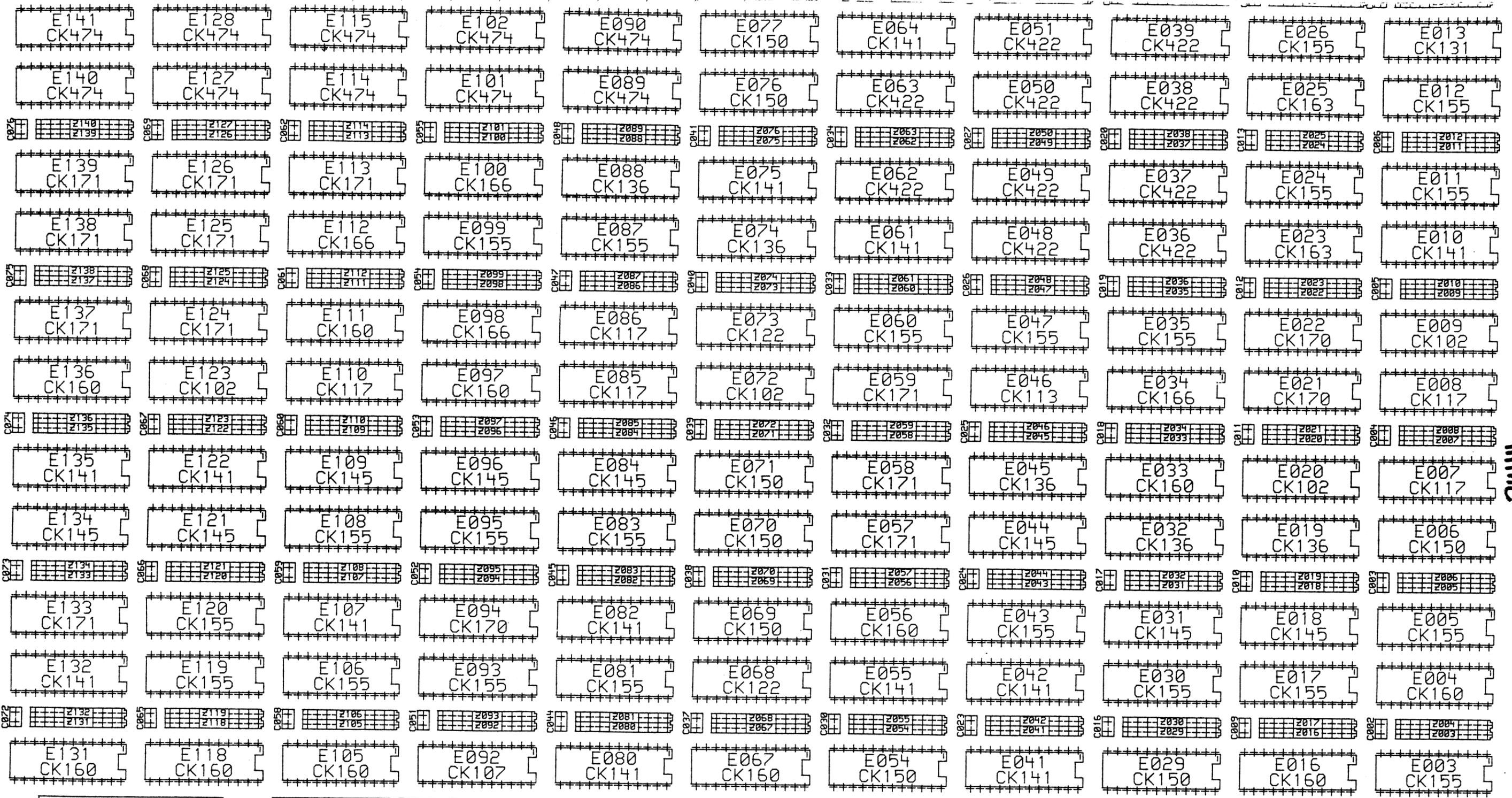
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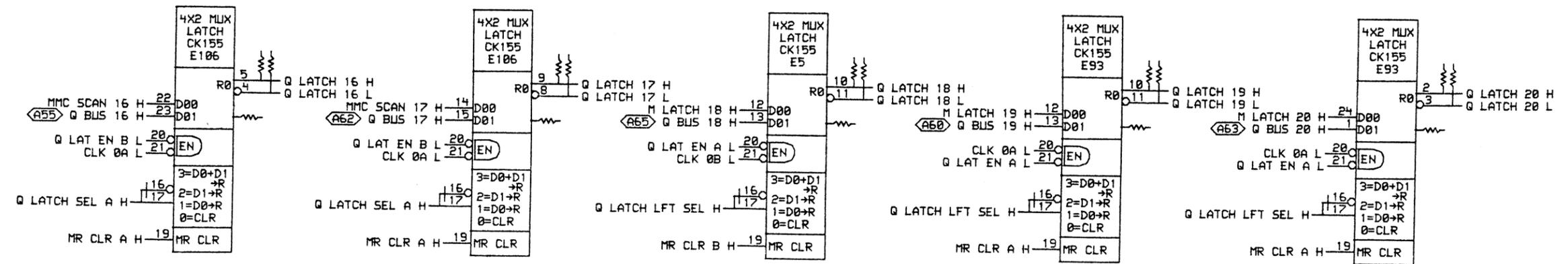
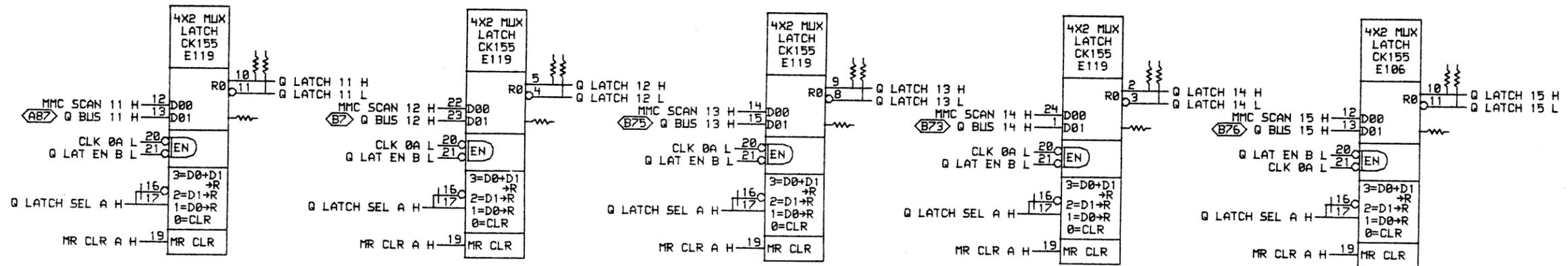
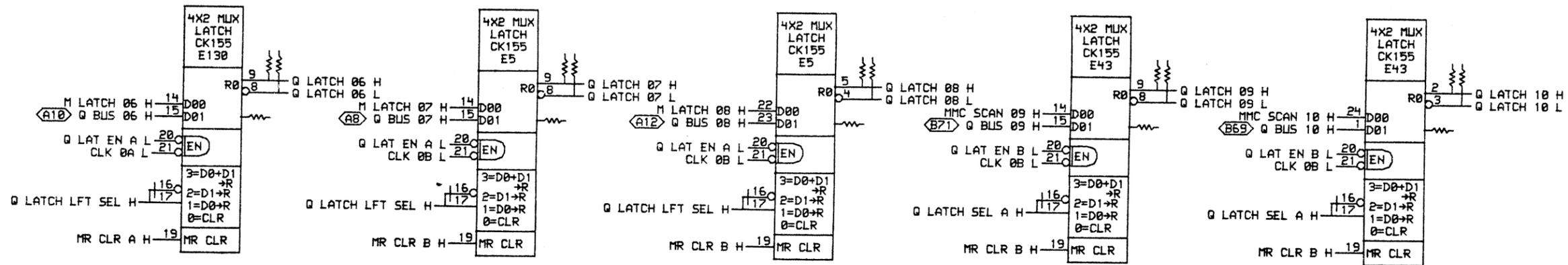
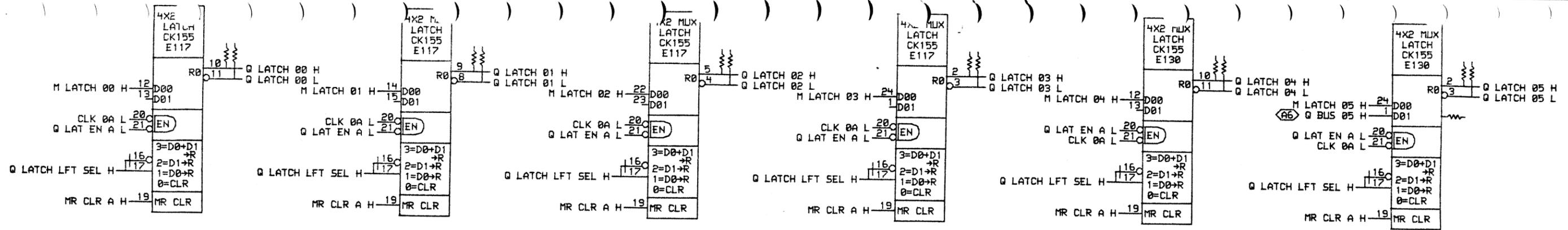
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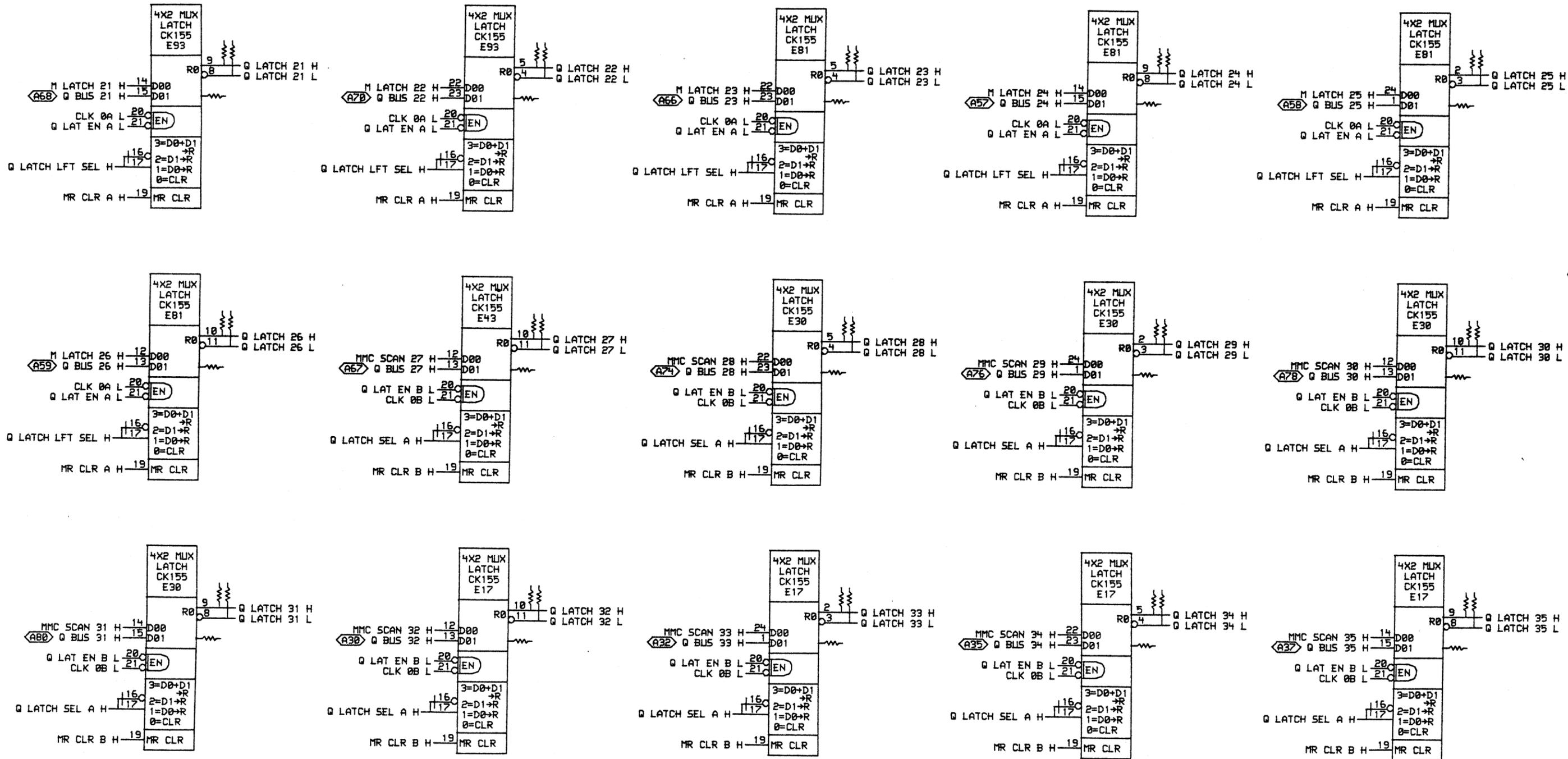
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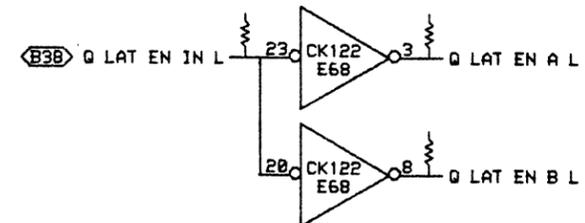
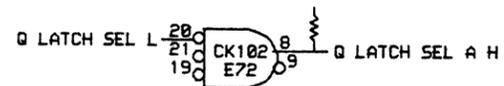
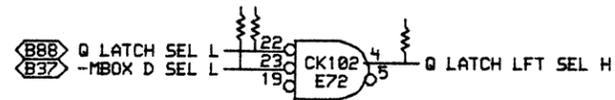
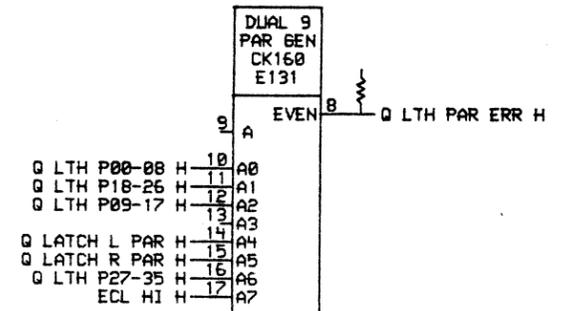
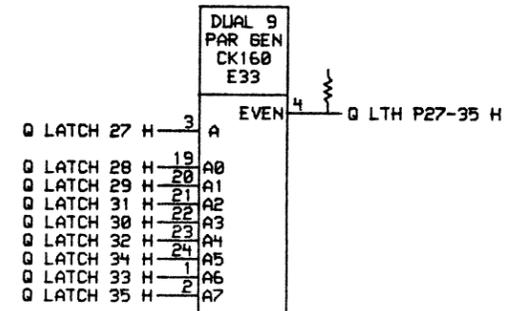
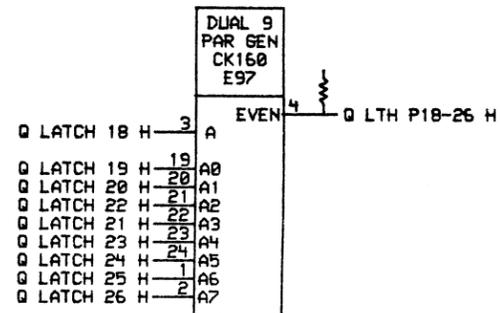
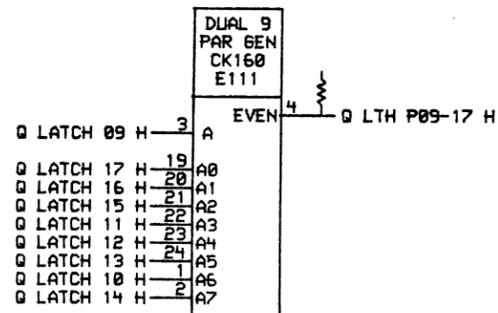
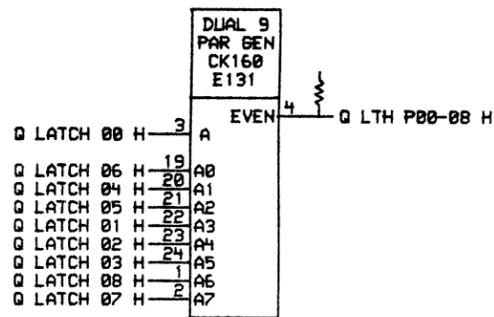
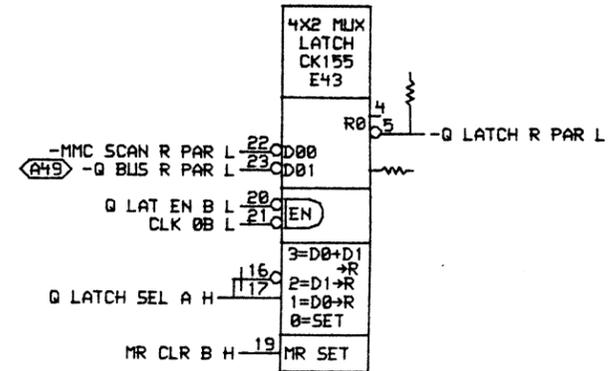
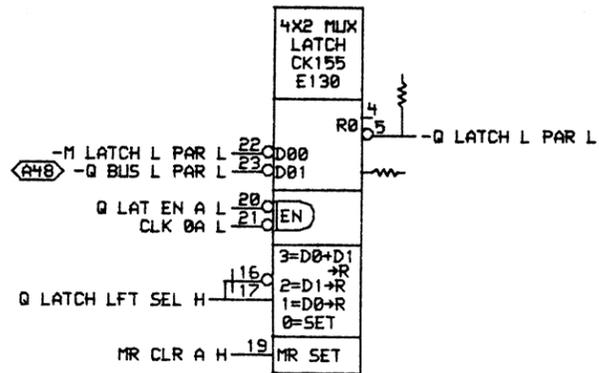




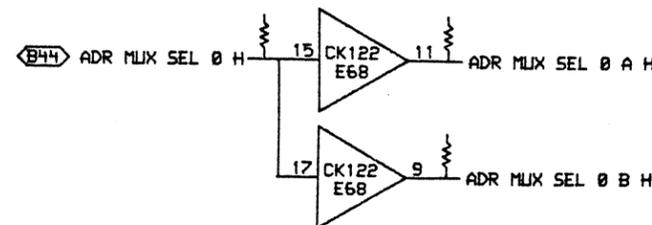
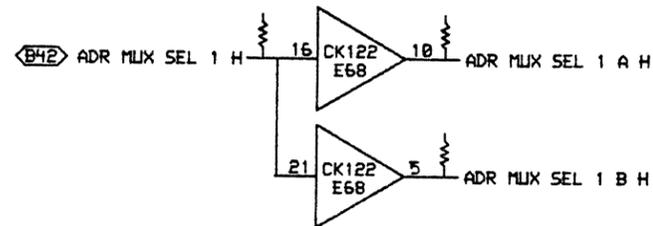
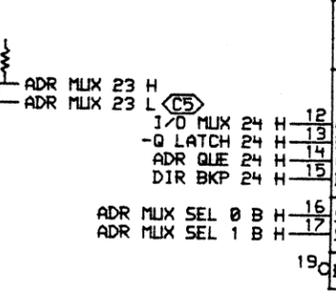
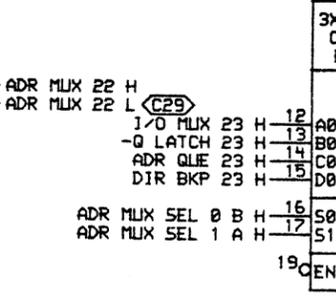
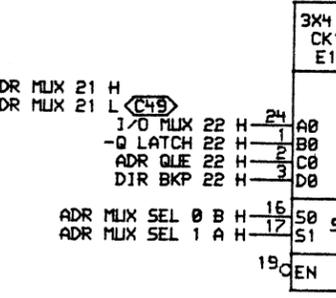
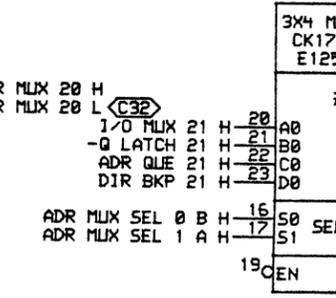
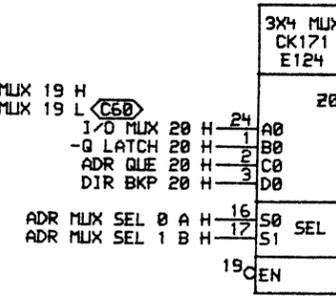
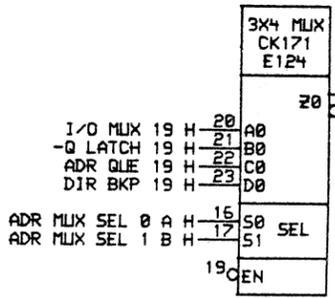
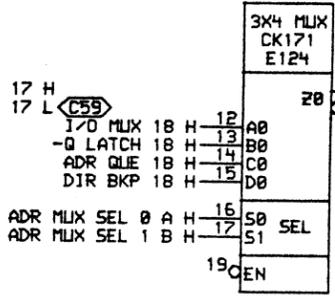
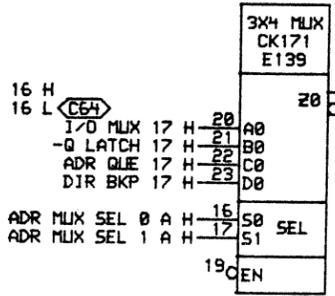
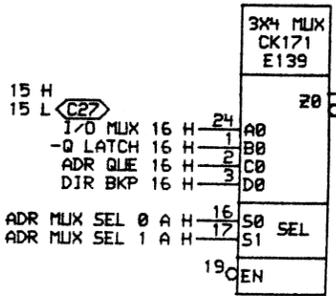
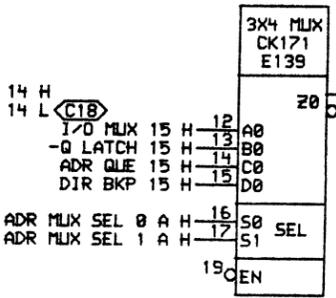
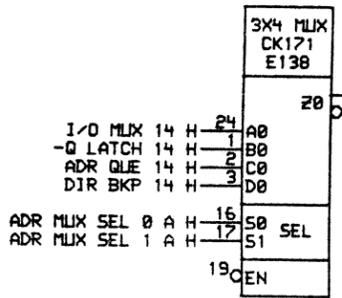
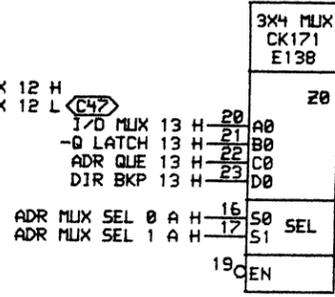
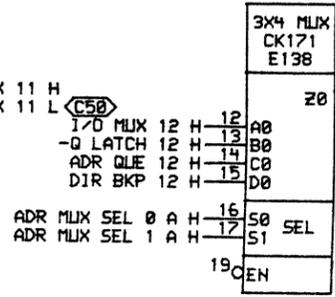
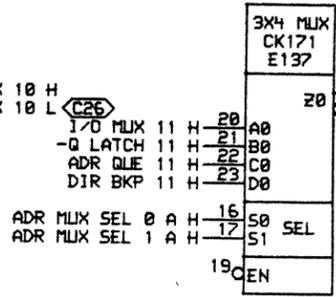
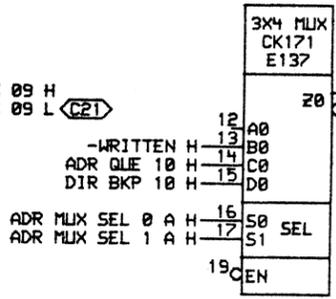
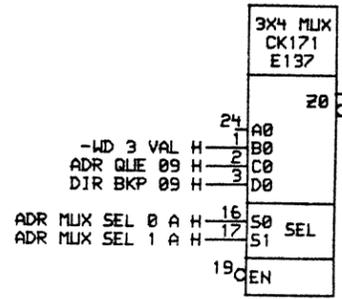
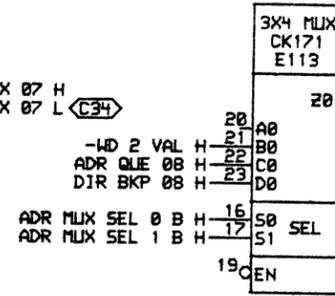
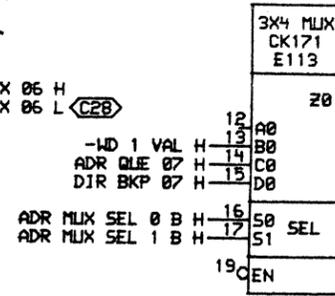
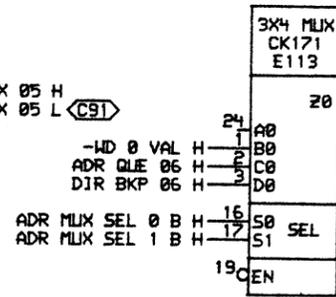
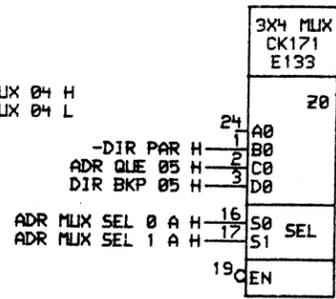
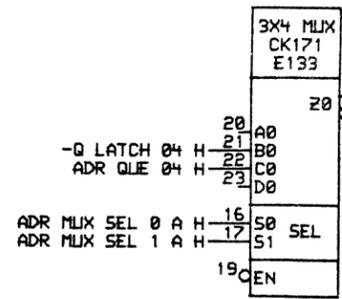
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	02-FEB-82			Q LATCH
		BOARD LOCATION:		BITS 00-20
		SHEET	OF	
VLL CAN (2000-L0061) 25MMC1.DRW 02-FEB-82 07:25 NEXT HIGHER ASSEMBLY:				SIZE CODE
FIRST USED ON OPTION/MODEL: 2000 B-DD-L0061-0				NUMBER
				REV.
				*



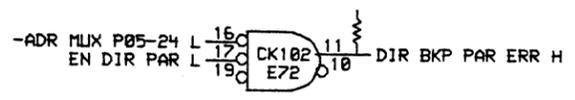
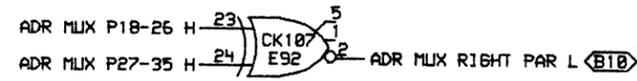
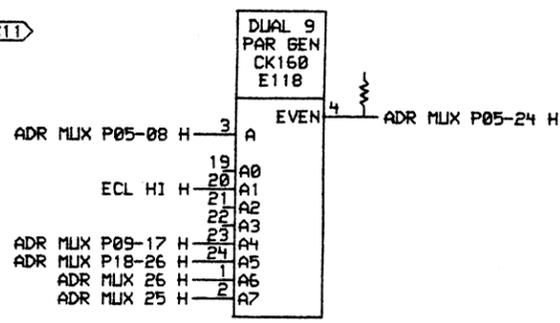
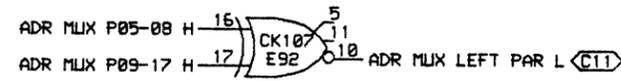
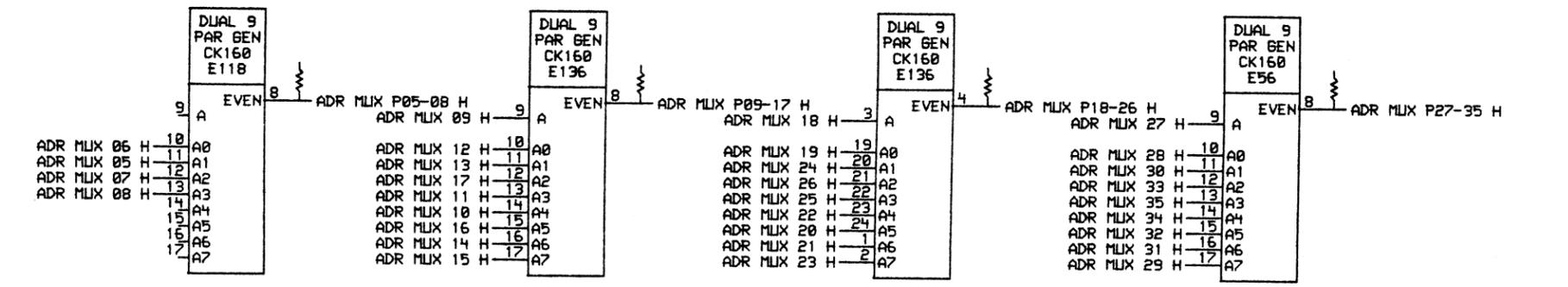
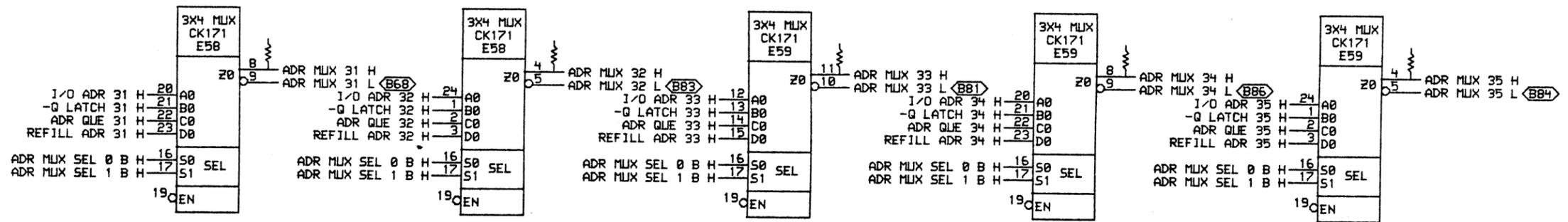
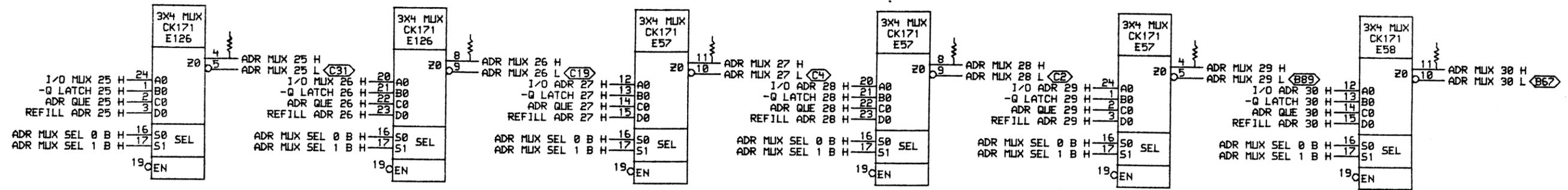
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CHK'D.	02-FEB-82	B-D		Q LATCH
		BOARD LOCATION:		BITS 21 - 34
		SHEET 2 OF 3		
VILCAM (2080-L0061)23MMC2.DRW 01-FEB-82 15:43 NEXT HIGHER ASSEMBLY:				SIZE CODE NUMBER
FIRST USED ON OPTION/MODEL: 2080 B-DD-L0061-0				D C5 L0061-0-MMC2
				REV. *



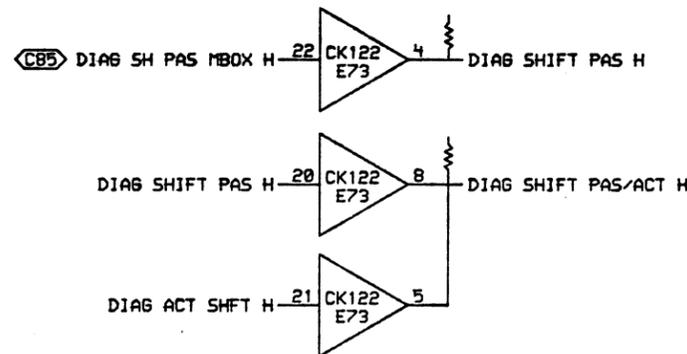
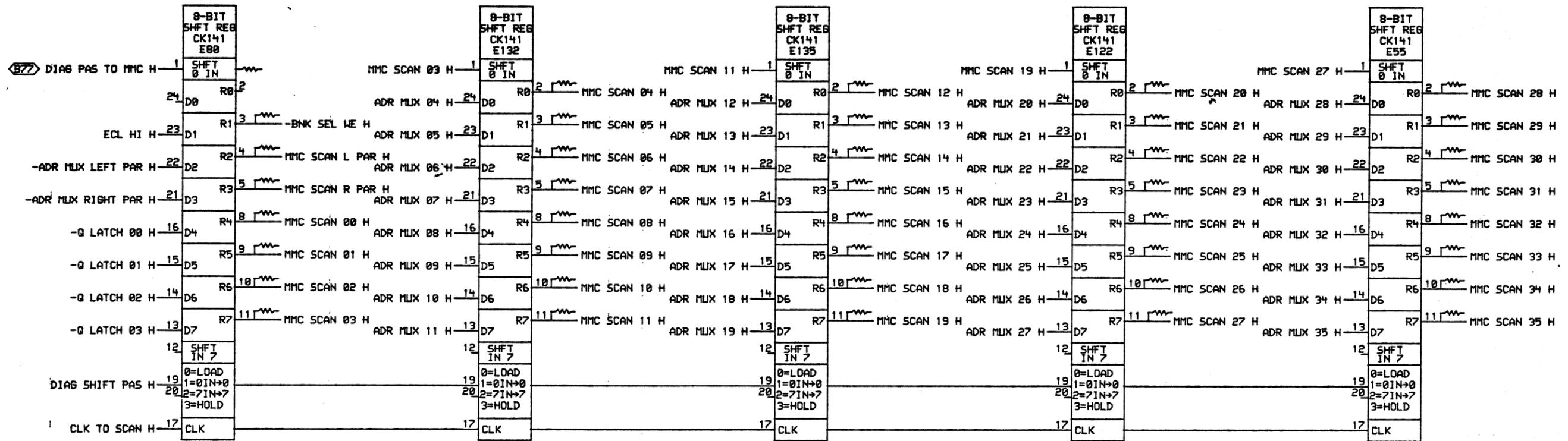
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	Q LATCH CTL & PARITY
VILCANI<E000>L006122MMC3.DRW 02-FEB-82 07:30 NEXT HIGHER ASSEMBLY:				SIZE CODE NUMBER
FIRST USED ON OPTION/MODEL: 2080				D 05 L0061-0-MMC3 REV. *



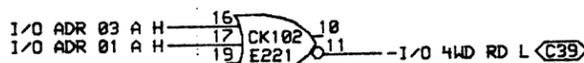
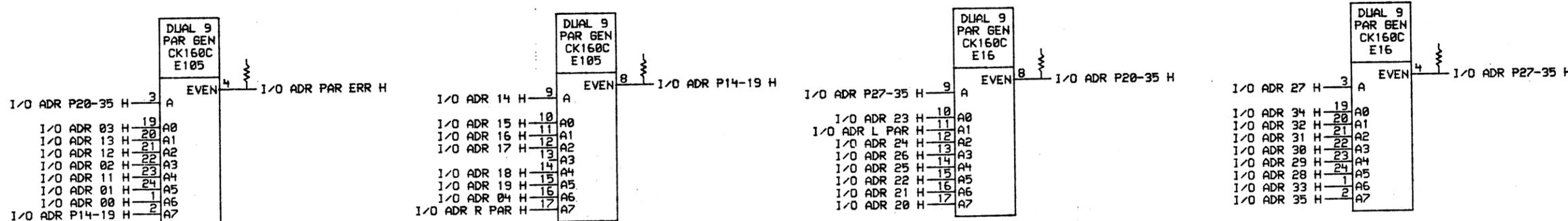
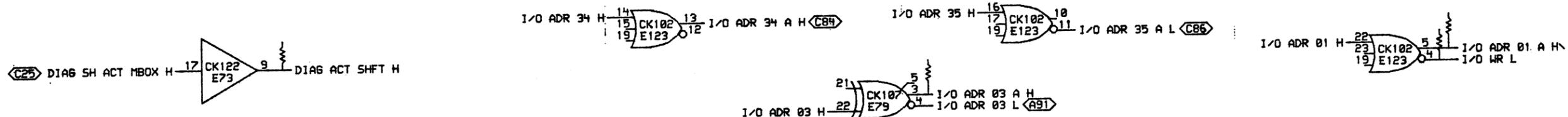
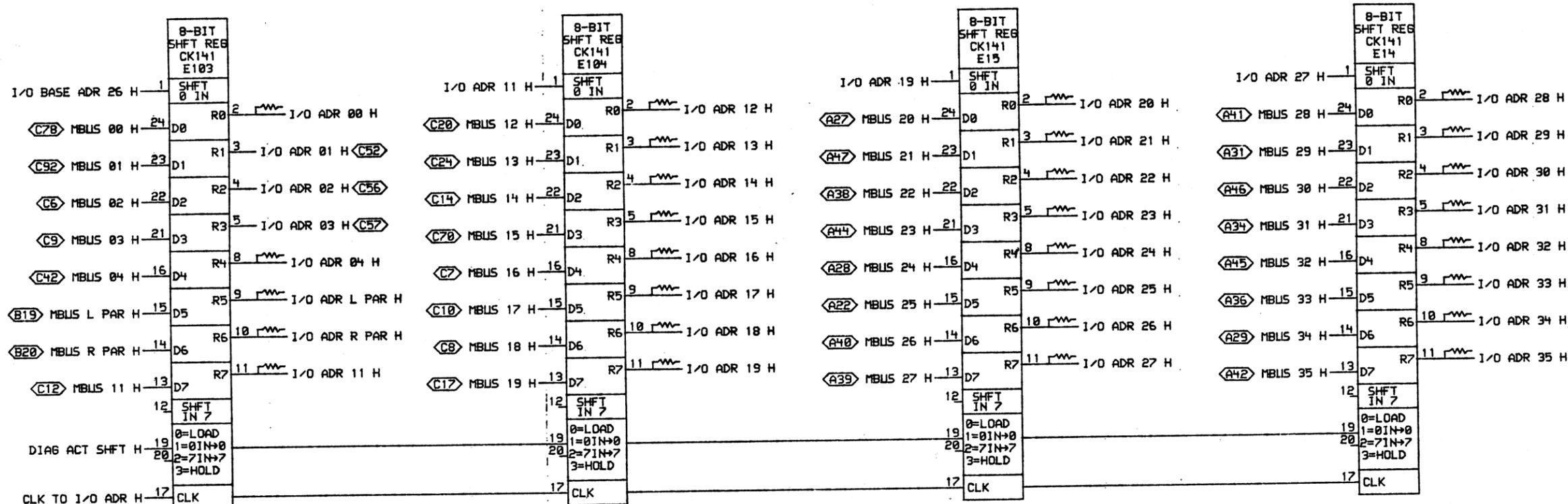
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CHK'D.	DATE	BOARD LOCATION:		ADR MUX
		SHEET	OF 2	BITS 04-24
FIRST USED ON OPTION/MODEL: 2080				NUMBER
B-DD-L0061-0				L0061-0-MMC4
SIZE	CODE	REV.		
D	CS	*		



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	02-FEB-82			ADR MUX
	DATE	BOARD LOCATION:		BITS 25-35 & P
	01-FEB-82 14:41	SHEET 2 OF 2		
VILCANT (2080.L0061)23MMCS.DRW	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL: 2080	B-DD-L0051-0	D	CS	L0061-0-MMC5
				REV. *

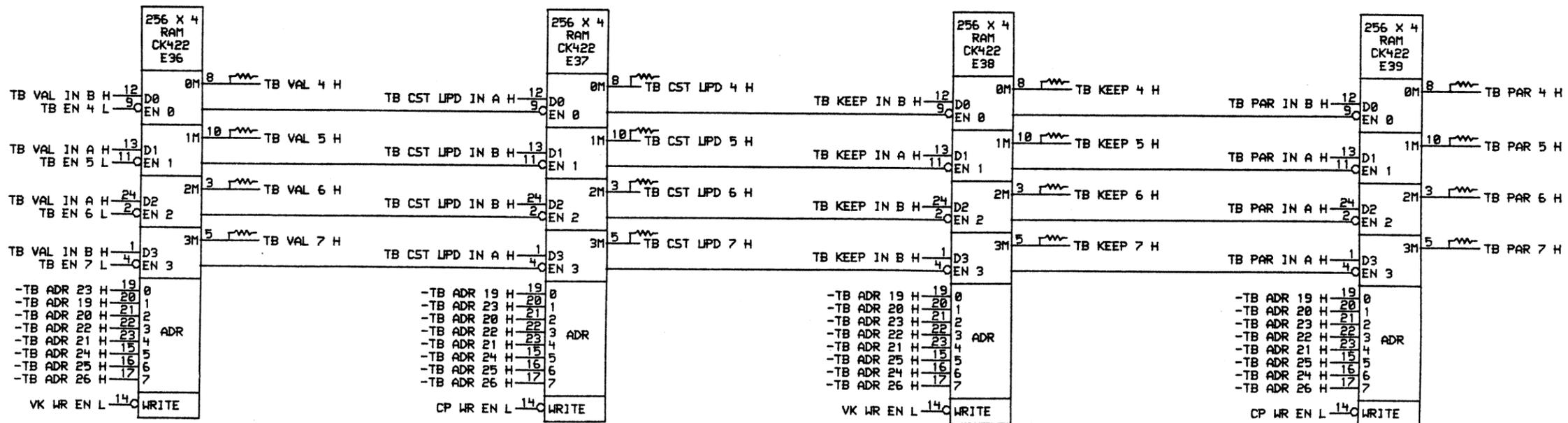
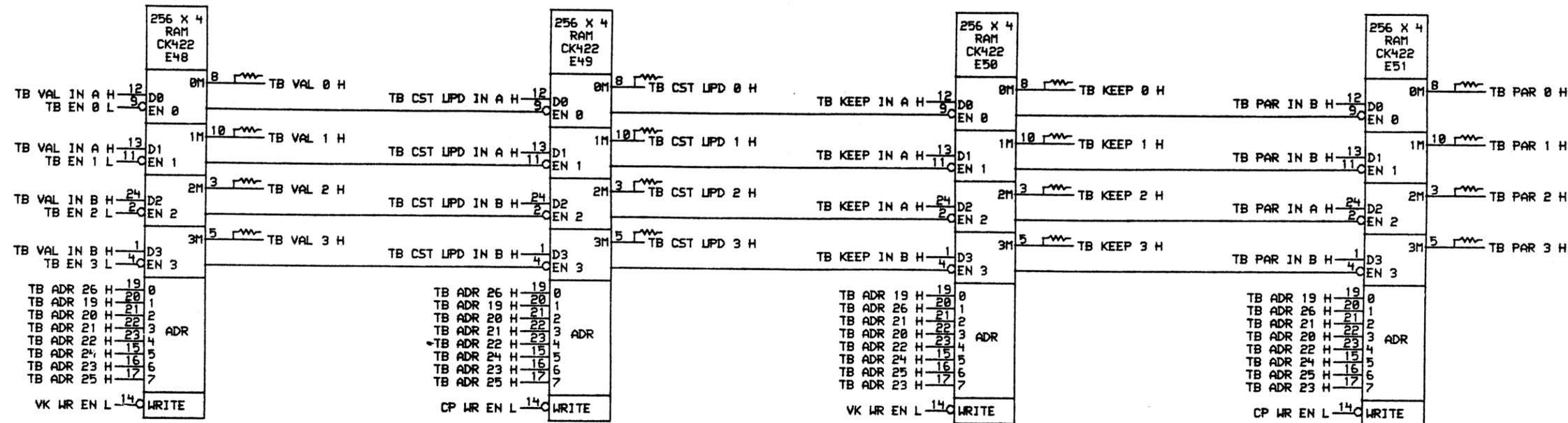


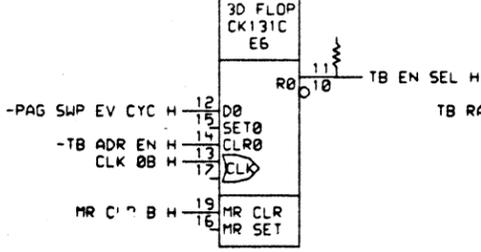
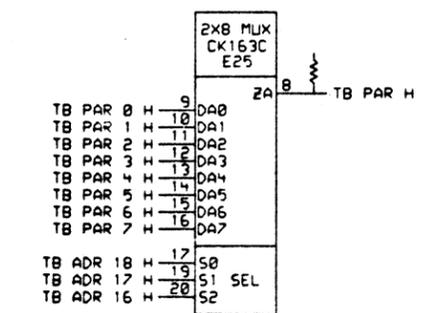
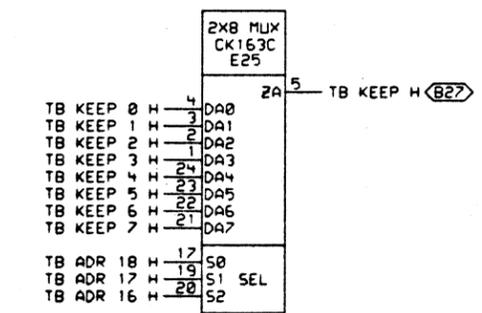
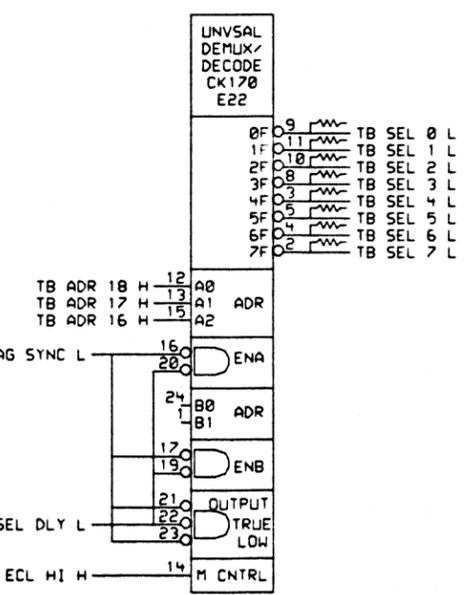
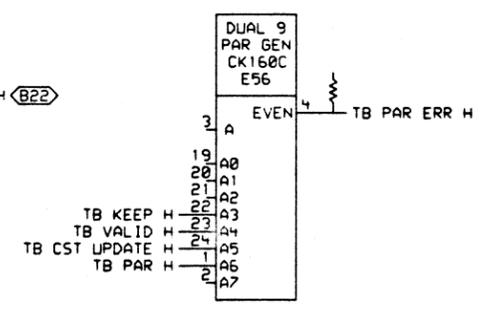
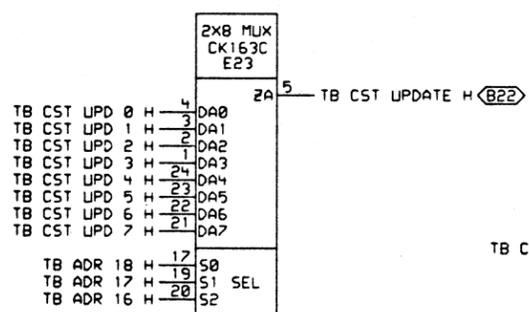
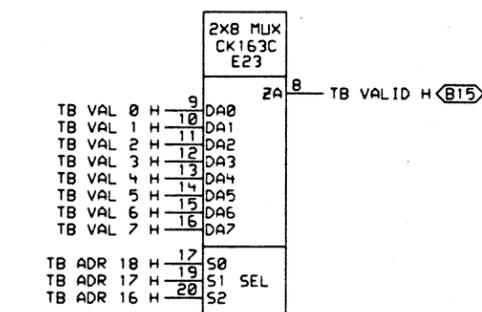
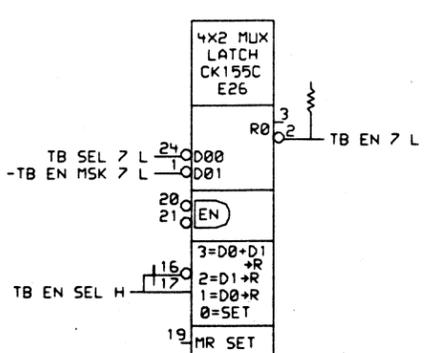
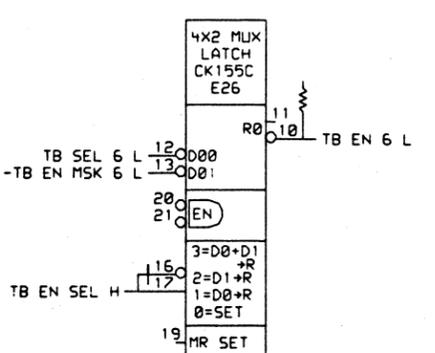
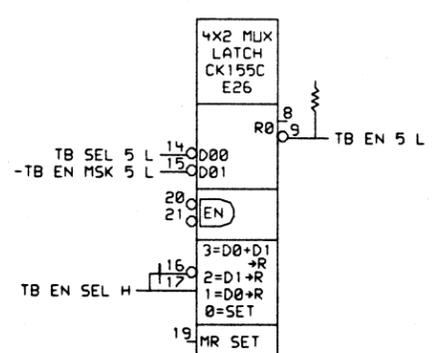
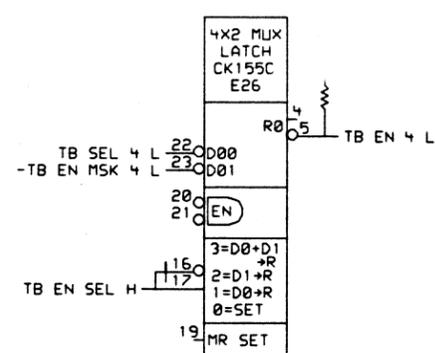
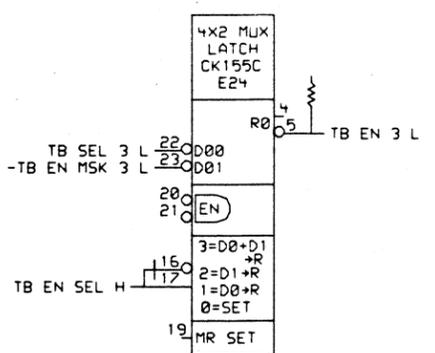
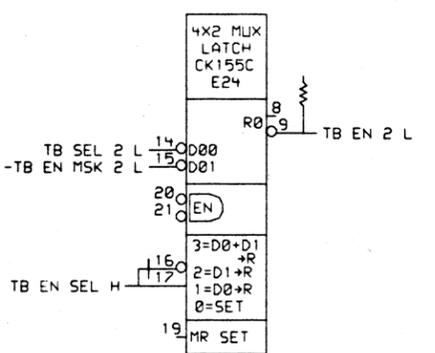
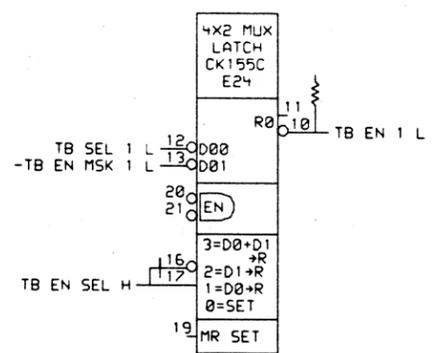
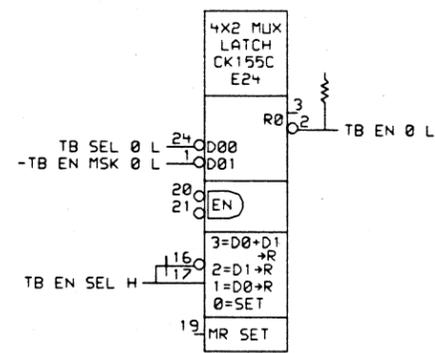
D  
 C  
 REV. #  
 NUMBER L0061-0-MMC6  
 CODE CS  
 SHEET 1 OF 1  
 B  
 A



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REVISIONS		DATE	ENG.	DATE	TITLE	SIZE	CODE	NUMBER	REV.
CHK	CHANGE NO.					D	CS	L0061-0-MMC7	*
		12-OCT-82			I/O ADR LATCH BITS 04-35 + P				
					BOARD LOCATION: NEXT HIGHER ASSEMBLY: B-DD-L0061-0				
					FIRST USED ON OPTION/MODEL: 2080				





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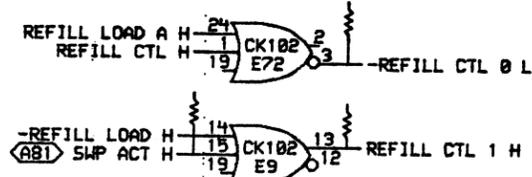
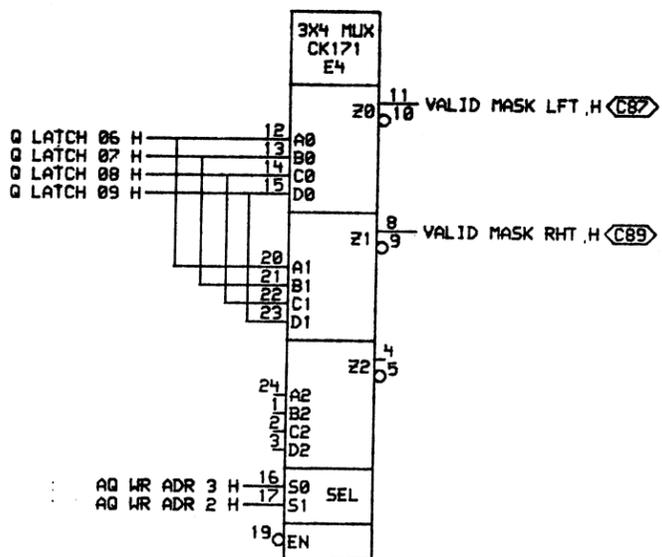
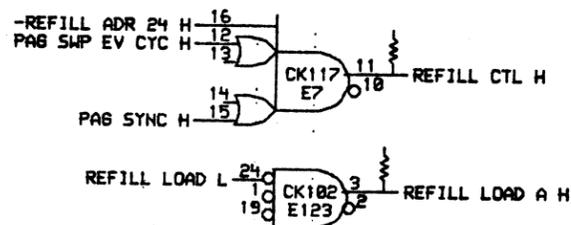
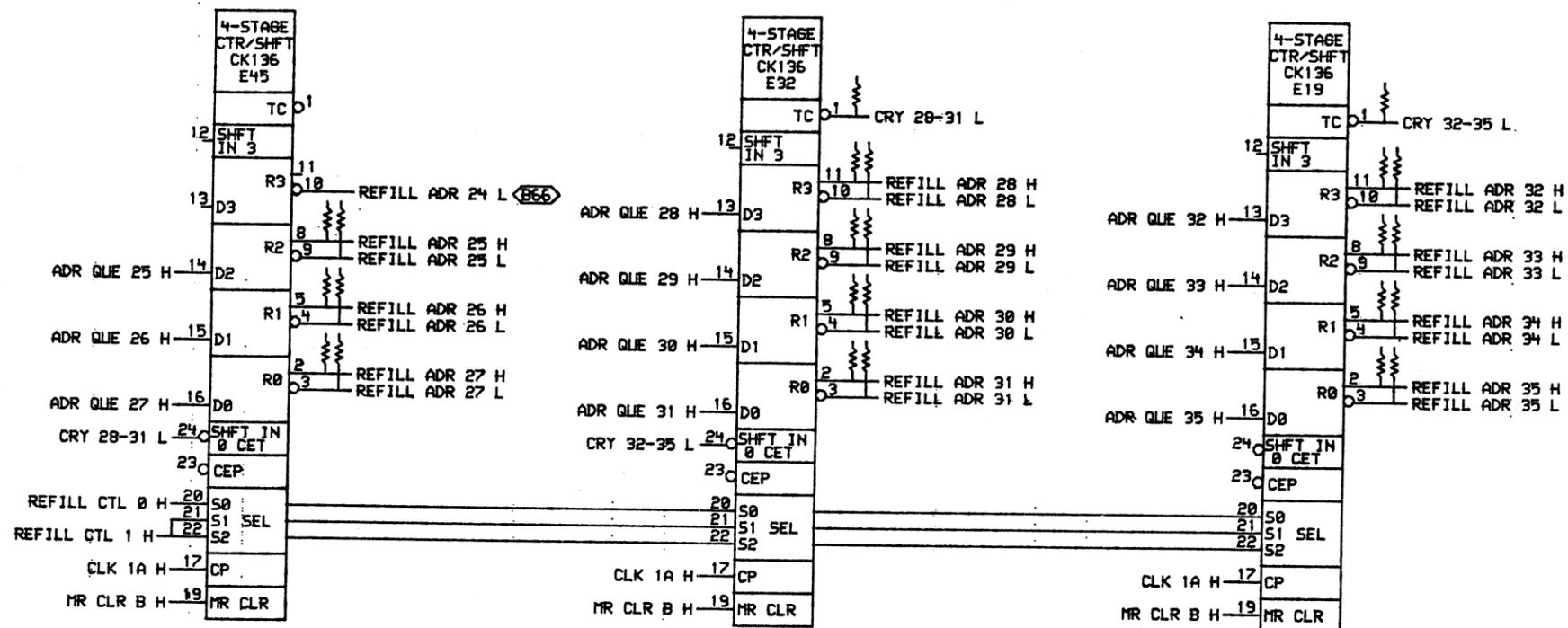
REVISIONS		
CHK	CHANGE NO.	REV

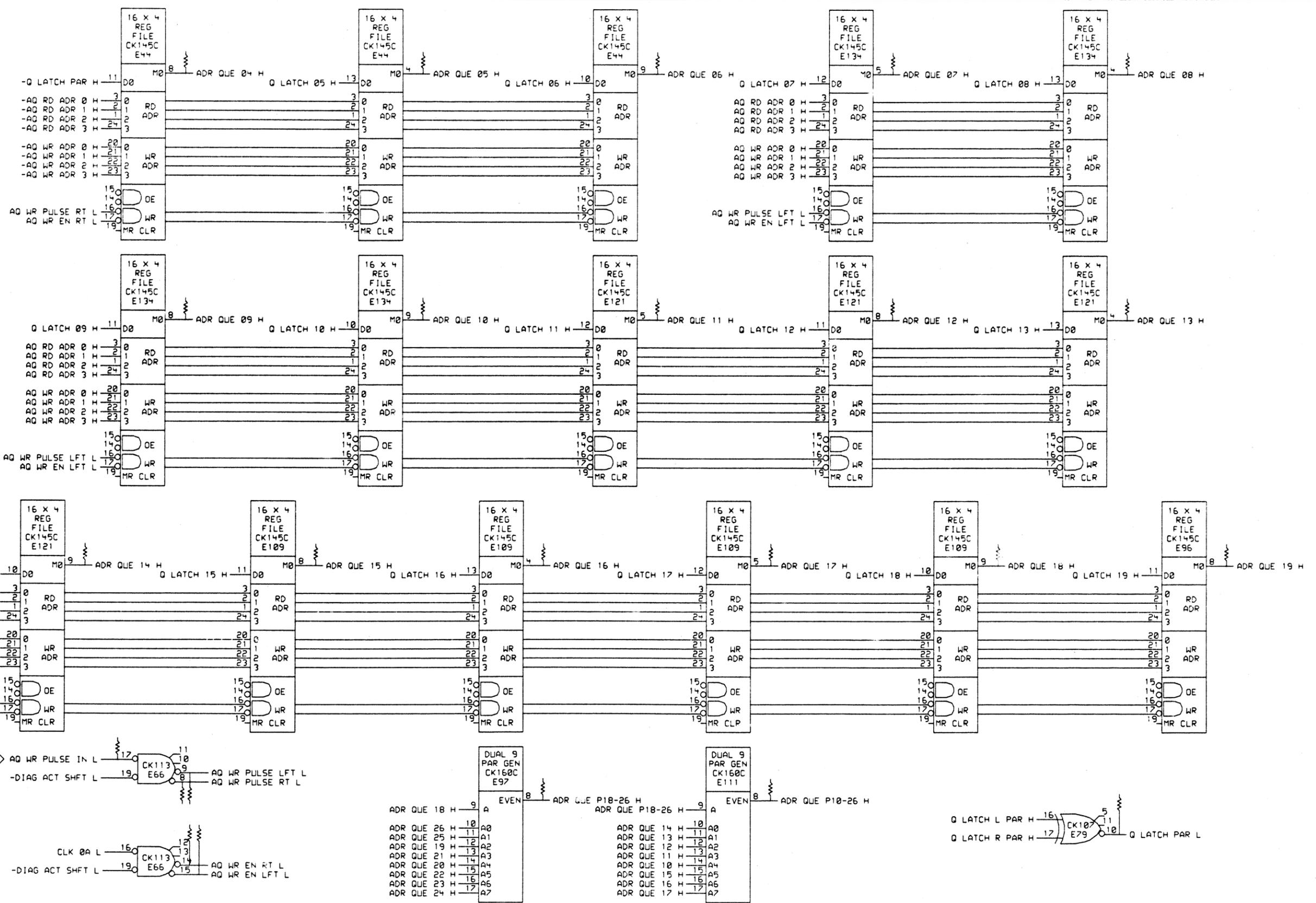
digital  
DATE: 30-DEC-82  
DATE: 29-DEC-82 12:57  
FIRST USED ON OPTION/MODEL: 2080

ENG. DATE: 30-DEC-82  
BOARD LOCATION: SHEET 2 OF 2  
NEXT HIGHER ASSEMBLY: B-DD-L0061-0

TITLE: TB CLR

SIZE CODE: D CS  
NUMBER: L0061-0-MMC9  
REV. \*

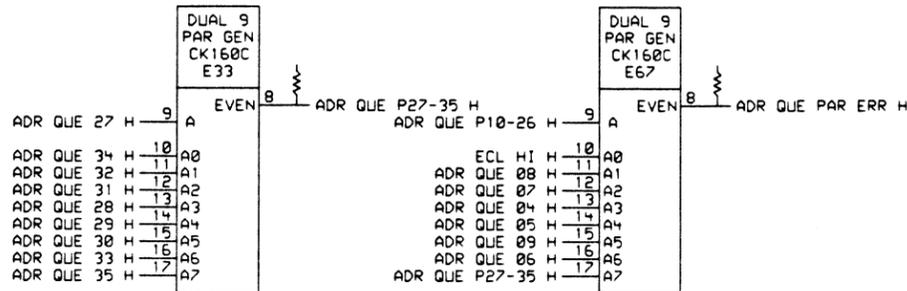
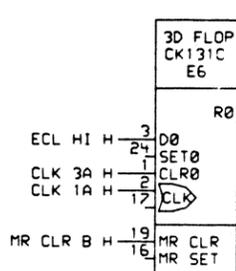
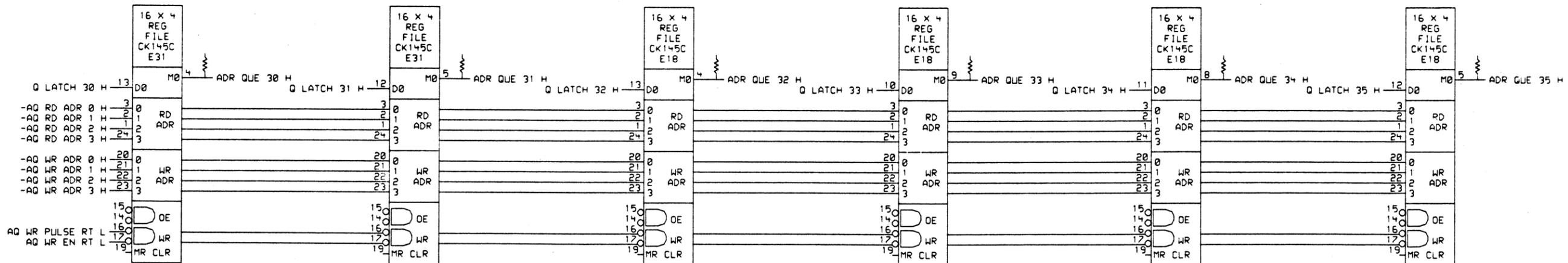
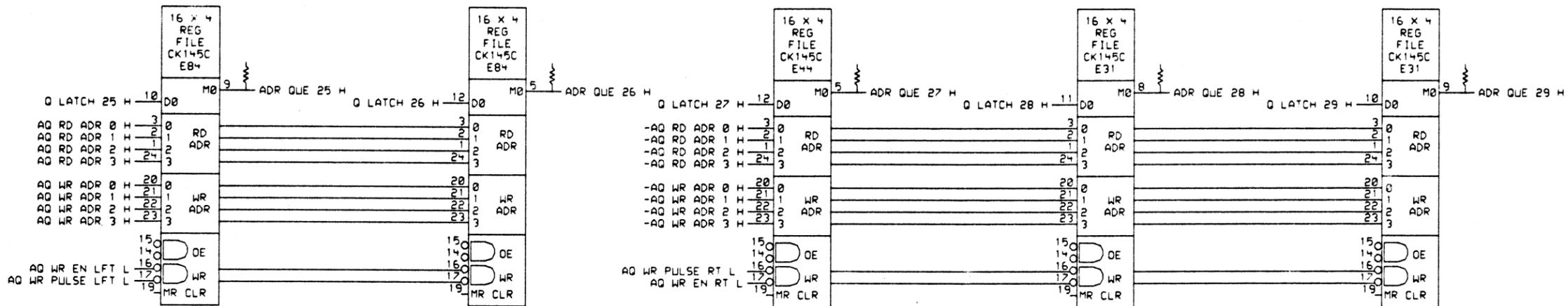
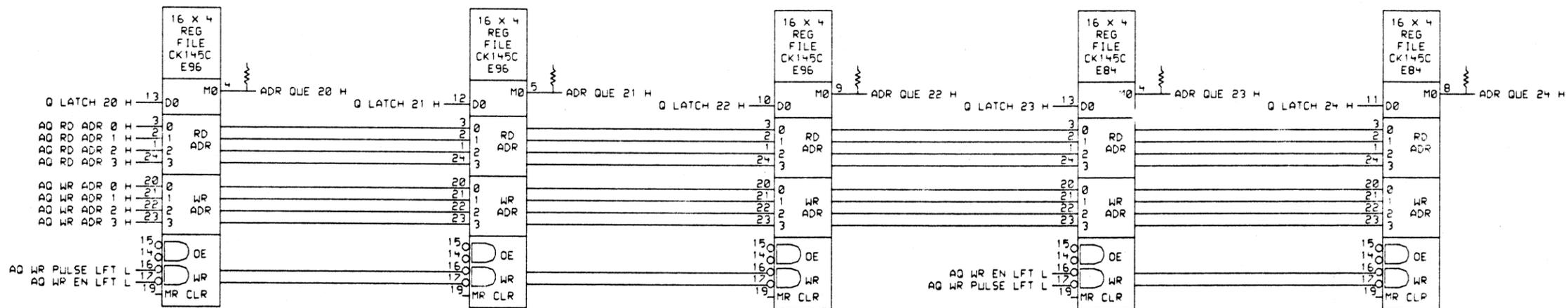




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CHK	CHANGE NO.	REV

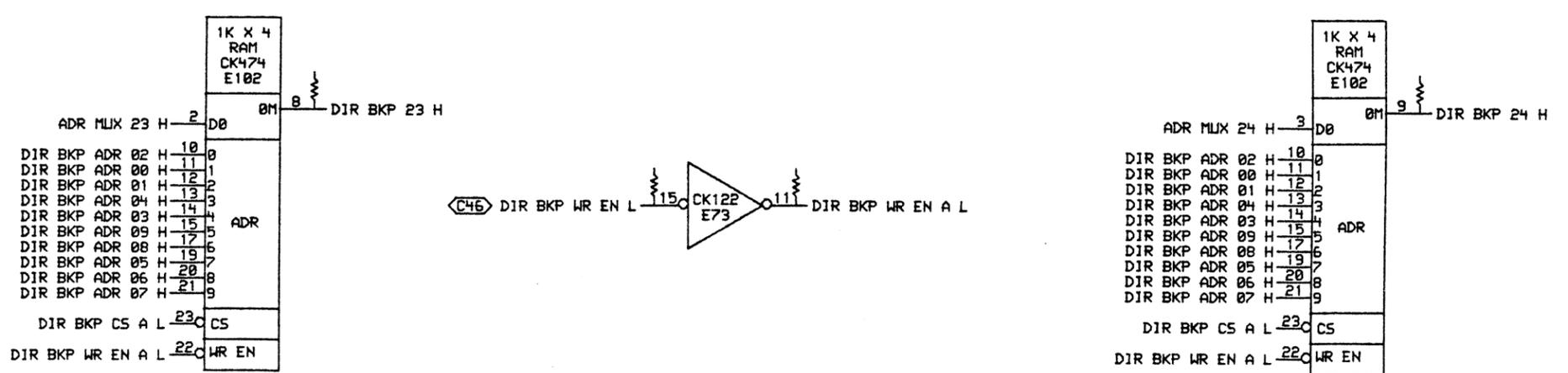
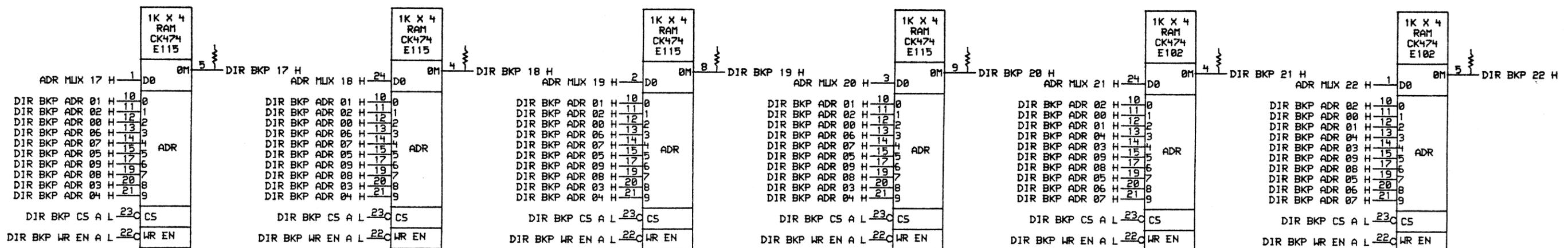
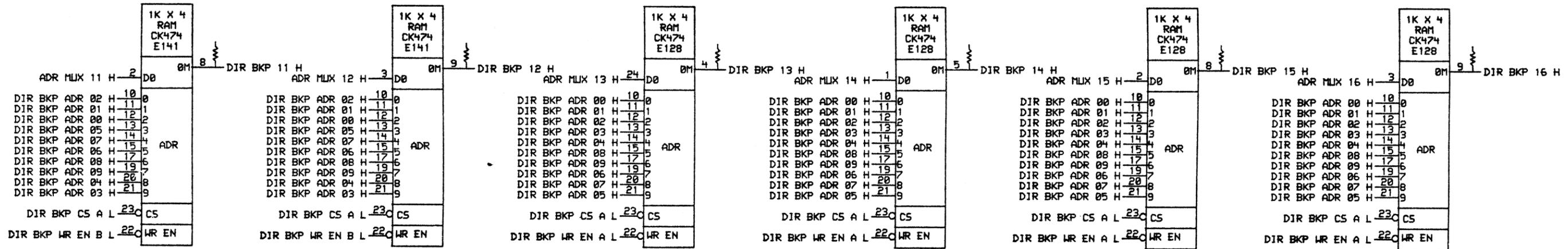
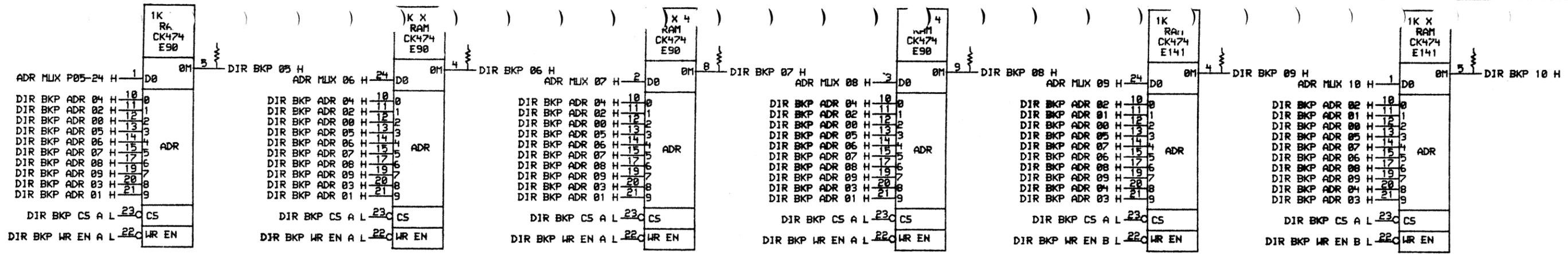
DATE	28-DEC-82	ENG.	DATE		TITLE:	ADR QUE BITS
DATE		BOARD LOCATION:	SHEET	1	OF	2
DATE	27-DEC-82 15:23	NEXT HIGHER ASSEMBLY:	SIZE	D	CODE	CS
FIRST USED ON OPTION/MODEL:	2880	B-DD-L0061-0	NUMBER	L0061-0-MMCB	REV.	*



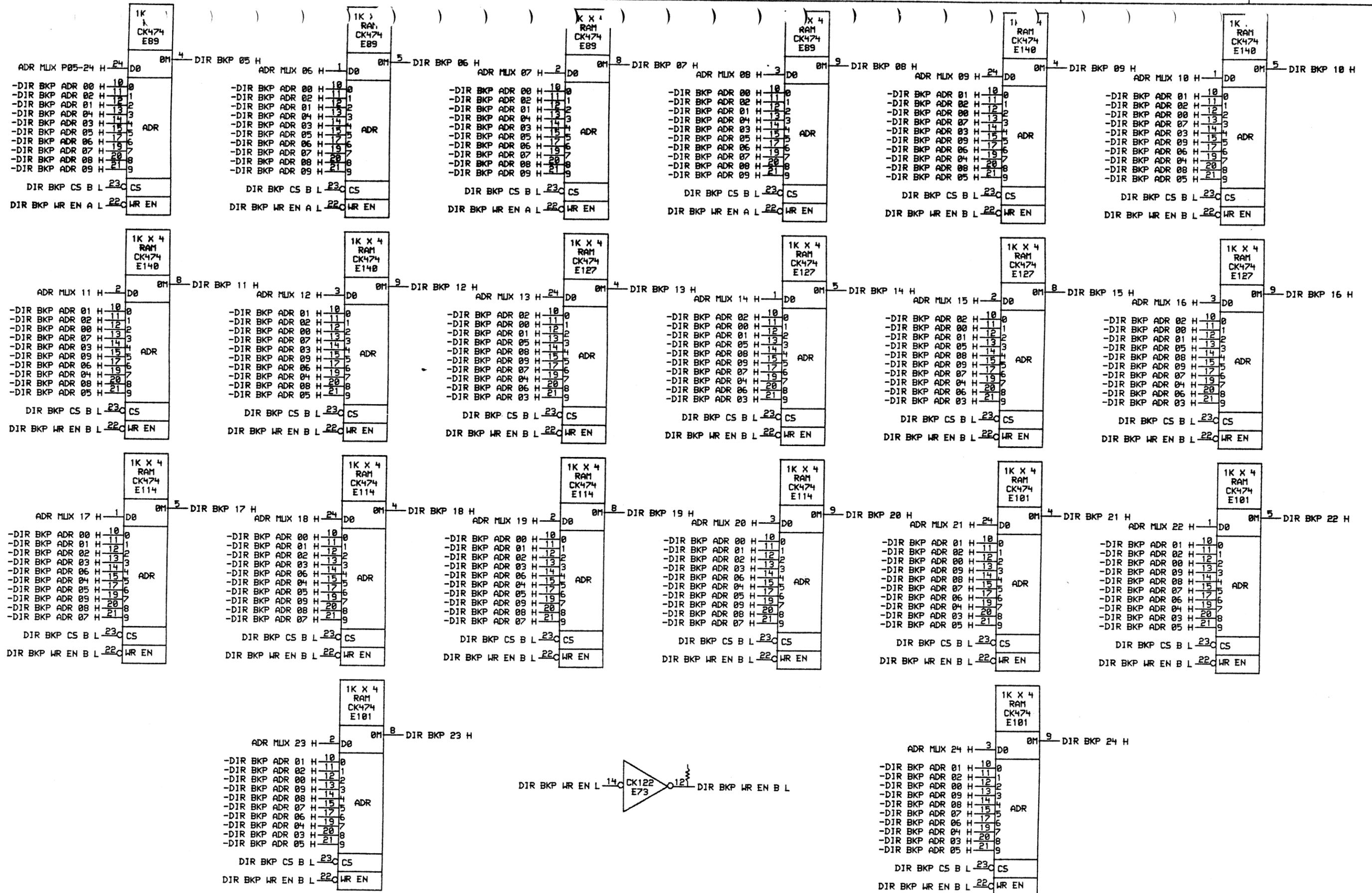
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CHK	CHANGE NO.	REV

digital  
 DATE: 28-DEC-82  
 ENG. DATE: 27-DEC-82 15:20  
 BOARD LOCATION: SHEET 2 OF 2  
 NEXT HIGHER ASSEMBLY: B-DD-L0061-0  
 TITLE: ADR QUE BITS 20 - 35  
 SIZE CODE: D CS  
 NUMBER: L0061-0-MMCC  
 REV. \*

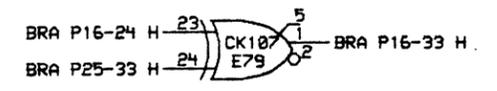
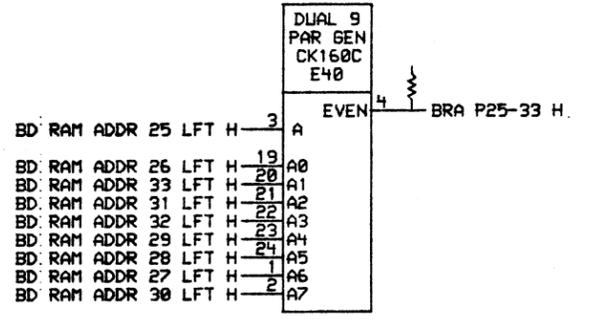
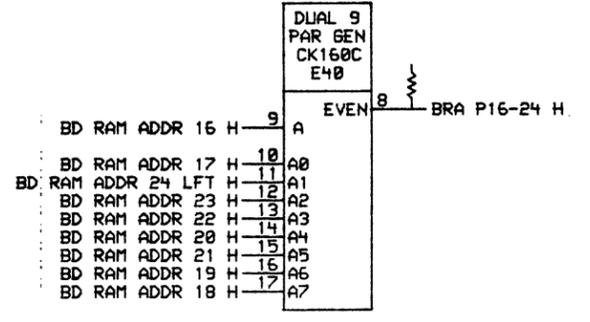
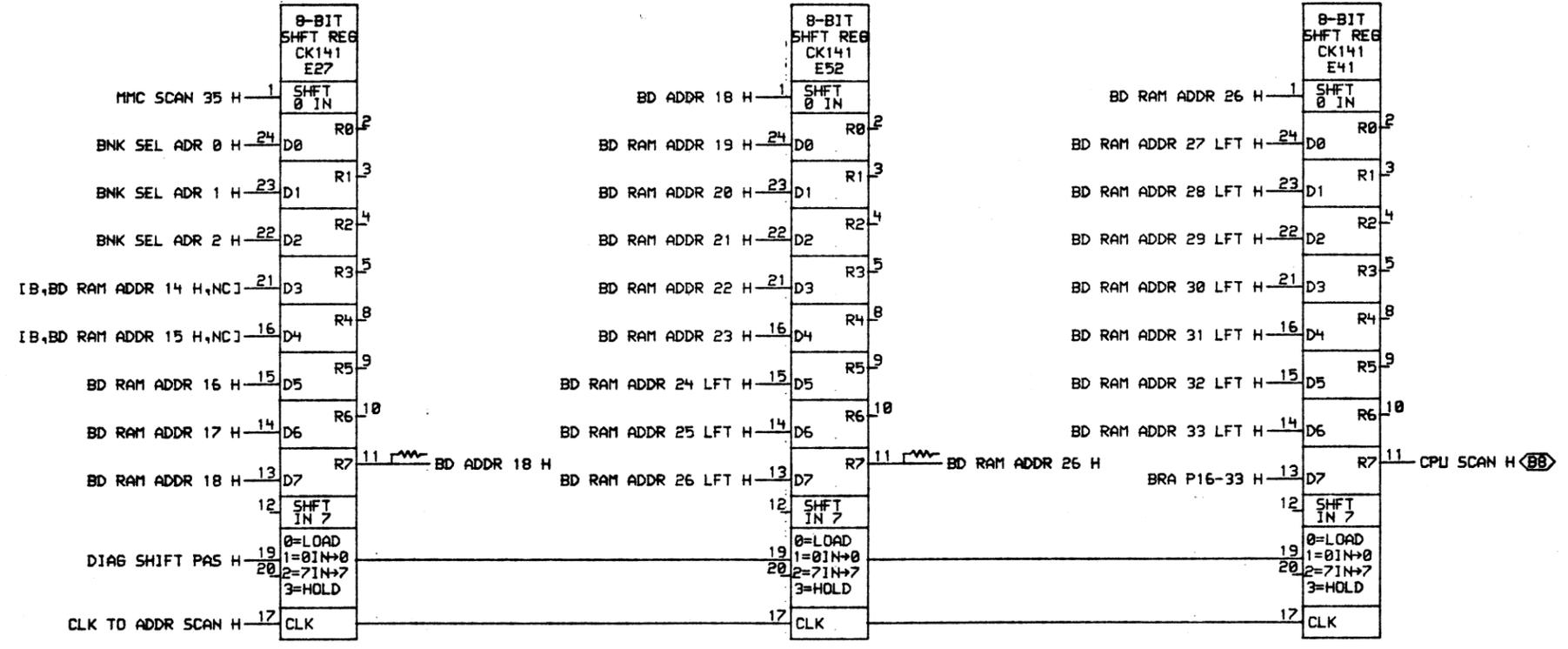


DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		DIR BACKUP
		SHEET	OF	BITS 05-24
VULCANI C2000-L006125MMCD-DRW	01-FEB-82 14:37	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0061-0	D	CS
			NUMBER	REV.
			L0061-0-MMCD	*



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	02-FEB-82			DIR BACKUP
		BOARD LOCATION:		05-24
		SHEET 2 OF 2		
VIL CAN (2000-L0061) 2000-DRH	01-FEB-82 14:35	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0061-0	D	CS
			NUMBER	REV.
			L0061-0-MMCE	*

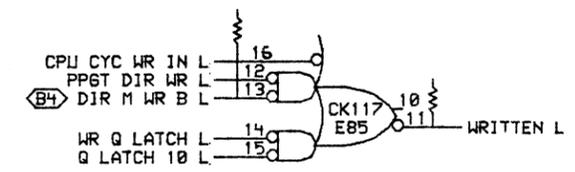
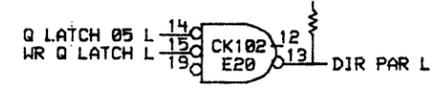
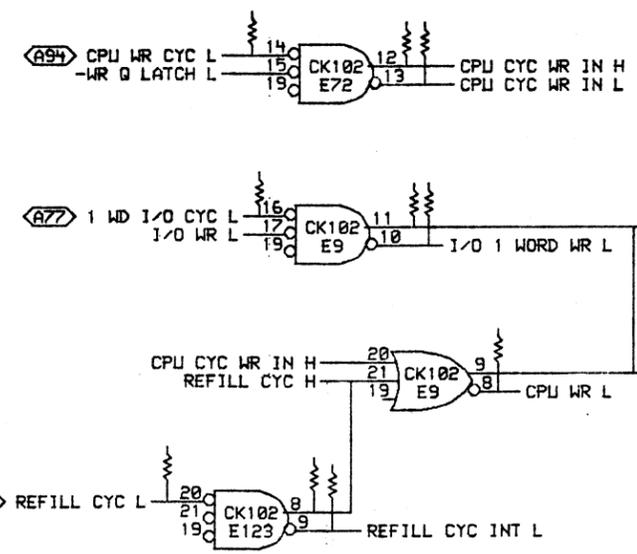
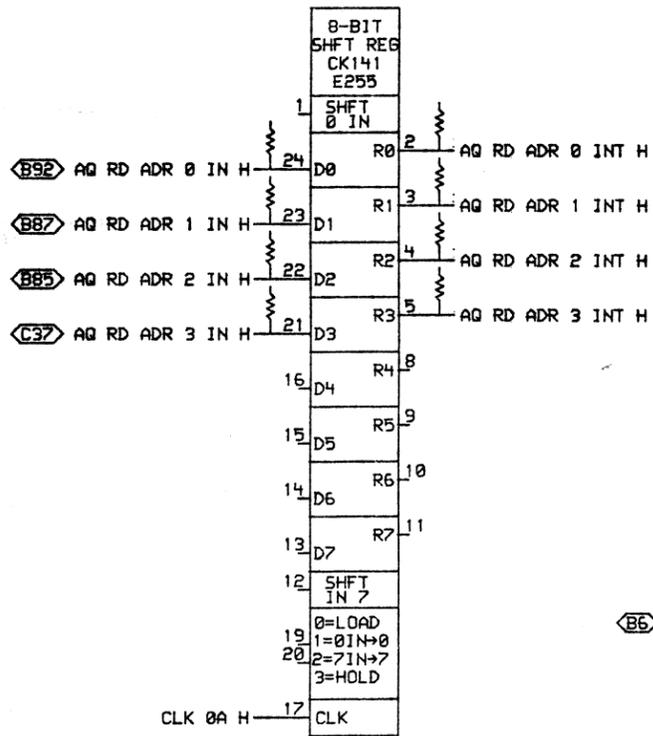
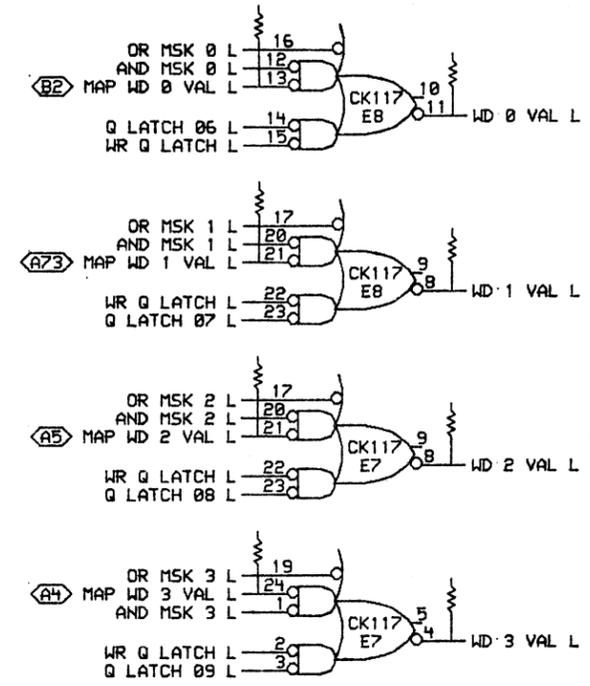
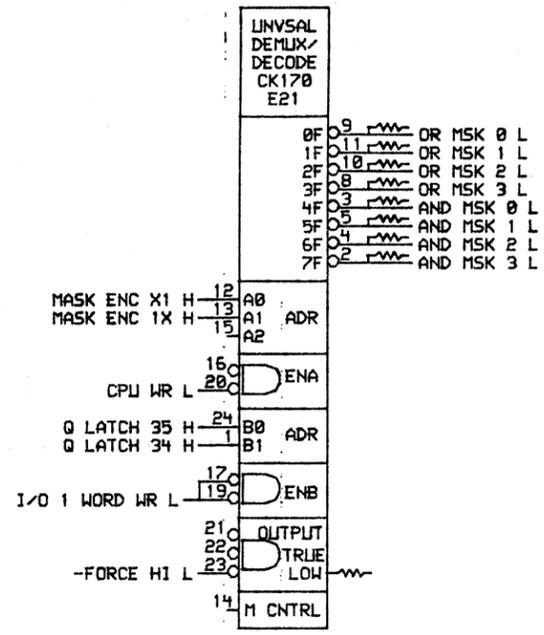
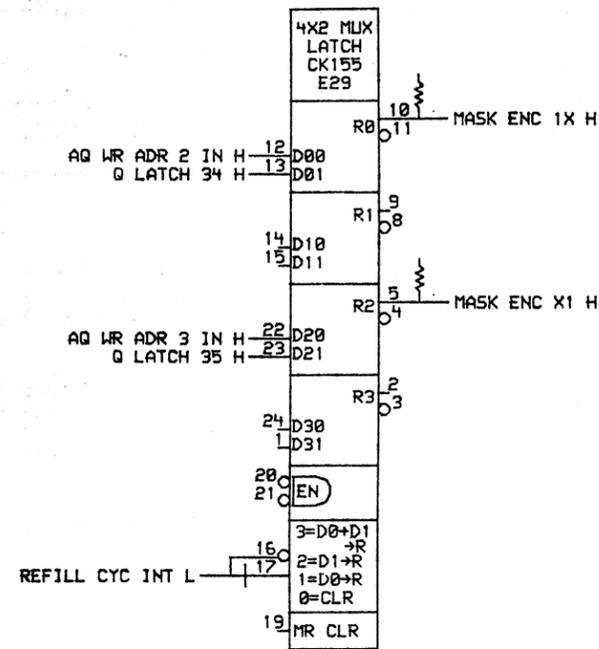




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REVISIONS		
CHK	CHANGE NO.	REV.

digital	DATE	ENG.	DATE	TITLE:
	04-OCT-82			
CHK'D.	DATE	BOARD LOCATION:	SHEET 1 OF 1	
VILCANI <2080.L0061> 51MMCS.DRW	03-OCT-82 15:27	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: 2080	B-DD-L0061-0		D	CS
			NUMBER	REV.
			L0061-0-MMCG	*



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REVISIONS	
CHK	CHANGE NO. REV

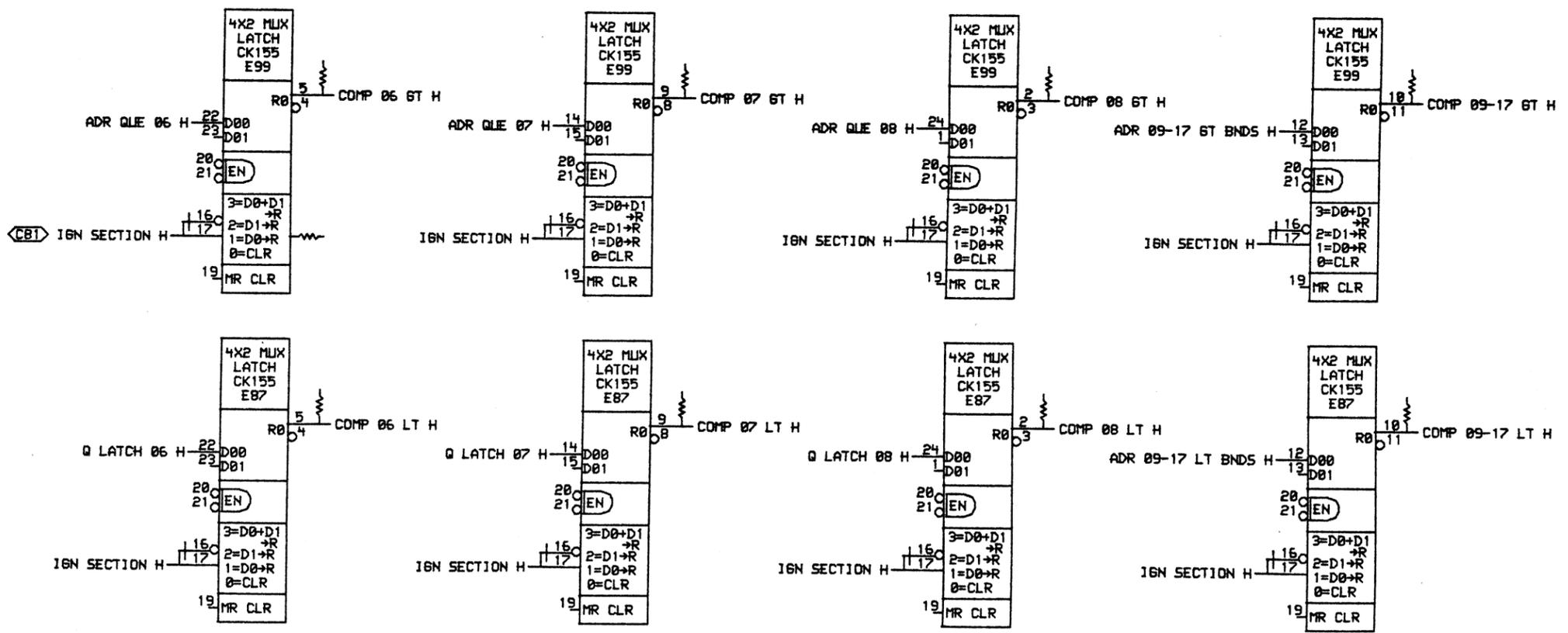
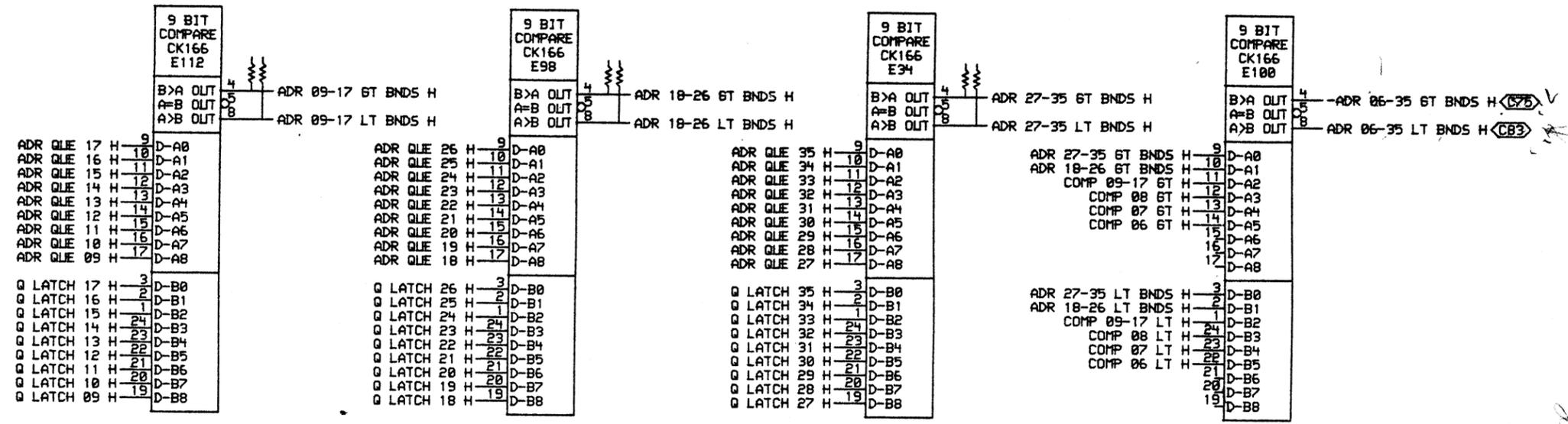
8	7	6	5	4	3	2	1
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digital	DATE	ENG.	DATE	TITLE:
	08-SEP-82			VALID & WRITTEN BITS
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
	107-SEP-82 11:39	NEXT HIGHER ASSEMBLY:	1	1
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0061-0		

SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMCH	*

REV. \* NUMBER L0061-0-MMCH SIZE CODE CS D B A

A = Bound  
B = Ref Adr



B=R  
A=cmp

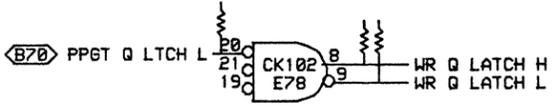
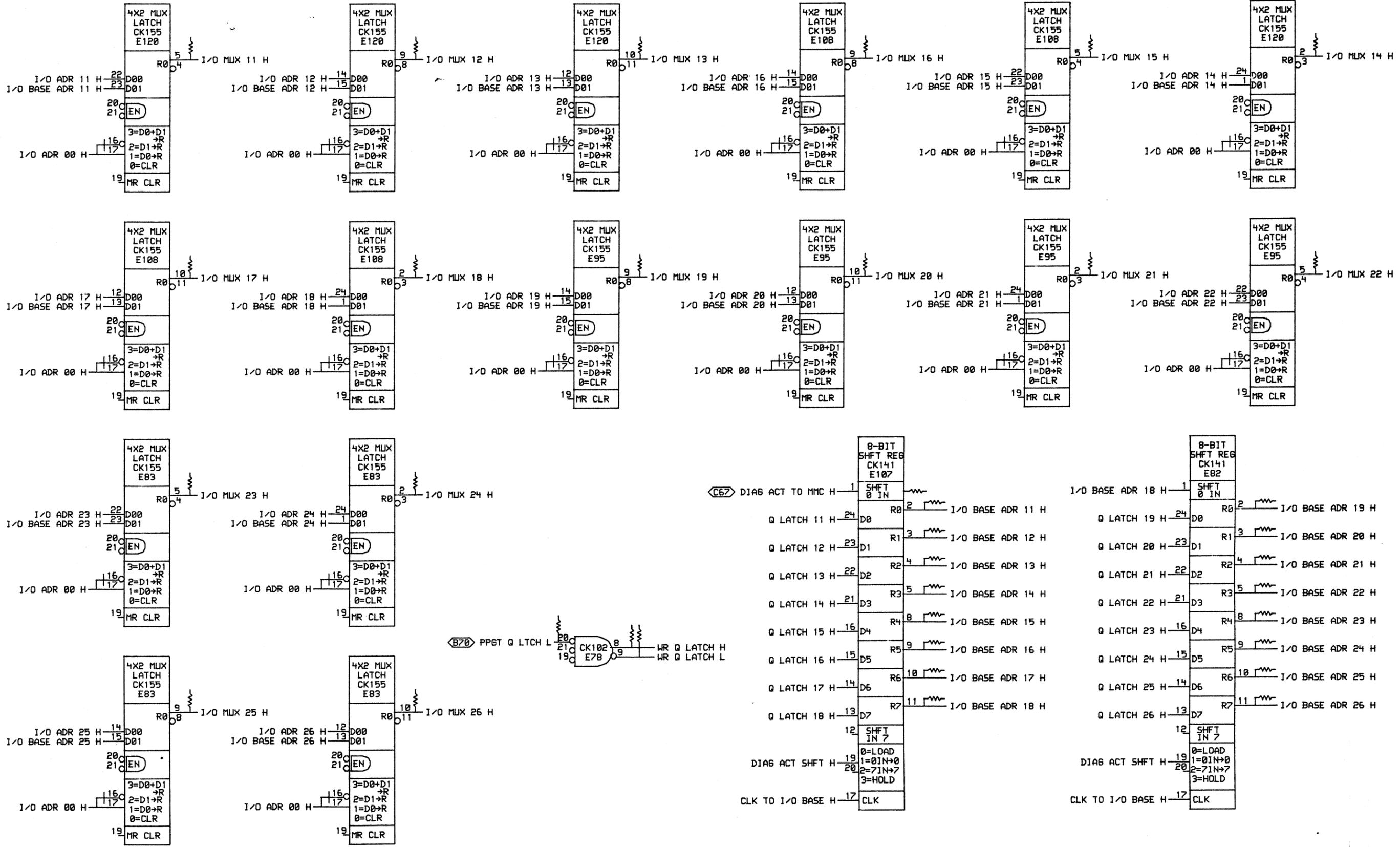
ADR ≤ END ≤

↓  
BND ≤ ADR ≤ Bnd  
↑

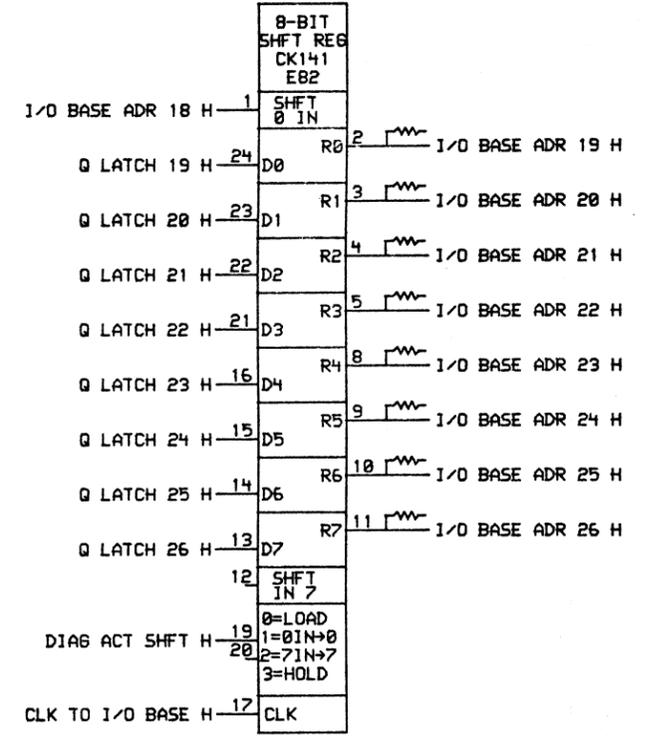
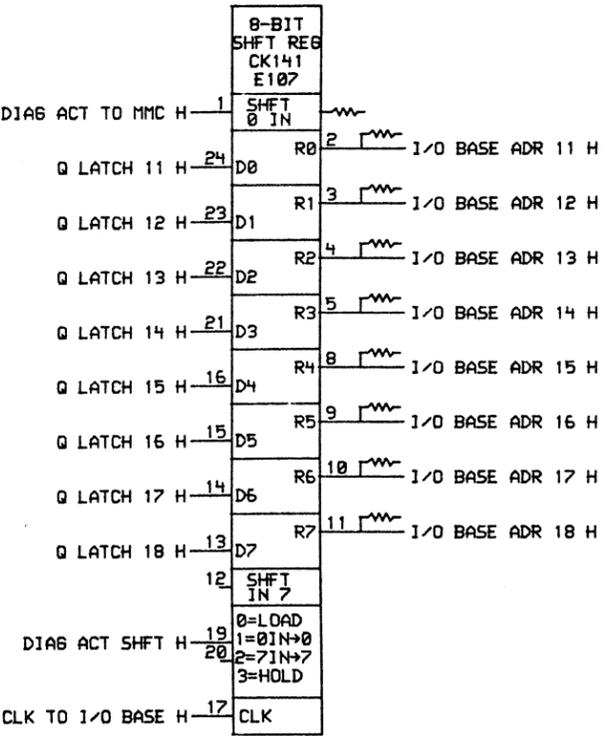
A > B ⇒ BND > ADR  
⇒ ADR ≤ BND

B > A ⇒ ADR > BND  
⇒ BND ≤ ADR

DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	02-FEB-82			ADR BRK
	DATE	BOARD LOCATION:		COMPARATORS
		SHEET	OF	
FIRST USED ON OPTION/MODEL: 2080				SIZE CODE
NEXT HIGHER ASSEMBLY: B-DD-L0061-0				NUMBER
				L0061-0-MMCI
				REV. *



(C67) DIAG ACT TO MMC H

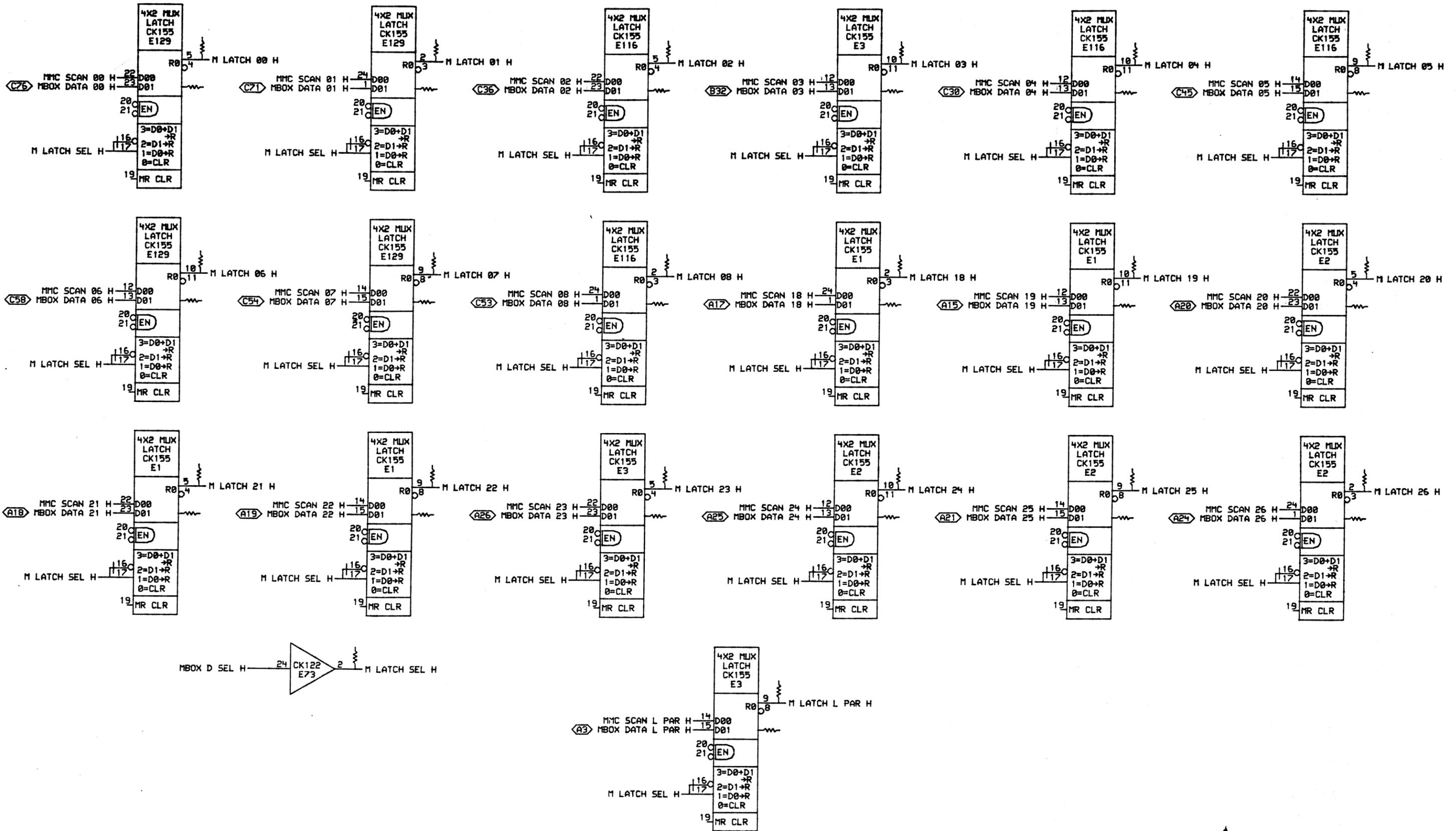


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REVISIONS	
CHK	CHANGE NO. REV.

digital	DATE	ENG.	DATE	TITLE:
	27-APR-82			I/O BASE ADR 11-26
	DATE	BOARD LOCATION:		
		SHEET	OF	
FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0061-0		

SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMCJ	*



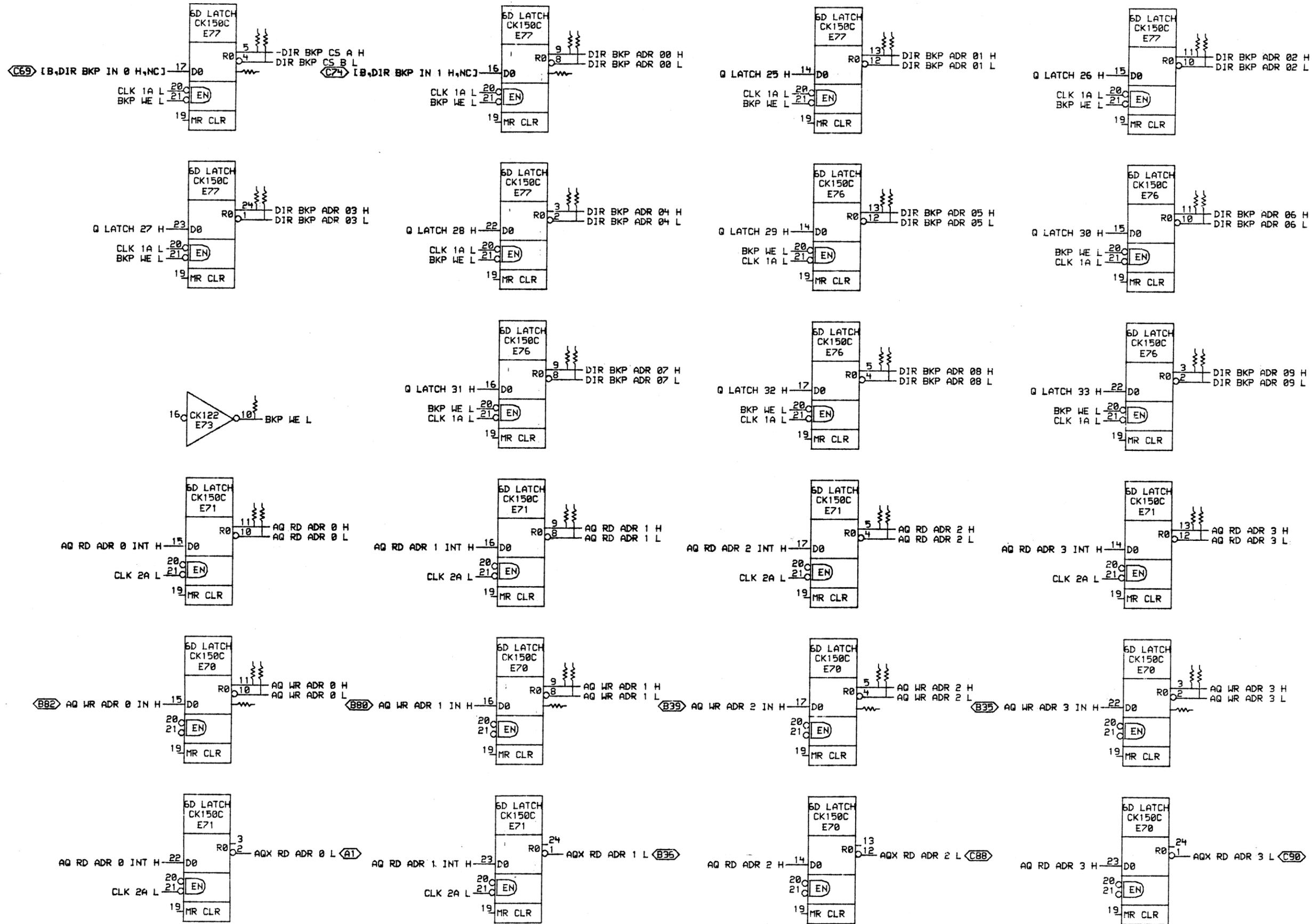
REV. #  
NUMBER L0061-0-MMCK  
CODE CS  
SIZE D  
B  
A

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REVISIONS	
CHK	CHANGE NO. REV

8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---

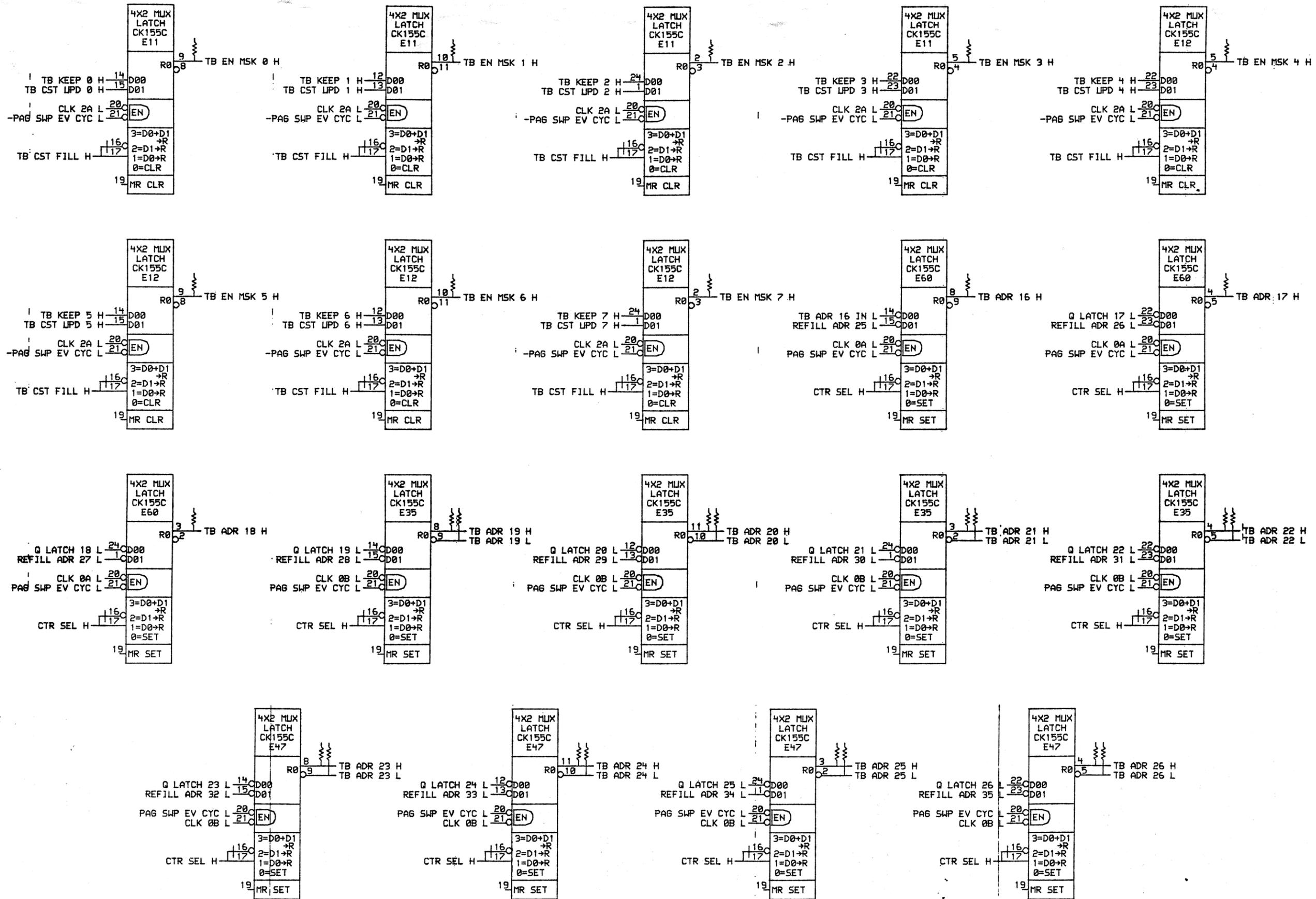
digital	DATE	ENG.	DATE	TITLE:	M LATCH 00-08 & 18-26		
	CHK'D.						
VULCAN (2000-L0061-MMCK.DRW) 08-AUG-82 14:01		NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0061-0		D	CS	L0061-0-MMCK	*



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REVISIONS		
CHK	CHANGE NO.	REV.

	DATE	ENG.	DATE	TITLE:
	28-SEP-82			
DATE	BOARD LOCATION:			
	SHEET 1 OF 1			
FIRST USED ON OPTION/MODEL: 2080				NEXT HIGHER ASSEMBLY: B-DD-L0061-0
SIZE	CODE	NUMBER	REV.	
D	CS	L0061-0-MMCL	*	



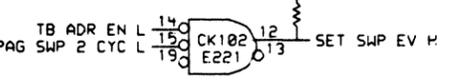
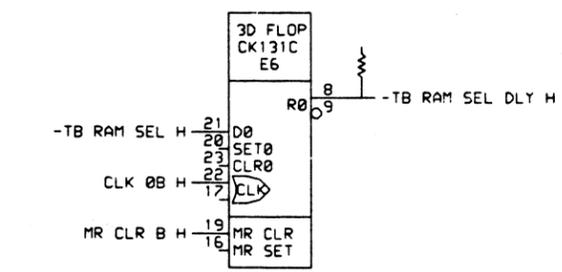
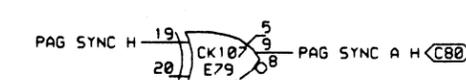
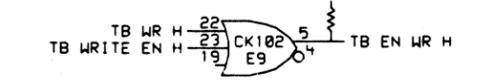
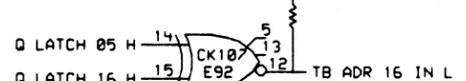
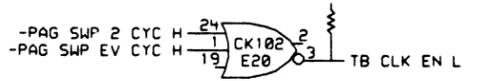
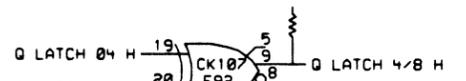
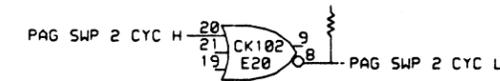
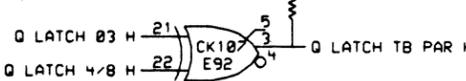
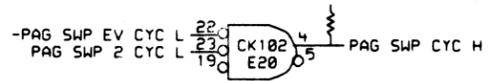
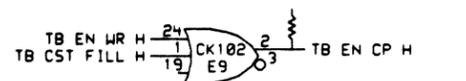
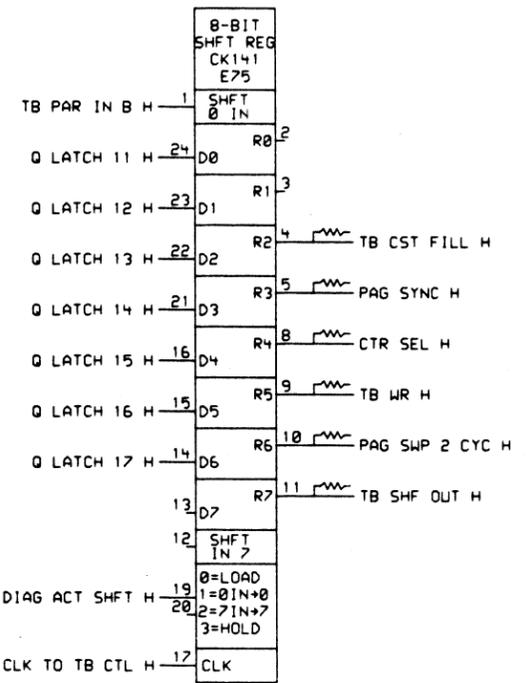
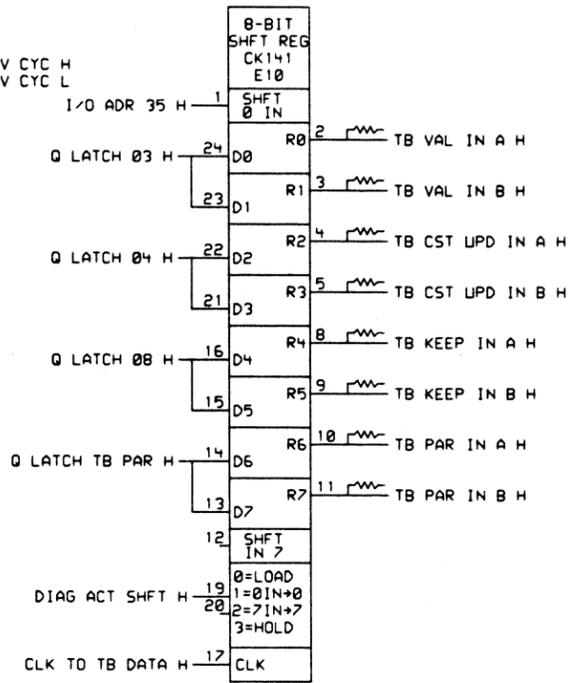
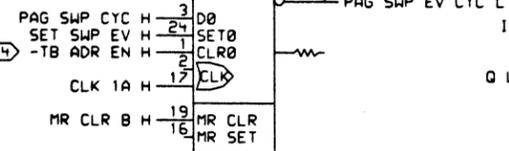
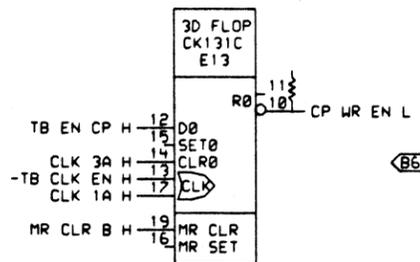
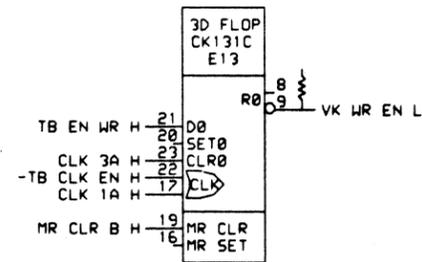
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REVISIONS	
CHK	CHANGE NO. REV

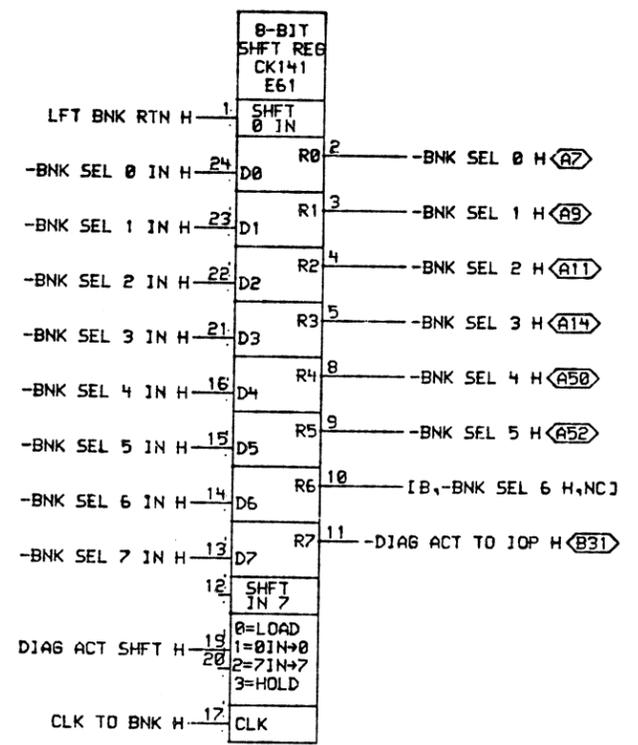
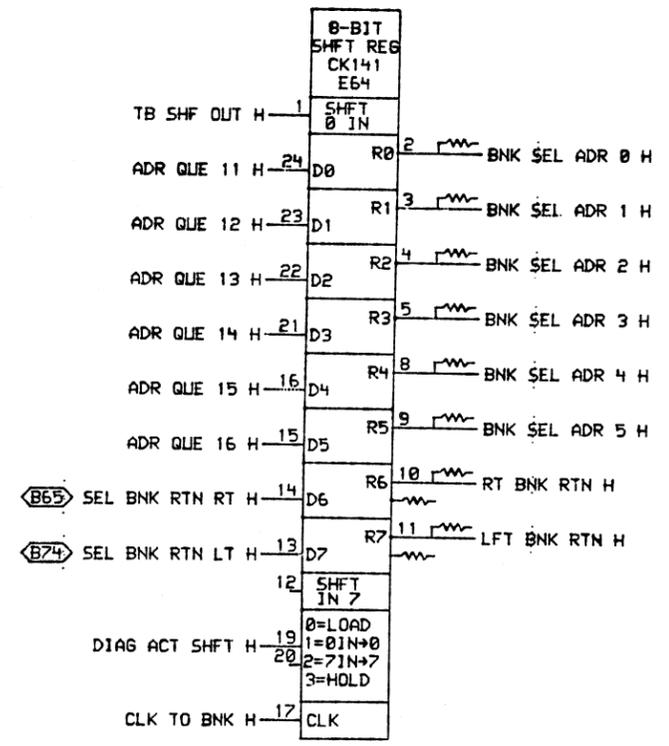
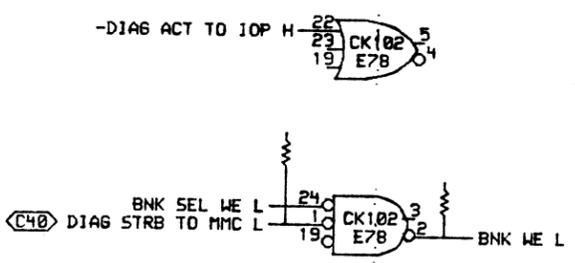
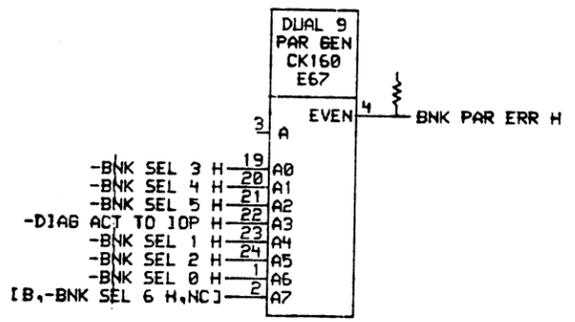
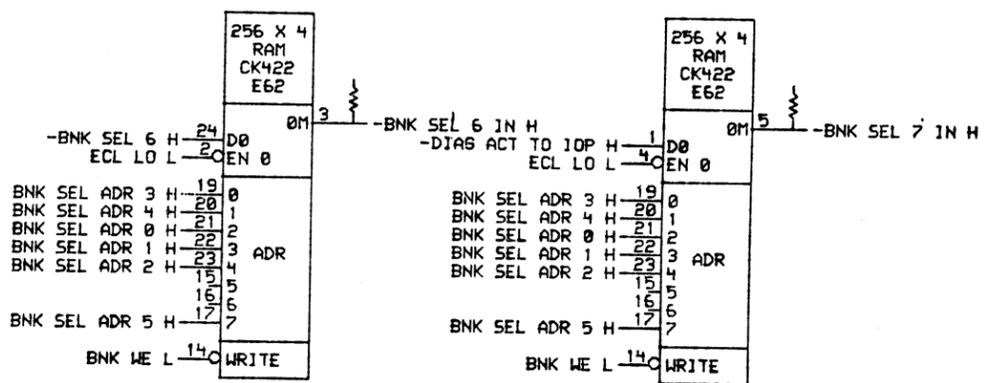
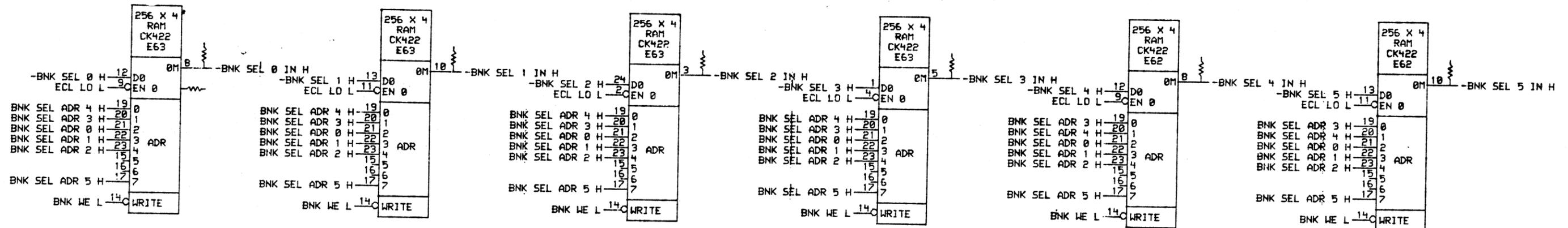
digital  
 DATE 03-NOV-82  
 DATE 02-NOV-82 20:46  
 BOARD LOCATION: SHEET 1 OF 1  
 NEXT HIGHER ASSEMBLY: B-DD-L0061-0  
 FIRST USED ON OPTION/MODEL: 2080

TITLE:	TB ADR BITS 16-26
SIZE	D
CODE	CS
NUMBER	L0061-0-MMCM
REV.	*

REV. \*  
 NUMBER L0061-0-MMCM  
 CODE CS  
 SIZE D  
 B  
 A



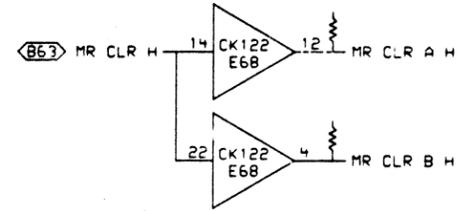
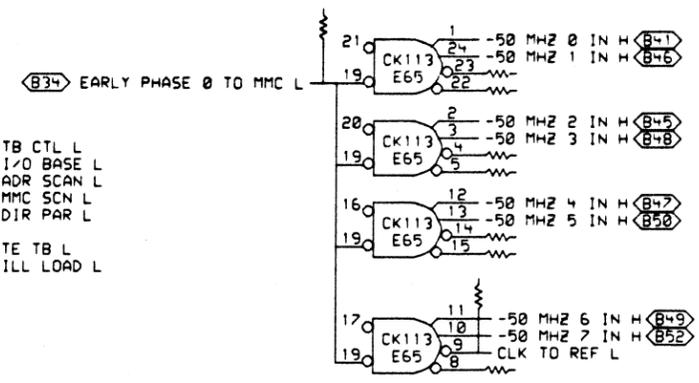
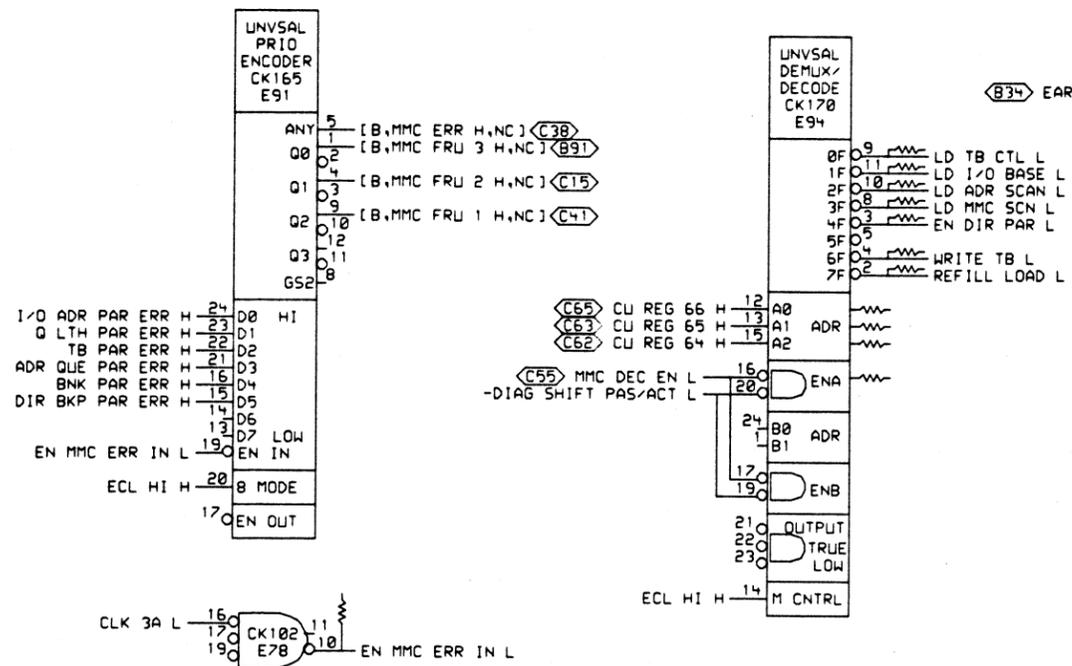
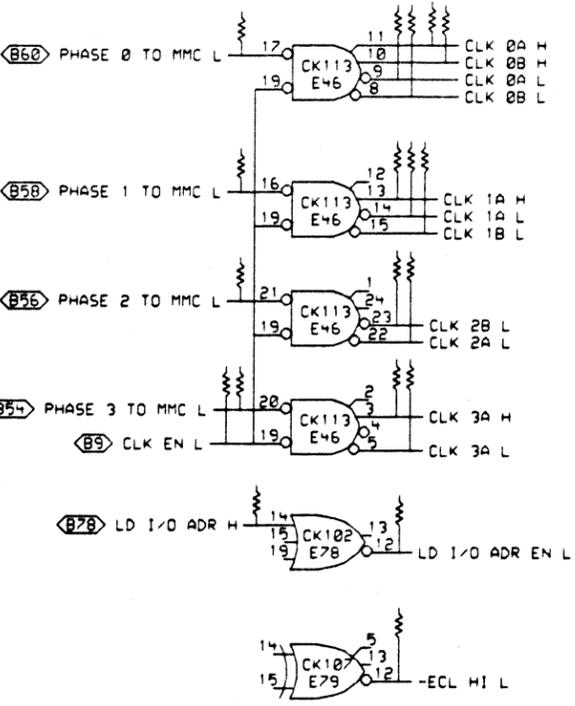
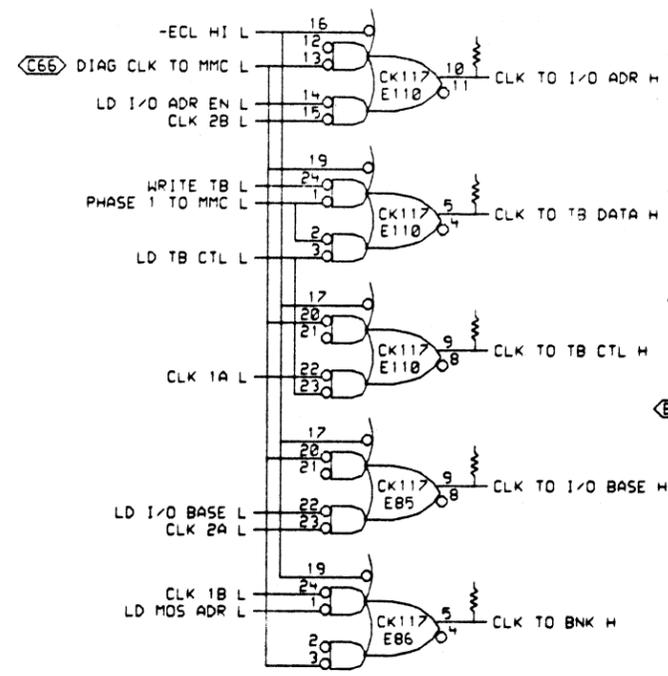
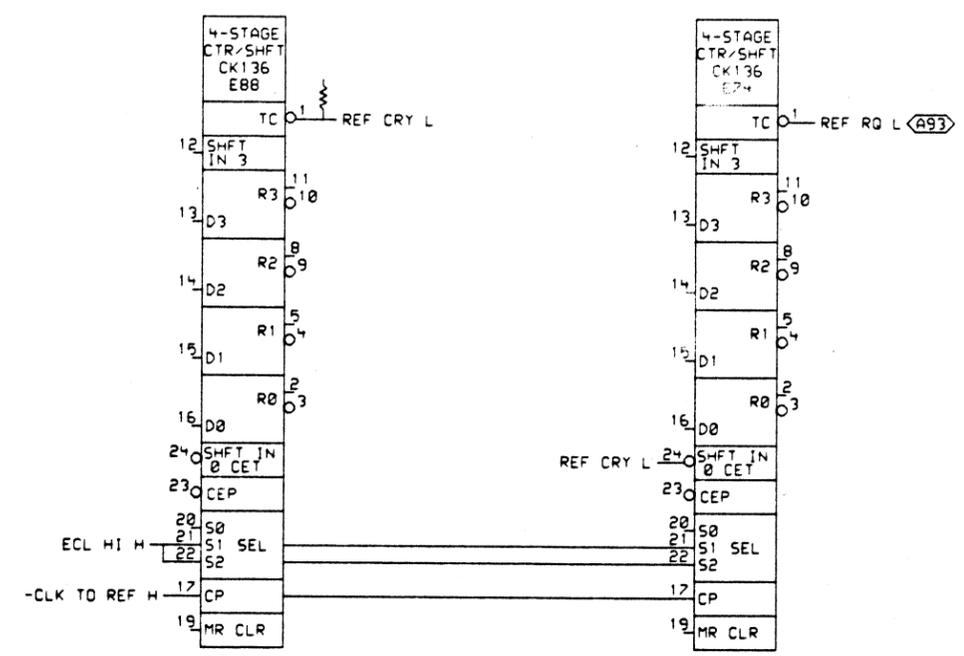
DRN.	DATE	ENG.	DATE	TITLE:	TB ADR CONTROL		
CHK'D.	22-NOV-82	DATE	BOARD LOCATION:				
ARCH: <UMLER.PLOTS>57MMCN.DRW 15-NOV-82 06:48				NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL: 2080				B-DD-L0061-0	D	CS	L0061-0-MMCN
							REV. *



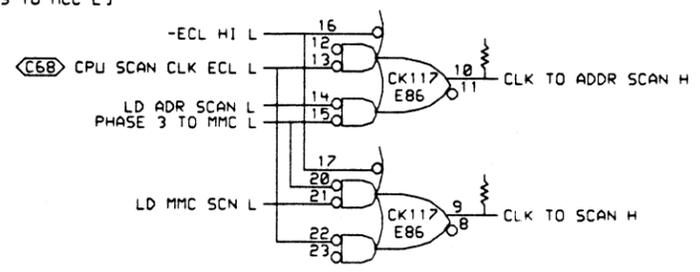
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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE:
	14-11-82			
CHK'D.	DATE	BOARD LOCATION:		
		SHEET		
VULCAN (2000 LAB 61) MMCP DRW 114-JUL-82 07:29		NEXT HIGHER ASSEMBLY:		SIZE CODE
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0061-0		NUMBER
				L0061-0-MMCP
				REV. *



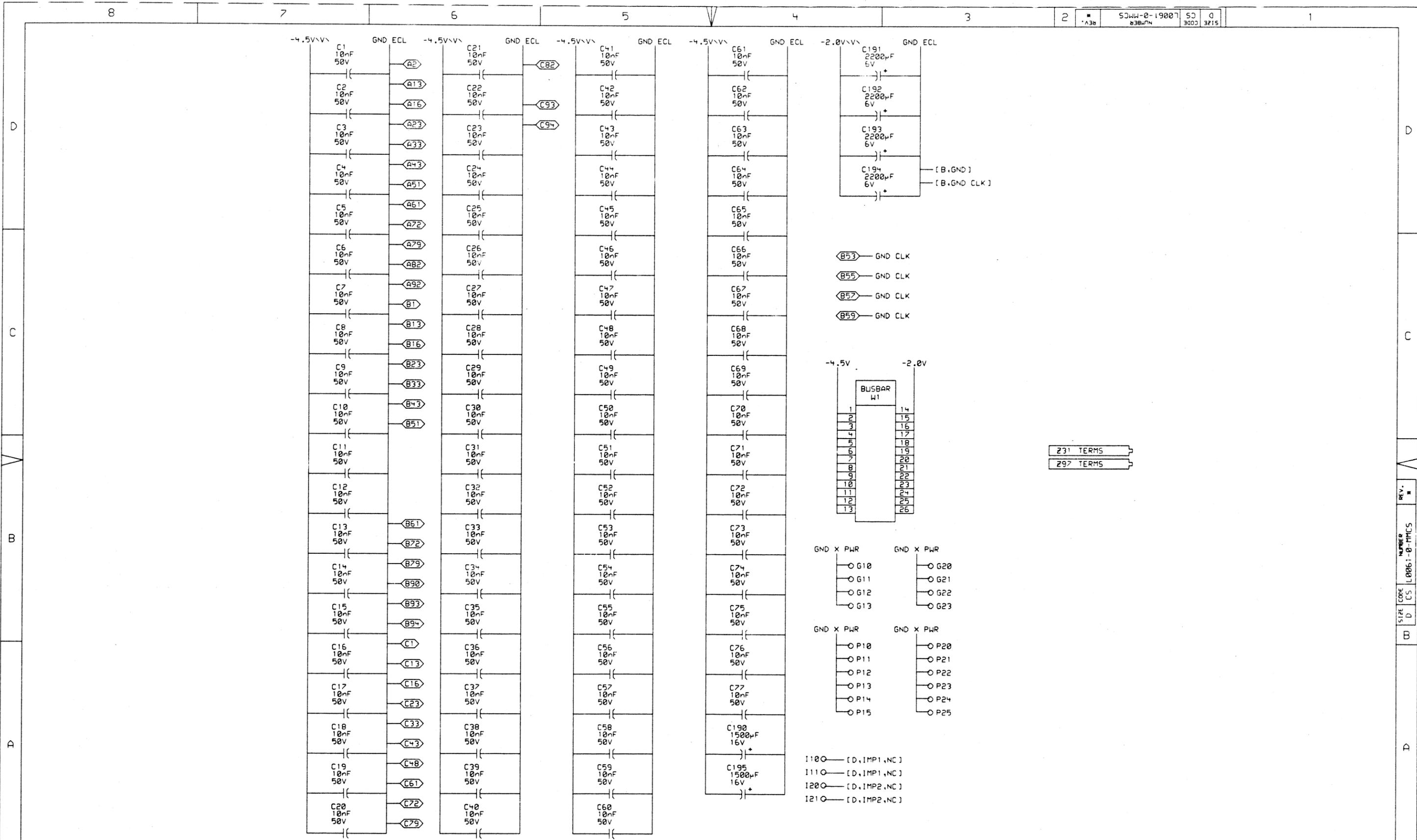
- (C77) [B,DIAG CLK TO MMC L,DIAG CLK TO MDP L]
- (C51) [B,CPU SCAN CLK ECL L,DIAG CLK PAS TO MCC L]



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DR. J. J. J.	DATE 28-DEC-82	ENG.	DATE	TITLE: CLK BUFFER
	CHK'D.	DATE	BOARD LOCATION:	SHEET 1 OF 1	
VULCAN: (2000.L0061)58MMCR.DRW 27-DEC-82 15:28		NEXT HIGHER ASSEMBLY: B-DD-L0061-0		SIZE CODE D CS	NUMBER L0061-0-MMCR
FIRST USED ON OPTION/MODEL: 2000					

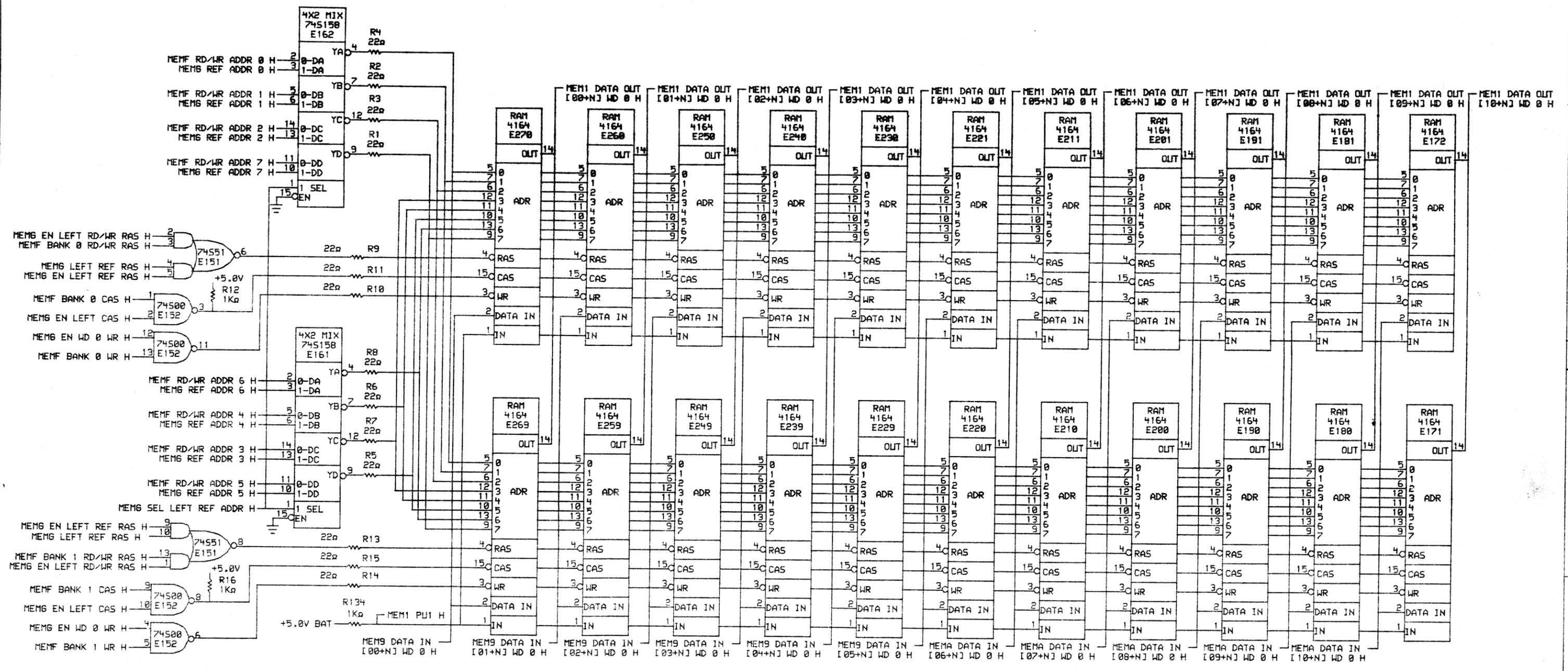


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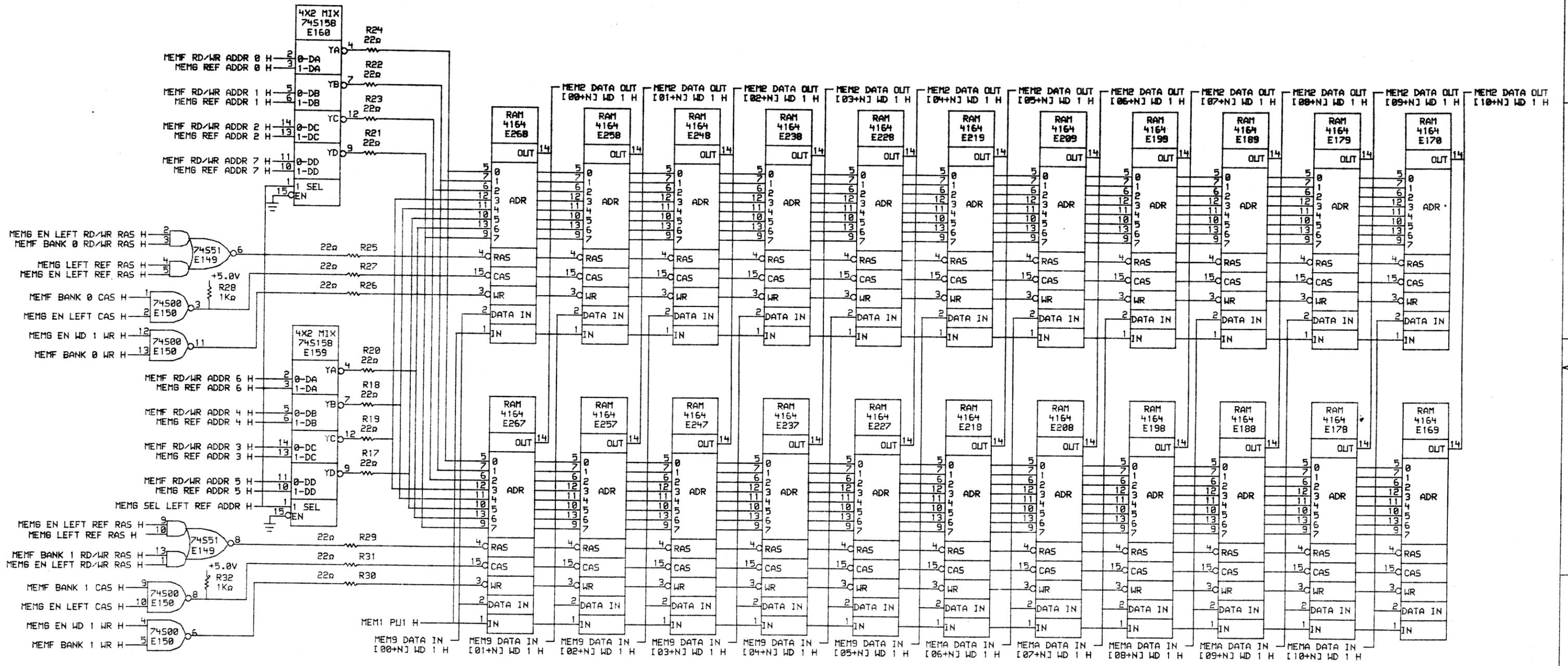
REVISIONS		
CHK	CHANGE NO.	REV

	DATE	ENG.	DATE	TITLE:
	18-FEB-83			MMC POWER AND GROUND
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
	18-FEB-83 18:04	NEXT HIGHER ASSEMBLY:	1	1
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0061-0	SIZE	CODE
			D	CS
			NUMBER	REV.
			L0061-0-MMCS	*

VULCAN:<2080.L0030>TARR1  
VULCAN:<2080.L0030>TARR2  
VULCAN:<2080.L0030>TARR3  
VULCAN:<2080.L0030>TARR4  
VULCAN:<2080.L0030>TARR5  
VULCAN:<2080.L0030>TARR6  
VULCAN:<2080.L0030>TARR7  
VULCAN:<2080.L0030>TARR8  
VULCAN:<2080.L0030>TARR9  
VULCAN:<2080.L0030>TARRA  
VULCAN:<2080.L0030>TARRB  
VULCAN:<2080.L0030>TARRC  
VULCAN:<2080.L0030>TARRD  
VULCAN:<2080.L0030>TARRE  
VULCAN:<2080.L0030>TARRF  
VULCAN:<2080.L0030>TARRG  
VULCAN:<2080.L0030>TARRH  
VULCAN:<2080.L0030>TARRI

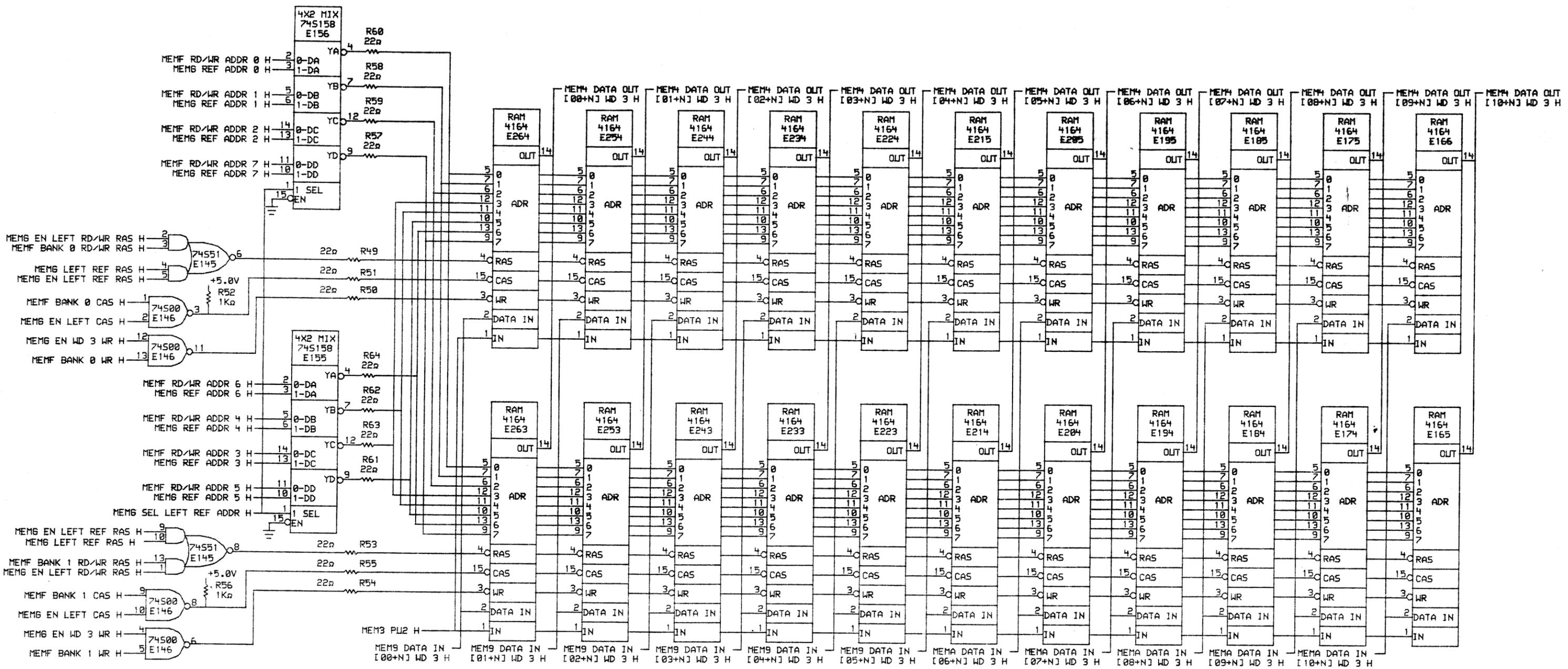


DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		DATA 00-10
		SHEET		WORD 0
FIRST USED ON OPTION/MODEL: 2080				REV.
NEXT HIGHER ASSEMBLY: B-DD-L0030-0				NUMBER
				L0030-0-MEM1

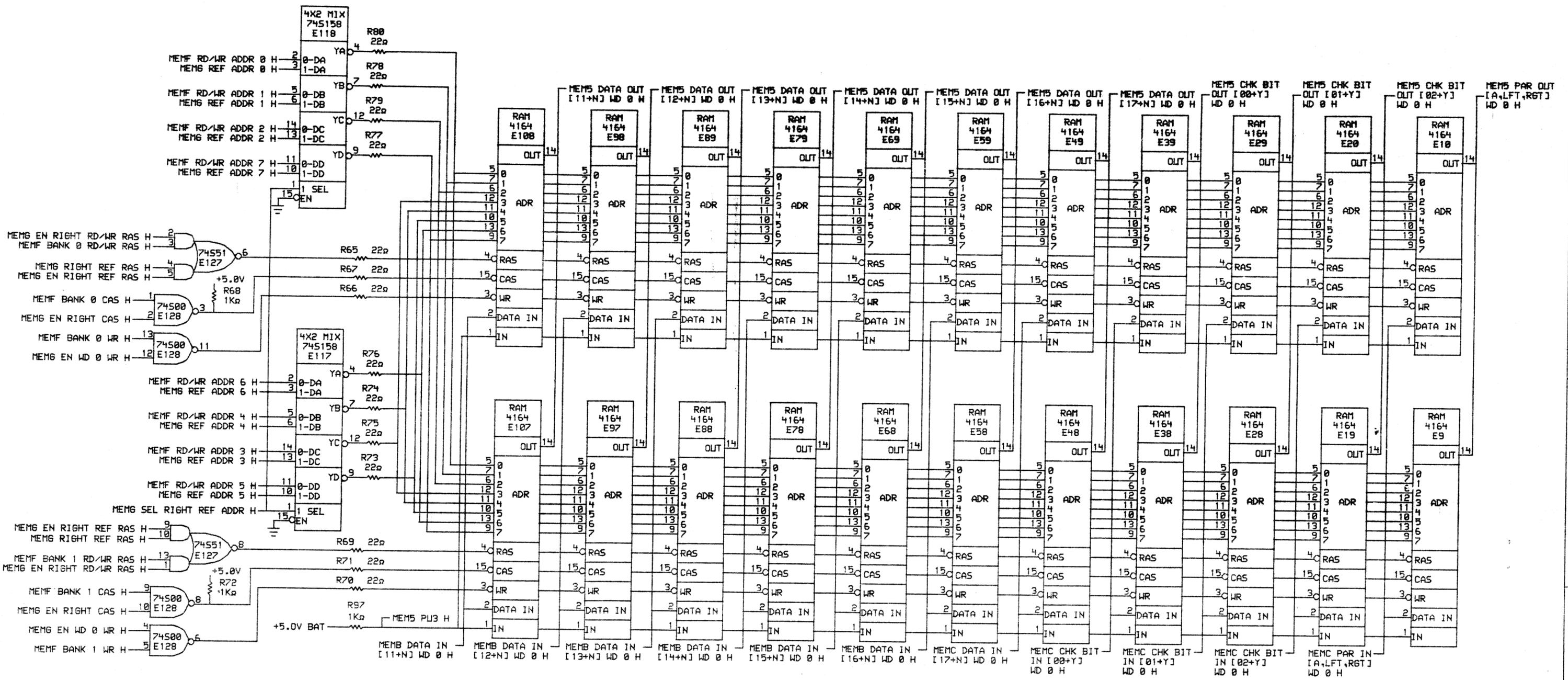


DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		DATA 00-10
		SHEET OF		WORD 1
VULCAN: (2080.L0030)TARR2.DRW 29-DEC-80 09:56 NEXT HIGHER ASSEMBLY:				SIZE CODE
FIRST USED ON OPTION/MODEL: 2080				B-DD-L0030-0
		NUMBER		REV.
		D 05 L0030-0-MEM2		

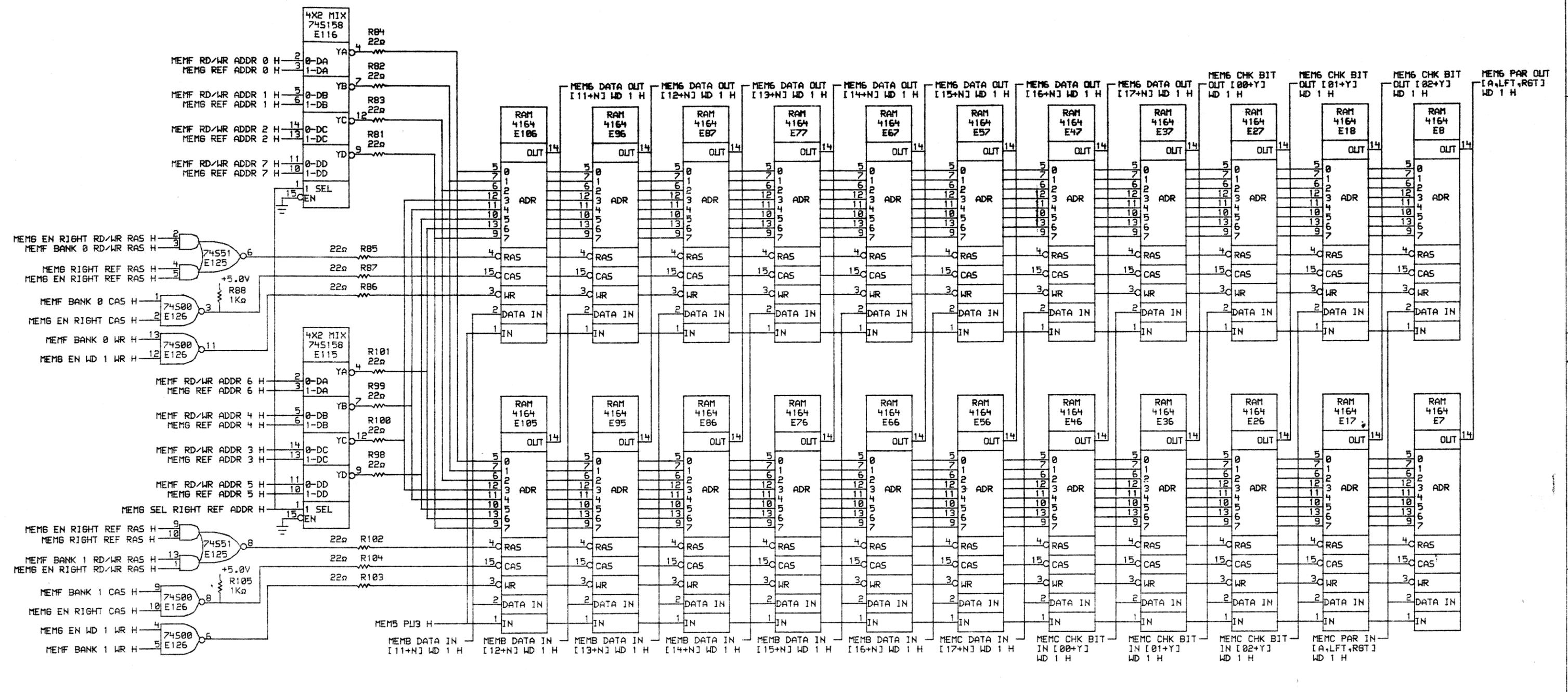




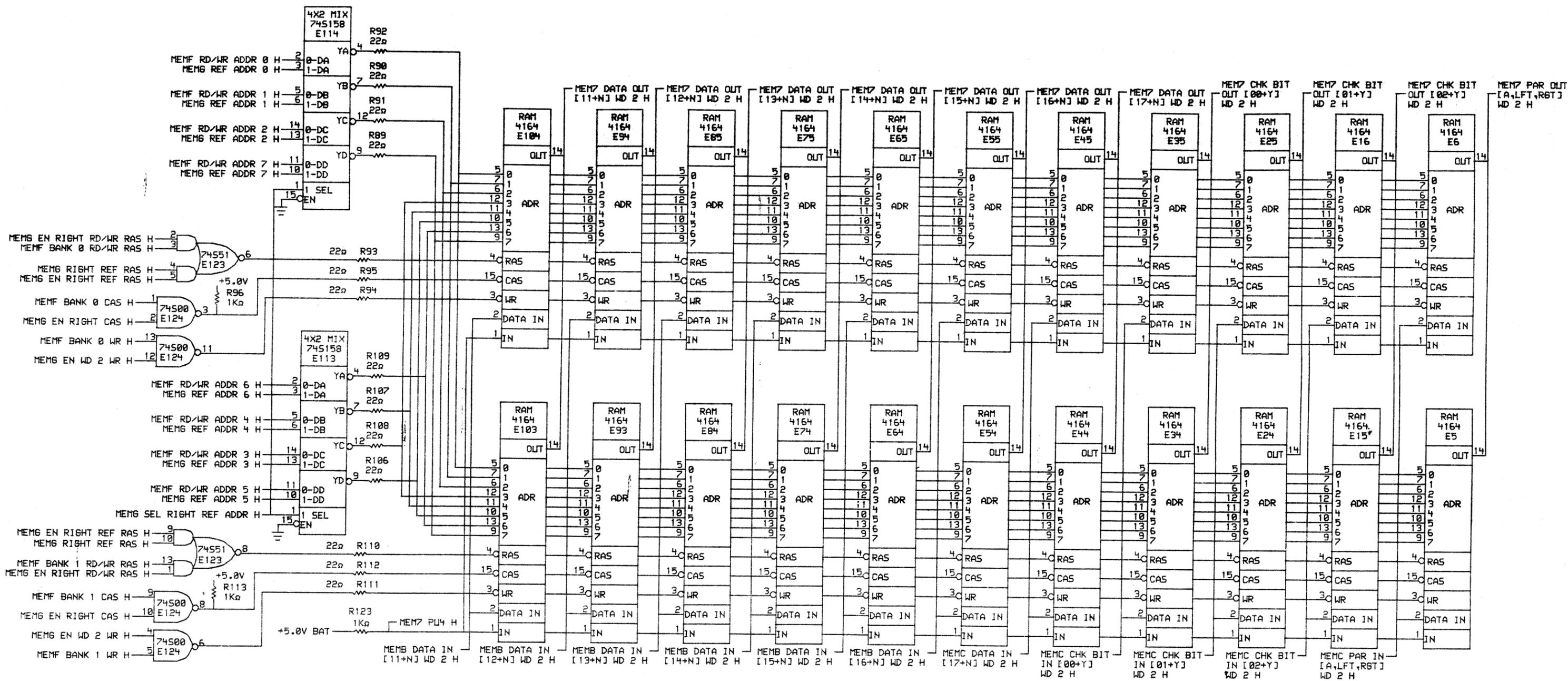
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		DATA 00-10
VILCANI (2000.L0030)TARRY.DRW [29-DEC-80 10:06				WORD 3
FIRST USED ON OPTION/MODEL: 2000				NUMBER
NEXT HIGHER ASSEMBLY: B-DD-L0030-0				REV.
				D CS L0030-0-MEM4



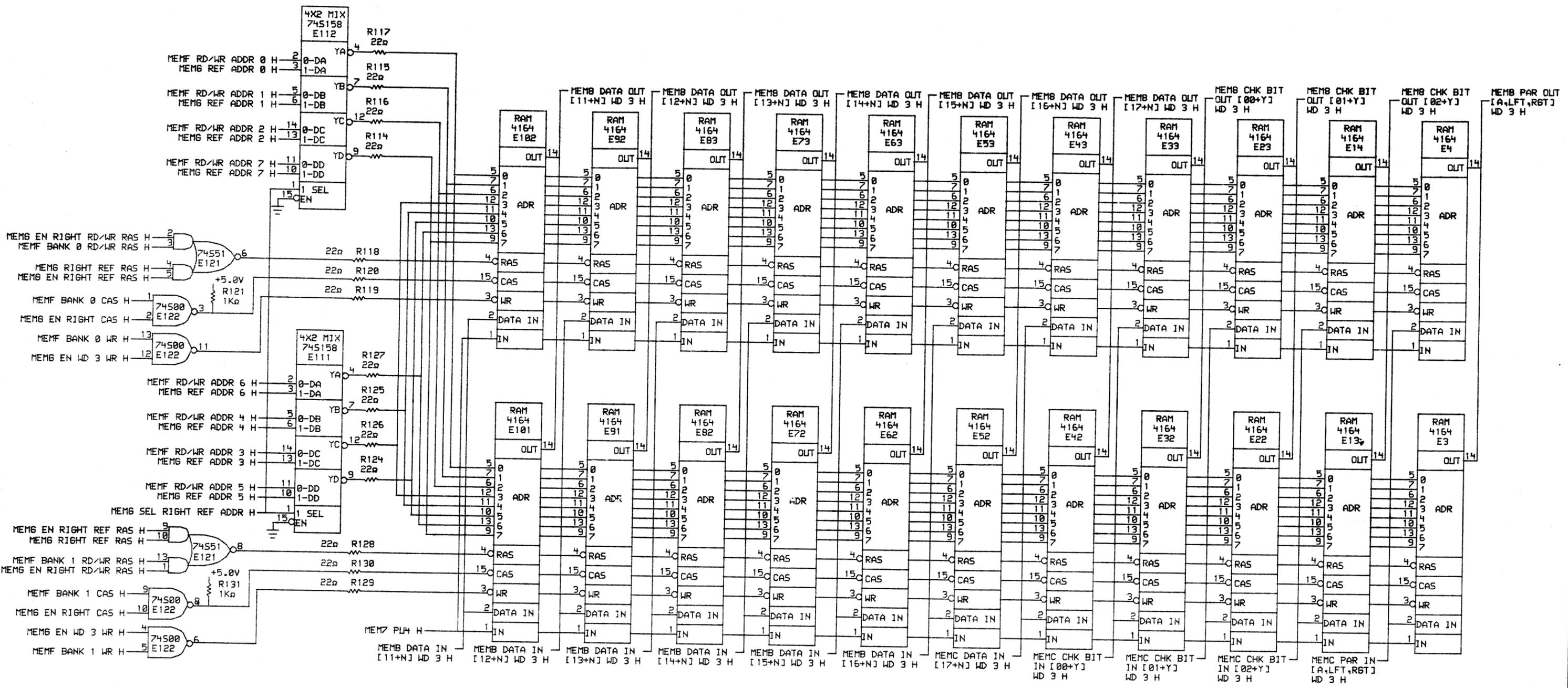
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		DATA 11-PAR
		SHEET		WORD 0
VULCAN (2000.L0030)TARRS.DRW 129-DEC-80 10:25	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL: 2000	B-DD-L0030-0	D	CS	L0030-0-MEM5
				REV.



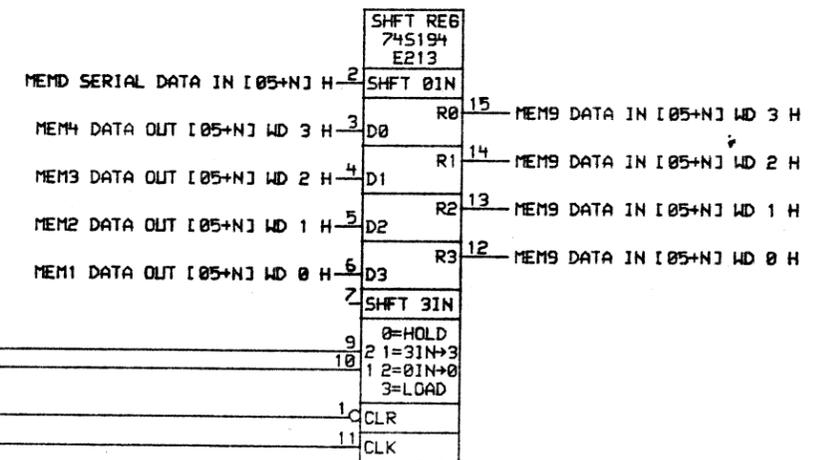
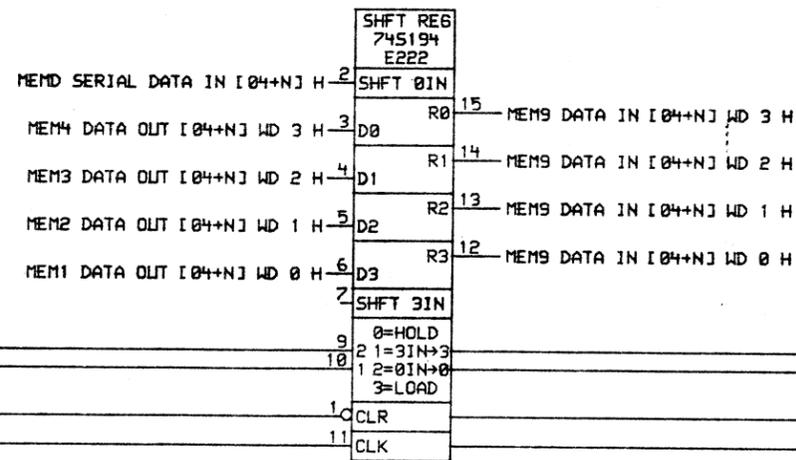
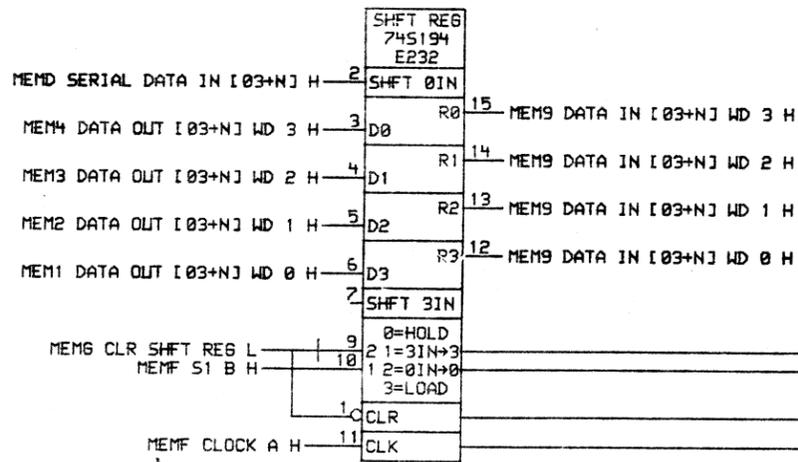
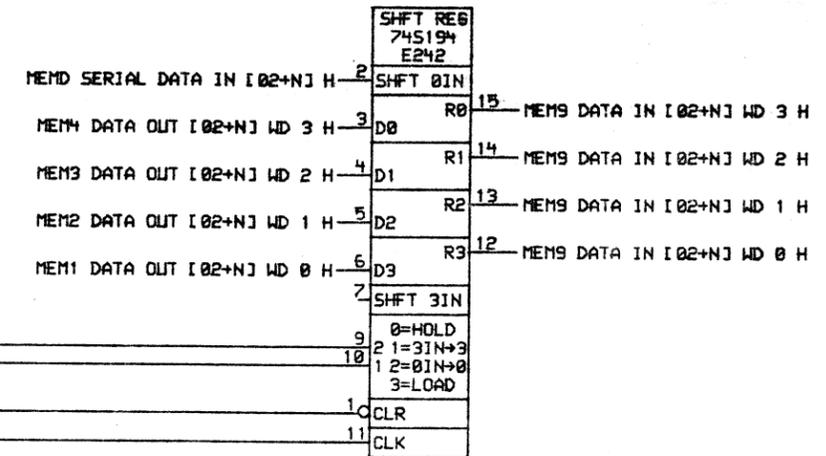
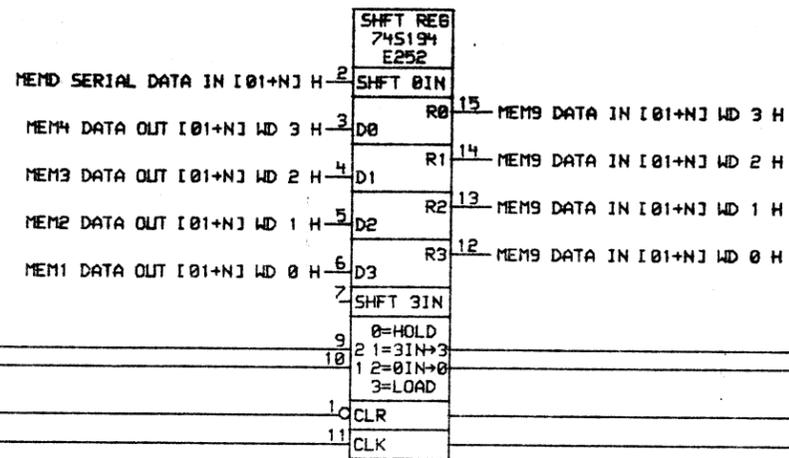
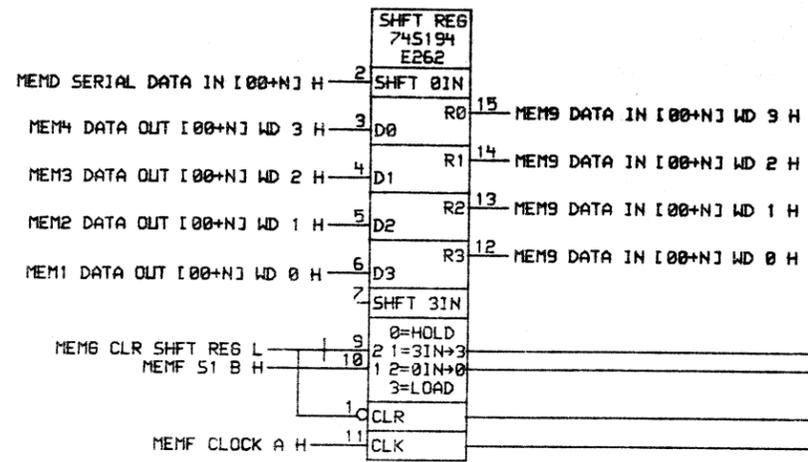
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	19-JAN-82	BOARD LOCATION:		11-PAR
		SHEET	OF	WORD 1
VULCAN (2000.L0030)TARR6.DRW 29-DEC-80 10:32 NEXT HIGHER ASSEMBLY:				SIZE CODE
FIRST USED ON OPTION/MODEL: 2080 B-DD-L0030-0				D CS L0030-0-MEM6
				NUMBER
				REV.



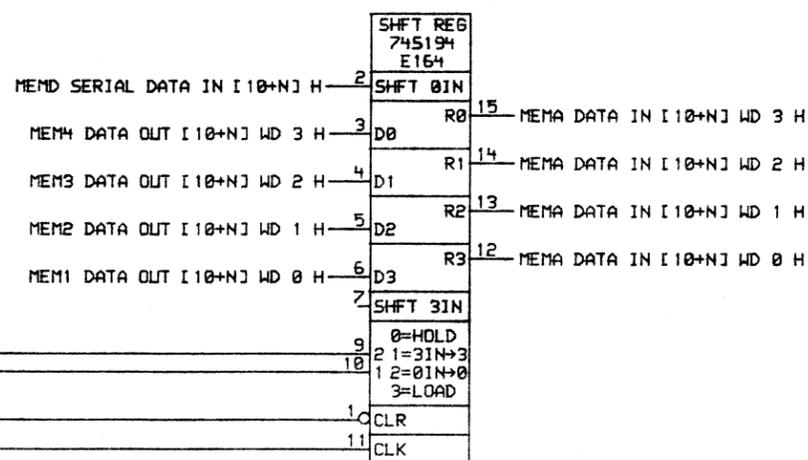
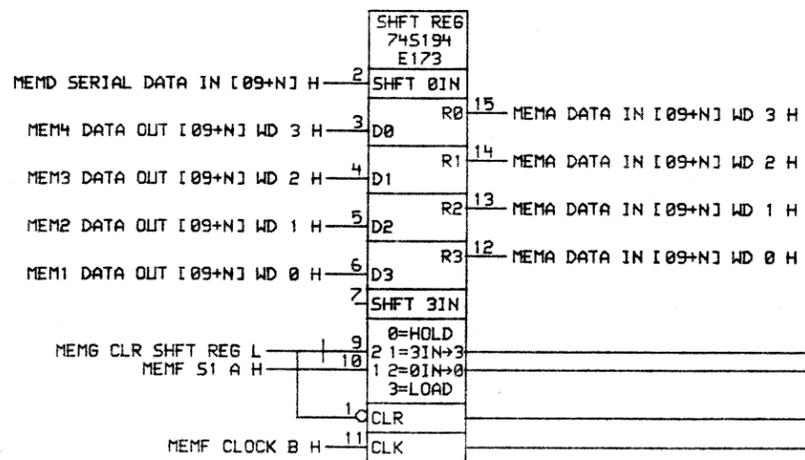
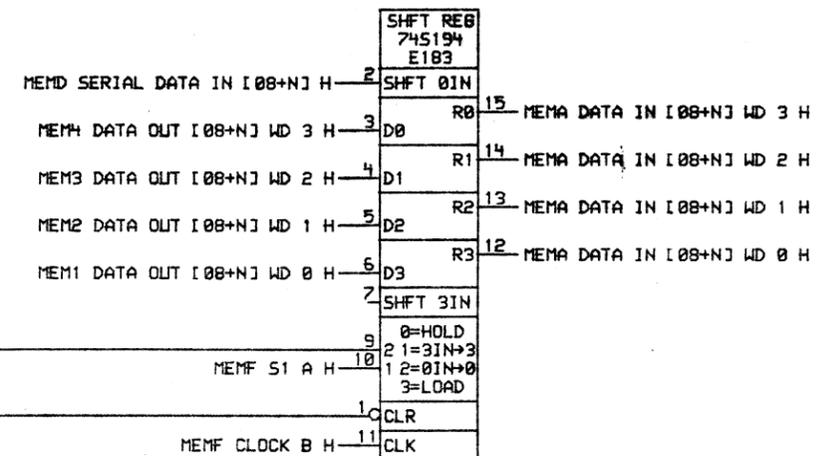
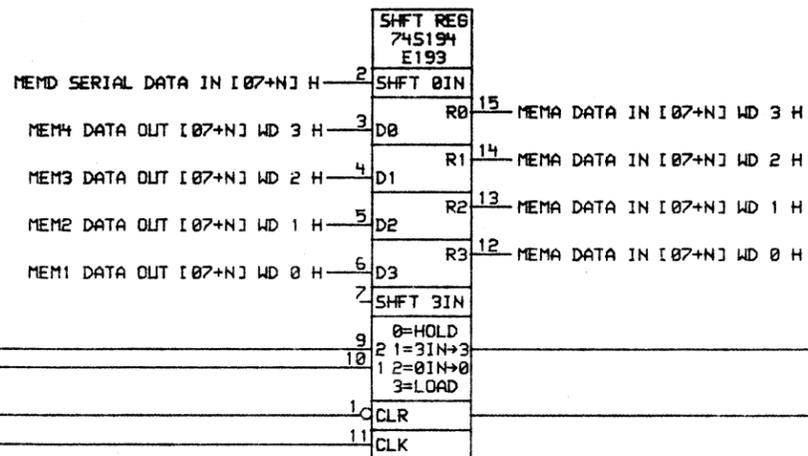
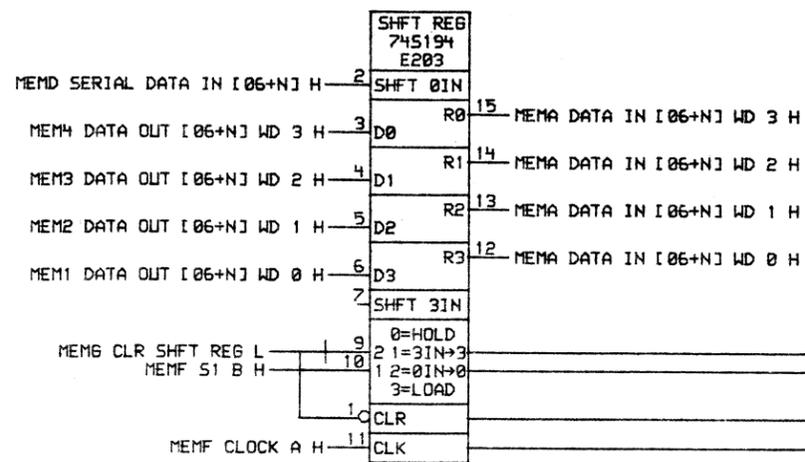
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		DATA 11-PAR
		SHEET		WORD 2
FIRST USED ON OPTION/MODEL: 2080				REV.
NEXT HIGHER ASSEMBLY: B-DD-L0030-0				NUMBER
SIZE	CODE	D C5 L0030-0-MEM7		



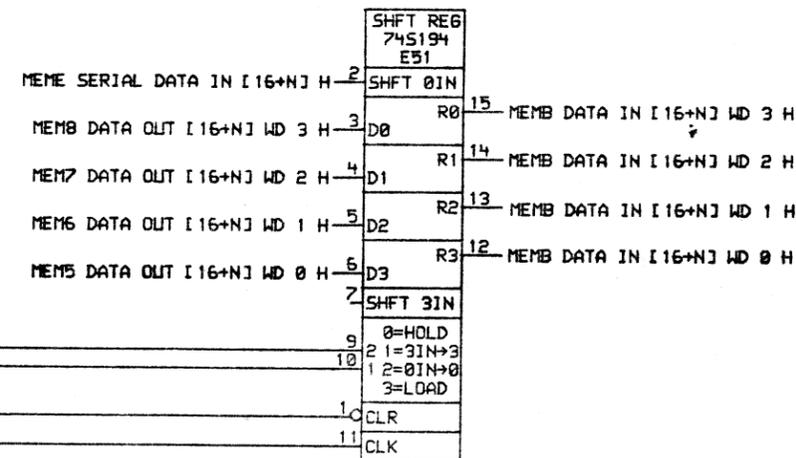
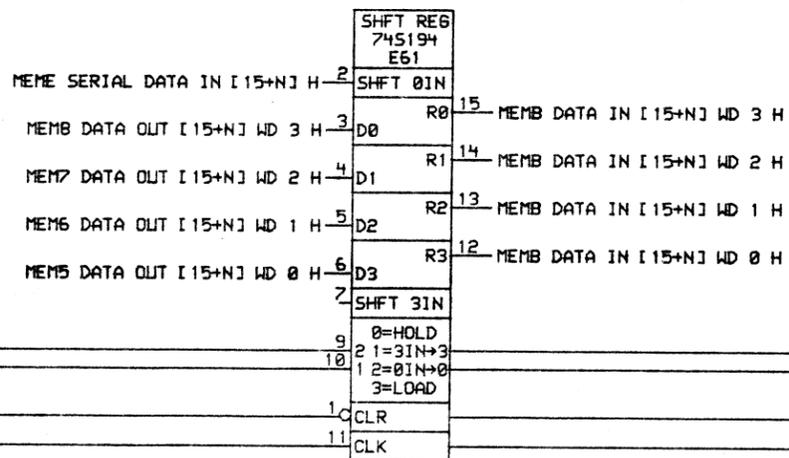
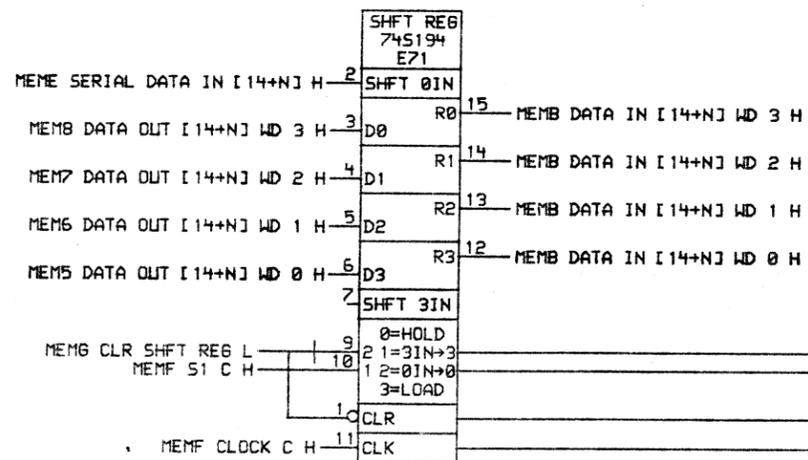
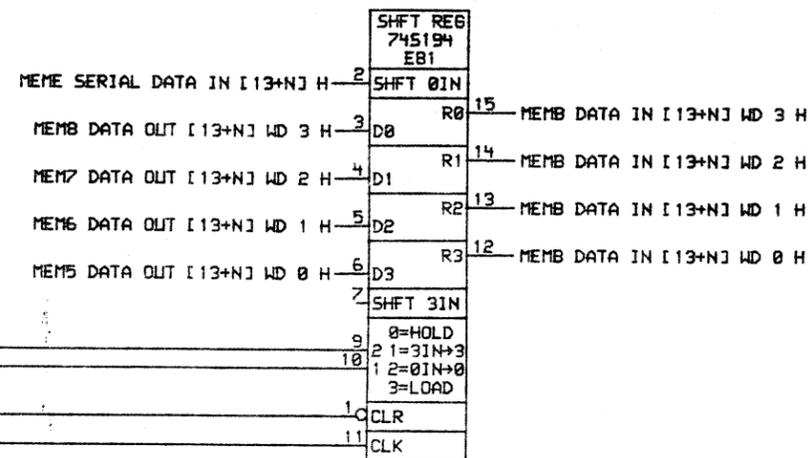
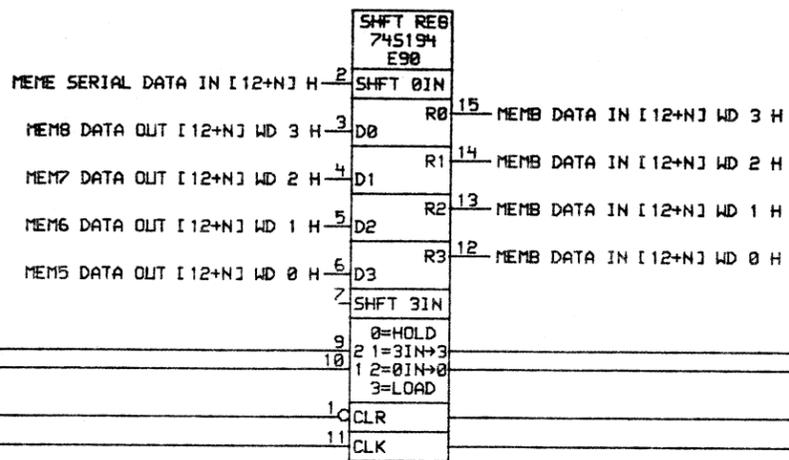
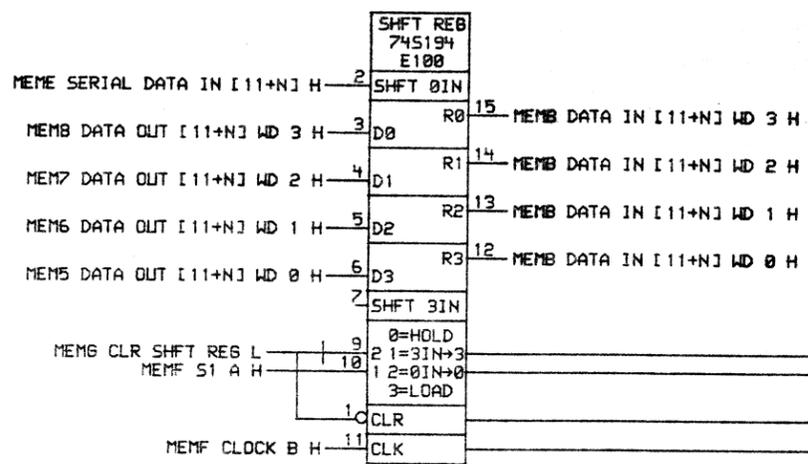
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		DATA 11-PAR
		SHEET	OF	WORD 3
VULCAN (2080-L0030) TARRS.DRW [29-DEC-80 10:35] NEXT HIGHER ASSEMBLY:				SIZE CODE
FIRST USED ON OPTION/MODEL: 2080 B-DD-L0030-0				NUMBER
				REV.
				D CS L0030-0-MEM8

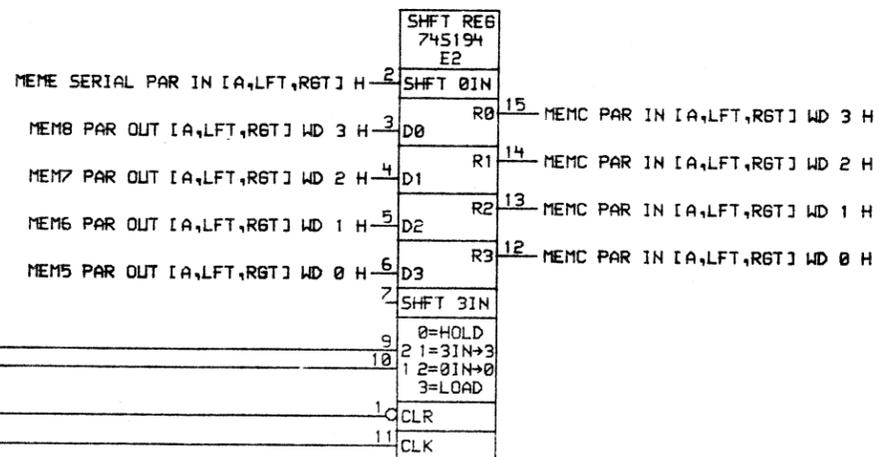
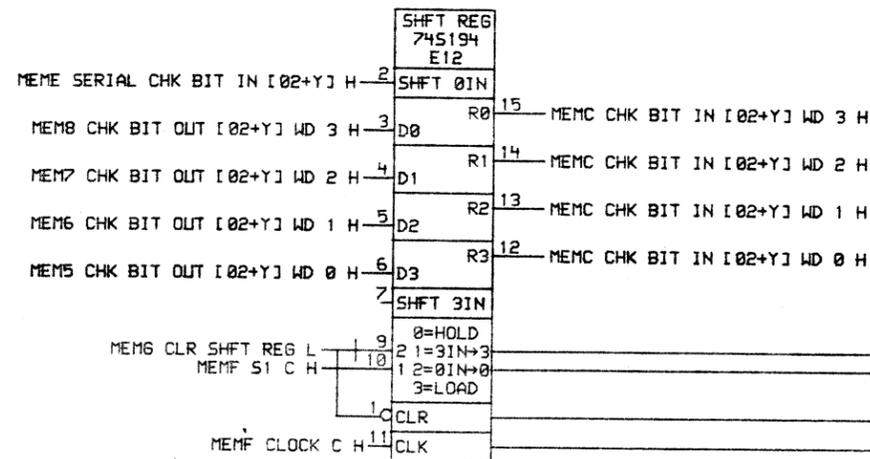
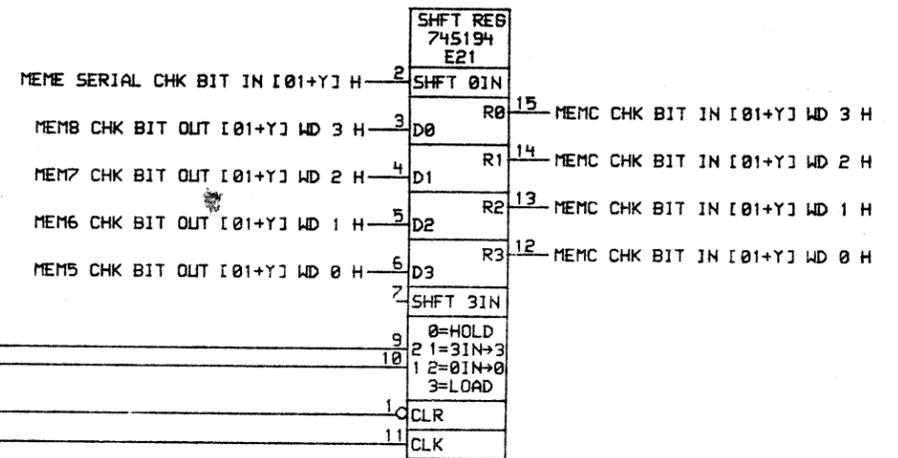
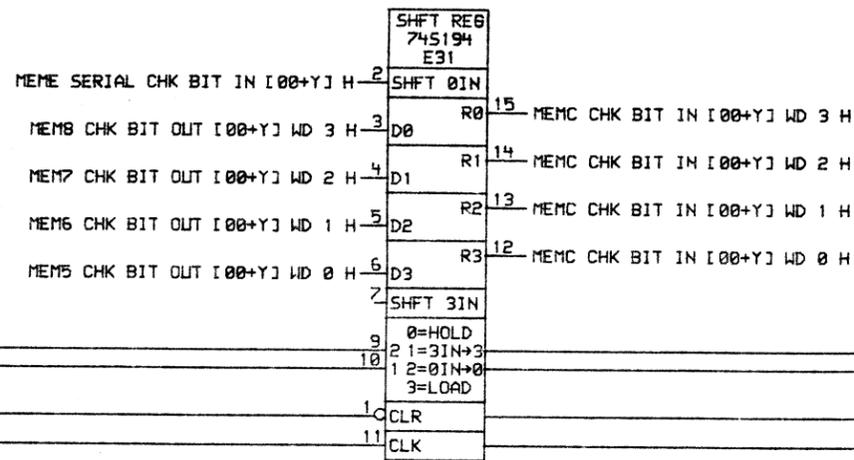
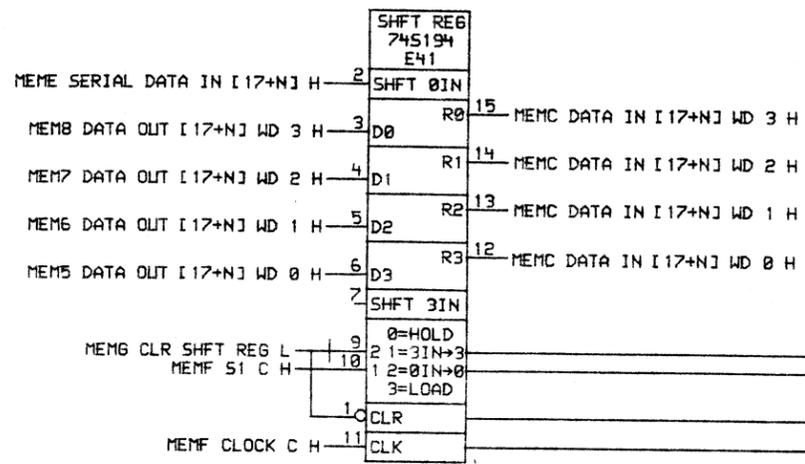


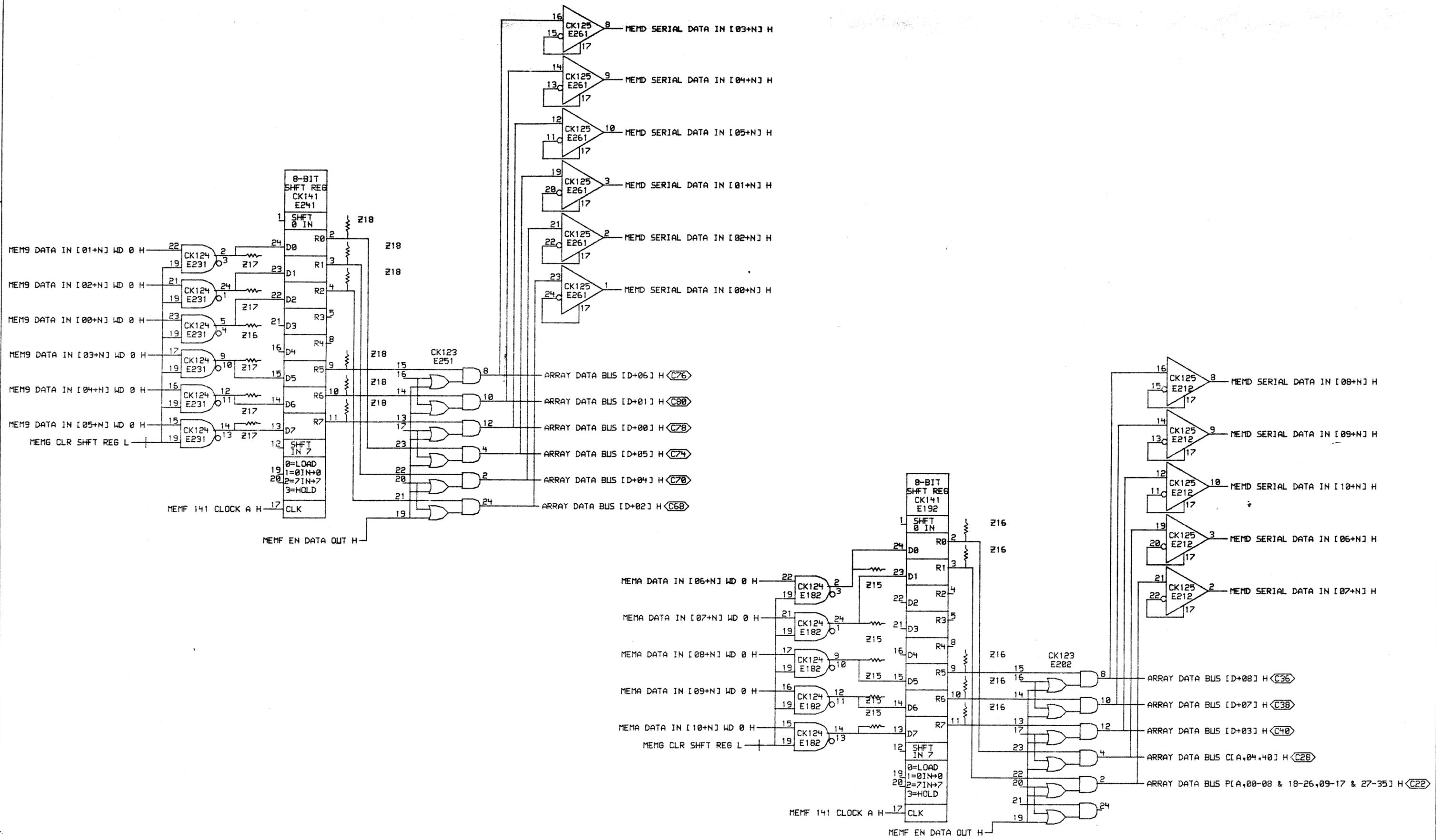
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		DATA REG 0-5
VULCAN: <2000.L0030>TARRS.DRW   24-AUG-00 09:12 NEXT HIGHER ASSEMBLY:				SIZE CODE
FIRST USED ON OPTION/MODEL: 2000				B-DD-L0030-0
NUMBER			REV.	
L0030-0-MEM9				



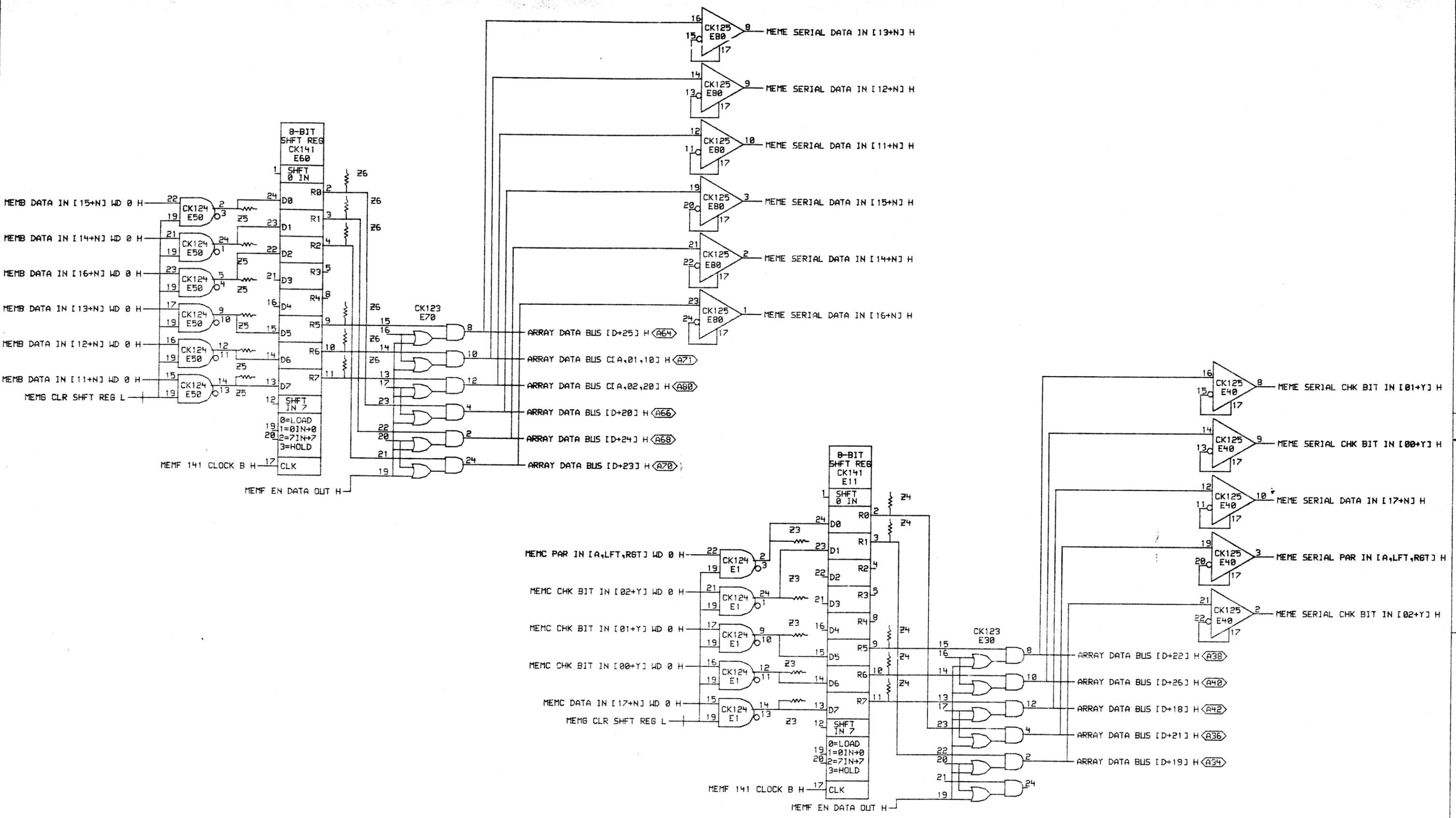
DRN.	DATE	ENG.	DATE	TITLE:	DATA REG 6-10				
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	SIZE	CODE	NUMBER	REV.	
VILCAN (2000-L0030)TARRA.DRW (24-AUG-80 09:13) NEXT HIGHER ASSEMBLY:				B-DD-L0030-0		D	CS	L0030-0-MEMA	
FIRST USED ON OPTION/MODEL: 2000									



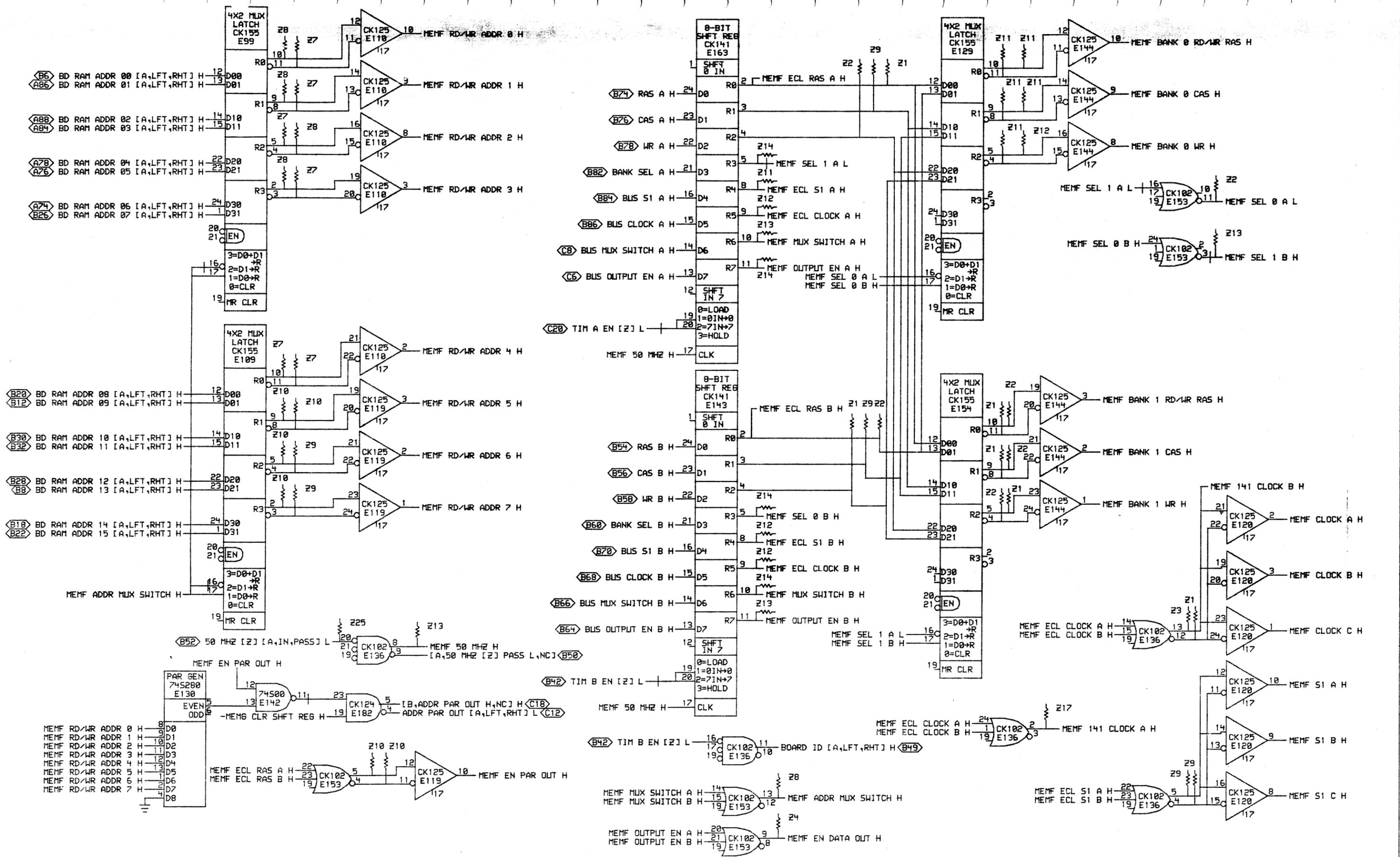




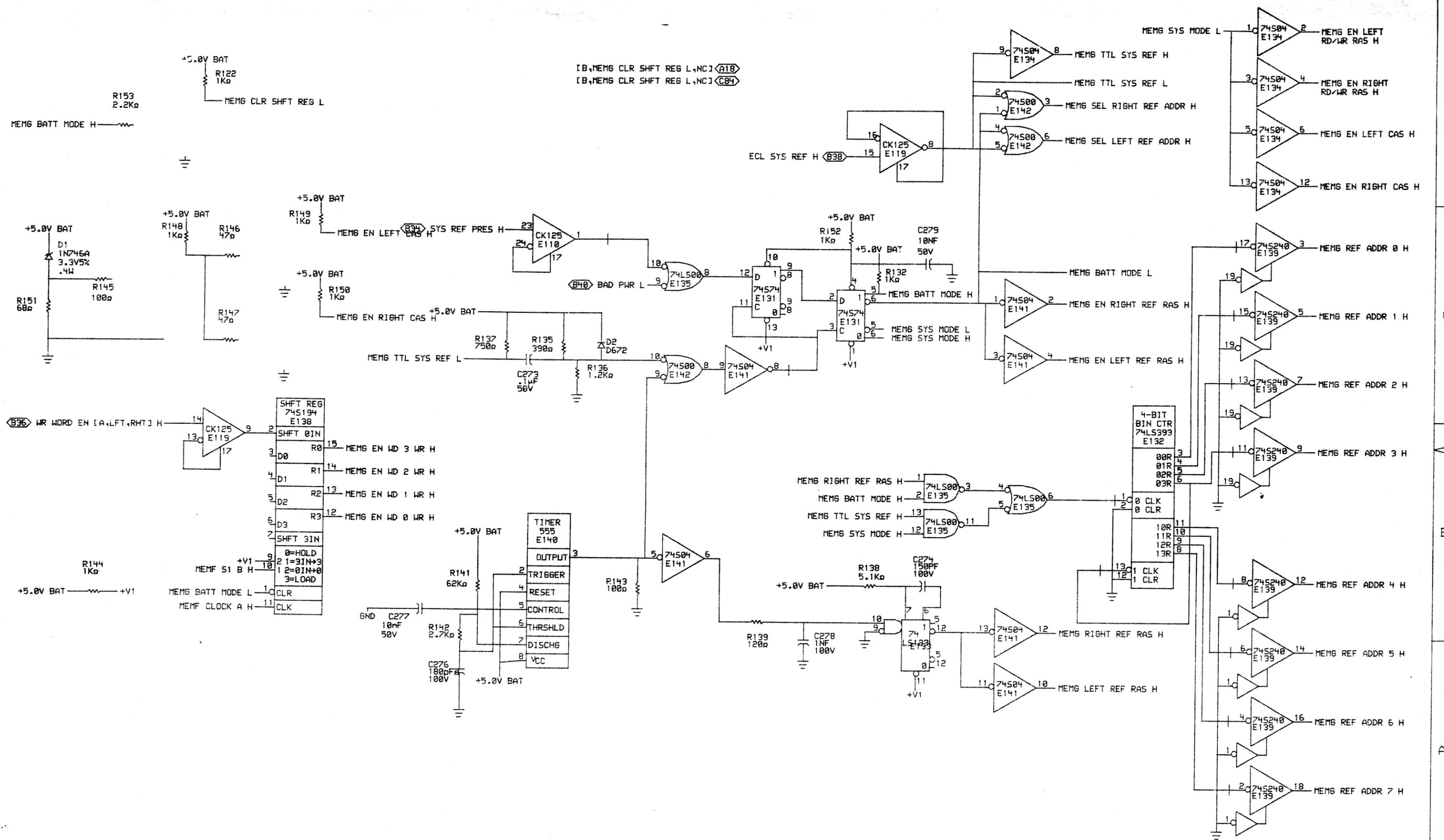
DRN.	DATE	ENG.	DATE	TITLE:	BUS 0-10
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	
VILCAN: <2080.L0030>TARD.DRW	29-DEC-80 10:50	NEXT HIGHER ASSEMBLY:	B-DD-L0030-0	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL:	2080			D CS	L0030-0-MEMD
					REV.



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		BUS 11-17
		SHEET		CHK 0-2, PAR
VILCAN: <2000.L0030>TARRE.DRW 129-DEC-00 10:56 NEXT HIGHER ASSEMBLY:				SIZE CODE NUMBER
FIRST USED ON OPTION/MODEL: 2000 B-DD-L0030-0				D CS L0030-0-MEME REV.



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		ECL CONTROL LOGIC
VLLCAN (2000.L0030)TARR.DRW	29-DEC-80 11:21	SHEET	OF	NUMBER
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:		REV.
				D CS L0030-0-MEMF

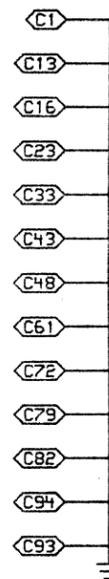
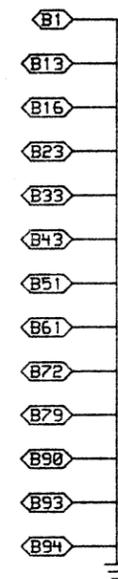
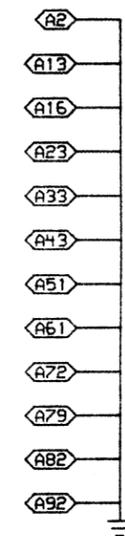
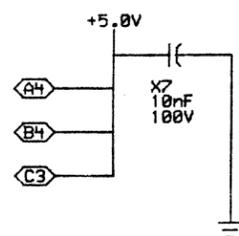
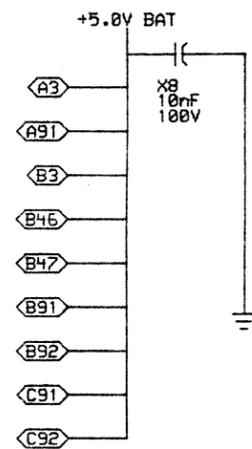


DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		TTL CONTROL LOGIC
VULCAN <2000.L0030>TARRS.DRW   27-AUG-80 05:11		NEXT HIGHER ASSEMBLY:		SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL:				D CS L0030-0-MEM

A21 I A,I C,NC,ARRAY DATA BUS 21 H,NC],I C,NC,NC,ARRAY DATA BUS 30 HJJ] Z219  
 A29 I A,I C,NC,ARRAY DATA BUS 19 H,NC],I C,NC,NC,ARRAY DATA BUS 28 HJJ] Z219  
 A37 I A,I C,NC,ARRAY DATA BUS 22 H,NC],I C,NC,NC,ARRAY DATA BUS 31 HJJ] Z219  
 A39 I A,I C,NC,ARRAY DATA BUS 26 H,NC],I C,NC,NC,ARRAY DATA BUS 35 HJJ] Z219  
 A41 I A,I C,NC,ARRAY DATA BUS 18 H,NC],I C,NC,NC,ARRAY DATA BUS 27 HJJ] Z219  
 A45 I A,I C,NC,ARRAY DATA BUS 25 H,NC],I C,NC,NC,ARRAY DATA BUS 34 HJJ] Z220  
 A49 I A,I C,NC,ARRAY DATA BUS 24 H,NC],I C,NC,NC,ARRAY DATA BUS 33 HJJ] Z220  
 A53 I A,I C,NC,ARRAY DATA BUS 20 H,NC],I C,NC,NC,ARRAY DATA BUS 29 HJJ] Z220  
 A55 I A,I C,NC,ARRAY DATA BUS 23 H,NC],I C,NC,NC,ARRAY DATA BUS 32 HJJ] Z220  
 A59 I A,I C,NC,ARRAY DATA BUS C01 H,NC],I C,NC,NC,ARRAY DATA BUS C10 HJJ] Z220  
 A63 I A,I C,NC,ARRAY DATA BUS C02 H,NC],I C,NC,NC,ARRAY DATA BUS C20 HJJ] Z220  
 A73 I A,I C,NC,BD RAM ADDR 06 LFT H,NC], I C,NC,NC,BD RAM ADDR 06 RHT HJJ] Z221  
 A75 I A,I C,NC,BD RAM ADDR 05 LFT H,NC], I C,NC,NC,BD RAM ADDR 05 RHT HJJ] Z221  
 A77 I A,I C,NC,BD RAM ADDR 04 LFT H,NC], I C,NC,NC,BD RAM ADDR 04 RHT HJJ] Z221  
 A83 I A,I C,NC,BD RAM ADDR 03 LFT H,NC], I C,NC,NC,BD RAM ADDR 03 RHT HJJ] Z221  
 A85 I A,I C,NC,BD RAM ADDR 01 LFT H,NC], I C,NC,NC,BD RAM ADDR 01 RHT HJJ] Z221  
 A87 I A,I C,NC,BD RAM ADDR 02 LFT H,NC], I C,NC,NC,BD RAM ADDR 02 RHT HJJ] Z221

B5 I A,I C,NC,BD RAM ADDR 00 LFT H,NC], I C,NC,NC,BD RAM ADDR 00 RHT HJJ] Z222  
 B7 I A,I C,NC,BD RAM ADDR 13 LFT H,NC], I C,NC,NC,BD RAM ADDR 13 RHT HJJ] Z222  
 B11 I A,I C,NC,BD RAM ADDR 09 LFT H,NC], I C,NC,NC,BD RAM ADDR 09 RHT HJJ] Z222  
 B17 I A,I C,NC,BD RAM ADDR 14 LFT H,NC], I C,NC,NC,BD RAM ADDR 14 RHT HJJ] Z222  
 B19 I A,I C,NC,BD RAM ADDR 08 LFT H,NC], I C,NC,NC,BD RAM ADDR 08 RHT HJJ] Z222  
 B21 I A,I C,NC,BD RAM ADDR 15 LFT H,NC], I C,NC,NC,BD RAM ADDR 15 RHT HJJ] Z222  
 B25 I A,I C,NC,BD RAM ADDR 07 LFT H,NC], I C,NC,NC,BD RAM ADDR 07 RHT HJJ] Z223  
 B27 I A,I C,NC,BD RAM ADDR 12 LFT H,NC], I C,NC,NC,BD RAM ADDR 12 RHT HJJ] Z223  
 B29 I A,I C,NC,BD RAM ADDR 10 LFT H,NC], I C,NC,NC,BD RAM ADDR 10 RHT HJJ] Z223  
 B31 I A,I C,NC,BD RAM ADDR 11 LFT H,NC], I C,NC,NC,BD RAM ADDR 11 RHT HJJ] Z223  
 B35 I C,NC,WR WORD EN LFT,WR WORD EN RHT] H Z223  
 B37 I C,NC,NC,ECL SYS REF HJ] Z223  
 B41 I A,NC,TIM B EN [Z] L] Z223  
 B53 I C,NC,NC,RAS B HJ] Z224  
 B55 I C,NC,NC,CAS B HJ] Z225  
 B57 I C,NC,NC,WR B HJ] Z224  
 B59 I C,NC,NC,BANK SEL B HJ] Z225  
 B63 I C,NC,NC,BUS OUTPUT EN B HJ] Z224  
 B65 I C,NC,NC,BUS MUX SWITCH B HJ] Z225  
 B67 I C,NC,NC,BUS CLOCK B HJ] Z224  
 B69 I C,NC,NC,BUS S1 B HJ] Z224  
 B71 I C,NC,SYS REF PRES H,NC] Z225  
 B73 I C,NC,NC,RAS A HJ] Z224  
 B75 I C,NC,NC,CAS A HJ] Z225  
 B77 I C,NC,NC,WR A HJ] Z226  
 B81 I C,NC,NC,BANK SEL A HJ] Z226  
 B83 I C,NC,NC,BUS S1 A HJ] Z226  
 B85 I C,NC,NC,BUS CLOCK A HJ] Z226  
 C5 I C,NC,NC,BUS OUTPUT EN A HJ] Z226  
 C7 I C,NC,NC,BUS MUX SWITCH A HJ] Z226  
 C19 I A,NC,TIM A EN [Z] L] Z227  
 C21 I A,I C,NC,ARRAY DATA BUS 03 H,NC], I C,NC,NC,ARRAY DATA BUS 12 HJJ] Z227  
 C23 I A,I C,NC,ARRAY DATA BUS 07 H,NC], I C,NC,NC,ARRAY DATA BUS 16 HJJ] Z227  
 C25 I A,I C,NC,ARRAY DATA BUS 08 H,NC], I C,NC,NC,ARRAY DATA BUS 17 HJJ] Z227  
 C27 I A,I C,NC,ARRAY DATA BUS P00-08 & 18-26 H,NC], I C,NC,NC,ARRAY DATA BUS P09-17 & 27-35 HJJ] Z227  
 C29 I A,I C,NC,ARRAY DATA BUS C04 H,NC], I C,NC,NC,ARRAY DATA BUS C40 HJJ] Z227  
 C31 I A,I C,NC,ARRAY DATA BUS 00 H,NC], I C,NC,NC,ARRAY DATA BUS 09 HJJ] Z228  
 C33 I A,I C,NC,ARRAY DATA BUS 01 H,NC], I C,NC,NC,ARRAY DATA BUS 10 HJJ] Z228  
 C35 I A,I C,NC,ARRAY DATA BUS 06 H,NC], I C,NC,NC,ARRAY DATA BUS 15 HJJ] Z228  
 C37 I A,I C,NC,ARRAY DATA BUS 04 H,NC], I C,NC,NC,ARRAY DATA BUS 13 HJJ] Z228  
 C39 I A,I C,NC,ARRAY DATA BUS 02 H,NC], I C,NC,NC,ARRAY DATA BUS 11 HJJ] Z228  
 C41 I A,I C,NC,ARRAY DATA BUS 05 H,NC], I C,NC,NC,ARRAY DATA BUS 14 HJJ] Z228  
 C11 I C,NC,ADDR PAR OUT LFT L, ADDR PAR OUT RHT L] Z215

DRN.	DATE	ENG.	DATE	TITLE:	TERMINATORS		
CHK *D.	DATE	BOARD LOCATION:	SHEET	OF	SIZE	CODE	NUMBER
VULCAN: <2000.L0030>TARRH.DRN   29-DEC-80 09:38 NEXT HIGHER ASSEMBLY:				D	CS	L0030-0-MEMH	REV.
FIRST USED ON OPTION/MODEL:							



GND TTL

GND ECL

-4.5V — -5.2V

DRN.	DATE	ENG.	DATE	TITLE:	POWER GND, CAPS
CHK'D.	DATE	BOARD LOCATION:			
VULCANI (2000.L0030)TARRI.DRW 124-AUG-80 11:06				NEXT HIGHER ASSEMBLY:	SIZE CODE
FIRST USED ON OPTION/MODEL:					D CS
				NUMBER	REV.
				L0030-0-MEMI	