The plug-compatible mainframe (PCM) industry was launched seven years ago with the installation of the first Amdahl 470V/6 system. Since that time, a number of other vendors have successfully competed for a share of the market. The primary thrust of the PCM manufacturers has been to provide cost-effective alternatives to the IBM System/370, 303X Series, 3081, and 4300 Series computers.

Plug-compatible mainframes can be installed easily, can replace or augment IBM mainframes with little or no need for changes in software or operating procedures, and can be expected to perform reliably and efficiently. What's more, most of the PCM suppliers have demonstrated their ability to provide first-class field maintenance and software support.

Should your organization install a PCM? And if so, which one? This report is designed to help you answer those questions by assessing the pros and cons of PCM's in general, profiling their current suppliers, and presenting the characteristics of 42 PCM's from 6 vendors in detailed comparison charts.

The PCM Concept

Plug-compatible mainframes are typically defined as computer mainframes that can directly execute all application programs and systems software written for the IBM System/370, 303X Series, 308X Series, and/or 4300 Series computers and can utilize the peripheral equipment available for these computers. The PCM concept would, of course, be equally applicable to the computers made by Burroughs, Honeywell, Univac, or any other mainframe supplier. IBM, with its large user base, attracts the most serious attention from the PCM vendors. Two manufacturers, Telefile Computer Products (Irvine, CA) and Foonly, Inc. (Mountain View, CA), have developed systems that are compatible with non-IBM product lines. Telefile's T-85 is compatible with the Xerox Sigma family of systems, while Foonly's F1 through F5 systems are

Plug-compatible mainframes offer attractive alternatives to IBM's medium- and large-scale computer systems. This report discusses the pros and cons of installing a PCM, profiles the current PCM suppliers, and provides detailed comparison charts describing 42 systems from 6 vendors.

compatible with the Digital Equipment DECsystem-10 and DECSYSTEM-20 families.

The PCM industry resulted from the convergence of two important trends:

- The widespread availability and user acceptance of plugcompatible peripherals designed to directly replace IBM's own magnetic tape units, disk storage units, printers, terminals, and even main memory units. From there, the next logical step was to offer replacements for the IBM central processors themselves.
- The acknowledgement that the IBM System/360 and System/370 instruction set has become a de facto standard for the industry, and that most IBM computer users will not seriously consider switching to a computer that requires extensive reprogramming. A number of systems were developed in the 60's by RCA and Univac which used the System/360 instruction set but were incompatible with systems software and peripherals. The next logical step, which was first taken by Amdahl Corporation, was to build computers which exhibited total functional compatibility with the IBM mainframes and could use all the same software and peripheral equipment.

To date, Amdahl, and a host of others, have proven the viability of the PCM concept, and it appears the industry will play an important role in the 1980's.

The current trend in the PCM industry is to target a family of systems toward a specific IBM product line, rather than



Amdahl, the original PCM supplier, offers two IBM-compatible computer families: the 470 Series and the 580 Series. The 580 Series was announced in November 1980 as a replacement for IBM's 3081 processor. The 580 Series consists of the single-processor 5860 (shown here) and the dual-processor 5870 and 5880. The 580 Series processors feature from 16 to 32 megabytes of main memory and from 16 to 34 I/O channels per CPU.

be all things to all users. For example, Amdahl Corporation pits its 470 and 580 Series against IBM's highend systems, the System/370 and 303X Series, and the 3081, respectively. Firms like Cambex, IPL Systems, and Magnuson compete with IBM's popular 4300 Series. Storage Technology Corporation, a maker of plugcompatible peripherals, has announced its intention to enter the PCM market to compete in the large mainframe arena. A new company, Trilogy, formed by Amdahl founder Gene Amdahl, intends to develop systems to compete in the 303X Series range. With high technology costs and the costs associated with maintenance and software support to consider, it is eminently more practical for a manufacturer to concentrate on a particular IBM product line. The various manufacturers and their product lines appear to bear this out.

User Reaction

Four PCM manufacturers—Amdahl, IPL, Magnuson, and NAS—were represented in Datapro's 1982 survey of computer users. We received a total of 45 responses from Ahdahl 470 Series users, 29 responses from Magnuson M80 Series users, 11 responses from NAS AS/5000, AS/7000, and AS/9000 users, and 8 responses from IPL 4400 Series users.

Using Datapro's 14 rating criteria and our usual scale of 4.0 for Excellent, 3.0 for Good, 2.0 for Fair, and 1.0 for Poor, we've compiled the weighted average ratings these users have assigned to their systems, and present the results in the chart below.

For comparison we've also included the weighted averages of the IBM system families the PCMs compete with, the System/370 (313 responses), 4300 (785 responses), 303X (271 responses), and 3081 (27 responses).

As you can see, the user ratings earned by the PCM vendors once again compared favorably with those of IBM in all 14 categories. The PCM vendors were rated comparable to or above IBM in key categories like overall satisfaction, ease of conversion, technical support, and both responsiveness and effectiveness of maintenance service. Equipment reliability was essentially a stand-off between IBM and the PCM's, with all the parties earning high ratings. Thus, it seems clear that a wisely chosen PCM can yield worthwhile cost savings without imposing offsetting penalties in any of the other areas that help to determine overall user satisfaction.

PCM Pros and Cons

The first and foremost advantage of plug-compatible mainframes is, of course, the prospect of substantial increases in processing power per dollar. The user can elect to realize this price/performance gain in either of two distinct ways: 1) by choosing a PCM that delivers performance comparable to that of a certain IBM mainframe but is offered at a lower price; or 2) by choosing a PCM that has a price tag comparable to that of a certain IBM mainframe but offers more processing power. The PCM vendors tend to position their product offerings so that users can elect either approach or, in some cases, a combination of the two (i.e., somewhat more power at a somewhat lower cost).

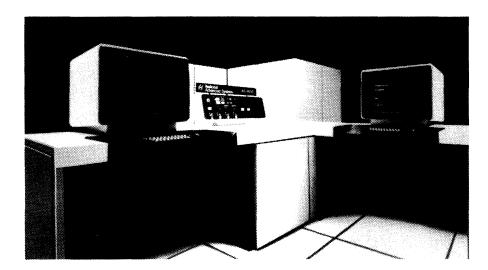
Faster delivery is another advantage that the PCM vendors will enjoy over IBM for at least another year or so. The slow delivery schedule of IBM's 4300 and 303X systems generated many sales opportunities for the PCM vendors, who typically could ship a system 30 to 60 days ARO. This situation is gradually diminishing, however, especially in the very large system arena. IBM's 3081 processor, the first in the H-Series, was scheduled for shipment late in 1981. Of its two announced competitors, the NAS AS/9000DPC was scheduled for a late-1981 first delivery, and the Amdahl 580 Model 5860 is slated for August, 1982. The tide is gradually turning.

Becoming a multiple-vendor shop can be viewed as either an advantage or disadvantage of installing a PCM. Some users are still "true-blue" IBM loyalists, who fear that their IBM service will deteriorate and every hardware problem will result in a nasty "finger-pointing" session if they allow any non-IBM equipment into their shops. Conversely, other users are convinced that dealing with multiple vendors helps to "keep IBM honest" and leads to better overall service and support.

Three potential disadvantages are commonly cited by prospective PCM users: the possibility of hardware or software incompatibilities, the possibility of weak vendor support, and the possibility that their PCM vendor may not survive. Each of these problems can be minimized through careful selection of a well-qualified vendor.

Incompatibilities in hardware or software were widely feared by early PCM users, but Datapro's user surveys have clearly shown that users who choose to deal with well-established PCM suppliers such as Amdahl or Control

	Amdahl	IPL	Magnuson	NAS	IBM S/370	IBM 4300	IBM 303X	IBM 3081
Ease of operation	3.49	3.88	3.83	3.64	3.03	3.25	3.21	3.22
Reliability of Mainframe	3.62	4.00	3.34	3.64	3.12	3.78	3.59	3.67
Reliability of Peripherals	3.13	2.57	3.15	2.91	3.13	3.29	3.27	3.30
Responsiveness of maintenance service	3.47	3.88	3.45	3.45	3.15	3.32	3.44	3.46
Effectiveness of maintenance service	3.33	3.75	3.21	3.18	3.05	3.21	3.34	3.46
Technical support								
Trouble-shooting	3.18	3.63	3.11	2.82	2.77	2.81	3.00	3.31
Education	2.98	3.86	3.08	2.78	2.76	2.73	2.95	2.96
Documentation	2.83	2.88	3.04	2.89	2.66	2.66	2.88	3.12
Ease of programming	3.00	3.50	3.33	3.00	2.89	2.97	2.96	2.96
Ease of conversion	3.24	3.71	3.59	3.00	2.94	2.95	3.02	3.22
Overall satisfaction	3.37	3.75	3.37	3.45	3.05	3.15	3.18	3.33



The NAS AS/9000 Series competes with IBM's 3033 and 3081 processors. Available in both single- and dual-processor models, the AS/9000 Series features from 4 to 32 megabytes of main memory and from 16 to 32 I/O channels. The service processor console includes two color display terminals, two independent processors, and two flexible disk drives.

Data need have no fears. What's more, most PCM manufacturers have demonstrated their ability to develop the specialized hardware and/or software needed to maintain full compatibility when IBM adds new functions to its systems. Conversely, users who decide to deal with a newer PCM vendor should demand proof (in the form of a rigorous benchmark test) and/or an iron-clad guarantee that the new mainframe will be totally compatible with their IBM equipment, systems software, and application programs.

Poor vendor support is another frequently expressed worry of prospective PCM users. Once again our user survey results make it clear that Amdahl, Control Data, Magnuson, and NAS have all established viable field service and support organizations whose effects are often judged to be superior to those of IBM. As always, it's up to the buyers to determine the amount of service and support they need and are willing to pay for, and then to select a PCM vendor that can and will provide it.

Vendor survival has always been a topic of concern to PCM buyers, and the PCMs' long-term survival will depend upon their continued ability to maintain full compatibility together with a worthwhile price/performance advantage over the steadily improving mainframes that IBM will undoubtedly offer.

The PCM Suppliers

Amdahl Corporation, which was formed in 1971 and delivered its first computer in June 1975, is the leading supplier (in terms of dollar volume) of IBM-compatible mainframes, with several hundred installations nationwide. The firm's software development efforts have resulted in significant improvements over comparable IBM products. Amdahl also offers its Universal Time-Sharing System (UTS), which is based on the Unix operating system developed by Bell Laboratories.

Amdahl focuses on the upper end of IBM's mainframe product line and has developed advanced technology that enables its computers to deliver more performance per dollar than the comparable IBM models. The current Amdahl 470 Series processor line ranges from the 470V/7 family which is comparable in performance to the IBM 3032 and 3033 uniprocessors, to the 470V/8, which is comparable to the dual-processor IBM 3033MP. Amdahl's largest systems, the 580 Series, are targeted at IBM's 3081, as well as future IBM offerings in that size range.

Cambex Corporation, formerly Cambridge Memories, Inc., is best known as a supplier of add-on memory for IBM System/360 and System/370-computers and for various minicomputers. Cambex entered the PCM market in 1977 with replacements for the System/370 Model 115 and 125, but the firm is now concentrating its attention on the IBM 4300 Series. The current product line consists of three models, the 1636, 1641, and 1651, that bracket the 4300 product line.

Control Data Corporation is the only established mainframe manufacturer that offers a line of IBM-compatible processors in addition to its own proprietary computer systems. CDC became the third major contender in the PCM market when it introduced its Omega family of System/370-compatible mainframes in June 1977. The present three models, the 480-I, 480-II, and 480-III, are made by IPL Systems. The 480-I and -II bracket the IBM 4331-2 in performance, and the larger 480-III exceeds both the IBM 3031 and 4341-2 in performance.

IPL Systems, Inc., a seven-year-old firm, was formed by Stephen J. Ippolito to build IBM 360/370-compatible processors. The first IPL systems were shipped in April 1977 as the Control Data Omega 480 Series. Today IPL systems are still sold by CDC and by Olivetti in Europe. With an installed base of over 170 systems, IPL decided to market its own products in the U.S. In October, 1980, IPL announced three systems to compete against the IBM 4300; the IPL 4436, 4443, and 4446. The systems all offer improved price/performance over their IBM counterparts, the 4331-2, 4341-1, and 4341-2, respectively. In November 1981, IPL announced the 4445, which competes against IBM's new 4341-11.

Systems Corporation has become a key PCM supplier with an excellent reputation, as indicated in our surveys. Users continually sing the praises of their M80 systems. Magnuson's "Strategic Architecture" permits easy field upgrading of the processor, memory, and I/O channels, as well as rapid adaptation to maintain compatibility with new IBM functions or features. The current product line consists of the M80/30, which competes with the IBM 4331-1; the M80/30E, which is a replacement for the IBM 4331-11; the M80/31 and M80/32, which are targeted at the IBM 4331-2 market; the M80/41, which competes with the IBM 4341-10; and the M80/42 and M80/43, which compete with the IBM 4341-1. Magnuson has discontinued the larger M80/44.

National Advanced Systems Corporation (NAS) is the wholly owned subsidiary of National Semiconductor Corporation that was formed in October, 1979 to take over nearly all of Itel Corporation's IBM-compatible mainframe business. NAS took over Itel's worldwide computer activities, acquired Itel's inventory of computers, and assumed the maintenance and support responsibilities for all of Itel's installed computer base, including those systems manufactured by Hitachi, Ltd.

The company's current product line, the AS/3000 Series, AS/5000 Series, AS/6100 Series, AS/7000 Series, and AS/9000 Series, range in performance from the IBM 4341-1 up through the 3081. The AS/6100 Series was announced in April 1982 and is intended to fill the performance gap between IBM's 4341-2 and the new 3083E. The AS/6100 Series will eventually replace the AS/3000 Series and AS/5000 Series, which are now in limited new production.

The Comparison Charts

The principal characteristics of 42 processors that are plugcompatible with IBM computers are presented in the accompanying comparison charts. The entries for each model are spread across two facing pages to maximize the amount of useful information in the charts. All information in the charts was furnished by the six vendors whose products are represented.

The entries on the left-hand pages of the comparison charts and their significance are explained in the following paragraphs:

Model refers to the product number as known in the equipment price book or list of the vendor or manufacturer.

Date of introduction indicates when the processor was first announced to the public in the U.S.

Production status indicates whether the processor is now in new production or being sold from returned and refurbished stocks.

Operating systems indicates the IBM monitoring software that will run on the processor. All operating systems that apply to a particular processor are specified.

Virtual storage capability defines the presence of a hardware/software feature enabling the user to access and utilize memory space without regard to its existence in real main memory or auxiliary memory space.

The *Clock comparator* is a hardware feature that causes an interruption when the time-of-day equals or exceeds the value specified by a program or virtual machine.

The *CPU timer* measures the elapsed processing unit time and causes an interruption when a previously specified amount of time has elapsed.

Control registers are used for operating systems control of relocation, priority interruption, program event recording, error recovery, and masking operations.

CPU one-level addressing is a synonym for direct addressing, where the instruction contains the actual address of the data being requested.

A doubleword buffer consists of a 64-bit area temporarily reserved for data used in performing an I/O operation.

The *interval timer* is a 32-bit decremental counter that is reduced by one several hundred times per second. The timer generates an interrupt when the contained value is decremented from a positive to a negative number.

Machine check handling analyzes errors and attempts recovery by retrying the failed instruction if possible. If retry is unsuccessful, it attempts to correct the malfunction or to isolate the affected task.

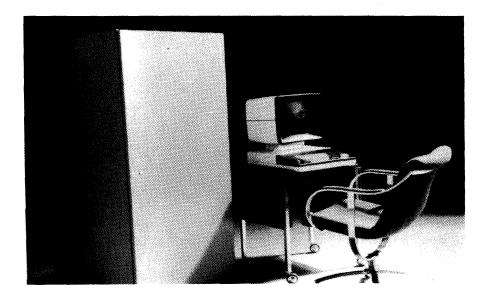
Multiple bus architecture implies that the various segments of the processor (namely, memory, arithmetic and logic, central control, etc.) are tied together by more than one central bus.

Storage protection determines the right of access to main storage by matching a protection key associated with a store or fetch reference to main storage with a storage key associated with each block of main storage.

The *time-of-day-clock* is incremented once every microsecond and provides a consistent measure of elapsed time suitable for the indication of data and time.

Some channels have the capability to perform *channel* command retry, a channel and control-unit procedure that causes a command to be retried without requiring an I/O interruption.

Channel indirect addressing (CIA) is a companion feature to dynamic address translation, providing data addresses for I/O operations. CIA permits a single channel command word to control the transmission of data that crosses noncontiguous pages in real main storage. If CIA is not indicated, then channel one-level (direct) addressing is employed.



The Magnuson M80 Series now consists of seven models that are compatible with IBM's 4300 Series. The entry-level M80/30 shown here was introduced in August 1981. The basic system includes 512K bytes of memory expandable to 8192K bytes, a console, and 2 I/O channels.

The byte oriented operand feature permits storage operands of most non-privileged operations to appear on any byte boundary. Instructions must appear on even byte boundaries. The feature does not pertain to instruction addresses.

The extended precision floating point feature provides instructions to handle floating point numbers with a fraction of 28 hexadecimal digits. The characteristic is seven bits plus sign in short and extended floating point numbers.

The high speed floating point feature provides a means for improved execution of the floating point instruction set.

The System/370 Universal Instruction set is composed of storage protection, standard instruction set, decimal arithmetic, extended precision, dynamic address translation, and instructions to facilitate programming and reduce execution times for record blocking and unblocking.

The console audible alarm is a device activated when predetermined events occur that require operator attention or intervention for system operation.

The *integrated console printer* is an integral part of the system console, furnishing hard copy output from the console display.

A *light pen* is a photosensitive stylus used to detect and identify elements displayed on the console CRT.

A remote console is a console attached to a system through a data link. The remote console is configured in addition to the standard console.

The remote data link allows establishment of communications with a technical data center to remotely diagnose system malfunctions.

The console file is the basic microprogram loading device for the system, containing a read-only file device. The medium read by this device contains all the microcode for field engineering device diagnostics, basic system features, and any optional system features.

The CPU activity monitor can be either hardware or software. It provides a measure of CPU utilization by various hardware or software elements.

The extended control mode (EC) is a mode in which all features of the System/370 computing system, including dynamic address translation, are operational.

Program event recording is a hardware feature used to assist in debugging programs by detecting and recording program events.

The virtual machine assist feature provides an assist to VM/370 firmware emulation of certain privileged operations. The feature causes a reduction in real supervisor time used by VM/370 to control the operation of virtual storage operating system such as DOS/VS and OS/VS1.

1401/1440/1460 compatibility provides the system with the ability to execute 1401/1440/1460 instructions under specific conditions of minimum and matching configurations.

Under other features and comments any additional information that may help to give you a feel for the distinctive attributes of each unit is included.

The right-hand pages of the charts compare Processor Performance, I/O Channels, Control Storage, Pricing, and Availability, and identify the manufacturer and vendor of each processor. These entries should all be self-explanatory.

MODEL	Amdahl 470V/5	Amdahl 470V/5-II	Amdahi 470V/6	Amdahi 470V/6-II
SYSTEM PARAMETERS				
Date of introduction	3/28/77	10/17/78	9/11/74	2/9/77
Date of first delivery	9/77	4/79	6/75	8/77
Number installed to date	Proprietary information	Proprietary information	Proprietary information	Proprietary information
Production status	Not in new production	Not in new production	Not in new production	Not in new production
Operating systems	The miner production	The in the production	The in the production	Total maney production
DOS/VS	Yes	Yes	Yes	Yes
DOS/VSE	Yes	Yes	Yes	Yes
OS/VS1	Yes	Yes	Yes	Yes
	Yes	Yes	Yes	Yes
SVS	Yes	Yes	Yes	Yes
MVS		Yes	Yes	
VM/370	Yes	1	1 ' '	Yes
VM/SP	Yes	Yes	Yes	Yes
Others	ACP, MFT, MVT	ACP, MFT, MVT	ACP, MFT, MVT	ACP, MFT, MVT
PROCESSING FEATURES				}
Virtual storage capability	Standard	Standard	Standard	Standard
Processor arrangements			1	
Uniprocessor	Yes	Yes	Yes	Yes
Attached processor		l —) —	1 —
Front end to	_	l —	\ _	
Back end to	-	l —	1 -	_
Multiprocessor	1 —	 _	1_	1 —
Minimum in complex	l _			
Maximum in complex	l <u> </u>		l	
Clock comparator	Standard	Standard	Standard	Standard
CPU timer	Standard	Standard	Standard	Standard
Control registers	Standard	Standard	Standard	Standard
CPU one-level addressing	Standard	Standard	Standard	Standard
Doubleword buffer	Standard	Standard	Standard	Standard
Interval timer	Standard	Standard	Standard	Standard
Machine check handling	Standard	Standard	Standard	Standard
-	Standard	Standard	Standard	Standard
Multiple bus architecture	Standard	Standard	Standard	Standard
Storage protection		Standard	Standard	Standard
Time-of-day-clock	Standard	l '	1	1
Channel command retry	Standard	Standard	Standard	Standard
Channel indirect addressing	Standard	Standard	Standard	Standard
Byte oriented operand feature	Standard	Standard	Standard	Standard
Extended precision floating point	Standard	Standard	Standard	Standard
High speed floating point	Standard	Standard	Standard	Standard
System/370 Universal Instruction set	Standard	Standard	Standard	Standard
Console audible alarm	Standard	Standard	Standard	Standard
Integrated console printer	No	No	No	No
Light pen	No	No	No	No
Remote console	Standard	Standard	Standard	Standard
Remote data link	Standard	Standard	Standard	Standard
Console file	Standard	Standard	Standard	Standard
CPU activity monitor	Optional	Optional	Optional	Optional
Extended control mode	Standard	Standard	Standard	Standard
Program event recording	Standard	Standard	Standard	Standard
Virtual machine assist	VM/SA	VM/SA	VM/SA	VM/SA
1401/1440/1460 compatibility	No	No	No	No
	470	0 4704/5 0	0 4701//5-0	G., 4701/5 G
OTHER FEATURES &	470 accelerator; two-byte	See 470V/5 Comments	See 470V/5 Comments	See 470V/5 Comments
COMMENTS	channel interface optional	1	1	
	on all models; all systems	1	1	1
	air cooled			
	1	I	l .	1

Amdahl 470V/5	Amdahl 470V/5-II	Amdahl 470V/6	Amdahl 470V/6-II	MODEL
32.5	32.5	32.5	32.5	PROCESSOR PERFORMANCE Machine cycle time, nanoseconds
]	j	Relative performance*
BM 370 Mod. 168-3 or 3032 9 to 1.1	IBM 370 Mod. 168-3 or 3032 1.0 to 1.2	IBM 370 Mod. 168-3 or 3032 1.3 to 1.5	IBM 370 Mod. 168-3 or 3032 1.4 to 1.6	To 'Performance of
—	-	_	-	То
 470V/5-II	470V/6	470V/6-II	_	Performance of Field Upgradable to
Dynamic NMOS	Dynamic NMOS	Dynamic NMOS	Dynamic NMOS	MAIN STORAGE Storage type
Yes	Yes	Yes	Yes	Checking Parity
Yes	Yes	Yes	Yes	Error detection & correction
1 	1	1	1_	No. of check bits per byte No. of check bits per word
320	320	320	320	Read cycle, nanoseconds
320 4	320	320	320	Write cycle, nanoseconds Bytes fetched per cycle
4M	4M	4M	4M	Minimum capacity, bytes
16M 4M	16M 4M	16M 14M	16M 14M	Maximum capacity, bytes Increment size, bytes
Yes	Yes	Yes	Yes	Interleaving
3 16	8 16	8 16	8 16	Minimum number of ways Maximum number of ways
	J	1		<u>'</u>
Yes Bipolar RAM	Yes Bipolar RAM	Yes Bipolar RAM	Yes Bipolar RAM	BUFFER (CACHE) STORAGE Storage type
65	65	65	65	Cycle time, nanoseconds
4 16K	4 32K	14 16K	4 32K	Bytes fetched per cycle Minimum capacity, bytes
16K	32K	16K	32K	Maximum capacity, bytes
				I/O CHANNELS
8	8 16	8 16	8 16	Selector channels standard Selector channels optional
16 3	18	18	18	Block multiplexers standard
16	16	16	16	Block multiplexers optional
8 16	8 16	8 16	8 16	Byte multiplexers standard Byte multiplexers optional
			<u> </u>	Subchannels per channel
256 256	256 256	256 256	256 256	On a block multiplexer On a byte multiplexer
256	256	256	256	On a selector
Yes	Yes	Yes	Yes	Channel to channel adapter Maximum channel data rates
2M	2M	2M	2M	Block multiplexer, bytes/sec.
110K	110K	110K 2M	110K 2M	Byte multiplexer, bytes/sec.
2M 13M	2M 13M	13M	13M	Selector channel, bytes/sec. Aggregate data rate, bytes/sec.
Yes	Yes	Yes	Yes	Data Streaming
N/A	N/A	N/A	N/A	CONTROL STORAGE
_]_]_		Storage type Access time, nanoseconds
_		_	_	Word size, bits
_			-	Minimum number of words Maximum number of words
_	NAMES OF THE PARTY	_	_	Control storage usage
_				PRICING & AVAILABILITY
Contact vendor Yes	Contact vendor Yes	Contact vendor Yes	Contact vendor Yes	Purchase of CPU with min. memory Lease terms offered
Yes	Yes	Yes	Yes	Vendor's
— Contact vendor	Contact vendor	Contact vendor	- \$45,500/mo. (4-yr)	Third party Lease of CPU with min. memory (1-
4MB	4MB	4MB	4MB	Memory increment size
\$150,000 Yes	\$150,000 Yes	\$150,000 Yes	\$150,000 Yes	Memory increment purchase Vendor offered maintenance
-	-	-	-	Prime time
— \$8,925∕mo.	 \$9,030/mo.	— \$9,275∕mo.	 \$9,380/mo.	Additional hours 24 hour
_		<u> </u>	-	Other plans
Amdahl	Amdahl	Amdahi	Amdahl	Manufacturer
Amdahl	Amdahl	Amdahl	Amdahl	Vendor
	Í	ł	1	1

^{*}As rated by the PCM vendor.

PROCESSING FEATURES Virtual storage capability Processor arrangements Uniprocessor Attached processor Front end to Back end to Multiprocessor Minimum in complex Clock comparator Clock comparator Control registers Standard Standard Standard Standard Standard Standard Control registers Standard Sta	idahi 470V/7B	Amdahi 470V/7C
11/79 3/80 3/8/77 8/1/79 3/80 3/		
Date of first delivery Number installed to date Production status Operating systems DDS/VS DDS/VSE Yes		1
Number installed to date Production status Operating systems DOS/VS DOS/VSE Yes		11/18/80
Production status Operating systems DOS/VS DOS/VSE Yes		3rd Quarter 1981
Operating systems DOS/VS DOS/VSE DOS/VSE Yes		l —
DOS/VSE OS/VS1 Ves		Active
DOS/VSE OS/VS1 Ves		1
DOS/VSE OS/VS1 Ves		Yes
SS/VS1 SVS WVS Ves		Yes
SVS MVS VVS VYes VYes VYes VYes VYes VYes VYes VYes		Yes
W/S VM/370 Yes		1
VM/SP Ves Ves Ves Ves MVS/XA, ACP, MFT, MVT ROCESSING FEATURES Virtual storage capability Processor arrangements Uniprocessor Attached processor Attached processor Front end to Back end to Multiprocessor Minimum in complex Maximum in complex Maximum in complex Maximum in complex Maximum in complex Clock comparator Clotk comparator Curtor registers Standard Standard Standard Standard Standard Standard Curtor registers Standard Stan		Yes
Others Ot		Yes
PROCESSING FEATURES Virtual storage capability Processor arrangements Uniprocessor Attached processor Attached processor Front end to Back end to Back end to CPU timer CPU timer CPU one-level addressing Unutiproce back handling Multiple bas architecture Standard S		Yes
PROCESSING FEATURES Virtual storage capability Processor arrangements Uniprocessor Attached processor Attached processor Attached processor Front end to Back end to Multiprocessor Minimum in complex — Maximum in complex — Clock comparator Clock comparator Clock comparator Control registers Standard Standard Standard Standard CPU one-level addressing Standard St		Yes
Virtual storage capability Processor arrangements Ves Ves Ves Ves Ves Ves Ves Ves Ves Ve	A, ACP, MFT, MVT	MVS/XA, ACP, MFT, MVT
Processor arrangements Uniprocessor Ves Yes Yes Yes Yes Yes Attached processor ———————————————————————————————————		
Processor arrangements Uniprocessor	1	Standard
Uniprocessor Attached processor Front end to Back end Standard Sta		1
Attached processor Front end to — — — — — — — — — — — — — — — — — —		Yes
Front end to Back end Standard St		1
Back end to Multiprocessor Minimum in complex Maximum in complex ————————————————————————————————————		1 —
Multiprocessor Minimum in complex Maximum in complex CPU timer Clock comparator CPU timer Standard Standard CPU timer Standard Standard Standard Standard Standard Standard CPU one-level addressing Standard Machine check handling Standard Machine check handling Standard Sta		<u> </u>
Minimum in complex Maximum in complex CPU timer CPU timer CPU timer CPU timer CPU timer Standard Machine check handling Standard		
Maximum in complex Clock comparator Clock comparator Clock comparator Control registers CPU timer Standard Control registers Standard Machine check handling Machine check handling Standard Sta		j —
Clock comparator CPU timer Standard Machine check handling Standard Standar		-
CPU timer Control registers Standard Control registers Standard St		1—
CPU timer Control registers CPU one-level addressing Doubleword buffer Doubleword buffer Standard Stan	1	Standard
Control registers CPU one-level addressing Standard Machine check handling Machine check handling Standard Stan		Standard
CPU one-level addressing Doubleword buffer Interval timer Standard Machine check handling Multiple bus architecture Standard Standard Standard Standar		Standard
Doubleword buffer Standard St		
Interval timer Machine check handling Multiple bus architecture Standard Channel command retry Standard Standar		Standard
Machine check handling Multiple bus architecture Standard		Standard
Multiple bus architecture Standard Storage protection Standard Channel command retry Standard	1	Standard
Storage protection Time-of-day-clock Standard St	ı	Standard
Storage protection Time-of-day-clock Standard St	1	Standard
Time-of-day-clock Channel command retry Channel indirect addressing Byte oriented operand feature Extended precision floating point High speed floating point Standard Standar	1	Standard
Channel command retry Channel indirect addressing Byte oriented operand feature Extended precision floating point High speed floating point Standard Integrated console printer No No No No Remote console Standard Standar		Standard
Channel indirect addressing Byte oriented operand feature Extended precision floating point High speed floating point Standard St		
Byte oriented operand feature Extended precision floating point High speed floating point System/370 Universal Instruction set Console audible alarm Integrated console printer Light pen No No No No Remote console Remote data link Console file CPU activity monitor Extended control mode Program event recording Virtual machine assist VM/SA 1401/1440/1460 compatibility Standard Sta		Standard
Extended precision floating point High speed floating point System/370 Universal Instruction set Console audible alarm Integrated console printer Light pen Remote console Remote data link Console file CPU activity monitor Extended control mode Program event recording Virtual machine assist VM/SA 1401/1440/1460 compatibility Extended precision floating point Standard St		Standard
High speed floating point System/370 Universal Instruction set Console audible alarm Integrated console printer Light pen Remote console Remote data link Console file Control mode Standard Coptional Coptional Coptional Coptional Coptional Coptional Coptional Console file Console file Control mode Coptional Coptional Coptional Coptional Coptional Coptional Console file Cooled; two-byte channel interface optional Coptional Cooled; two-byte channel interface optional Coptional Coptional Coptional Coptional Coptional Coptional Cooled; two-byte channel interface optional Coptional Coptional Coptional Coptional Coptional Coptional Coptional Coptional Cooled; two-byte channel interface optional Coptional		Standard
System/370 Universal Instruction set Console audible alarm Integrated console printer Light pen Remote console Remote data link Console file COPU activity monitor Extended control mode Program event recording Virtual machine assist VM/SA 1401/1440/1460 compatibility Standard Stand	ı	Standard
System/370 Universal Instruction set Console audible alarm Integrated console printer Light pen Remote console Remote data link Console file Console	ı	Standard
Console audible alarm Integrated console printer Light pen Remote console Remote data link Console file CPU activity monitor Extended control mode Program event recording Virtual machine assist VM/SA 1401/1440/1460 compatibility Standard No Standard Standard Standard Standard Standard Standard Optional Standard No No No No No No Air cooled; two-byte channel interface optional Standard interface optional Standard Air cooled; accelerator; two-byte channel interface optional	1	Standard
Integrated console printer Light pen Remote console Remote data link Console file CPU activity monitor Extended control mode Program event recording Virtual machine assist 1401/1440/1460 compatibility COMMENTS No		Standard
Light pen Remote console Remote data link Console file CPU activity monitor Extended control mode Program event recording Virtual machine assist 1401/1440/1460 compatibility DTHER FEATURES & COMMENTS No Standard Stand	•	No
Remote console Remote data link Console file CPU activity monitor Extended control mode Program event recording Virtual machine assist 1401/1440/1460 compatibility CIMIENTS Standard VM/SA No Air cooled; two-byte channel interface optional Standard Sta		
Remote data link Console file CPU activity monitor Extended control mode Program event recording Virtual machine assist 1401/1440/1460 compatibility DTHER FEATURES & COMMENTS Remote data link Standard VM/SA VM/SA No Air cooled; two-byte channel interface optional Standard Stand		No
Console file CPU activity monitor CPU activity monitor Extended control mode Program event recording Virtual machine assist 1401/1440/1460 compatibility CPU activity monitor Standard Standard Standard Standard Standard VM/SA No Air cooled; two-byte channel interface optional Standard Sta		Standard
CPU activity monitor Extended control mode Program event recording Virtual machine assist 1401/1440/1460 compatibility DTHER FEATURES & COMMENTS COMMENTS COMMENTS Optional Standard Standard Standard VM/SA No Optional Standard Standard VM/SA No Air cooled; two-byte channel interface optional Optional Standard Standard VM/SA No Air cooled; 470		Standard
Extended control mode Program event recording Virtual machine assist 1401/1440/1460 compatibility DITHER FEATURES & COMMENTS Air cooled; two-byte channel interface optional Standard Standard Standard VM/SA No Air cooled; two-byte channel interface optional Standard St		Standard
Extended control mode Program event recording Virtual machine assist VM/SA 1401/1440/1460 compatibility OTHER FEATURES & COMMENTS Air cooled; two-byte channel interface optional Standard Standard VM/SA No Air cooled; two-byte channel interface optional Standard Standa		Optional
Program event recording Virtual machine assist 1401/1440/1460 compatibility DITHER FEATURES & COMMENTS Air cooled; two-byte channel interface optional Air cooled; two-byte optional Air cooled; two-byte optional Air cooled; two-byte channel interface optional		Standard
Virtual machine assist 1401/1440/1460 compatibility VM/SA No VM/SA No VM/SA No VM/SA No VM/SA No VM/SA No Air cooled; two-byte channel interface optional VM/SA No VM/SA No VM/SA No Air cooled; 470 accelerator; two-byte channel interface optional accelerator channel interface optional		Standard
1401/1440/1460 compatibility No No No No No No No No Air cooled; two-byte channel interface optional No No No No No No No No No N	•	VM/SA
Air cooled; two-byte channel interface optional Air cooled; accelerator; two-byte accelerator channel interface optional channel interface optional accelerator channel interf		
COMMENTS channel interface optional accelerator; two-byte optional channel interface optional accelerator extended poptional accelerator channel interface optional accelerator channel interface optional accelerator channel interface optional accelerator optional accelerator.		No
COMMENTS channel interface optional accelerator; two-byte accelerator channel interface optional accelerator extended poptional accelerator channel interface optional accelerator channel interface optional accelerator channel interface optional accelerator optional accelerator extended poptional accelerator optional accelerator.	d: 470	Air appled: 470
optional channel interface extended p optional accelerator channel int		Air cooled; 470
optional accelerator channel int		accelerator; two-byte
channel int	l performance	channel interface
	tor; two-byte	optional
optional	interface	
		1

Amdahl 470V/7	Amdahi 470V/7A	Amdahl 470V/7B	Amdahl 470V/7C	MODEL
29	29	29	29	PROCESSOR PERFORMANCE Machine cycle time, nanoseconds
BM 3033U 1.1	IBM 3033N 1.1 to 1.2	IBM 3032 1.4 to 1.6	IBM 3033S	Relative performance* To Performance of
 470V/8	 470V/7	 470V/7A	 470V/7B	To Performance of Field Upgradable to
Oynamic NMOS	Dynamic NMOS	Dynamic NMOS	Dynamic NMOS	MAIN STORAGE Storage type
/es /es	Yes Yes 1	Yes Yes 1	Yes Yes 1	Checking Parity Error detection & correction No. of check bits per byte
	320 320 4	320 320 4	320 320 4	No. of check bits per word Read cycle, nanoseconds Write cycle, nanoseconds Bytes fetched per cycle
BM 12M IM (es	8M 32M 4M Yes	8M 32M 4M Yes	8M 16M 4M Yes	Minimum capacity, bytes Maximum capacity, bytes Increment size, bytes Interleaving
3 6	8	16	8 16	Minimum number of ways Maximum number of ways
Yes Bipolar RAM 54 32K 32K	Yes Bipolar RAM 58 4 32K 32K	Yes Bipolar RAM 58 4 32K 32K	Yes Bipolar RAM 58 4 32K 32K	BUFFER (CACHE) STORAGE Storage type Cycle time, nanoseconds Bytes fetched per cycle Minimum capacity, bytes Maximum capacity, bytes
12 32 12 32 32 12	8 32 8 32 8 32	8 32 8 32 8 32	8 16 8 16 8	I/O CHANNELS Selector channels standard Selector channels optional Block multiplexers standard Block multiplexers optional Byte multiplexers optional Byte multiplexers optional
256 256 256 Yes	256 256 256 256 Yes	256 256 256 256 Yes	256 256 256 256 Yes	Subchannels per channel On a block multiplexer On a byte multiplexer On a selector Channel to channel adapter
2M 110K 2M 18M fes	2M 110K 2M 18M Yes	2M 110K 2M 18M Yes	2M 110K 2M 18M Yes	Maximum channel data rates Block multiplexer, bytes/sec. Byte multiplexer, bytes/sec. Selector channel, bytes/sec. Aggregate data rate, bytes/sec. Data Streaming.
N/A	N/A	N/A	N/A	CONTROL STORAGE Storage type
- - - -	<u>-</u>	= = = =		Access time, nanoseconds Word size, bits Minimum number of words Maximum number of words Control storage usage
\$1,850,000 /es	\$1,375,000 Yes	\$1,225,000 Yes	\$1,150,000 Yes	PRICING & AVAILABILITY Purchase of CPU with min. memory Lease terms offered
/es - -70,960/mo. (4-γr) - -150,000 /es	Yes 	Yes \$54,285/mo. (4-yr) 4MB \$150,000 Yes	Yes 	Vendor's Third party Lease of CPU with min. memory (1- Memory increment size Memory increment purchase Vendor offered maintenance
 \$12,170/mo. 	 \$11,400/mo. 	- \$11,140/mo. -	\$9,550/mo.	Prime time Additional hours 24 hour Other plans
amdahl amdahl	Amdahi Amdahi	Amdahi Amdahi	Amdahl Amdahl	Manufacturer Vendor
			* 1	

*As rated by the PCM vendor.

MODEL	Amdahi 470V/8	Amdahl 5860	Amdahl 5870	Amdahl 5880
SYSTEM PARAMETERS				
Date of introduction	10/17/78	11/18/80	10/27/81	11/18/80
	9/79	3rd Quarter 1982	3rd Quarter 1983	3rd Quarter 1983
Date of first delivery	9/ /9	3rd Quarter 1982	Sid Quarter 1963	3rd Quarter 1983
Number installed to date	Antina		A sais as	\ _
Production status	Active	Active	Active	Active
Operating systems	1	1	1	
DOS/VS	Yes	Yes	Yes	Yes
DOS/VSE	Yes	Yes	Yes	Yes
OS/VS1	Yes	Yes	Yes	Yes
SVS	Yes	Yes	Yes	Yes
MVS	Yes	Yes	Yes	Yes
VM/370	Yes	Yes	Yes	Yes
VM/SP	Yes	Yes	Yes	Yes
Others	MVS/XA, ACP, MFT, MVT	ACP, MVS/XA	MVS/XA	MVS/XA
PROCESSING FEATURES				
Virtual storage capability	Standard	Standard	Standard	Standard
Processor arrangements	1	1	l	
Uniprocessor	Yes	Yes	No	No
Attached processor	_	_	1-	l —
Front end to		-	_	
Back end to			-	_
Multiprocessor	_	_	Yes	Yes
Minimum in complex	1_	I _	2	2
Maximum in complex	_	l _	2	2
Clock comparator	Standard	Standard	Standard	Standard
CPU timer	Standard	Standard	Standard	Standard
Control registers	Standard	Standard	Standard	Standard
CPU one-level addressing	Standard	Standard	Standard	Standard
Doubleword buffer	Standard	Standard	Standard	Standard
Interval timer	Standard	Standard	Standard	Standard
	Standard	Standard	Standard	Standard
Machine check handling				
Multiple bus architecture	Standard	Standard	Standard	Standard
Storage protection	Standard	Standard	Standard	Standard
Time-of-day-clock	Standard	Standard	Standard	Standard
Channel command retry	Standard	Standard	Standard	Standard
Channel indirect addressing	Standard	Standard	Standard	Standard
Byte oriented operand feature	Standard	Standard	Standard	Standard
Extended precision floating point	Standard	Standard	Standard	Standard
High speed floating point	Standard	Standard	Standard	Standard
System/370 Universal Instruction set	Standard	Standard	Standard	Standard
Console audible alarm	Standard	Standard	Standard	Standard
Integrated console printer	No	No	No	No
Light pen	No	No	No	No
Remote console	Standard	Standard	Standard	Standard
Remote data link	Standard	Standard	Standard	Standard
Console file	Standard	Standard	Standard	Standard
CPU activity monitor	Optional	Standard	Standard	Standard
Extended control mode	Standard	Standard	Standard	Standard
Program event recording	Standard	Standard	Standard	Standard
Virtual machine assist	VM/SA	VM/SA	VM/SA	VM/SA
1401/1440/1460 compatibility	No	No No	No	No No
		-		-
OTHER FEATURES &	Air-cooled; two-byte	Distributed microcode;	See 5860 Comments	See 5860 Comments
COMMENTS	channel interface	Macrocode in all models	1	
	optional			
	ľ	1	1	1

Amdahl 470V/8	Amdahl 5860	Amdahl 5870	Amdahi 5880	MODEL
••				PROCESSOR PERFORMANCE
26	24	24	24	Machine cycle time, nanoseconds Relative performance*
IBM 3033U	IBM 3081D	IBM 3081D	IBM 3081D	l To
1.3	1.3	2.2	2.3	Performance of To
	_	1-	<u> </u> —	Performance of
_	5870, 5880	5880	-	Field Upgradable to
				MAIN STORAGE
Dynamic NMOS	Dynamic NMOS	Dynamic NMOS	Dynamic NMOS	Storage type
Yes	Yes	Yes	Yes	Checking Parity
Yes	Yes	Yes	Yes	Error detection & correction
1	1	11	1.0	No. of check bits per byte No. of check bits per word
320	280	280	280	Read cycle, nanoseconds
320 4	280	280	280	Write cycle, nanoseconds
4 8M	8 16M	8 16M	8 16M	Bytes fetched per cycle Minimum capacity, bytes
32M	32M	32M	32M	Maximum capacity, bytes
4M Yes	8M Yes	8M Yes	8M Yes	Increment size, bytes Interleaving
8	16	16	16	Minimum number of ways
16	16	16	16	Maximum number of ways
Yes	Yes	Yes	Yes	BUFFER (CACHE) STORAGE
Bipolar RAM	Two Bipolar RAMs	Two Bipolar RAMs	Two Bipolar RAMs	Storage type
52 4	8	8	8	Cycle time, nanoseconds Bytes fetched per cycle
64K	64K	64K	64K	Minimum capacity, bytes
64K	64K	64K	64K	Maximum capacity, bytes
				I/O CHANNELS
12	_	_	_	Selector channels standard
32 12	16	16	16	Selector channels optional Block multiplexers standard
32	32	32	32	Block multiplexers optional
12 32	2 0	2	2 0	Byte multiplexers standard
32	10	10	10	Byte multiplexers optional Subchannels per channel
256	256	256	256	On a block multiplexer
256 256	256	256	256	On a byte multiplexer On a selector
Yes	Yes	Yes	Yes	Channel to channel adapter
2M	6M	6м	6M	Maximum channel data rates
110K	200K	200K	200K	Block multiplexer, bytes/sec. Byte multiplexer, bytes/sec.
2M				Selector channel, bytes/sec.
21M Yes	80M Yes	80M Yes	80M Yes	Aggregate data rate, bytes/sec. Data Streaming
	1.55	1.00	1.55	
N/A	4K RAM	4K RAM	4K RAM	CONTROL STORAGE Storage type
<u>-</u> -	7.5	7.5	7.5	Access time, nanoseconds
	Variable	Variable	Variable	Word size, bits
-	Variable Variable	Variable Variable	Variable Variable	Minimum number of words Maximum number of words
_	Variable	Variable	Variable	Control storage usage
				PRICING & AVAILABILITY
\$2,075,000	\$3,600,000	\$5,400,000	\$7,100,000 (32M memory)	Purchase of CPU with min. memor
Yes Yes	Yes Yes	Yes Yes	Yes Yes	Lease terms offered Vendor's
	 	! —	<u> </u>	Third party
\$76,100/mo. (4-yr)	\$90,350/mo. (4-yr)	\$135,525/mo. (4-yr.)	\$78,200/mo. (4-yr.)	Lease of CPU with min. memory (1
4MB \$150,000	8MB \$200,000	8MB \$200,000	8MB \$200,000	Memory increment size Memory increment purchase
Yes	Yes	Yes	Yes	Vendor offered maintenance
				Prime time Additional hours
\$12,650/mo.	\$11,300/mo.	\$18,675/mo.	\$20,815/mo.	24 hour
	-	-	<u> </u> -	Other plans
Amdahl	Amdahi	Amdahl	Amdahl	Manufacturer
Amdahl	Amdahl	Amdahl	Amdahl	Vendor

^{*}As rated by the PCM vendor.

MODEL	Cambex 1636	Cambex 1641	Cambex 1651	Control Data Omega 480-l
SYSTEM PARAMETERS				
Date of introduction	August 1980	August 1980	August 1980	6/77
	4th Quarter 1980	4th Quarter 1980	3rd Quarter 1981	6/77
Date of first delivery	4th Quarter 1960	401 Quarter 1980	3rd Quarter 1961	1
Number installed to date		A set us	Assissa	Over 120
Production status	Active	Active	Active	Not in new production
Operating systems				
DOS/VS	Yes	Yes	Yes	Yes
DOS/VSE	Yes	Yes	Yes	Yes
OS/VS1	Yes	Yes	Yes	Yes
SVS	Yes	Yes	Yes	Yes
MVS	No	Yes	Yes	Yes
VM/370	Yes	Yes	Yes	Yes
VM/SP	Yes	Yes	Yes	No
Others	ACP	ACP	ACP	No
	1			
PROCESSING FEATURES	1		Ì	l
Virtual storage capability	Standard	Standard	Standard	Standard
Processor arrangements	1			
Uniprocessor	Yes	Yes	Yes	Yes
Attached processor	-		_	l —
Front end to	-	_	l —	1 —
Back end to	_	_	_	-
Multiprocessor	l —	_	_	1 _
Minimum in complex			_	_
Maximum in complex			_	
Clock comparator	Standard	Standard	Standard	Standard
CPU timer	Standard	Standard	Standard	Standard
Control registers	Standard	Standard	Standard	Standard
CPU one-level addressing	Standard	Standard	Standard	No
Doubleword buffer	Standard	Standard	Standard	Standard
	Standard	Standard	Standard	Standard
Interval timer			Standard	
Machine check handling	Standard	Standard		Standard
Multiple bus architecture	No	No Occasional	No	No
Storage protection	Standard	Standard	Standard	Standard
Time-of-day-clock	Standard	Standard	Standard	Standard
Channel command retry	Standard	Standard	Standard	Standard
Channel indirect addressing	Standard	Standard	Standard	Standard
Byte oriented operand feature	Standard	Standard	Standard	Standard
Extended precision floating point	Standard	Standard	Standard	Standard
High speed floating point	Standard	Standard	Standard	Standard
System/370 Universal Instruction set	Standard	Standard	Standard	Standard
Console audible alarm	Standard	Standard	Standard	Standard
Integrated console printer	Optional	Optional	Optional	Optional
Light pen	No	No	No	No
Remote console	Optional	Optional	Optional	No
Remote data link	Optional	Optional	Optional	No
Console file	Standard	Standard	Standard	Standard
CPU activity monitor	No	No	No	No
Extended control mode	Standard	Standard	Standard	Standard
Program event recording	Standard	Standard	Standard	Standard
Virtual machine assist	Standard	Standard	Standard	Standard
1401/1440/1460 compatibility	No	No	No	No
		· ·-		"-
	1			
OTHER FEATURES &	Formerly Cambridge	1641 upgraded from 1636	1651 available on field	
COMMENTS	Memories; 1636 upgraded		upgrade basis only	1
	from 1638			}
	1		\	1
	1			
	İ		l	Į.
	1		l	Į
			ł	1
	·			i e

Mail	Cambex 1636	Cambex 1641	Cambex 1651	Control Data Omega 480-I	MODEL
Mail					
M4331 2 IBM 4341 1 IBM 4341 2 IBM 4331 2 IBM 43	50	1		50	Machine cycle time, nanoseconds Relative performance*
### Test	BM 4331-2 1.1 to 1.3				То
Sambex 1641 Cambex 1651 — 480-ll Field Upgradable to Wyramic NMOS Dynamic NMOS Dynamic NMOS Dynamic NMOS Dynamic NMOS Static NMOS MAIN STORAGE Storage type Checking Party Part	- -	_			То
Visaria NMOS Dynamic NMOS Dynamic NMOS Static MMOS Check his per word	Cambex 1641	Cambex 1651	-	480-II	
Yes	Dynamic NMOS	Dynamic NMOS	Dynamic NMOS	Static NMOS	
No. of check bits per byte		_		Yes	
A00	Yes 	Yes —			
A00	 400	400	400	400	No. of check bits per word
16	400 400				Write cycle, nanoseconds
Maximum capacity, bytes Maximum capacity	8			8	Bytes fetched per cycle
Mo	1M				Minimum capacity, bytes
No	1M				
Pes	No				
Yes Bipolar RAM Bi		-		l —	Minimum number of ways
Bipolar RAM	_		_		Maximum number of ways
100	No —			1	
Section	_	100	100	,	Cycle time, nanoseconds
BK BK BK — Maximum capacity, bytes				_	
Selector channels standard Selector channels optional Selector channels optional Block multiplexers standard Block multiplexers optional Block multiplexers optional Byte multiplexers standard Block multiplexers optional Byte multiplexers optional Byte multiplexers optional Subchannels per channel On a block multiplexer of Byte multiplexer optional Byte multiplexers optional Byte multiplexer optional Byte multiplexers optional Byte multiplexer On a byte multiplexer optional Byte multiplexer optional By	_			_	
Selector channels optional Block multiplexers standard Block multiplexers optional Subchannels per channel On a block multiplexer block multiplexer block multiplexer block multiplexer bl					I/O CHANNELS
2	ALIAN .	-	<u> </u>		
2	2	2	4		
O O O O System assist	2	2		2	Block multiplexers optional
256	1				Byte multiplexers standard
56	0	0	10	0	Byte multiplexers optional
Ves Ves — — — — — — — — — — — — — — — — — — —	256			256	
Ses Yes Yes Yes Yes Yes — Channel to channel adapter Maximum channel data rates Maximum channel data rates SOK DK 50K 50K 50K 50K 50K 50K 50K 50K 50K 50	256	256	256	1	
86M OK	 Vac	Vac	Vos	-	
1.86M 1.86M 50K	165	165	1	_	
In Mole of the No	1.86M				Block multiplexer, bytes/sec.
ITM No	50K	50K		1	Byte multiplexer, bytes/sec.
No No No No No Data Streaming	11M	11M	l l		Aggregate data rate bytes/sec
Bipolar RAM 5	No	No	No		
25 36 36 37 2K 44K 44K 144K 144K 15struct. microcode, op- rating system assist 120,000 85; check vendor 96; check vendor 15c, check vendo	Division DANA	Discolor DAM	D: de DAM	B: 1 8 044	
36 2K 44K 144K 1A4K 1A4K 1nstruct. microcode, operating system assist 120,000	Bipolar KAIVI 25				Storage type
2K 44K 144K 144K 144K 144K 144K 144K 144	36	36	36		
Instruction microcode, operating system assist Instruction microcode, operations assist Instruction microcode, operating system assist Instruction microcode, operations asperations. Instruction microcode operations. Instruction microcode, operations asp	72K		72K	54K	
rating system assist 120,000 styles; check vendor es; check vendor yes; check vendor yes; check vendor check vendor 1.5,000 1				144K	
120,000 es; check vendor es; check vendor es; check vendor object vendor	erating system assist				Control storage usage
res; check vendor es; check vendor yes; check vendor yes; check vendor yes; check vendor yes; check vendor yes; check vendor yes; check vendor yes check v		4170.000		****	
es; check vendor heck vendor Check vendor Check vendor MB 1 MB 15,000 es Yes Yes 1 MB 15,000 es Yes Yes Yes 1 MB 15,000 es Yes Yes Yes Yes Yes Yes Yes Yes Memory increment size Memory increment purchase Yes Yes Yes Yes Yes Yes Yes Yes Yes Y					Purchase of CPU with min. memory
Check vendor MB 1MB 1MB 15,000 se Yes Yes Yes Yes Yes Yes Y	Yes; check vendor				
MB 1MB \$15,000	Check vendor	Check vendor	Check vendor		Third party
15,000 es 445/mo. es 4750/mo. yes Y	 1 M R	— 1MB	1NAP	\$6,267 (3-yr.)	Lease of CPU with min. memory (1-
es Yes Yes Yes Yes 445/mo. 445/mo.	\$15,000				
445/mo. s 750/mo. Yes es es hird party available Third party available Tambex ambex ambex lote: Upgrade costs for 19750/mo. Yes Yes Yes Third party available Third party avail	Yes	Yes	Yes	Yes	Vendor offered maintenance
es	\$445/mo.				Prime time
hird party available Third party available Third party available Weekend, holiday Other plans Tambex Cambex Cambex Cambex Control Data Note: Upgrade costs for Note: Upgrade costs for	Yes Yes				
ambex Cambex Cambex Control Data Vendor lote: Upgrade costs for Note: Upgrade costs for	Third party available				
lambex Cambex Cambex Control Data Vendor Note: Upgrade costs for Note: Upgrade costs for	Cambex		Cambex	IPL Systems	Manufacturer
	Cambex	Cambex			
	Note: Upgrade costs for 1636 to 1641; \$58,000				
	222 12 12 11, 400,000		1.547 10 1001, 470,000		
					1

^{*}As rated by the PCM vendor.

MODEL	Control Data Omega 480-II	Control Data Omega 480-III	IPL 4436	IPL 4443
OVOTERA DADAMETEDO				
SYSTEM PARAMETERS	6/77	1070	10/80	10/80
Date of introduction	6/77	1979) ' ' '	
Date of first delivery	1978	1979	4th Quarter 1980	2nd Quarter 1980*
Number installed to date	Over 120	Over 120	Proprietary information	Proprietary information
Production status	Active	Active	Active	Active
Operating systems			1	l
DOS/VS	Yes	Yes	Yes	Yes
DOS/VSE	Yes	Yes	Yes	Yes
0\$/V\$1	Yes	Yes	Yes	Yes
SVS	Yes	Yes	Yes	Yes
MVS	Yes	Yes	Yes	Yes
VM/370	Yes	Yes	Yes	Yes
VM/SP	No	No	Yes	Yes
Others	No	No	MVS/SP	MVS/SP
ROCESSING FEATURES				
Virtual storage capability	Standard	Standard	Standard	Standard
Processor arrangements	Claridara			
Uniprocessor	Yes	Yes	Yes	Yes
• · · · · · · · · · · · · · · · · · ·		_	1	1_
Attached processor	_	-		[_
Front end to	_	1-	-	
Back end to		_	-	-
Multiprocessor	_	-		<u> </u>
Minimum in complex		_	[-	_
Maximum in complex		-	-	
Clock comparator	Standard	Standard	Standard	Standard
CPU timer	Standard	Standard	Standard	Standard
Control registers	Standard	Standard	Standard	Standard
CPU one-level addressing	No	Standard	Standard	Standard
Doubleword buffer	Standard	Standard	Standard	Standard
Interval timer	Standard	Standard	Standard	Standard
Machine check handling	Standard	Standard	Standard	Standard
_	No	No	Standard	Standard
Multiple bus architecture	Standard	Standard	Standard	Standard
Storage protection			Standard	Standard
Time-of-day-clock	Standard	Standard	1	1
Channel command retry	Standard	Standard	Standard	Standard
Channel indirect addressing	Standard	Standard	Standard	Standard
Byte oriented operand feature	Standard	Standard	Standard	Standard
Extended precision floating point	Standard	Standard	Standard	Standard
High speed floating point	Standard	Standard	No	No
System/370 Universal Instruction set	Standard	Standard	Standard	Standard
Console audible alarm	Standard	Standard	Standard	Standard
Integrated console printer	No	Optional	Optional	Optional
Light pen	No	No	No	No
Remote console	No	No	No	No
Remote data link	No	No	Standard	Standard
		Standard	Standard	Standard
Console file	Standard		1	No
CPU activity monitor	No	No	No Standard	
Extended control mode	Standard	Standard	Standard	Standard
Program event recording	Standard	Standard	Standard	Standard
Virtual machine assist	Standard	Standard	Standard	Standard
1401/1440/1460 compatibility	No	No	No	No
OTHER FEATURES &			Over 170 systems installed	System introduced as
COMMENTS	· ·	1	worldwide by IPL licensees	Control Data Omega 480
	1		Control Data (Omega	in March 1979.
•			Series) and Olivetti. All	*First end user system
			systems support the IBM	installed May, 1980
			4300 ECPS mode	
		1	1]
		1		i

Control Data Omega 480-II	Control Data Omega 480-III	IPL 4436	IPL 4443	MODEL
				PROCESSOR PERFORMANCE
50	50	50	50	Machine cycle time, nanoseconds
IBM 4331-2	IBM 4341-2	IBM 4331-2	IBM 4341-1	Relative performance*
1.25	1.22	1.5	1.0	Performance of
-		IBM 4341-10		To
-	=	1.0		Performance of
480-III	-	IPL 4443	IPL 4445	Field Upgradable to
				MAIN STORAGE
Static NMOS	Dynamic NMOS	Dynamic NMOS	Dynamic NMOS	Storage type
Yes	Yes	Yes	Yes	Checking Parity
Yes	Yes	Yes	Yes	Error detection & correction
-		1	1	No. of check bits per byte
		4	4	No. of check bits per word
400	400 400	500 500	500 500	Read cycle, nanoseconds
400 16	116	8	8	Write cycle, nanoseconds Bytes fetched per cycle
1M	2M	ĬĭM	2M	Minimum capacity, bytes
4M	8M	8M	8M	Maximum capacity, bytes
1M	2M	1M or 2M	2M	Increment size, bytes
No —	No	No —	No —	Interleaving Minimum number of ways
_		1=	[=	Maximum number of ways
	1		1	
Yes	Yes	No	Yes	BUFFER (CACHE) STORAGE
ECL 100	ECL 100		ECL 100	Storage type Cycle time, nanoseconds
4	4		4	Bytes fetched per cycle
8K	8K	_	8K	Minimum capacity, bytes
8K	8K		8K	Maximum capacity, bytes
				I/O CHANNELS
		_	—	Selector channels standard
_	4	<u> -</u>	2	Selector channels optional
4 1	4	2 3	3	Block multiplexers standard Block multiplexers optional
i	li	l i	1	Byte multiplexers standard
Ò	o	Ö	Ó	Byte multiplexers optional
		10-50		Subchannels per channel
256	256 256	256 256	256 256	On a block multiplexer
256		256	256	On a byte multiplexer On a selector
_	_	No	No	Channel to channel adapter
			la	Maximum channel data rates
1.85M 50K	1.85M 50K	2M 180K	2M 180K	Block multiplexer, bytes/sec.
50K —	50K	1800		Byte multiplexer, bytes/sec. Selector channel, bytes/sec.
5 M	5M	11M	11M	Aggregate data rate, bytes/sec.
No	No	Yes	Yes	Data Streaming
				CONTROL STORAGE
Bipolar R/W	Bipolar R/W	ECL	ECL	Storage type
5Ó	50	20	20	Access time, nanoseconds
8 72K	8 72K	36 16K	36 16K	Word size, bits Minimum number of words
144K	144K	32K	32K	Maximum number of words
_	-	Instruction microcode, op-	Instruction microcode, op-	Control storage usage
		erating system assist	erating system assist	PRICING & AVAILABILITY
\$279,000	\$375,000	\$140,000	\$182,765	PRICING & AVAILABILITY Purchase of CPU with min. memory
Yes	Yes	Yes	Yes	Lease terms offered
Yes	Yes	Yes	Yes	Vendor's
Yes \$9,300 (3-yr.)	Yes \$12,000 (3-yr.)	Yes \$4,885 (3-yr.)	Yes \$6,695 (3-yr.)	Third party Lease of CPU with min. memory (1-)
99,300 (3-yr.) 1MB	2MB	1MB or 2MB	2MB	Memory increment size
\$45,000	\$90,000	\$15,700 or \$31,400	\$31,400	Memory increment purchase
Yes	Yes	Yes	Yes	Vendor offered maintenance
\$1,915/mo. Yes	\$2,315/mo. Yes	\$485/mo. Yes	\$605/mo. Yes	Prime time Additional hours
res Yes	Yes	Yes	Yes	24 hour
Weekend, holiday	Weekend, holiday	_	_	Other plans
DI Svetame	IPL Systems	IPL	IPL	Manufacturor
IPL Systems Control Data	Control Data	IPL	IPL	Manufacturer Vendor
			1	
		1	i	
			1	

*As rated by the PCM vendor.

MODEL	IPL 4445	IPL 4446	Magnuson M80 Model 30	Magnuson M80 Model 30E
SYSTEM PARAMETERS		į	1	1
Date of introduction	11/81	10/80	8/81	11/81
Date of first delivery	3rd Quarter 1982	3rd Quarter 1981	9/81	12/81
Number installed to date	_	Proprietary information	_	l —
Production status	Active	Active	Active	Active
Operating systems	7.0	1 101110		1
DOS/VS	Yes	Yes	Yes	Yes
	1			Yes
DOS/VSE	Yes	Yes	Yes	1
OS/VS1	Yes	Yes	Yes	Yes
SVS	Yes	Yes	Yes	Yes
MVS	Yes	Yes	Yes	Yes
VM/370	Yes	Yes	Yes	Yes
VM/SP	Yes	Yes	Yes	Yes
Others	MVS/SP	MVS/SP	_	_
PROCESSING FEATURES				
Virtual storage capability	Standard	Standard	Standard	Standard
Processor arrangements	3.0		1	
_	l vos	Vos	Yes	Yes
Uniprocessor	Yes	Yes	l tes	162
Attached processor	_	-	1-	-
Front end to	_	-	1-	1-
Back end to	_	-	_	1-
Multiprocessor		_	_	
Minimum in complex	_	_	-	
Maximum in complex			_	 —
Clock comparator	Standard	Standard	Standard	Standard
CPU timer	Standard	Standard	Standard	Standard
Control registers	Standard	Standard	Standard	Standard
		Standard	1	Standard
CPU one-level addressing	Standard		Standard	
Doubleword buffer	Standard	Standard	Standard	Standard
Interval timer	Standard	Standard	Standard	Standard
Machine check handling	Standard	Standard	Standard	Standard
Multiple bus architecture	Standard	Standard	Standard	Standard
Storage protection	Standard	Standard	Standard	Standard
Time-of-day-clock	Standard	Standard	Standard	Standard
Channel command retry	Standard	Standard	Standard	Standard
Channel indirect addressing	Standard	Standard	Standard	Standard
	Standard	Standard	Standard	Standard
Byte oriented operand feature		Standard	1	Standard
Extended precision floating point	Standard	- · · · · · · · · · · · · · · · · · ·	Standard	
High speed floating point	No	No	No	Standard
System/370 Universal Instruction set	Standard	Standard	Standard	Standard
Console audible alarm	Standard	Standard	Standard	Standard
Integrated console printer	Optional	Optional	Optional	Optional
Light pen	No	No	No	No
Remote console	No	No	Optional	Optional
Remote data link	Standard	Standard	Optional	Optional
Console file	Standard	Standard	Standard	Standard
		1	1 '	Standard
CPU activity monitor	No Considered	No Sharidard	Standard	
Extended control mode	Standard	Standard	Standard	Standard
Program event recording	Standard	Standard	Standard	Standard
Virtual machine assist	Standard	Standard	Standard	Standard
1401/1440/1460 compatibility	No	No	No	No
OTHER FEATURES &			All M80 systems have	1
COMMENTS	1		Cullinane IDMS data	1
			base manager available	
			as option; also sup-	
				1
	1		ported are OS/MFT,	1
		1	OS/MVT, and DOS	1
			Release 26	
	I	1	1	

IPL 4445	IPL 4446	Magnuson M80 Model 30	Magnuson M80 Model 30E	MODEL
				PROCESSOR PERFORMANCE
50	50	100	100	Machine cycle time, nanoseconds
IBM 4341-11	IBM 4341-2	IBM 4331-1	IBM 4331-11	Relative performance* To
1.11	1.07	1.5	1.1	Performance of
		_	1-	To Performance of
 IPL 4446		 M80/31	 M80/31	Field Upgradable to
Dynamic NMOS	Dynamic NMOS	Dynamic NMOS	Dynamic NMOS	MAIN STORAGE Storage type
Byriainie 141003	Dyriamic Minos		· ·	Checking
Yes	Yes	Yes	Yes	Parity
Yes 1	Yes 1	Yes 1	Yes 1	Error detection & correction No. of check bits per byte
4	4	4	4	No. of check bits per word
500	500	600	600 500	Read cycle, nanoseconds
500 8	500 8	500 8	8	Write cycle, nanoseconds Bytes fetched per cycle
2M	2M	0.5M	1 M	Minimum capacity, bytes
BM	16M	16M	16M	Maximum capacity, bytes
2M No	2M No	O.5M No	1 M No	Increment size, bytes Interleaving
	1—	_	-	Minimum number of ways
	_	_	-	Maximum number of ways
Yes	Yes	No	No	BUFFER (CACHE) STORAGE
ECL	ECL	_	1-	Storage type
100 4	100] —	Cycle time, nanoseconds Bytes fetched per cycle
4 8K	16K			Minimum capacity, bytes
BK	16K			Maximum capacity, bytes
				I/O CHANNELS
_		0	0	Selector channels standard
	_	15	15	Selector channels optional
2 3	2 3	1 14	13	Block multiplexers standard Block multiplexers optional
3 1	1	114	113	Byte multiplexers standard
Ó	0	15	15	Byte multiplexers optional
256	256	256	256	Subchannels per channel On a block multiplexer
256	256	256	256	On a byte multiplexer
_	_	256	256	On a selector
No	No	Optional	Optional	Channel to channel adapter Maximum channel data rates
2M	2M	зм	зм	Block multiplexer, bytes/sec.
180K	180K	500K	500K	Byte multiplexer, bytes/sec.
 11M	11M	3M 10M	3M 10M	Selector channel, bytes/sec. Aggregate data rate, bytes/sec.
Yes	Yes	Yes	Yes	Data Streaming
		1.55		
ECL	ECL	Static NMOS	Static NMOS	CONTROL STORAGE Storage type
20	20	45	45	Access time, nanoseconds
36	36	32	32	Word size, bits
16K 32K	16K 32K	4K 16K	4K 16K	Minimum number of words Maximum number of words
Instruction microcode,	Instruction microcode,	Instruction microcode,	Instruction microcode,	Control storage usage
operating system assist	operating system assist	operating system assist	operating system assist	PRICING & AVAILABILITY
\$228,335	\$289,965	\$86,000	\$104,000	PRICING & AVAILABILITY Purchase of CPU with min. memor
Yes	Yes	Yes	Yes	Lease terms offered
Yes Yes	Yes Yes	- Yes	Yes	Vendor's Third party
res \$7,795 (3-yr.)	\$9,855 (3-yr.)	\$3,753	\$4,404	Lease of CPU with min. memory (1-
2MB	2MB	0.5MB	0.5MB	Memory increment size
\$31,400 Yes	\$31,400 Yes	\$7,850 Yes	\$15,700 Yes	Memory increment purchase Vendor offered maintenance
\$780/mo.	\$880/mo.	Yes	Yes	Prime time
Yes	Yes	Yes	Yes	Additional hours
Yes 	Yes —	Yes	Yes	24 hour Other plans
			.	· ·
PL	IPL	Magnuson	Magnuson	Manufacturer
PL	IPL	Magnuson	Magnuson	Vendor
		1	1	
				i
		1		1

^{*}As rated by the PCM vendor.

MODEL	Magnuson M80 Model 31	Magnuson M80 Model 32	Magnuson M80 Model 41	Magnuson M80 Model 42
SYSTEM PARAMETERS				
Date of introduction	6/80	3/79	11/81	3/79
Date of first delivery	6/80	5/80	2/82	9/81
Number installed to date		1-	_	_
Production status	Active	Active	Active	Active
Operating systems	ł			
DOS/VS	Yes	Yes	Yes	Yes
DOS/VSE	Yes	Yes	Yes	Yes
OS/VS1	Yes	Yes	Yes	Yes
SVS	Yes	Yes	Yes	Yes
MVS	Yes	Yes	Yes	Yes
VM/370	Yes	Yes	Yes	Yes
VM/SP	Yes	Yes	Yes	Yes
Others	MVS/SP	MVS/SP	MVS/SP	MVS/SP
PROCESSING FEATURES				
Virtual storage capability	Standard	Standard	Standard	Standard
Processor arrangements		1		
Uniprocessor	Yes	Yes	Yes	Yes
Attached processor	l _	_		l _
Front end to	l _	1-	_	<u> </u>
Back end to	1 _	1_	1	1_
Multiprocessor	l _	_	<u> </u> _	1_
Minimum in complex	_	1_		
Maximum in complex	<u> </u>			1
Clock comparator	Standard	Standard	Standard	Standard
CPU timer	Standard	1	l '	
	1	Standard	Standard	Standard
Control registers	Standard	Standard	Standard	Standard
CPU one-level addressing	Standard	Standard	Standard	Standard
Doubleword buffer	Standard	Standard	Standard	Standard
Interval timer	Standard	Standard	Standard	Standard
Machine check handling	Standard	Standard	Standard	Standard
Multiple bus architecture	Standard	Standard	Standard	Standard
Storage protection	Standard	Standard	Standard	Standard
Time-of-day-clock	Standard	Standard	Standard	Standard
Channel command retry	Standard	Standard	Standard	Standard
Channel indirect addressing	Standard	Standard	Standard	Standard
Byte oriented operand feature	Standard	Standard	Standard	Standard
Extended precision floating point	Standard	Standard	Standard	Standard
High speed floating point	No	No	Standard	No
System/370 Universal Instruction set	Standard	Standard	Standard	Standard
Console audible alarm	Standard	Standard	Standard	Standard
Integrated console printer	Optional	Optional	Optional	Optional
-	1 '	, ·	1 '	1 * '
Light pen	No	No	No	No
Remote console	Optional	Optional	Optional	Optional
Remote data link	Optional	Optional	Optional	Optional
Console file	Standard	Standard	Standard	Standard
CPU activity monitor	Standard	Standard	Standard	Standard
Extended control mode	Standard	Standard	Standard	Standard
Program event recording	Standard	Standard	Standard	Standard
Virtual machine assist	Standard	Standard	Standard	Standard
1401/1440/1460 compatibility	No	No	No	No
OTUED 554 TUD50 0				
OTHER FEATURES &	All M80 systems have	1	1	1
COMMENTS	Cullinane IDMS data base	1		
	manager available as	1		
	option; also supported	1		1
	are OS/MFT, OS/MVT,	1		1
	DOS Release 26	1		
		i		
	1	1	1	1

Magnuson M80	Magnuson M80	Magnuson M80	Magnuson M80	MODEL
Model 31	Model 32	Model 41	Model 42	
00	100	50	50	PROCESSOR PERFORMANCE Machine cycle time, nanoseconds Relative performance* To Performance of
BM 4331-2	IBM 4331-2	IBM 4341-10	IBM 4341-1	
.2	1.5	1.1	1.1	Terrormance of To Performance of Field Upgradable to
	-	—	-	
	-	—	-	
/80/32	M80/41	M80/42	M80/43	
oynamic NMOS fes fes foo MOO MOO MOO MOO MOO MOO MOO	Dynamic NMOS Yes Yes 1 4 600 500 8 1M 16M 1M No	Dynamic NMOS Yes Yes 1 4 2000 1900 64 2M 16M 1M No —	Dynamic NMOS Yes Yes 1 4 2000 1900 64 2M 16M 1M No	MAIN STORAGE Storage type Checking Parity Error detection & correction No. of check bits per byte No. of check bits per word Read cycle, nanoseconds Write cycle, nanoseconds Bytes fetched per cycle Minimum capacity, bytes Maximum capacity, bytes Interleaving Minimum number of ways Maximum number of ways
	Yes	Yes	Yes	BUFFER (CACHE) STORAGE Storage type Cycle time, nanoseconds Bytes fetched per cycle Minimum capacity, bytes Maximum capacity, bytes
-	Static TTL	Static ECL	Static ECL	
-	300	50	50	
-	8	4	4	
-	16K	24K	32K	
-	16K	24K	32K	
5	0	0	O	I/O CHANNELS Selector channels standard Selector channels optional Block multiplexers standard Block multiplexers optional Byte multiplexers optional Byte multiplexers optional Subchannels per channel On a block multiplexer On a byte multiplexer On a selector Channel to channel adapter Maximum channel data rates Block multiplexer, bytes/sec. Byte multiplexer, bytes/sec. Selector channel, bytes/sec.
5	15	15	15	
3	2	2	2	
5	13	13	13	
5	1	1	1	
256	15	15	15	
256	256	256	256	
256	256	256	256	
256	256	256	256	
256	Optional	Optional	Optional	
3M	3M	3M	3M	
8M	500K	500K	500K	
8M	3M	3M	3M	
OM	10M	10M	10M	Aggregate data rate, bytes/sec. Data Streaming CONTROL STORAGE Storage type Access time, nanoseconds Word size, bits Minimum number of words Maximum number of words Control storage usage
/es	Yes	Yes	Yes	
Static NMOS	Static NMOS	Static ECL	Static ECL	
15	45	35	35	
32	32	80	80	
IK	4K	8K	8K	
6K	16K	16K	16K	
nstruction microcode, op-	Instruction microcode, op-	Instruction microcode, op-	Instruction microcode, op-	
erating system assist \$116,000 /es (es -5,601/mo. MB -615,700 /es /es /es /es	erating system assist \$146,000 Yes — Yes \$6,936/mo. 1MB \$15,700 Yes Yes Yes Yes Yes Yes	erating system assist \$163,000 Yes	erating system assist 183,000 Yes Yes \$8,727 1MB \$15,700 Yes Yes Yes Yes Yes	PRICING & AVAILABILITY Purchase of CPU with min. memory Lease terms offered Vendor's Third party Lease of CPU with min. memory (1-y Memory increment size Memory increment purchase Vendor offered maintenance Prime time Additional hours 24 hour Other plans
Magnuson	Magnuson	Magnuson	Magnuson	Manufacturer
Magnuson	Magnuson	Magnuson	Magnuson	Vendor

^{*}As rated by the PCM vendor.

MODEL	Magnuson M80 Model 43	NAS AS/3000N	NAS AS/3000	NAS AS/5000N
SYSTEM PARAMETERS				
Date of introduction	3/79	Jan. 1980	Jan. 1980	Sept. 1980
Date of first delivery	9/81	Jan. 1980	Jan. 1980	Sept. 1980
·		·	1 -	_
Number installed to date		Proprietary	Proprietary	
Production status	Active	Limited new production	Limited new production	Limited new production
Operating systems	V	V	V	Vac
DOS/VS	Yes	Yes	Yes	Yes
DOS/VSE	Yes	Yes	Yes	Yes
0S/VS1	Yes	Yes	Yes	Yes
SVS	Yes	Yes	Yes	Yes
MVS	Yes	Yes	Yes	Yes
VM/370	Yes	Yes	Yes	Yes
VM/SP	Yes	Yes	Yes	Yes
Others	MVS/SP	No	No	_
PROCESSING FEATURES				
Virtual storage capability	Standard	Standard	Standard	Standard
Processor arrangements			Ì	
Uniprocessor	Yes	Yes	Yes	Yes
Attached processor	1 _	1_	1 —	1 _
Front end to	_	1_	1_	! _
Back end to	l <u> </u>	1_	l _	!
Multiprocessor				1_
Minimum in complex				
•		-	1	
Maximum in complex	Standard	Standard	Standard	Standard
Clock comparator		Standard	Standard	Standard
CPU timer	Standard	1	1 -	
Control registers	Standard	Standard	Standard	Standard Standard
CPU one-level addressing	Standard	Standard	Standard	- I
Doubleword buffer	Standard	Standard	Standard	Standard
Interval timer	Standard	Standard	Standard	Standard
Machine check handling	Standard	Standard	Standard	Standard
Multiple bus architecture	Standard	Standard	Standard	Standard
Storage protection	Standard	Standard	Standard	Standard
Time-of-day-clock	Standard	Standard	Standard	Standard
Channel command retry	Standard	Standard	Standard	Standard
Channel indirect addressing	Standard	Standard	Standard	Standard
Byte oriented operand feature	Standard	Standard	Standard	Standard
Extended precision floating point	Standard	Standard	Standard	Standard
High speed floating point	Optional	No	No	No
System/370 Universal Instruction set	Standard	Standard	Standard	Standard
Console audible alarm	Standard	Standard	Standard	Standard
Integrated console printer	Optional	Optional	Optional	Optional
Light pen	No	No	No	Standard
Remote console	Optional	No	No	Optional
Remote data link	Optional	No	No	No
Console file	Standard	Standard	Standard	Standard
CPU activity monitor	Standard	Standard	Standard	Standard
Extended control mode	Standard	Standard	Standard	Standard
Program event recording	Standard	Standard	Standard	Standard
Virtual machine assist	Standard	Standard	Standard	Standard
1401/1440/1460 compatibility	No	Standard	Standard	Standard
1-01/ 1-10/ 1-100 compatibility	140	Standard	Standard	Junaara
OT				
OTHER FEATURES &				
COMMENTS		1		
	}			
		1		1
			1	1
		1	1	
		1	1	
•		1	1	1
			1	

Magnuson M80 Model 43	NAS AS/3000N	NAS AS/3000	NAS AS/5000N	MODEL
				PROCESSOR PERFORMANCE
50	115	115	92	Machine cycle time, nanoseconds
	1		1	Relative performance*
BM 4341-1	IBM 4341-1	IBM 370/158-3	IBM 4341-1	То
1.3	1.0	1.0	1.2	Performance of To
_		_	1=	Performance of
	AS/3000		AS/5000E, AS/5000	Field Upgradable to
		l .		
		1		MAIN STORAGE
Dynamic NMOS	NMOS	NMOS	NMOS	Storage type Checking
'es	Yes	Yes	Yes	Parity
es	Yes	Yes	Yes	Error detection & correction
	11	li T	11	No. of check bits per byte
ļ	I	1.	1	No. of check bits per word
2000	920	920	460	Read cycle, nanoseconds
900 4	690 8	690 8	460 8	Write cycle, nanoseconds Bytes fetched per cycle
M	2м	2M	2M	Minimum capacity, bytes
6M	4M	8M	8M	Maximum capacity, bytes
М	1M	1M	2M	Increment size, bytes
lo	No	No	No	Interleaving
_	 -	I —	_	Minimum number of ways
-	1-	-	1-	Maximum number of ways
'es	1	1	1	BUFFER (CACHE) STORAGE
tatic ECL	Bipolar ECL	Bipolar ECL	Bipolar ECL	Storage type
0	230	230	184	Cycle time, nanoseconds
	8	8	8	Bytes fetched per cycle
8K	8K	16K	8K	Minimum capacity, bytes
8K	8K	16K	8K	Maximum capacity, bytes
				I/O CHANNELS
ı	!	_	<u> </u>	Selector channels standard
5	<u> </u> _	\ -	}_	Selector channels optional
<u></u>	4	4	4	Block multiplexers standard
3	-	-	1]	Block multiplexers optional
5	1	1_	11	Byte multiplexers standard Byte multiplexers optional
5	-	_	1'	Subchannels per channel
256	256	256	256	On a block multiplexer
56	256	256	256	On a byte multiplexer
56	l 	 	l=	On a selector
Optional	No	No	Optional	Channel to channel adapter Maximum channel data rates
М	1.5M	1.5M	1.86M	Block multiplexer, bytes/sec.
ook	100K	100K	100K	Byte multiplexer, bytes/sec.
M	l_	l —	<u> </u>	Selector channel, bytes/sec.
OM	5.5M	5.5M	6.75M	Aggregate data rate, bytes/sec.
'es	No	No	No	Data Streaming
		j '	1	CONTROL STORAGE
Static ECL	Bipolar ECL	Bipolar ECL	Bipolar ECL	Storage type
85	10 to 20	10 to 20	10 to 20	Access time, nanoseconds
0	72	72	72	Word size, bits
K .	8K	8K	16K	Minimum number of words
6K	8K	8K	16K Instruction microcode, op-	Maximum number of words Control storage usage
nstruction microcode op- rating system assist	Instruction microcode, op- erating system assist	Instruction microcode, op- erating system assist	erating system assist	, control storage usage
rating system assist	Journa Oystom assist		- '	PRICING & AVAILABILITY
228,000	\$220,000	\$225,000	\$250,000	Purchase of CPU with min. memory
es	Yes	Yes	Yes	Lease terms offered
-	Yes	Yes	Yes	Vendor's Third party
es 10,004	Contact vendor	Contact vendor	Contact vendor	Lease of CPU with min. memory (1-
MB	1MB	1MB	2MB	Memory increment size
15,700	\$12,500	\$12,500	\$50,000	Memory increment purchase
es	Yes	Yes	Yes	Vendor offered maintenance
es	Yes	Yes	Yes	Prime time Additional hours
es	 \$937/mo.	937/mo.	\$2,646/mo.	24 hour
es -	— V93//1110.	= 1 = 377 IIIO.		Other plans
		1	1	·
agnuson	NAS	NAS	NAS	Manufacturer
Magnuson	NAS	NAS	NAS	Vendor
		İ		
	1	1	1	
		i	1	1
	1	l	1	1
	1	1		
	İ	i		i

*As rated by the PCM vendor.

MODEL	NAS AS/5000E	NAS AS/5000	NAS AS/6130	NAS AS/6150
OVOTENA DADAMETERO				
SYSTEM PARAMETERS	Same 1000	h- 1000	A	A
Date of introduction	Sept. 1980	Jan. 1980	April 1982	April 1982
Date of first delivery	Sept. 1980	Jan. 1980	4th Quarter 1982	1st Quarter 1983
Number installed to date	<u> </u>	-	i —	i –
Production status	Limited new production	Limited new production	Active	Active
Operating systems			1	
DOS/VS	Yes	Yes	No	No
DOS/VSE	Yes	Yes	Yes	Yes
OS/VS1	Yes	Yes	Yes	Yes
SVS	Yes	Yes	No	No
MVS	Yes	Yes	Yes	Yes
VM/370	Yes	Yes	Yes	Yes
VM/SP	Yes	Yes	Yes	Yes
Others	No	MVS/SP	No	No
PROCESSING FEATURES		_		
Virtual storage capability	Standard	Standard	Standard	Standard
Processor arrangements	1		1	ľ
Uniprocessor	Yes	Yes	Yes	Yes
Attached processor	_	! —	-	_
Front end to	l _	1 —	I -	
Back end to	l _	_	l –	_
Multiprocessor	_	l _	_	_
Minimum in complex	! _	l _	i _	
Maximum in complex		l	ł	
Clock comparator	Standard	Standard	Standard	Standard
•				
CPU timer	Standard	Standard	Standard	Standard
Control registers	Standard	Standard	Standard	Standard
CPU one-level addressing	Standard	Standard	Standard	Standard
Doubleword buffer	Standard	Standard	Standard	Standard
Interval timer	Standard	Standard	Standard	Standard
Machine check handling	Standard	Standard	Standard	Standard
Multiple bus architecture	Standard	Standard	Standard	Standard
Storage protection	Standard	Standard	Standard	Standard
Time-of-day-clock	Standard	Standard	Standard	Standard
Channel command retry	Standard	Standard	Standard	Standard
Channel indirect addressing	Standard	Standard	Standard	Standard
Byte oriented operand feature	Standard	Standard	Standard	Standard
Extended precision floating point	Standard	Standard	Standard	Standard
High speed floating point	No	No	No	No
System/370 Universal Instruction set	Standard	Standard	Standard	Standard
Console audible alarm	Standard	Standard	Standard	Standard
	f · · ·	Standard		1
Integrated console printer	Optional		Optional	Optional
Light pen	Standard	Standard	No .	No
Remote console	Optional	Optional	Standard	Standard
Remote data link	No	No	Standard	Standard
Console file	Standard	Standard	Standard	Standard
CPU activity monitor	Standard	Standard	Standard	Standard
Extended control mode	Standard	Standard	Standard	Standard
Program event recording	Standard	Standard	Standard	Standard
Virtual machine assist	Standard	Standard	Standard	Standard
1401/1440/1460 compatibility	Standard	Standard	Standard	Standard
			Standard	Standard
OTHER FEATURES &			1	
COMMENTS			Operates in either	Operates in either
	1		System/370 or 4300	System/370 or 4300
			mode	mode
		1		
	1			
				1

M 4341-2 BM 3031	NAS AS/5000E	NAS AS/5000	NAS AS/6130	NAS AS/6150	MODEL
Machine cycle time, nanoesconds Mach					PROCESSOR PERFORMANCE
M 4341-2 IBM 4031 IBM 4341-2 IBM 4341-	2	92	75	60	Machine cycle time, nanoseconds
1.15	DM 4241 2	IDM 2021	IBM 4241 2	IDAA 4241 2	
Comparison	.0		- IBIVI 4341-2		
Mos	_	l —	-	<u> </u> —	То
MAINGS NMOS	_ AS/5000	1	AS/6150	_	
Yes	IMOS	NMOS	NMOS	NMOS	Storage type
Yes	'es	Yes	Yes	Ves	
1	'es	Yes			
10					No. of check bits per byte
100	-	I .			
8	60				
Max Max		8	8 or 16		Bytes fetched per cycle
Mo	M				Minimum capacity, bytes
No	M				Maximum capacity, bytes
	0		•		
Depart ECL Bipolar ECL 184 184 150 120 1	-	l and the second			Minimum number of ways
Dispolar ECL Bipolar ECL 150 120	_		_	_	Maximum number of ways
184	inglar ECI	Ringlar ECI			
Second Comment Seco	84		150	120	
	;	8	14	4	
	2K				Minimum capacity, bytes
	2K	32K	16K	32K	Maximum capacity, bytes
Contact vendor Cont	_		_	<u> </u> _	I/O CHANNELS Selector channels standard
1	_	_			
1		F -			
1		1 7			
256 256		1 :			
256 256 256 256 256 256 256 256 256 256		· ·	ľ	ľ	
Standard Optional Opt	256				On a block multiplexer
Standard Standard Optional Maximum channel data rates Block multiplexer, bytes/sec. Byte multiplexer, bytes/sec. Aggregate data rate, bytes/sec. Aggregate data rate, bytes/sec. Aggregate data rate, bytes/sec. Aggregate data rate, bytes/sec. Aggregate data rate, bytes/sec. Aggregate data rate, bytes/sec. Aggregate data rate, bytes/sec. Aggregate data rate, bytes/sec. Aggregate data rate, bytes/sec. Aggregate data rate, bytes/sec. Data Streaming CONTROL STORAGE Storage type Storage type Storage type Storage type Storage type Storage type Storage type Storage type Storage type Storage type Storage type Storage type Storage type St	256	256	256	256	On a byte multiplexer
BBM 1.86M 2M 100K 100K 100K 100K 2M 100K 2M 100K 2M 100K 2M 100K 2M 100K 2M 100K 2M 100K 2M 100K 2M 100K 2M 100K 2M 100K 2M 2M 100K 2M 2M 2M 2M 2M 2M 2M 2M 2M 2M 2M 2M 2M	 Optional	Standard	Optional	Optional	
100K	•			1.	Maximum channel data rates
Selector channel, bytes/sec. Aggregate data rate, bytes/sec. Aggregate data rate, bytes/sec. Data Streaming CONTROL STORAGE Storage type Access time, nanoseconds Word size, bits Minimum number of words Maximum number of words Mordisize, bits Mordisize, bits Mordisize, bits Minimum number of words Maximum number of words Mordisize, bits Mordisize, bits Mordisize, bits Mordisize, bits Mordisize, bits Mordisize, bits Mordisize, bits Mor	.86M				
Aggregate data rate, bytes/sec. Data Streaming Control Storage type	OUK	100K	100K		Byte multiplexer, bytes/sec.
Optional Standard Standard Data Streaming CONTROL STORAGE Storage type Access time, nanoseconds Word size, bits Word size, bits Minimum number of words Naximum number of words Instruction microcode, operating system assist S50,000 S450,000 S450,000 S450,000 S525,000 S525	- 5.75	6.75M	12M		Aggregate data rate bytes/sec
polar ECL 0 to 20 0 to 20 0 to 20 10 to 20 72 72 73 8K 16K 16K 16K 16K 16K 16K 16K 16K 16K 16	o				Data Streaming
10 to 20 72 72 72 72 73 75 76 78 78 78 78 78 78 78 78 78 78 78 78 78	inelar ECI	Pinelor CCI			CONTROL STORAGE
72 16K 5K 5K 5K 16K 16K 16K 1nstruction microcode, operating system assist 850,000 85 85 87 90	0 to 20		75	60	Access time, nanoseconds
16K Instruction microcode, operating system assist 850,000 \$\frac{950,000}{950,000}\$ \$950	2	72	72	72	
Instruction microcode, operating system assist Instruction microcode, as posi	6K				
PRICING & AVAILABILITY Purchase of CPU with min. memory Lease terms offered Vendor's Third party Lease terms offered Vendor's Third party Lease of CPU with min. memory (1-) Lea	struction microcode,		Instruction microcode,	Instruction microcode,	
Yes	perating system assist	operating system assist		' ' '	PRICING & AVAILABILITY
Yes	350,000				Purchase of CPU with min. memory
Contact vendor Contact vendor Contact vendor AMB A	es es				
Contact vendor Contact vendor 2MB Contact vendor 4MB AMB Memory increment size Memory increment purchase Ves	- -	169		——————————————————————————————————————	
\$50,000 \$50,00	ontact vendor				Lease of CPU with min. memory (1-y
Yes Yes	MB FO 000				
Yes Yes Yes Prime time 2,793/mo. \$3,542/mo. Contact vendor Contact vendor 24 hour AS NAS NAS NAS Manufacturer					Vendor offered maintenance
2,793/mo. \$3,542/mo. Contact vendor Contact vendor Contact vendor Contact vendor Contact vendor NAS NAS NAS NAS Manufacturer	es es				
AS NAS NAS NAS Manufacturer	-	_	_	_	Additional hours
	-		_	_	
NAS IVAS Vendor	AS				
	no.	INA	1170	11/20	Vendoi

*As rated by the PCM vendor.

MODEL	NAS AS/7000N	NAS AS/7000	NAS AS/7000 DPC
SYSTEM PARAMETERS			
Date of introduction	Jan. 1980	Jan. 1980	Jan. 1980
	2nd Quarter 1980	2nd Quarter 1980	2nd Quarter 1980
Date of first delivery	21d Quarter 1980		211d Quarter 1300
Number installed to date			
Production status	Active	Active	Active
Operating systems			1
DOS/VS	Yes	Yes	No
DOS/VSE	Yes	Yes	No
OS/VS1	Yes	Yes	No
SVS	Yes	Yes	No
MVS	Yes	Yes	Yes
	Yes	Yes	Yes
VM/370		No	Yes
VM/SP	Yes		
Others	MVS/SP	MVS/SP	MVS/SP
PROCESSING FEATURES			
Virtual storage capability	Standard	Standard	Standard
Processor arrangements		[
Uniprocessor	Yes	Yes	_
•		_	_
Attached processor		1_	_
Front end to	_	_	
Back end to	-		
Multiprocessor		_	Yes
Minimum in complex	-	_	2
Maximum in complex	l —	_	2
Clock comparator	Standard	Standard	Standard
CPU timer	Standard	Standard	Standard
Control registers	Standard	Standard	Standard
CPU one-level addressing	Standard	Standard	Standard
-	Standard	Standard	Standard
Doubleword buffer	■ 1	Standard	Standard
Interval timer	Standard		T
Machine check handling	Standard	Standard	Standard
Multiple bus architecture	Standard	Standard	Standard
Storage protection	Standard	Standard	Standard
Time-of-day-clock	Standard	Standard	Standard
Channel command retry	Standard	Standard	Standard
Channel indirect addressing	Standard	Standard	Standard
Byte oriented operand feature	Standard	Standard	Standard
-,	Standard	Standard	Standard
Extended precision floating point	1	Standard	Standard
High speed floating point	No Secondaria		_
System/370 Universal Instruction set	Standard	Standard	Standard
Console audible alarm	Standard	Standard	Standard
Integrated console printer	Standard	Standard	Standard
Light pen	Standard	Standard	Standard
Remote console	_	-	-
Remote data link	No	No	No
Console file	Standard	Standard	Standard
	Standard	Standard	Standard
CPU activity monitor	III	Standard	Standard
Extended control mode	Standard	i	•
Program event recording	Standard	Standard	Standard
Virtual machine assist	Standard	Standard	Standard
1401/1440/1460 compatibility	No	No	No
OTHER FEATURES &	A second service processor	A second service processor	Two service processor
COMMENTS	console is available	console is available as an	consoles are standard;
	as an option	option	a third is optional
	as an option	1	
	\	1	
			•
		1	i

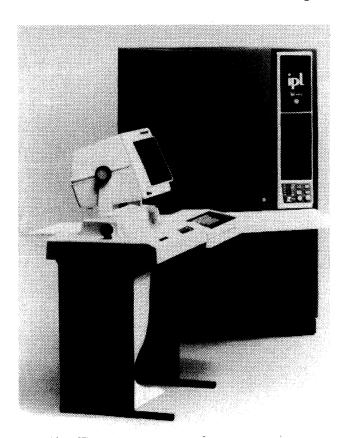
NAS AS/7000N	NAS AS/7000	NAS AS/7000 DPC	MODEL
72	72	72	PROCESSOR PERFORMANCE
	/2	/2	Machine cycle time, nanoseconds Relative performance*
IBM 3031 Up to 2.0	IBM 3033S 1.15	IBM 3033N 1.25	To Performance of
	— —	_	To
 AS/7000	AS/7000 DPC	No	Performance of Field Upgradable to
			MAIN STORAGE
NMOS	NMOS	NMOS	Storage type Checking
Yes	Yes	Yes	Parity
Yes 1	Yes	Yes	Error detection & correction No. of check bits per byte
<u>.</u>	_	\ _	No. of check bits per word
360 360	360 360	360 360	Read cycle, nanoseconds Write cycle, nanoseconds
8	8	8	Bytes fetched per cycle
2M 8M	4M 16M	4M 16M	Minimum capacity, bytes Maximum capacity, bytes
2M	2M	2M	Increment size, bytes
Yes 4	Yes 4	Yes 4	Interleaving Minimum number of ways
4	4	4	Maximum number of ways
			BUFFER (CACHE) STORAGE
Bipolar ECL 144	Bipolar ECL 144	Bipolar ECL 144	Storage type Cycle time, nanoseconds
8	8	8	Bytes fetched per cycle
16K 16K	64K 64K	64K/CPU 64K/CPU	Minimum capacity, bytes Maximum capacity, bytes
	3-11	0410 01 0	
_	-	_	I/O CHANNELS Selector channels standard
	6	-	Selector channels optional Block multiplexers standard
1	6	14	Block multiplexers optional
1 1	2 2	1 5	Byte multiplexers standard Byte multiplexers optional
			Subchannels per channel
256 256	256 256	256 256	On a block multiplexer On a byte multiplexer
 Standard	 Standard	Standard	On a selector Channel to channel adapter
1.5M	1.5M	1.5M	Maximum channel data rates Block multiplexer, bytes/sec.
100K	100K	100K	Byte multiplexer, bytes/sec.
 11M	21M)	Selector channel, bytes/sec. Aggregate data rate, bytes/sec.
Optional	Optional	Optional	Data Streaming
Bipolar ECL	Bipolar ECL	Bipolar ECL	CONTROL STORAGE
10 to 20	10 to 20	10 to 20	Storage type Access time, nanoseconds
99 6K) 99 6K	99	Word size, bits
or 6K	6K	6K 6K	Minimum number of words Maximum number of words
Instruction microcode, operating system assist	Instruction microcode, operating system assist	Instruction microcode, operating system assist	Control storage usage
			PRICING & AVAILABILITY
\$950,000 Yes	\$1,100,000 Yes	\$1,700,000 Yes	Purchase of CPU with min. memor Lease terms offered
Yes	Yes	Yes	Vendor's
 Contact vendor	Contact vendor	Contact vendor	Third party Lease of CPU with min. memory (1)
2MB	i 2MB	2MB	Memory increment size
\$100,000 Yes	\$100,000 Yes	\$100,000 Yes	Memory increment purchase Vendor offered maintenance
Yes	Yes	Yes	Prime time
 \$8,000/mo.		- \$11,708/mo.	Additional hours 24 hour
	_	_	Other plans
NAS NAS	NAS NAS	NAS NAS	Manufacturer Vendor
	, who		1
			1
	1	i.	1

^{*}As rated by the PCM vendor.

MODEL	NAS AS/9000N	NAS AS/9000-2	NAS AS/9000 DPC
SYSTEM PARAMETERS			
Date of introduction	Jan. 1981	Sept. 1980	Jan. 1981
Date of first delivery	4th Quarter 1981	1981	4th Quarter 1981
Number installed to date		_	l _
Production status	Active	Active	Active
Operating systems	7.0.10	7.00.00	7.00.00
DOS/VS	No	No	No
	1		No
DOS/VSE	No	No	1
OS/VS1	Yes	Yes	No
SVS	No	No	No
MVS	Yes	Yes	Yes
VM/370	Yes	Yes	Yes
VM/SP	Yes	Yes	Yes
Others	MVS/SP	MVS/SP	MVS/SP
PROCESSING FEATURES			
Virtual storage capability	Standard	Standard	Standard
Processor arrangements			
Uniprocessor	Yes	Yes	
Attached processor	_	-	_
Front end to	_	_	_
Back end to	<u> </u>	_	
Multiprocessor	Í _	_	Yes
			2
Minimum in complex			2
Maximum in complex			
Clock comparator	Standard	Standard	Standard
CPU timer	Standard	Standard	Standard
Control registers	Standard	Standard	Standard
CPU one-level addressing	Standard	Standard	Standard
Doubleword buffer	Standard	Standard	Standard
Interval timer	Standard	Standard	Standard
Machine check handling	Standard	Standard	Standard
Multiple bus architecture	Standard	Standard	Standard
Storage protection	Standard	Standard	Standard
Time-of-day-clock	Standard	Standard	Standard
Channel command retry	Standard	Standard	Standard
Channel indirect addressing	Standard	Standard	Standard
	Standard		Standard
Byte oriented operand feature	1	Standard	Standard
Extended precision floating point	Standard	Standard	i
High speed floating point	Standard	Standard	Standard
System/370 Universal Instruction set	Standard	Standard	Standard
Console audible alarm	Standard	Standard	Standard
Integrated console printer	Optional	Optional	Optional
Light pen	No	No	No
Remote console	-		_
Remote data link	Standard	Standard	Standard
Console file	Standard	Standard	Standard
CPU activity monitor	Standard	Standard	Standard
Extended control mode	Standard	Standard	Standard
Program event recording	Standard	Standard	Standard
Virtual machine assist	Standard	Standard	Standard
1401/1440/1460 compatibility	No	No	No
1401/1440/1460 compatibility	NO	NO	140
OTHER FEATURES &	A second service	A second service processor	Two service processor
COMMENTS	processor console is	console is available as an	consoles are standard;
	available as an option	option	two additional consoles
	available as all option	Οριιοι	are optional

NAS AS/9000N	NAS AS/9000-2	NAS AS/9000DPC	MODEL
		00	PROCESSOR PERFORMANCE
48	38	38	Machine cycle time, nanoseconds Relative performance*
IBM 3033U	IBM 3033U	IBM 3081	To
1.1 to 1.3	1.5 to 1.6	1.4	Performance of To
	_	-	Performance of Field Upgradable to
AS/9000-2	AS/9000 DPC	_	
AUA 400	1,1100	NMOS	MAIN STORAGE Storage type
NMOS	NMOS		Checking
Yes Yes	Yes	Yes Yes	Parity Error detection & correction
1	Yes	1 1	No. of check bits per byte
 336	266		No. of check bits per word Read cycle, nanoseconds
288	228	228	Write cycle, nanoseconds
8 4M	8 12M	8 16M	Bytes fetched per cycle Minimum capacity, bytes
24M	32M	32M	Maximum capacity, bytes
4M Yes	4M Yes	4M Yes	Increment size, bytes Interleaving
8	8	16	Minimum number of ways Maximum number of ways
8	8	16	,
Director FOL	D: 4- FO	Binder ECI	BUFFER (CACHE) STORAGE Storage type
Bipolar ECL 96	Bipolar ECL 76	Bipolar ECL 76	Cycle time, nanoseconds
8 32K	8	8 64K/CPU	Bytes fetched per cycle Minimum capacity, bytes
32K 32K	64K 64K	64K/CPU	Maximum capacity, bytes
	•		I/O CHANNELS
	_	-	Selector channels standard
- 5	9	12	Selector channels optional Block multiplexers standard
10	14	18	Block multiplexers optional
1 3	1 5	1 7	Byte multiplexers standard Byte multiplexers optional
			Subchannels per channel
256 256	256 256	256 256	On a block multiplexer On a byte multiplexer
	_		On a selector Channel to channel adapter
Optional	Optional	Optional	Maximum channel data rates
1.5M 100K	1.5M	1.5M 100K	Block multiplexer, bytes/sec. Byte multiplexer, bytes/sec.
	100K —		Selector channel, bytes/sec.
21M Optional	21M	80M Standard	Aggregate data rate, bytes/sec. Data Streaming
Орнова	Optional	Standard	
Bipolar ECL	Bipolar ECL	Bipolar ECL	CONTROL STORAGE Storage type
5.5	5.5	5.5	Access time, nanoseconds
160 16K	160 16K	160 16K	Word size, bits Minimum number of words
16K	16K	16K	Maximum number of words Control storage usage
Instruction microcode, operating system assist	Instruction microcode, operating system assist	Instruction microcode, operating system assist	
\$1.950.000		\$4,150,000	PRICING & AVAILABILITY Purchase of CPU with min. memor
Yes	\$2,750,000 Yes	Yes	Lease terms offered
Yes —	Yes	Yes —	Vendor's Third party
Contact vendor	Contact vendor	Contact vendor	Lease of CPU with min. memory (1
4MB \$100,000	4MB \$100,000	4MB \$100,000	Memory increment size Memory increment purchase
Yes	Yes	Yes	Vendor offered maintenance Prime time
Yes	Yes —	Yes —	Additional hours
\$9,953/mo. —	\$11,450/mo. —	\$12,995/mo.	24 hour Other plans
Hitachi	Hitachi	Hitachi	Manufacturer Vendor
NAS	NAS	NAS	venuoi
	I	I	Ī

^{*}As rated by the PCM vendor.



IPL Systems has been making PCMs since 1977, and the systems have been marketed worldwide by such firms as Control Data and Olivetti. In late 1980, IPL announced its own end-user family of PCMs, the 4400 Series. The product line, which includes the IPL 4436, 4443 (shown here), 4445, and 4446, compete with the IBM 4300 Series, have memory sizes ranging from one to 16 megabytes, and include from three to six channels.

➤ Manufacturers / Vendors

Amdahl Corporation 1250 East Arques Avenue Sunnyvale, California 94086 Telephone (408) 746-6000

Cambex Corporation 360 Second Avenue Waltham, Massachusetts 02154 Telephone (617) 890-6000

Control Data Corporation 8100 34th Avenue South Minneapolis, Minnesota 55440 Telephone (612) 853-8100

IPL Systems Inc.360 Second AvenueWaltham, Massachusetts 02154Telephone (617) 890-6620

Magnuson Systems Corporation 2902 Orchard Park Way San Jose, California 95134 Telephone (408) 946-8100

National Advanced Systems 800 East Middlefield Road Mountain View, California 94043 Telephone (415) 962-6100□