MANAGEMENT SUMMARY

With the introduction of the DPS 8000, Bull has expanded the range of large systems running under the GCOS 8 operating system. The DPS 8000 joins the DPS 8, DPS 88, and DPS 90 as large-scale systems in the Groupe Bull line. At present, the DPS 8000 range consists of the Bull DPS 8000/81 monoprocessor system and the Bull DPS 8000/ 82T tandem processor system with redundant architecture.

Within the Bull world, both DPS 8000 processors are positioned above the DPS 8/70, the top-end model within DPS 8 model line. The Model 81 is said to have 1.6 times the performance of the DPS 8/70 in commercial/scientific batch environments and 1.8 times within Data Management IV-Transaction Processor environments. Jobs that use long floating-point operands and BCD jobs may have substantially less performance, according to Bull. Compared to the DPS 8/49, the Model 81 is said to have a relative performance range of 2.6 to 3.0.

The DPS 8000 Series extends the power range of the DPS 8 Series via architectural features and circuit technology that are similar to Bull's newer products such as the DPS 90 and DPS 88. The company's medium-to-large scale offerings now employ VLSI Current Mode Logic circuitry and one-megabit memory chips compared to the larger models of the DPS 8 Series that use LSI MSI Schottky TTL logic and 16K-bit and 64K-bit memory chips. The DPS 8000 Series can be configured with 16 megabytes to 256 megabytes of main memory compared with the DPS 8 Series which can be configured with eight megabytes to 64 megabytes of main memory.

Designed and developed by Honeywell Bull, Inc. in Phoenix, Arizona, the Bull DPS 8000 is marketed by both Bull and Honeywell Bull. The new system uses state-of-the-art The two-processor Bull DPS 8000 Series, offered as an upgrade path for DPS 8 Series users, are large-scale mainframes designed to handle interactive on-line and distributed processing.

MODELS: DPS 8000 Model 81 and Model 82.

CONFIGURATION: DPS 8000 Series features one or two CPUs, 16 megabytes to 256 megabytes of main memory, and 16 to 32 channels.

COMPETITION: Amdahl 580 Series, IBM 4381 and 3090 Model 120E and Model 150E, NAS AS/XL 50 and AS/XL 60, and NCR 8800 and 9800 Series.

PRICE: A Bull DPS 8000/81 central processor unit costs FF 3,365,000; a Bull DPS 8000/82T costs FF 6,729,000. These prices include a one-year hardware guarantee.

CHARACTERISTICS

MANUFACTURER: Bull, 121 avenue Malakoff, 75116, Paris, France. Telephone (01) 4502 9090. Telex 614050 F; Honeywell Bull Limited, Honeywell House, Great West Road, Brentford, Middlesex TW8 9DH, U.K. Telephone 01-568-9191. Telex 28453/4.

MODELS: Single-processor DPS 8000 Model 81 and dual-processor Model 82.

DATA FORMATS

BASIC UNIT: Information not available from vendor.

FIXED-POINT OPERANDS: Information not available from vendor.

The DPS 8000 Series, which provides a new migration path for DPS 8 users, features up to 256 megabytes of main memory and up to 32 channels.



TABLE 1. SYSTEM COMPA	RISON	
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MODEL	DPS 8000/81	DPS 8000/82
SYSTEM CHARACTERISTICS		
Date announced	June 1987	June 1987
Date first delivered	December 1987	December 1987
Field upgradable to	DPS 8000/82	
Relative performance		—
Number of processors	1	2
Cycle time, nanoseconds	85	85
Word size, bits	36	36
Operating systems	GCOS 8, CP-6	GCOS 8, CP-6
MAIN MEMORY		
Туре	1 megabit MOS	1 megabit MOS
Minimum capacity, bytes	16 megabytes	32 megabytes
Maximum capacity, bytes	128 megabytes	256 megabytes
Increment size, bytes	16 megabytes	16 megabytes
Cycle time, nanoseconds		—
BUFFER STORAGE		
Minimum capacity	256KB	256KB
Maximum capacity	256KB	256KB
Increment size	—	—
INPUT/OUTPUT CONTROL		
Number of channels:		
Byte multiplexer	—	—
Block multiplexer		—
Word		· · · ·
Other	Up to 16	Up to 32

technology: VLSI gate array circuits implementing CML logic, 1MB memory chips, 256KB cache memory, redundant system architecture, multipipeline instruction processing, memory multileaving, and support of segment sizes up to 4 billion bytes. Running under GCOS 8, the new DPS 8000 Series offers complete compatibility with the preceding hardware base of GCOS 8 products.

The CML technology incorporated into the DPS 8000 results in the consumption of less energy than other types of circuitry, generates less heat, and increases overall system reliability. Each CML chip contains 1,200 to 1,500 bipolar gates with switching times of 0.4 nanoseconds. The chips are encapsulated in single-chip packages mounted on advanced technology circuit boards.

These original packaging techniques reduce the number of central processor circuit boards to eleven basic boards, each consisting of 30 packages for a single central processor. This technology reduces floor space by three-quarters over previous methods. A Bull DPS 8000 central system, including memory, input/output processors, channels, and connectors, resides in a single cabinet.

The DPS 8000 Model 81 single-processor system features 16 megabytes of main memory, expandable to 128 megabytes, and one Input/Output Processor (IOP) which supports up to 16 physical channels. It is field upgradable to a DPS 8000 Model 82. The Model 82T features 32 megabytes of memory, expandable to 256 megabytes, and two IOPs which each support up to 16 physical channels. The Model 82 contains two of each major central system component to ensure maximum availability in the event of component failure.

The DPS 8000/82T model delivers approximately 1.8 times the processor performance of the single processor \triangleright

FLOATING-POINT OPERANDS: Such operands may be represented as single-precision or double-precision binary floating point, or mixed with a range of $10\pm^{38}$, and single- or double-precision hexadecimal floating point with a maximum range of $10\pm^{153}$.

INSTRUCTIONS: Information not available from vendor.

INTERNAL CODE: Standard nine-bit ASCII code.

MAIN MEMORY

Main memory ranges from 16 megabytes to 256 megabytes. Memory supports internal memory interlacing to speed up data access and uses automatic error detection and correction. Users can expand memory through the addition of memory boards.

STORAGE TYPE: Main memory uses one-megabit dynamic random access memory (DRAM) circuits obtained from another chip vendor.

CAPACITY: See Table 1.

CYCLE TIME: See Table 1.

CHECKING: Information not available from vendor.

RESERVE STORAGE: Information not available from vendor.

CENTRAL PROCESSORS

DPS 8000 complexes use Very-Large-Scale Integration (VLSI) gate arrays and make use of one-megabit chip, dynamic random access memory (DRAM). A gate-array chip consists of 1,500 bipolar gates and 399 internal logic cells using Current Mode Logic (CML). Gate-array circuit chips are reside in multilayered, ceramic Single-Chip Packages (SCP), which greatly reduce the number of required circuit boards compared to processors using former

MODEL	MSU0451	MSU0500	MSU0501	MSU3380/82	MSU3381/83
Cabinets per subsystem	16	8-15	8-15	8	8
Disk packs/HDAs per cabinet	1	2	2	2	2
Capacity	157MB	626MB	1.1GB	2.52GB	5.04GB
Tracks/segments per drive unit	815	1630 per surface	1686 per surface	13,275	
Average seek time, msec.	30	25	25	15	17
Average access time, msec.	38.3	33.3	33.3	23.3	25.3
Average rotational delay, msec.	8.3	8.3	8.3	8.3	8.3
Data transfer rate	716K bps	1065K bps	1065K bps	3M bps	3M bps
Controller model	MSP0611/0612	MSP0611/0612	MSP0611/0612	MSP3880/3884	MSP3882/3886
Comments	Removable	Fixed	Fixed	Fixed	Fixed

TABLE 2. MASS STORAGE

8000/81. The fully duplicated version includes two of each central system component in a fully cross-barred configuration, implementing automatic rerouting of processing to help ensure maximum system availability for continuous operation. A DPS 8000/81 can be field upgraded to the DPS 8000/82T.

In keeping with its BlueGreen strategy of corporate information and communications solutions, Bull has integrated the DPS 8000 into its ISO/DSA architecture, thereby enabling the DPS 8000 to serve as a centralized or decentralized database server in local or long distance connections.

GCOS 8 now features the Rapid Access Data System (RADS), a software disk cache buffer technique introduced last year. The RADS feature improves throughput by storing a larger body of information within main memory to reduce the number of disk fetches. When information for a given task is called for, the system first searches the RADS area before accessing a disk. This technique takes advantage of faster semiconductor memories on board the mainframe and avoids the problems associated with I/O bottlenecks.

In the auxiliary storage area, Bull now offers the MSU3381 and MSU3383 double-capacity disk units, which feature unformatted capacities of 5.04 gigabytes. Last year, Bull made the MSU3380/3382 disks available to its DPS 8 users. These units each have a capacity of 1.8 gigabytes.

Bull is also reemphasizing distributed processing and compatibility with the Open System Interconnect (OSI) standard. In addition, the GCOS 8 operating system runs on all Bull large-scale mainframes beginning at the DPS 8 and now the DPS 8000 level, making it possible for applications written for one large machine to work with minimal or no modification on other Bull hardware.

Meanwhile, Bull is phasing out other operating systems such as Multics, which continues to be available on a DPS 8/70 mainframe designed specifically to run Multics, despite protests from a devoted following.

COMPETITIVE POSITION

Bull has positioned the DPS 8000 Series against IBM's 4381 Series and the low-end of the 3090 Series. The vendor claims to have a two-to-one advantage over IBM in

circuit technology. A circuit board can contain up to 30 SCPs. The use of the newer circuit technology reduces space, power, and cooling requirements and improves performance and reliability. A central complex includes the central processing unit (CPU), System Control Unit (SCU), main memory component, Input/Output Processor (IOP), and Maintenance Subsystem (MS). The use of compact SCPs makes it possible to house these components in a single cabinet.

The Model 82 dual-redundant system features two of every major central system component to ensure a high degree of system availability. The dual system uses a tightly coupled configuration, with CPUs and other components sharing central processing load, I/O processing, communications processing, and all memory. In dual redundant configurations, a duplicate module will take over the functions of a failed module while continuing to maintain its existing work loads or functions.

The CPU makes use of instruction pipeline processing to speed up instruction execution. It also uses virtual memory addressing and contains an associative memory that holds the most recently referenced page table words. Descriptorcontrolled access provides system security. Extended segment capability supports segment sizes up to four billion bytes. Execution control store holds up to 64K bytes of command information.

In addition to main memory, the DPS 8000 Series comes with 256 kilobytes of cache memory to provide high-speed access to the most recently used instructions and data.

The SCU component acts as the interface between the CPU and main memory and the input/output processor and connected peripherals. The SCU regulates data transfer traffic and controls system interrupts and demands for central memory. The SCU features eight connection ports and supports concurrent data requests from multiple ports. The SCU can process requests concurrently from up to five ports. Memory commands conform to either a high or low priority. Generally, read commands have a high priority while write commands have a low priority. Additionally, high-priority requests take precedence during SCU port selection, and low-priority requests.

The Maintenance Subsystem handles DPS 8000 maintenance and service chores, incorporating both local and remote maintenance. Users can obtain remote testing and diagnosis by optionally accessing the HBI Technical Assistance Center.

The Service Processor (SP), a microcomputer with its own operating system and storage units, handles all diagnostic testing for the CPU and SCU, while the Maintenance Console Adapter, under the control of the SP, provides similar functions for the IOP. The SP is required for hardware initialization, loading of firmware, and initialization

TABLE 3. TERMINALS									
MODEL	VIP 7816/7826	VIP 7817/7827	HDS 7101/ 7102	HDS 7302/ 7304	HSD 7403/ 7404	HDS 7807/ 7808			
DISPLAY PARAMETERS									
Max. chars./screen	2000	2000	2000	2000	2000, 3300	2000, 3300			
Screen size (lines x chars.)	25 x 80	25 x 80	25 x 80	25 x 80	25 x 80, 25 x 132	25 x 80, 25 x 132			
Symbol formation	7 x 8 upper/ 7 x 9 lower	7 x 8 upper/ 7 x 9 lower	10 x 14	9 x 12	10 x 10 (80 col.)	10 x 14, 6 x 14			
Character phosphor	P31 green	P31 green	Green or amber	Green or amber	Green or amber	Green or amber			
Total colors/no. simult. displayed KEYBOARD PARAMETERS	<u> </u>	_		_	—				
Style	Typewriter	Typewriter	Typewriter	Typewriter (mul- tifunc., low-pro- file)	Typewriter	Typewriter (mul- tifunction)			
Character/code set	128 ASCII	128 ASCII	128 ASCII	190 ASCII/30 spec.	190 ASCII	190 ASCII/30 spec.			
Detachable	Standard	Standard	Standard	Standard	Standard	Standard			
Program function keys	12 standard	_	10 standard	12 standard	14 standard	12 standard			
OTHER FEATURES									
Buffer capacity	3 pages								
Tilt/swivel	Adj. keyboard (7826)	Adj. keyboard (7827)	Optional	Standard	Standard	Standard			
Graphics capability	_								
TERMINAL INTERFACE	RS-232-C; RS- 422-A	RS-232-C; RS- 422-A	RS-232-C; RS- 422-A	RS-232-C; RS- 442	RS-232-C; RS- 422	RS-232-C; RS- 422-A			

Bull DPS 8000 Series

> interactive processing within mixed workload environments involving both batch and interactive processing, with batch being about equal. In May, IBM announced four new 4381 models that are more powerful than existing 4300 models, and also announced a new low end 3090, the Model 120E.

Many of Bull's recent announcements stressing hardware integration and easier data access are in step with industry trends in general and IBM pronouncements in particular. For the last year especially, IBM has been offering hardware- and software-based connectivity products to let PC, mini, and mainframe users operating under incompatible hardware and operating systems share information. Unlike Bull which offers GCOS 8 as the primary operating environment for its large-scale mainframes, IBM continues to cope with problems of having to support incompatible operating systems. To encourage 4381 VSE users to migrate to MVX XA, IBM announced in May the Migration System (MVS-MS) product, which is intended to reduce the time and cost required to convert from VSE/SP Version 1, 2, and 3 to an MVA operating environment. MVS-MS was announced with the May 11 introduction of the four new 4381 mainframes.

In addition to price/performance issues, both IBM and Bull are addressing hardware/software connectivity concerns. Bull announced its Integrated Information Architecture (IIA), a framework that lets users working within mainframe, mini, or micro environments access data at different computing levels within an organization. In conjunction with this concept, the new Interel relational data management system is designed to make it easier for end users to access data bases and find the information they need. Interel, using an SOL interface, can access both relational and nonrelational data structures. The Personal Computer Interconnect product lets PC users access mainframe data and download the data to the PC for incorporation into popular PC-based application packages, such as Lotus 1-2-3, dBASE III, MultiMate Advantage, and WordStar Professional.

of the bootload process. Additional SP functions include direct error diagnosis to capture intermittent errors; background CPU test, called Patrol, to handle areas not addressed by direct diagnosis; comprehensive native fault tests to diagnose steady-state failures; and on-line/off-line testing which includes functional and stress tests.

In addition to the basic central system hardware, each central system features one console that allows users to control and interact with the system. The console consists of a 14-inch display station, command keyboard, an IOP console channel connection, and optional 100-charactersper-second printer. Each IOP allows a maximum of four consoles.

SPECIAL FEATURES: To address user access and security requirements, the CPU operates in three modes: slave, master, and privileged master modes. For general user applications, slave mode limits user access to memory and furnishes a restricted instruction set. Privileged master mode lets users access certain memory domains, but protects them against alteration while also protecting the GCOS 8 operating system from corruption.

PHYSICAL SPECIFICATIONS: The dimensions and weights for Bull mainframes follow:

	Width (in.)	Ht. (in.)	Depth (in.)	Wt. (lb.)
Bull Mainframes				
DPS 8000 Model 81; one processor	71	71	32	2300
DPS 8000 Model 82; two processors	174	71	32	4700

The operating environment for DPS 8000 systems should hold to between 68 degrees and 78 degrees Fahrenheit at a relative humidity of 40 percent to 60 percent, noncondensing. The systems can operate on 200, 208, 220, 380, 415 VAC, three-phase power at a frequency of 60 Hz. Bull also offers models operating on 50 Hz. The DPS 8000 con-

TABLE 4. INPUT/OUTPUT UNITS

Magnetic Tape Units	Number of Tracks	Recording Density, Bits/Inch	Encoding	Tape Speed, Inches/Sec.	Transfer Rate, Bytes/Sec.
MTU0610	9	800/ 1600/ 6250	NRZI/ PE/ GCR	200	100K/200K/1250K
MTU0630	9	800/ 1600/ 6250	NRZI/ PE/ GCR	75 or 125	60-100K/120-200K/ 468.7-781.2K
MTU8205	9	800/ 1600/	NRZI/ PE	125	100-200K
MTU8206	9	1600/ 6250	PE/ GCR	125	200-780K
MTU8208	9	1600/ 6250	PE/ GCR	200	320-1250K
MTS8225	9	800/ 1600/	NRZI/ PE	125	100-200K
MTS8226	9	1600/ 6250	PE/ GCR	125	200-780K
MTS8228	9	1600/ 6250	PE/ GCR	200	320-1250K
Printers	Printing Speed	Print Positions	Horizontal Spacing, Chars./Inch	Vertical Spacing, Lines/Inch	Form Size, Inches
PRU0903	900 lpm	136	10	6 or 8	4-to-19 width x
PRU1203	1200 lpm	136	10	6 or 8	4-to-19 width x
PRU1600	1600 lpm	136 or 160	10	6 or 8	4-to-22 width x 3-to-22 length

Besides IBM, the Bull systems compete against the lowend of the NAS AS/XL Series of IBM plug-compatible systems and the NCR 9800 and 8800 systems. In a strategy similar to Bull's, NCR has targeted its product line towards the fault-tolerant and on-line transaction processing market, a niche market NCR has exploited successfully over the years.

ADVANTAGES AND RESTRICTIONS

Of all the recent Bull offerings announced within the last year, the introduction of the DPS 8000 Series may prove to be the most critical. By Bull's own estimation, DPS 8 installations make up about 80 percent of the company's 10,000 worldwide user base. The two-processor DPS 8000, the apparent follow-up series for the eight-year old DPS 8 product line, directly targets this substantial user base. Before the introduction of the DPS 8000 Series, DPS 8/70 users looking for more room to grow could jump to a DPS 88 model or move to a Bull DPS 90. The more powerful DPS 8000 Series, however, represents a potentially less costly move.

Bull has also singled out the following users as potential customers for the DPS 8000 Series: those who want to replace installed Level 66 systems; those who need hard-ware platforms for dedicated server systems such as infocenters, videotex servers, and application development centers; and those who require decentralized systems. The flexibility afforded by the DPS 8000 Series should accommodate those target areas.

sumes 10.7 kVA, operating on 208 VAC, 60 Hz. The systems have a heat output of 31.4k Btus per hour operating on 208 VAC, 60 Hz.

CONFIGURATION RULES

The DPS 8000 Model 81, a single-processor complex, consists of one CPU with 16 megabytes of main memory, one System Console, one System Control Unit (SCU), one Maintenance Subsystem (MS), one Input/Output Processor (IOP) supporting up to 16 physical channel connections, and one modem for remote maintenance. Memory can expand to 16, 32, 64, or 128 megabytes in 16-megabyte increments. Users can also configure three additional consoles per IOP for a maximum of four, an optional 23-inch large screen monitor for the system console, and an optional system console printer.

The DPS 8000 Model 82, a fully redundant processor complex, consists of two CPUs, 32 megabytes of main memory, two SCUs, two MSs, two IOPs, each supporting up to 16 physical channels, and two modems for remote maintenance. Main memory can extend to 128 megabytes per SCU for a total of 256 megabytes. Memory per SCU can expand to 16, 32, 64, or 128 megabytes in 16-megabyte increments. Users can also configure the system with up to six additional system consoles or a maximum of four consoles per IOP and up to eight per system. Additionally, users can configure an optional 23-inch large screen monitor for a system console and an optional system console printer.

INPUT/OUTPUT CONTROL

The DPS 8000 IOP handles data transfers between connected peripheral subsystems and network processors and

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➤ On the physical level, the architecture inherent in the DPS 8000 saves users space and energy costs. The gate-array circuit chips significantly reduce the number of required circuit boards, thereby conserving space, power, and cooling requirements. Through the use of multilayered, ceramic single-chip packages, the components of the central complex are housed in a single cabinet.

In the peripheral area, DPS 8 users planning to migrate to a DPS 8000 will be able to move most of their existing disk, tape, printer, and card unit peripherals over to the DPS 8000 Series. To make it possible to attach peripheral devices to DPS 8000 machines, users must purchase channel connection exchange features to connect peripherals to the DPS 8000 IOP. In addition, users who want to attach older peripherals, such as the MSU0451 mass storage unit, MTU610 magnetic tape unit, and the PRU1600 line printer, will have to confer with Bull.

In the software area, Bull's new Interel data management software, using an SQL interface, can access relational as well as non-relational files, including UFAS and DM-IV in retrieval mode only. The vendor claims it is one of the few data management products that has this capability. Bull's other data management offering continues to be DM-IV, a CODASYL-type offering that features on-line transaction processing and query/reporting in addition to data management.

The GCOS 8 operating system, extremely flexible and user friendly, offers many features that ease the burden of the user because the system does not require knowledge of system architecture on the part of the user. GCOS 8 is hardware-transparent, allowing users to function quite well without detailed studies into the hardware of the system, its input/output devices, and the style of its processors. In high-volume environments, users will be attracted by the capability of GCOS 8 to execute up to 477 user programs simultaneously.

Users will benefit from the inclusion of the Rapid Access Data System (RADS) in Software Release 3000. RADS improves response times, notably in interactive environments, by enabling users to access data residing in a special section of memory instead of in disk memory. \Box

the SCU. An IOP supports up to 16 physical channels and 64 logical channels, which are the number of channels seen by software. Up to 128 logical channels per IOP are supported. Under this arrangement, a single physical channel accepts data from multiple logical channels. Channel pipelining makes possible concurrent input and output operations. Maximum transfer speed reaches three megabytes per channel. Each IOP has an aggregate throughput rate of up to 17.8 megabytes per second.

MASS STORAGE

Please refer to Table 2 for information about mass storage devices.

INPUT/OUTPUT UNITS

Please refer to Table 3 for information about magnetic tape and printer products.

TERMINALS

Please refer to Table 4 for information about terminals.

COMMUNICATIONS

DATANET 8 Series front-end network processors handle communications functions for the DPS 8000 mainframes. The DATANET 8 Series includes the 8/10, 8/20, and 8/30, which operate within Bull's Distributed Systems Architecture (DSA), an open communications architecture. Users can configure up to eight DATANET processors per system.

Bull has designed the DPS 8000 Series to accommodate distributed processing and to conform with the Open Systems Interconnection (OSI) standard for connecting Bull equipment and hardware from other vendors.

The DATANET 8 Series of network processors operates under the control of the Distributed Network Supervisor (DNS) software executive. These processors manage data communications traffic for Bull DPS 7, DPS 7000, DPS 8, DPS 8000, DPS 88, or DPS 90 large systems operating under GCOS 8 or GCOS, or DPS 7 and DPS 7E, and DPS 7000 medium systems operating under GCOS 7.

Features common to all network processors in the DATA-NET 8 Series include:

- Interconnection of systems using private networks that use High-Level Data Link Control data communications links
- Connection to PDNs, to VANs, via the CCITT X.25 interface, and to most types of standard digital or analog, leased or switched data communications lines
- Attaching terminal devices
- Remote maintenance through System Control Facility
- · Console access for network management functions
- Integrated 5¹/₄-inch diskette drive for executive software maintenance functions

In addition, all DATANET 8 Series processors require a visual display console. Physical connections between DA-TANET 8 Series network processors and central systems occur through network processor channel options which include Peripheral Subsystem Interface (PSI) for DPS 7 and DPS 7000 systems or Direct Interface Adapter (DIA) channels for DPS 8, 8000, 88, and 90 systems. The options are determined by the central system. The network processor provides support for one to four connections to large systems, or two connections to medium systems.

The DATANET 8 Series processors include the DATA-NET 8/10, 8/20, and 8/30. All three network processors can expand from a basic to maximum configuration and use the same executive software, Distributed Network Supervisor.

DATANET 8/10, a single processor system, provides support for a maximum of 31 data communications lines. The basic system includes one megabyte of central memory expandable to a maximum of two megabytes. A 5¹/₄-inch diskette drive for executive software support comes with the basic system. A second 5¹/₄-inch diskette drive is offered as an option. A console visual display terminal is required with each DATANET 8/10. In addition, a hard copy printer may be required.

Each 8/10 includes three RS-232-C asynchronous data communications ports. Data communications interface adaptors and line interface module options make it possible to expand data communications ports to a maximum of 31 lines. These options accommodate line characteristics such as data transmission speed, asynchronous/synchronous operation, and physical interfaces such as RS-232-C, V.35, and X.21.

DATANET 8/20 is a single-processor system with cache memory and one megabyte of central memory. To increase 8/20 performance, users can add a second processor and its associated cache memory. Central memory can expand to two megabytes through the addition of a one megabyte memory option. A basic system also includes a 5¹/₄-inch diskette drive for executive software support. A second 5¹/₄-inch diskette drive is offered as an option. A console visual display terminal is required with each 8/20, and a hard copy console printer may be required. Each DATA-NET 8/20 includes three RS-232-C asynchronous data communications ports. The basic system provides support for up to 31 data communications ports, expandable to 127 ports with power and line expansion module options. Users can expand individual data communications ports by ordering various communications interface adaptors and line interface module options. These options accommodate line characteristics such as data transmission speed, asynchronous/synchronous operation, and physical interfaces such as RS-232-C, V.35, and X.21.

DATANET 8/30 is a single-processor system with cache memory and two megabytes of central memory. To increase 8/30 performance, users can add a second processor and its associated cache memory. Central memory can expand to four megabytes through the addition of a two-megabyte memory option. A 5¹/₄-inch diskette drive is included for executive software support, while a second 5¼-inch diskette drive is offered as an option. A console visual display terminal is required with each 8/30 and a console hard copy printer may be required. Each 8/30 includes three RS-232-C asynchronous data communications ports. The basic system provides support for 159 data communications ports expandable to 255 ports with a line expansion module option. Data communications interface adaptors and line interface module options make it possible to expand individual data communications ports. These options specify line characteristics such as maximum speed, asynchronous/ synchronous operation, and physical interfaces such as RS-232-C, V.35, and X.21.

DATANET 8/10, 8/20, and 8/30 processors are upwardly compatible with each other and prior DATANET 8 DSA product offerings. These network processors can coexist with pre-DSA products, making it possible for a user to migrate to a DSA network environment and retain pre-DSA network products.

Line options common to all three DATANET models include:

- Multiline Communications Controller-16 (DCF8052). It accommodates up to four Communications Interface Adaptors.
- RS-232-C Asynchronous/Character Synchronous Integrated Communications Interface Adaptor (DCF8073). The adaptor has four RS-232-C communications ports, includes four 50-foot cables (DCE to DTE) for device attachment, and has a maximum data transfer rate per port to 19.2K bps.

- RS-232-C Bit Synchronous HDLC Integrated Communications Interface Adaptor (DCF8049) The adaptor has two RS-232-C communications ports, includes two 50foot cables (DCE to DTE) for device attachment, and has a maximum data transfer rate per port to 19.2K bps.
- Low/Medium Speed Asynchronous/Character Synchronous Communications Interface Adaptor (DCF8053). A nonintegrated adaptor, this device requires a minimum of one line interface module. It accommodates up to four line interface modules. Maximum data transmission rate per line interface module is 19.2K bps.

The following line interface modules are allowed with DCF8053:

- RS-232-C/V.24 Asynchronous/Character Synchronous Line Interface Module (DCF8055). This module has one RS-232-C/V.24 data communications port, includes one 50-foot cable (DCE to DTE) for device attachment, and has a maximum data transmission speed of 19.2K bps.
- MIL-188-C Asynchronous/Character Synchronous Line Interface Module (DCF8059). This module has one MIL-188-C data communications port, includes one 50foot cable (DCE to DTE) for device attachment, and has a maximum transmission speed of 19.2K bps.
- Medium/High Speed Character Synchronous/Bit Synchronous Communications Interface Adaptor (DCF8061). An nonintegrated adaptor, this option can support one medium speed (to 19.2K bps) or one high speed (to 64K bps) data communications line via one of the following line interface modules.

The following line interface modules are allowed with DCF8061:

- RS-232-C/V.24 Bit Synchronous HDLC Line Interface Module (DCF8062). This module has one RS-232-C/ V.24 data communications port, includes one 50-foot cable (DCE to DTE) for device attachment, and has a maximum data transmission rate of 19.2K bps.
- X.21 Bit Synchronous HDLC Line Interface Module (DCF8064). This module has one X.21 data communications port, includes one 50-foot cable (DCE to DTE) for device attachment, and has a maximum data transmission rate to 64K bps.

MIL-188-C Bit Synchronous HDLC/Character Synchronous Line Interface Module (DCF8067). This module has one X.21 data communications port, includes one 50-foot cable (DCE to DTE) for device attachment, and has a maximum data transmission rate of 64K bps.

- V.35 Bit Synchronous HDLC/Character Synchronous Line Interface Module (DCF8069). This module has one V.35 data communications port, includes one 50-foot cable (DCE to DTE) for device attachment, and has a maximum data transmission rate of 64K bps.
- Bell 301/303 Bit Synchronous/Character Synchronous Line Interface Module (DCF8071). This module has one Bell 301/303 data communications port, includes one 50foot cable (DCE to DTE) for device attachment, and has a maximum data transmission rate of 64K bps.

SOFTWARE

OPERATING SYSTEM: GCOS 8 Software Release 3000 is the latest version of the company's primary operating

system for large-scale mainframes. The new release offers support for relational data management software, improved transaction processing, and new, larger capacity disk drives. In addition, Software Release 3000 includes the Rapid Access Data System (RADS), a feature for improving system throughput. Through RADS, users can access a larger percentage of data held in a special section of main memory rather than disk memory to improve response times, particularly within interactive environments.

In addition to GCOS 8, DPS 8000 mainframes also operate under the Control Program (CP) 6 operating system without modification to hardware. Introduced in 1979 with the DPS 8 systems, GCOS 8 General Comprehensive Operating Supervisor 8 is a product that dates back to the early 1960s. GCOS 8 is a multiprocessing, multiprogramming, communications-oriented operating system that supports distributed systems requirements. Bull intends to keep the operating system dynamic through a series of planned releases that capitalize on new technology while preserving the user's investment in software. Bull's direction for distributed systems is moving toward the eventual linking of an organization's entire complex of physically separate data processing systems into a single logical network system regardless of physical boundaries.

With the introduction of the DPS 8000, Bull announced Integrated Information Architecture, which divides Bull systems into three different operating levels that range from the enterprise level at the top of a computing organization, departmental level at the middle, and workstation level at the bottom. Users can access information pertinent to an entire organization at the enterprise level. Data that serves the specialized needs of separate groups within an organization can reside at the departmental level. Workstation-level processing meets the informational needs of individuals. Each level can be interconnected in two or three tiers to meet specific user requirements.

GCOS 8 Software Release 3000 runs on all Bull largescale processor lines from the DPS 8, DPS 8000, and DPS 88 to the top-end DPS 90. Migration to the new release from Release 2500, the former GCOS 8 version, can occur without the recompilation of application software. Generally, applications written for GCOS 8 can run without modification on any Bull large system running GCOS 8.

GCOS 8 is a user-defined and user-oriented virtual operating system with multidimensional capabilities. GCOS 8 balances the use of system resources, and gives multiple options for customizing the system for each user's needs. GCOS 8 concurrently supports:

- batch processing
- remote job entry (RJE)
- interactive remote job entry (IRJE)
- timesharing
- transaction processing
- · direct program access
- on-line test and diagnostics
- on-line program test and development
- decision support for end users

In addition, the operating system features multiprogramming, multiprocessing, and flexible job entry capabilities. GCOS 8 also has file protection and file sharing, testing and diagnostics, communications, timesharing, data management facilities, language processors, diagnostic and system protection facilities, and various system utilities. Various activities, such as batch, timesharing, transaction processing, and others, can be individually tailored and dynamically varied throughout the day. Peripherals are allocated before memory so that processing is not delayed by operator or mechanical delays.

GCOS 8 is a flexible operating system that features hardware transparency, thereby relieving the user of the need to know the particular architecture of the system, its hardware, I/O devices, or processor types. All processors can access all of memory and can execute any program. GCOS 8 can address up to 256 megabytes of real memory, and up to 477 user programs can be executed concurrently. GCOS 8 can use up to 256 megabytes of real memory for all dimensions. It sustains high throughput by efficient and rapid scheduling of all activities, which reduces operator intervention.

The system architecture with GCOS 8 provides dynamic memory management, descriptor-controlled access, and shared access to both data and procedures. Each of these functions is based on a hardware-protected memory segment. The memory segment is defined by a segment descriptor that contains the logical address of the beginning of the segment, the size of the segment, and the permissions that control its use.

Dynamic memory management permits programmers to develop software as if an unlimited logical memory existed. The available physical memory, on the other hand, depends on the system configuration and the work load.

GCOS 8 controls the physical organization of up to 512 work spaces of up to four million pages of virtual storage each, with each page consisting of 4096 bytes. Up to 477 separate working spaces (out of 512 total working spaces) accommodate user processes.

Any available page of main memory can be used for any page-sized block of logical memory. Although pages may be located anywhere in memory, they can be accessed as if they were physically contiguous. With memory access, segment descriptors and page table words translate the virtual address to a main memory address.

Operating system security is provided through built-in protection features, These features include log-on controls, file access controls, control of TP user access to applications, and control of data base access to the data item (field) level through schemas/subschemas.

Hardware and software system security is maintained in several ways. The operating system will abort an activity if it receives an illegal operation. The File Management Supervisor provides a common file system for all processor operating dimensions as well as protective and restorative functions to ensure file integrity. Access to files is controlled through several levels. Files are grouped in a hierarchical order by user name, access restrictions, and resource control. File names are qualified by comparing them to the user names under which they are cataloged. Passwords may be required as an additional form of user identification. Access to files falls under the originator's discretion and control. Each user can have a multilevel, hierarchical subcatalog structure, with the ability to assign access controls and passwords at each subcatalog level. In addition, a hardware implementation controls access to sets of memory segments called domains. This structure protects programs and files from intentional access by unauthorized personnel and unintentional access during debugging procedures.

GCOS 8 SR3000 supports additional security features. Interel, the Bull relational data management product, includes the Information Resource Dictionary System (IRDS), which adds fine grain access controls to relational data bases. The optional System Security Manager (SSM) will be available with an update of SR3000 and will be based on the capabilities previously provided to the World Wide Military Command and Control System (WWMCCS).

The SSM will provide: levels of classification; categories of access; audit trail of all system logons, file accesses; security breaches; project identification; person identification; strong enforcement; and centralized administration. The SSM will be available to commercial customers in late 1988.

The IRDS, an ANSI-standard active data dictionary, furnishes execution time controls including access controls down to fields that are one character wide. The IRDS security features include Data Administrator functions, session management, and access controls. Session management includes authentication of person, password, project, process, schema/content, session type, account, and natural language. Session types can be development, administrative, or user. Access controls include limits, permissions, and levels. Limits can be public, private, semi-private, and system software only. Permissions can be applied to session type, schema/contents, processes, and commands. Access control enforcement can be applied at table level, column level, value level, where clause, and content object.

PROGRAMMING LANGUAGES: The language processors offered for use on Bull large systems running under GCOS 8 include Cobol-74, Cobol-68, Cobol-85, Fortran-66, Fortran-77, PL/1, GMAP, GPSS, Basic, data Basic, Simscript, Pascal, Compiler "B," Lisp, APL, RPG II, C, and Ada.

DATA BASE MANAGEMENT: The DM-IV Data Manager component of Data Management-IV handles the data base management functions on Bull mainframes. In addition to the DM-IV CODASYL-style product, Bull also offers Interel, which provides a relational view of the data base. (Please refer to the DATA MANAGEMENT section for more details.) The Data Manager component of DM-IV, also referred to as Integrated Data Store/II (I-D-S/ II), administers the creation of the physical and logical structures of the data base and controls the creation of the application-specific views of that data base which are used in processing. It further serves as the interface between the data base and the various DM-IV processors that access the data base and perform operations upon it.

The I-D-S/I and I-D-S/II systems are enhanced versions of I-D-S, a data base management system originally developed by GE. I-D-S/II is based on the CODASYL Data Base Facility specifications. I-D-S/II is fully integrated with Bull's Cobol-74 compiler, and user interfaces are also implemented for Fortran.

Relational Access Manager, which allows interfacing to many standard file types, adds a relational access to nonrelational data bases. The facility accomplishes this without restructuring data files or programs. User-friendly, the facility lets nontechnical and technical users access data through a simple command structure. DATA MANAGEMENT: Data management on Bull systems is handled through Data Management-IV (DM-IV), a product containing a collection of facilities to handle data base management, transaction processing, querying and report processing, as well as providing batch and interactive data base capabilties. (Data base management capabilities are detailed in the DATA BASE MANAGEMENT section.) In addition to the CODASYL-style data base product included with DM-IV, Bull now offers Interel as its relational data management facility. Interel will be available with GCOS 8 Software Release 3000 in December. Other facilitiesunderdatamanagementincludetheData/Dictionary/ Directory System (DD/DS), File Management Supervisor, Indexed Sequential Processor, Unified File Access System (UFAS), Management Data Query System, TOTAL Central, and Common Files Facility (CFF).

DM-IV has evolved from earlier software systems such as Integrated Data Store-I, Transaction Processing System, Transaction Driven System and Management Data Query System. DM-IV is a fully operational on-line, integrated data base management system. Non-data processing professionals can perform data extraction and updating from data bases with various file organizations and data structures. DM-IV consists of the following functional modules: the Data Manager (described in the Data Base Management section), the Transaction Processor, the Query and Reporting Processor, and the Procedural Language Processor. It also supports batch and timesharing programs.

The DM-IV Transaction Processor (TP) provides the facility for on-line data base processing. It is most effectively used in applications where the end user has little or no knowledge of the operating system or storage structure, or data processing in general. Its internal design is optimized for high-volume transaction processing where extremely fast response and fast, automatic restart/recovery are required. The TP system includes on-line software components for processing the actual transaction, as well as a wide variety of support software products for program testing, library updating, and TP system generation. The five major functional components within DM-IV/TP include Transaction Manager, Database Manager, Integrity Manager, Message Manager, and Executive Manager.

The *Executive Manager* schedules and coordinates all Transaction Processor activities. It manages the allocation of system resources for transaction processing.

The *Transaction Manager* controls and coordinates all activities during the processing of a transaction. It initiates each transaction control task that TP processes and controls the communication between application routines.

The *Database Manager* controls all data base activities for on-line files assigned to TP. The executive software also provides for dynamic allocation and deallocation of data base files to TP for uninterrupted, continuous operation.

The Integrity Manager provides for fast, automatic recovery and restart after any type of application or system failure, ranging from rollback of the data base after an application program abort to the complete reconstruction of a destroyed data base.

The Message Manager is the executive software component that actually handles the communications interface with the terminal network supported by the Front-end Network Processor (FNP). The Message Manager provides both the physical and logical interface to the on-line network of terminals and handles the acceptance and delivery of input and output messages.

► The Data Dictionary/Directory System (DD/DS) is a comprehensive set of software modules that can implement a centralized data dictionary/directory. Users can enter data into the dictionary database via batch or interactive operations. The DD/DS supports up to 19 entity-types such as fields, records, files, programs, procedures, jobs, schemas, reports, etc. DD/DS also supports multiple versions and status of each entity-type, alias names, narrative, and attributes unique to the entity type.

The DD/DS user can select several report generation facilities. The reporting system extracts information from the data dictionary and presents it to the user in various formats. The system includes an extensive cross-reference (where used) reporting capability for all entity-type occurrences and an Impact Analysis Report which analyzes and reports the effect of change to an entity-type occurrence. A complete set of utilities assists in the maintenance of the data dictionary system and its database.

The DM-IV Query and Reporting Processor (QRP) provides the user with several different subsystems that access the defined data base and its structure and generate reports on the results of the requested access. The DM-IV QRP end-user facilities provide access to the data base by noncomputer oriented personnel. Within QRP, simple, straight-line procedures can be written to retrieve the desired data and process exception conditions such as no data qualifier and end of retrieval conditions.

The *Personal Computing Facility (PCF)* is a screen-oriented, interactive system that provides a user-friendly application environment. A person without knowledge of conventional computer programming can use PCF to create any type of VDU or CRT form as a basis for interactive problem solving.

Example Query (EQ) is an end-user facility consisting of an easy-to-learn language and support program. EQ aids application-oriented users in the queuing of data through the Relational Access Manager, which is included. User interface is through CRT devices (VIP7800), and alternatives to CRT display include printed output and file output. The interactive language facilities are designed for fast and simple formulation of requests which provide answers to application questions. The language has minimal syntax that is easily constructed into graphic representation of user processing requests.

Interactive Query (IQ) is an end-user facility that allows users to interface in nonforms mode with any type of terminal. Included with IQ is the Relational Access Manager, which allows interface to many standard file types.

The Comprehensive Report Examination/Display Option (CREDO) is an optional Personal Data Query (PDQ) facility that can format report data generated through the EQ and IQ facilities into refined, individualized reports. CREDO reports are defined, created, examined, and distributed according to user-specified or system-default options.

The Transaction Application Test System (TATS) is a software tool that provides an interactive timesharing environment for writing, compiling, testing, and debugging Transaction Processing Routines (TPRs) using a DM-IV (IDS-II) database. TATS also provides a TPR program skeleton generator, and forms mode support is currently provided for the DM-IV TP Forms Option (TPFO). The TATS package can also be used to interactively verify database updates and to integrate completed TPRs into the production system. The Transaction Screen Management System (TSM) is a set of tools designed to enhance the development of application programs in a DM-IV TP environment. This system enables the developer to design, develop, test, and implement screen formats for application systems. Little or no knowledge of the communications network or the DM-IV TP operating environment is required.

The *File Management Supervisor (FMS)* provides powerful file management capabilities, including multilevel user catalogs, file sharing, and access control. The system employs a hierarchical, "tree-structured" design. A System Master Catalog lists the various user Master Catalogs, and each user can, in turn, define one or more levels of subcatalogs. Users can authorize general sharing of their files or specify individual users who may access them on either a read/ write or read-only basis. Password access control can be imposed at any or all levels of the file structure. Security is also provided by the optional logging of file access attempts and by a timesharing command allowing users to encrypt their files through a predefined algorithm.

The Indexed-Sequential Processor (ISP) supports the widely used indexed-sequential file organization and access method, which permits mass storage files to be accessed in either random or sequential fashion. For each logical file, ISP maintains a data file and an independent key file that serves as an index. The key file can be placed on a faster random-access device to speed up the access process.

The Unified File Access System (UFAS) provides automatic management for file processing, including record location and automatic blocking and deblocking. File organizations supported include sequential, relative, indexed, and integrated files. UFAS also includes facilities for error checking and initiation of error processing as defined by ANSI Cobol-74, as well as file integrity protection for normal and abort processing.

The Management Data Query System (MDQS) is a data management system that permits interrogation of sequential, indexed sequential, or I-D-S/I file organizations. MDQS operates as a subsystem to GCOS in both batch and timesharing environments and comes in two versions: MDQS/II, a database retrieval and report generation system, and MDQS/IV, a system that offers all MDQS/II capabilities plus database creation and maintenance features.

The Common Files Facility (CFF) controls the sharing of user program and data files between GCOS III and GCOS 8 as well as between GCOS 8 hosts without requiring manual partitioning of data or mass storage devices. The CFF allows a Level 66, DPS 8, DPS 8000, DPS 88, and DPS 90 system(s) to share disk files on a single common group of disk drives.

CFF allows up to four computer systems share common disk drives. Concurrent access to files is controlled by lock bytes in the mass storage processor that supervise disk drive operation. Locking occurs at the single file level, which ensures that only one computer system in the cluster can update a file at a time. CFF clusters can also exist within communications networks based on Bull's DSA.

Interel, announced with the DPS 8000, is an integrated relational data management product. Interel includes Structured Query Language (SQL) and the Integrated Relational Dictionary System (IRDS), both of which conform to ANSI standards. Interel's implementation of SQL is compatible with IBM's SQL/DS and DB2, ORACLE from Oracle Corporation, Ingres, from Relational Technology ▶ Inc., and the Teradata DBC/1012 data base computer. Interel's SQL language accesses both non-relational and relational data bases.

Interel uses English-like commands to create, update, or delete tables or authorize access to system users. Interel automatically creates table definition and data base table space, establishes the necessary controlling information, and deposits this information in the IRDS central dictionary.

Interel accesses storage data through the Data Manager. Through the use of this this component, users can add full relational files to existing non-relational models, and merge information through SQL. This functionality provides users with a seamless interface from third-generation data base technologies to newer fourth-generation technologies and allows users to migrate to relational data base functions without the need to duplicate information through copy management.

Infoedge, a software product also required with the installation of Interel, contains a set of productivity tools for experienced data processing professionals, technical end users, and non-data processing professionals. The product contains integrated facilities that end users can access through terminals and professional workstations. The product includes a spread sheet, a forms generator, and a menu system. Infoedge includes Infoedge-Menu Services, which provide a consistent menu selection regardless of terminal or workstation types, systems, function sets, or user sophistication. Menu selections include the Resource Dictionary Services, which allow access to the Information Resource Dictionary System within Interel.

Infoedge-Personal Computing, a facility that interfaces with Interel, lets users create and maintain forms. The facility allows users to define forms with variable text and results fields. The facility also supports data storage and retrieval functions, math and logic functions, editing and graphics, and an on-line help facility.

Options for Infoedge include decision support services, query facilities, and workstation facilities. Decision support facilities include Infoedge-Graph for interactive facilities and Infoedge-Financial Planner for financial modeling and business planning. Query Facilities include *Example Query*, an end-user request facility, and *Reporter*, a facility for formatting data obtained from Example Query and SQL into report form. Workstation facilities include *Infoedge-Mail*, a distributed electronic message facility, and *Infoedge-Meetings*, a facility that permits authorized users to attend "meetings" through a terminal device.

DATA COMMUNICATIONS: Designed specifically for use in the DATANET 8 processor, *Distributed Network Supervisor (DNS)* belongs to a set of communication software products based on Bull's Distributed Systems Architecture (DSA). DNS supports up to four DPS 88 Host connections, thereby enabling one DATANET 8 to serve multiple hosts.

DNS operates in the DATANET 8 in conjunction with a GCOS 8-based host to support transaction processing, distributed transaction processing, distributed transaction processing, distributed terminal concentration, timesharing, remote job entry, direct program access, and networks made up of DPS 90s, DPS 88s, DPS 8000, DPS 8s, DPS 7s, DPS 7000s, and DPS 6s in any combination. DNS supports private networks, Public Data Networks (PDNs) and Value Added Networks (VANs), including X.25 packet switched and X.21 circuit switched networks.

The administrative functions distributed throughout the various systems that make up the DSA network include network monitoring, cross-network software loading, dumping, data logging for statistics, billing and maintenance, in-line tests, and software generation.

DNS supports a variety of Bull terminals and also supports the DPS 6-DSA software package that allows a DPS 6 or Level 6 system to function as a distributed processor and to communicate with a DPS 8000 host in a DSA network.

The Network Processing Supervisor and the DPS 8000 support five types of remote processing in any combination: remote job entry (RJE), transaction processing, timesharing, message switching, and direct program access. The following four standard interfaces for remote computers support RJE: remote computer interface, remote network processor multimessage interface, BSC interface, and HDLC interface.

The information network is controlled by a combination of the DATANET Front-end Network Processor and the NPS software, and can range in size from several terminals to a comprehensive, distributed information network with multiple host processing facilities.

NPS supports a variety of remote terminals, computers, and communications facilities, such as the Bull TWU/PRU 1003 and 1005, Teletype Models 28/33/35/37/38, GE TermiNet 300/1200, Hazeltine 2000, IBM 2741 and 2780, and Bull VIP 765/776/786, VIP 7100/7200, VIP 7700/7700R/ 7760/7800, RNP 702/707, and RNP 6/DPS 6 minicomputers. NPS also offers customization and parameterization facilities to implement additional terminal types and network protocols into the system, journalization of message traffic on mass storage, restart/recovery capability, supervisory Control through one or more Network Control Supervisory Stations, statistical recording and reporting, and a high level of line/terminal control through parameterization.

The Remote Terminal Supervisor II (GRTS-II) provides controls for five types of remote processing: remote job entry, transaction processing, timesharing, message concentration, and direct program access. RJE supports the same standard interfaces as NPS. Programming subsystems supported under timesharing are the same as those for NPS. GRTS-II does not support the direct program access communications-queued (DAC-queued) mode provided in NPS, nor does it support any host interface that makes use of the DAC-queued method.

GRTS-II includes a *Communication On-Line Test System* (COLTS) and support for remote terminals and devices with speeds from 75 to 56,000 bps. GRTS-II can coexist with NPS or DNS, each residing and executing in a different network processor. The Data Link System supports host-to-host file transmission.

The *Transaction Processing System (TPS)* invokes the loading and execution of the appropriate application programs for processing transactions received from remote terminals. The Transaction Processing System requires a front-end network processor and can accept transactions from various terminals.

Modular in design, TPS consists of the Transaction Processing Executive (TPE), user-written Transaction Processing Applications Programs, the Transaction Input Interface at each remote terminal, and the Interslave Communication (INTERCOM) Facility. Transaction Processing Applications Programs (TPAPs) can be written in any language processor supported by GCOS 8 including Cobol, Fortran, or GMAP, and are stored in the GCOS file system for activation as required.

The *Transaction Input Interface* provides simplified procedures for entering transactions from teletypewriter or keyboard/display consoles. The INTERCOM facility enables data to be exchanged between the Transaction Processing Executive and applications programs through direct bufferto-buffer transfers. The Transaction Processing Executive, activated by an operator command, functions as a privileged slave program under the GCOS 8 operating system.

Transaction Driven System (TDS), designed for highvolume, on-line transaction processing, differs substantially in internal architecture from the GCOS Transaction Processing System (TPS), but it complements TPS by giving a total DPS 88 transaction processing capability. The TDS internal design has been optimized for high-volume transaction processing where extremely fast response and fast, automatic restart/recovery are required.

The TDS Executive program executes under GCOS 8 much like the Time-Sharing System Executive. An executive operating under GCOS 8, it has the major responsibilities for scheduling and coordination of all TDS activities and tasks. TDS manages the allocation of system resources for transaction processing and handles all communications between TDS and GCOS 8.

The Time-Sharing System (TSS), in connection with a DATANET front-end processor, provides time-sharing computing services to multiple users at remote terminals. The system resources allocated to time-sharing can be dynamically varied under operator control. The time-sharing executive, operating as a slave activity under GCOS 8, sub-allocates storage and dispatches the processor to the programs of individual time-sharing users. Time-sharing on GCOS 8 utilizes the GCOS 8 memory architecture to enable any desired amount of system memory to be allocated to time-sharing. A single copy of TSS can support up to 600 users, assuming the existence of sufficient memory, I/O, and communications facilities. In multiple-processor systems, the time-sharing users' programs can simultaneously run on as many processors as desired by the site. A separately priced Multicopy Support Option allows from two to four copies of the time-sharing executive to run on one DPS 88 system, thereby increasing the number of users that can be supported.

GCOS timesharing users have a choice of six major programming languages: Cobol-74, Extended Basic, Pascal, Time-Sharing Fortran-66, Fortran-77, and APL. Timesharing users can communicate directly with batch-mode facilities, permitting the development and testing of programs, data entry, control of batch program execution, and manipulation of results from remote terminals.

Interactive Integrated Data Store/II (I-D-S/II) provides the capability for interactively updating and retrieving information from an I-D-S/II database. Access is a conversational file management system for creating, deleting, and maintaining catalogs and files as well as for assigning passwords and accessing criteria. The *FDUMP* facility can be used for inspection and maintenance of permanent files. The *LODT* routine permits execution of experimental user subsystems, including trace analysis and debugging of user programs from remote terminals. The *Time-Sharing Activity Report* generates reports on the accumulated utilization of the time-sharing system resources. *Personal Computing Facility* is now available under timesharing, offering spread sheet capabilities. TP-8, a transaction processing facility, is said to enhance productivity within organizations with heavy transaction processing work loads. The product can serve as a growth path for DM-IV/TP users who need increased transaction processing performance and functionality, according to Bull. TP-8 is compatible with DM-IV/TP and the Transaction Processing Executive (TPE-II) (described in the DATA COMMUNICATIONS section), while also offering several improvements. Through this product, users can tailor transaction processing applications to specific needs. Users can implement applications through routines and programs written in several languages including Cobol 74 or Fortran. While in execution, each routine or program is processed independently and can access the range of facilities available in GCOS 8.

To enhance connectivity, Bull has introduced *Personal Computer Interconnect (PCI)* for MS-DOS-based personal computers. With PCI, users can access data from the mainframe and download the data to a personal computer. PCI features a window manager, graphics, VIP terminal emulation, file transfer, and script processing. PCI can work with Lotus 1-2-3, dBASE III, MultiMate Advantage, and WordStar Professional.

PROGRAM DEVELOPMENT: Bull offers a number of products that can function as program development tools. These products include the Text Executive Processor, (TEX), DM-IV Procedural Language Processor (PLP), the Transaction Application Test System (TATS), the Transaction Screen Management System (TSM), the DM-IV Query and Reporting Processor (QRP), and the Personal Computing Facility (PCF). TEX and PLP are described in the following paragraph; these other products were described in other parts of the SOFTWARE section.

The *Text Editor* permits terminal users to create a body of text, edit it, save it, and print it in a specified format. *TEX* is an interpretive language that integrates the capabilities of the Text Editor with text processing, providing additional verbs and subroutine calls. The optional DM-IV Procedural Language Processor (PLP) is an extension of QRP which provides a high-level, procedure-oriented language for use by application and system programmers. When using the Query and Reporting Processor end-user facilities, the user need not be concerned with the database structure or access methods.

Syntax Directed Editor (SDE) is a productivity tool designed to support the creation or modification of Cobol-74 programs. SDE reduces the amount of code that a programmer must enter and immediately checks for format and syntax errors.

System-80 reduces the time and effort of coding, maintenance, and documentation normally associated with Cobol program development. It includes several functional programs and associated files that interact with the user to acquire information about files, fields, screen formats, and validations and edits.

Softool is a set of software tools designed for cost-effective management, development, and maintenance of application software. The Softool Development Environment Product Set offered by Bull consists of the Cobol Programming Environment (Cobol-74) and the Change and Configuration Control.

Simscript provides the user with a simulation-oriented language that translates complex mathematical and logical models into meaningful simulation sequences. An eventoriented language, Simscript has a timing routine that performs the analysis of activities in a controlled sequence in simulated time.

The General-Purpose Simulator System (GPSS) is a simplified, simulation-oriented language that establishes mathematical models in order to provide results for further analysis.

The General Macro Assembler Program (GMAP) enables the programmer to code either in an open-ended macro language or directly in machine-oriented symbolic instructions.

The *Debug Support System (DSS)* supports batch or on-line debugging of user programs and can trace programs, display memory contents, and modify memory locations. Object-level debug can be performed with any language. Symbolic debug is supported by Cobol-74, Fortran-77, and PL/1.

UTILITIES: System utilities include a Sort/Merge Facility, the File Generation Facility, FMS Utilities, Visual Information Display for Efficient Operation, Comprehensive System Utilities Facility, System Utility 8, General Loader, Bulk Media Conversion, and Source and Object Library Editor.

OTHER SOFTWARE: Bull offers application programs that address Finance, Management Sciences, Manufacturing, Health Care, and Banking. A rundown of some of the packages for each area follows.

Financial applications include Accounts Receivable, Accounts Payable, General Ledger, and Payroll systems. Other packages include Payroll Tax Update, Accounts Receivable On-line, General Ledger On-line, Bull Cashflow Management System, Bull Financial & Corporate Planning System, HFCS Hierarchical Consolidation Option, HFCS Risk Analysis Option, and HFCS Graphics Option.

Management packages include TEX Application & Demonstration Library, MPS Common File Management System, General Purpose Simulator System, Concordance Generator Program, Coordinate Geometry, MPS Basic System, MPS Mixed Integer Feature, MPS Generalized Upper Bound, Time Sharing Application, Simscript, PMCS Network Processor, PMCS Resource Scheduler, PMCS Interactive Input Processor, and PMCS Interactive On-line Reporting Processor. Others include Slave Program Activity Monitor/II, APT Postprocessors, types A and B, APT Automatically Programmed Tools, Applications Services Library, and Interactive Mathematical Programming System.

Manufacturing packages include APT Automatically Programmed Tools, HMS Inventory Record Management, HMS Manufacturing Data Control, HMS Material Requirement Planning, HMS Master Production Scheduling, HMS Statistical Forecasting, HMS Capacity Requirements Planning, and HMS Purchased Material Control. Products packaged under the Anvil-4000 name include Basic System, Extended Geometry, Drafting, Numerical Control, Analysis, and Complete Package.

Health Care packages include HHS Foundation System, HHS General Ledger/Responsibility Reporting, HHS Preventive Maintenance, HHS Property Ledger, HHS Accounts Payable, HHS Cost Allocation, and HHS Medical Records.

Banking packages include DES Document Entry Subsystem, CHECS Proof & Transit Subsystem, CHECS, and CHECS On-line Balancing and Item Correction.

PRICING & SUPPORT

POLICY: DPS 8000 equipment is available for purchase or for rental under a one-year or four-year lease.

SUPPORT: Bull offers several maintenance plans falling under basic hardware maintenance, extended maintenance converage, and premium services. Basic monthly hardware maintenance provides for contracted on-call remedial maintenance service during the Principal Period of Maintenance (PPM). PPM covers a period between 8 a.m. and 6 p.m., Monday through Friday, excluding locally observed Bull holidays.

TYPICAL CONFIGURATIONS: Sample configurations for the DPS 8000 Series are shown below. Complete equipment and software prices follow these configurations.

In France, a Bull DPS 8000/81 central processor unit costs FF 3,365,000. A Bull DPS 8000/82T costs FF 6,729,000. These prices include a one-year hardware guarantee.

In the U.K., a single-processor DPS 8000/81 from Honeywell Bull, Ltd.that includes a 10GB disk, four tape drives, and fifty communications lines is typically about £1 million. For a dual processor DPS 8000/82 with a 20GB disk, four tape drives, and one hundred communications lines, the price is typically about £1.7 million.

Honeywell Bull Prices (U.S.\$)

		Pur-			
		chase	Monthly	1-Year	4-Year
		Price	Maint.	Lease	Lease
		(\$)	(\$)	(\$)	(\$)
PROCESSO	DBS				
THOULOU					
CPS8681	DPS 8000/81 Central System Complex features one CPU, 16 megabytes of main memory, one Input/Output Processor (IOP) with 16 physical channels, one System Console with large screen monitor interface, 14-inch CRT, Keyboard, one Service Processor w/related storage devices, one Monitor, and one Modem	675,000	850	45,000	33,750
CPS8682	DPS 8000/82 fully redundant System Complex features two CPUs; two System Control Units each with 16 megabytes of main memory; two IOPs each with 16 physical channels; two System Consoles with large-screen interfaces, 14-inch CRTs, keyboard, and IOP connections; two Service Processes, two Modems	1,300,000	1,600	87,850	65,000
	System Upgrades:				
CPK8681	DPS 8000/81 to DPS 8000/82	675 000	750	45 000	33 750
CMM8601	Additional 16 megabytes of memory	120,000	210	8,000	6,000
	System Consoles and Console Options:				
CSU8600	Additional System Console	10,000	50	480	400
CSF8600	Optional printer for system console; 100 characters per second	1,225	50	121	103
CSF8603	Optional Printer Pedestal for CSF8600	395	NA	NA	NA
CSF8601	Optional 23-inch Large Screen Monitor for System Console	2,358	16	157	135
CSF8602	Optional Ceiling Mount for CSF8601	195	NA	NA	NA
MASS STO	DRAGE				
MSP3880 MSP3882	Mass Storage Processor; includes two storage directors and two channels 3880 Mass Storage Subsystem includes mass storage processor; two-channel, FIPS-compliant system supports up to two MSU3380/81s and six	74,270 74,270	200	4,400 4,400	3,745 3,745
MSD3884	WISU3302/035 Mass Storage Processor: includes two storage directors and four channels	88 270	224	5 230	4 450
MSP3886	3880 Mass Storage Subsystem includes mass storage processor; four-channel, FIPS-compliant system supports up to two MSU3380/81s and six	88,270	224	5,320	4,550
MSPO611	WISU3302/035 Freetanding Single-Channel Mass Storage Processor	50,000	132	1 828	1 507
MSP0612	Freestanding Dual Channel Mass Storage Processor	64 375	181	2 133	1,507
MSK0612	Upprade Kit, MSP0611 to an MSP0612	23.000	65	861	711
PSS8001	Capacitor Ridethrough Option for MSP0611, 0612	3,120	14	124	104
Mass Stora	age Units:				
	- 3				
MSU3380 MSU3381	Head of String Mass Storage Unit; includes four actuators Mass Storage Unit; head-of-string double-capacity system provides 3.697 giga- bytes of formatted capacity	88,800 122,500	295 295	4,780 7,100	4,070 6,500
MSU3382	Mass Storage Slave Unit: includes four actuators	64,450	215	3,470	2.960
MSU3383	Mass Storage Unit, secondary double-capacity system; up to three may be added to each MSU3380/3381	98,150	215	5,750	5,200
MSU0451	Removable Disk Mass Storage Unit, 200M bytes, unformatted; requires MSF007	18,500	122	1,140	950
MSF0006	Dual Access Feature for MSU0451	2,070	14	89	76
MSF0007	Rotational Position Sensing Option for MSU0451	500	14	87	76
MSU0500	Dual Fixed Disk Mass Storage Unit, 626 megabytes of capacity	38,850	198	1,412	1,180
MSU0501	Dual Fixed Disk Mass Storage Unit; 1101 megabytes of capacity	43,850	227	1,777	1,482
MSK0501	Upgrade kit; MSU0500 to MSU0501	5,000	25	361	297
MSF0011	Dual Access Feature for MSU0501/0500	4,140	23	163	136
NISA 1140	Unit Addressing for up to four WS004XX Units for WSP0611/0612	3,500	17	157	101
MGA 1141	Dual Addressing for up to four MSLI04xx Units for MSF001 1/0012	3,500	10	147	101
MSA 1142	Dual Addressing for up to two MSU05xx Units	4,500	19	216	178
MSE0500	Spare Head Disk Assembly for MSU0500	12 340			<u> </u>
MSF0501	Spare Head Disk Assembly for MSU0501	15,808	_	_	—
MSF1140	Single channel device adapter for MSU04xx devices on MSP0611/0612	3,500	_	109	89
MSF1141	Device Adapter for configuring MSU04xx devices (max 16) to MSP0612	6,000		187	152
MSF1142	Unit expansion for configuring additional MSU05xx devices (max 7) to MSP0612	4,000		125	105
MSF1143	Nonsimultaneous (Switched) Datanet Channel for MSP0611/0612	8.237	16	284	234
MSF1150	Second Nonsimultaneous (Switched) Datanet Channel for MSP0611/0612	8,237	16	284	234
	Mass Storage Exchange Features:				
MSF8030	Primary Disk Channel Connection Feature for MSP8021/22/23; each connection feature provides one IOP channel, cables, and associated interface logic in the mass storage processor	6,000	10	360	305

NC—No charge. NA—Not available.

Bull DPS 8000 Series

		Pur- chase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	4-Year Lease (\$)
MSF8031	Switched Disk Channel Connection Feature for MSP8021/22/23; each connection feature provides one IOP channel, cables, and associated interface logic in the mass storage processor	4,850	8	290	245
MSF3882	Upgrade from MSP3882 to MSP3886; upgrade kit includes two switched general disk channel connections to the IOP	15,000	24	920	805
	Mass Storage Exchange Features:				
MXF8622 MXF8624 MSF8626 MXF8636 MXF8638	Channel Exchange Feature for MSP0607/0609 Channel Exchange Feature for MSP0611/0612 Channel Exchange Feature for MSF8012/8013/8016/8017 channel connections Channel Exchange Feature for MSP3880 Channel Exchange Feature for MSP3884	5,000 5,000 5,000 18,350 36,700	NA NA NA NA	NA NA NA NA	NA NA NA NA
MAGNETIC	C TAPE SUBSYSTEMS				
MTS8205	Magnetic Tape Subsystem; includes tape processor, one MTU8205 tape unit, and	48,000	410	2,913	2,516
MTS8206	Magnetic Tape Subsystem; includes tape processor, one MTU8206 tape unit, and	45,000	410	2,774	2,395
MTS8208	Magnetic Tape Subsystem; includes tape processor, one MTU8208 tape unit, and	47,000	515	2,876	2,484
MTS8225	Magnetic Tape Subsystem; FIPS-compliant, 125 inches per second (ips), 800/ 1600 bits per inch (bpi) supports up to eight tape devices and requires either MTF8200 or MTF8201	48,000	410	2,913	2,516
MTS8226	Magnetic Tape Subsystem; FIPS-compliant, 125 ips, 1600/6250 bpi supports up to eight tape devices and requires either MTE8200 or MTE8201	45,000	410	2,774	2,395
MTS8228	Magnetic Tape Subsystem; FIPS-compliant, 200 ips, 1600/6250 bpi supports up to eight tape devices and requires either MTF8200 or MTF8201	47,000	515	2,876	2,484
MTP0611 MTF8030	Magnetic Tape Processor for MTU0610/0630; supports up to eight tape units Primary Tape Channel Connection Feature for MTP8021/8022/8023; each con- nection feature provides one IOP channel, cables, and associated interface logic in the magnetic tape processor	29,400 5,000	191 9	1,093 300	918 255
MTF8031	Switched Cape Channel Connection Feature for MTP8021/8022/8023	4,850	8	290	245
MTF8209 MTF8200	Magnetic Tape Subsystem 1 x 8 Switch; either this feature or MTF8201 required for each MTS82xy	8,000 NC	NC	421 NC	NC
MTF8201 MTF8202	Magnetic Tape Subsystem 2 x 8 Switch Magnetic Tape Subsystem Switched Channel; includes IOP or CBU channel	6,130 8,000	14 12	323 421	279 364
Magnetic ⁻	Гаре Units:				
MTU8205	Magnetic Tape Unit; 125 ips, 800/1600 bpi	19,050	240	1,038	897
MTU8206 MTU8208	Magnetic Tape Unit; 125 ips, 1600/6250 bpi Magnetic Tape Unit: 200 ips, 1600/6250 bpi	18,500 21,000	240 342	1,061 1,163	916 1.005
MTU0610	Magnetic Tape Unit; includes cartridge load, requires speed/density feature	21,000	188	814	690
Frature f		14,015	101	604	510
Features to	or the MILOOBIO:				
MTF0607 MTF0608	800/1600 bpi, 200 ips feature for MTU0610 1600/6250 bpi, 200 ips feature for MTU0610	6,090 13,319	81 124	306 520	266 441
MTK0678	Upgrade Kit; MTF0607 to MTF0608 performance	10,784	52	215	176
Features for	or the MTU0630:				
MTF0634	75 ips, PE/NRZI feature	4,725	151	297	268
MTF0636	125 ips, PE/NRZI feature	9,805	170	472	410
MTF0637	125 ips, PE/GCR feature	10,330	148	470	408
MTK0630	Performance upgrade MTF0634 to MTF0635 Performance upgrade MTF0636 to MTF0637	2,385	21	76 57	61 47
MTK0632	Performance upgrade MTF0634 to MTF0636	5,080	21	176	146
MTK0633 MTK0634	Performance upgrade MTF0635 to MTF0637 High Altitude Adapter	3,220 240	21	121 8	101 6
	Magnetic Tape Channel Exchange Features:				
MXF8616	Channel Exchange Feature for MTP0610; each exchange feature provides one IOP channel connection to connect tape processor that was previously attached to a Level 66 or DPS 8 system to a DPS 8000 system	5,000	NA	NA	NA
NCNo cha	nge.				

NA-Not available.

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		Pur- chase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	4-Year Lease (\$)
MXF8618	Channel Exchange Feature for MTP0611 Magnetic Tape Processor; provides one IOP channel connection to connect existing processor previously attached to a Level 66 or DPS 8 to a DPS 9000 existem	5,000	NA	NA	NA
MSF8620	Channel Exchange Feature for MTF8012/8013/8016/8017 channel connections; provides one IOP channel connection to attach existing systems previously con-	5,000	NA	NA	NA
MXF8634	Channel Exchange Feature for MTS8205/8206/8208 and MTF8202; provides one IOP channel connection to attach existing system previously attached to a DPS 8 to a DPS 8000 system	9,175	NA	NA	NA
LINE PRIN	ITERS				
PRU0903 PRU1203 PRU1600	High-speed belt printer; 900 lpm High-speed belt printer; 1200 lpm High-speed belt printer; 1600 lpm, 136 print positions	34,975 38,275 64,940	453 503 538	2,097 2,375 2,910	1,752 1,955 2,472
PRU0903	/1203 Options:	-			
PRB3213 PRB3300 PRB3500 PRB3501 PRB3513 PRB3524 PRB3549 PRB3600 PRB3703 PRK0903 PRK0907	64-character Uppercase ASCII Print Belt, optimized 96-character Upper/Lowercase ASCII Print Belt, optimized 64-character Series 400/600/600/L66 Print Belt 64-character Standard IBM Print Belt 64-character Uppercase ASCII Print Belt 64-character OCR-A Numeric Print Belt 64-character OCR-A Alphanumeric Print Belt 96-character Upper/Lowercase ASCII Print Belt 64-character Series 200/2000 Print Belt Upgrade PRU903 to PRU1203 Exchange of PDSI to DAI interface for 0903.1203; includes control panel	NC NC NC NC NC NC NC 5,000 3,000	NC NC NC NC NC NC NC S4 NC	NC NC NC NC NC NC NC NC NC NC NC NC NC N	NC NC NC NC NC NC NC 229 NC
PRU1600	Options:				
PRB0500 PRB0524 PRB0532 PRB0549 PRB0600 PRF0022	63-character OCR-B Print Belt 63 character OCR A/B Print Belt 63-character Puerto Rico Print Belt 63-character OCR-A Alphanumeric Print Belt 94-character ASCII Belt; upper-/lowercase 24 additional print positions; 136 to 160	2,460 2,460 2,460 2,460 2,567 2,610	97 97 101 97 97 17	186 186 186 186 191 113	171 171 171 171 173 94
UNIT REC	ORD PROCESSORS				
URP8600	Embedded Unit Record Processor; supports up to two CRU050/1050, PCU0120/	4,500	8	300	225
URP8601	Embedded Unit Record Processor; supports up to two PRU0903/1201/1203 printers	4,500	8	300	225
URP8602	Embedded Unit Record Processor; supports up to two PRU1600 printer	4,500	8	300	225
PUNCH C	ARD EQUIPMENT				
CRU0501 URA0056	Card Reader (500 cpm); requires URA0056 Addressing capability for CRU0501	19,500 256	129 NC	694 9	578 6
		Pur- chase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	3-Year Lease (\$)
TERMINA	LS				
VIP7816	Synchronous/Asynchronous Multiple Mode Terminal with 12-inch CRT, green phosphor, high profile keyboard, 25-foot RS-232-C cable; includes VIP7800 and VIP7700 modes	NA			
VIP7817	Synchronous/Asynchronous Multiple Mode Terminal with 15-inch CRT, green phosphor, high-profile keyboard, 25-foot RS-232-C cable; includes VIP7800 and VIP7700 modes	NA		_	—
VIP7826	Synchronous/Asynchronous Multiple Mode Terminal with 12-inch CRT, green phosphor, low profile multifunctional keyboard, RS-232-C and RS-422-A inter- face, and 25-foot RS-422-A cable; includes VIP7800 and VIP7700 modes	NA	_		
VIP7827	Synchronous/Asynchronous Multiple Mode Terminal with 15-inch CRT, green phosphor, low-profile multifunctional keyboard, 25-foot RS-422-A cable; includes VIP7800 and VIP7700 modes	NA	_	—	

NC—No charge. NA—Not available.

Bull DPS 8000 Series

		Pur- chase Price {\$)	Monthly Maint. (\$)	1-Year Lease (\$)	4-Year Lease (\$)
DATANET	8 SERIES NETWORK PROCESSORS AND OPTIONS	<u></u>			
DCU8110	DATANET 8/10 Network Processor system with 1MB of memory expandable to 2MB; supports a maximum of 31 data communications ports and includes 3 RS- 222 C/V 24 expension (abstractor support ports)	23,900	130	1,195	795
DCU8120	DATANET 8/20 Network Processor system with cache memory, 1MB of memory expandable to 2MB. System is upgradable to dual-processor system with dual- cache memory; supports 31 data communications ports extendable to 127	38,000	215	1,900	1,275
DCU8130	ports, and includes 3 RS-232-C/V.24 asynchronous/char. synchronous ports DATANET 8/30 Network Processor system with cache memory and 2MB of memory expandable to 4MB. System is upgradable to dual-processor system with dual- cache memory; supports 159 data comm. ports expandable to 255 ports,*and includes 3 RS-232-C/V.24 asynchronous/char. synchronous ports	80,000	350	4,000	2,675
DATANET	OPTIONS				
	OPTIONS FOR THE DATANET 8/10 ONLY:				
DCM8110	One-megabyte Memory Expansion Module	7,000	50	350	230
	OPTIONS FOR DATANET 8/20 ONLY:				
DCP8120	Extended Performance Option; includes second processor and associated cache	14,000	115	700	475
DCM8120	One-megabyte Memory Expansion Module	7,000	50	350	235
DCE8121	First Line Expansion Module; provides support for up to 32 additional data com- munications ports (max. 63 ports per DATANET 8/20)	2,500	5	125	85
DCE8122	Second Line Expansion Module; provides support for up to 64 additional data communications ports (max. 127 ports per DATANET 8/20); requires DCM8120 and DCE8121	5,000	10	250	170
	OPTIONS FOR DATANET 8/30 ONLY:				
DCP8130	Extended Performance Option; includes second processor and associated cache memory	27,000	220	1,350	900
DCM8130 DCE8131	Two-megabyte Memory Expansion Module Line Expansion Module; provides support for up to 96 additional data communica- tions ports (max. 255 ports per DATANET 8/30); requires DCM8130	14,000 7,500	100 15	700 375	470 250
	OPTIONS FOR DATANET 8/10, 8/20, AND 8/30:				
DCF8002	Visual Display Terminal Console, 24-by-80 character screen; one required for each	795	20	40	30
DCF8003	Hard Copy Console Receive Only Printer (100 cps); one required for each system that uses DATANET 8, 8/10, 8/20, or 8/30 Network Processors	1,195	22	60	40
DCF8004	Console Table for Console Components	750	NA 18	40	25
DCF8005	Second 5 4-inch Diskette Drive Network Processor Channel Connection to DPS 8 with Input/Output Multiplexer	8,000	65	339	25
DCE8109	Network Processor Channel Connection to DPS 88 System with Channel Adapter	8,000	65	339	288
DCE8111	Network Processor Channel Connection to DPS 90 System with Input/Output	8,000	65	339	288
DCF8052	Multiline Communications Controller-16 (MLC-16) accommodates up to four Com- munications Interface Adapters; maximum of 16 data communications ports per MLC-16	2,700	15	135	90
	LOW- AND MEDIUM-SPEED OPTIONS:				
DCF8073	RS-232-C Asynchronous/Character Synchronous Integrated Communications In- terface Adapter with four RS-232-C/V.24 data communications ports; includes four 50-ft, cables. Maximum port speed is 19 2K bps	2,000	16	100	70
DCF8049	RS-232-C Bit Synchronous HDLC Integrated Communications Interface Adapter with two RS-232-C/V.24 data communications ports; includes two 50-ft. cables.	3,200	26	160	110
DCF8053	Low- and Medium-Speed Asynchronous/Character Synchronous Communications Interface Adapter; accommodates up to four Line Interface Module Connections;	1,000	7	50	35
DCF8055	RS-232-C/V.24 Asynchronous/Character Synchronous Line Interface Module with one RS-232-C/V.24 data communications port; includes one 50-ft. cable. Maxi-	275	3	15	10
DCF8059	Mil-188-C Asynchronous/Character Synchronous Line Interface Module with one Mil-188-C data communications port; includes one 50-ft. cable. Maximum port speed is 19.2K bps	275	3	15	10

NC—No charge. NA—Not available.

		Pu cha Pri (\$	ir- ise Ma ce N 5)	onthly laint. (\$)	1-Year Lease (\$)	4-Year Lease (\$)
	MEDIUM- AND HIGH-SPEED OPTIONS:					
DCF8061	Medium- and High-Speed Character Synchronous or Bit Synchronous Communica- tions Interface Adapter accommodates one Line Interface Module Connection (DCF8062, DCF8063, DCF8064, DCF8065, DCF8067, DCF8069, or DCF8071)	- 2,	200	16	110	75
	MEDIUM-SPEED OPTIONS FOR DCF8061					
DCF8062	RS-232-C/V.24 Bit Synchronous (HDLC) Line Interface Module with one RS-232-C/V.24 data communications port; includes one 50-ft. cable. Maximum port speed is 19.2K bps.	:	275	3	15	10
	HIGH-SPEED OPTIONS FOR DCF8061					
DCF8064	X.21 Bit Synchronous (HDLC) Line Interface Module with one X.21 data communi	i- 4	450	3	25	15
DCF8067	Mil-188-C Bit Synchronous (HDLC)/Character Synchronous Line Interface Module with one Mil-188-C data communications port; includes one 50-ft. cable; Maxi-		450	3	25	15
DCF8069	V.35 Bit Synchronous (HDLC) Character Synchronous Line Interface Module with one V.35 data communications port; includes one 50-ft. cable; Maximum port		450	3	25	15
DCF8071	Bell 301/303 Bit Synchronous (HDLC)/Character Synchronous Line Interface Mod ule with one Bell 301/303 data communications port; includes one 50-ft. cable; Maximum port speed is 64K bps.	- 4	450	3	25	15
	OTHER OPTIONS FOR DATANET 8, 8/10, 8/20, AND 8/30:					
DCF8024	Direct Connect Capability for one Asynchronous or Character Synchronous Line	:	350	2	14	12
DCF8026	with KS-232-C Physical Interface Universal Modem Bypass; character synchronous to 19.2K bps. RS-232-C physi- cal interface		415	2	16	13
NETWORI	(PROCESSOR CHANNEL CONNECTIONS					
DCE8020	Datanet 8 Network Processor Channel Connection Feature; each connection fea-	8,0	000	72	346	295
DCE8119	Datanet 8/10, 8/20, 8/30 Network Processor Channel Connection Feature; pro- vides one IOP channel, cables, and associated interface in the Datanet systems	8,0	000	72	346	295
	Network Processor Channel Exchange Features:					
MXF8628	Channel Exchange Feature for Datanet 8 and PPS	3,!	500	NA	NA	NA
MXF8630	Channel Exchange Feature for Datanet 6611/6651/6658; provides one IOF host connection feature to connect Datanet 66 processor previously connected to a Level 66 or DPS 8 system to the DPS 8000 system	o 3,1	500	NA	NA	NA
	Hyperchannel Connections:					
MXF8640 MXF8632	DPS 8000 Series Hyperchannel Connection Feature A161 DPS 8000 System Channel Exchange Feature for connecting existing A161 Hyper channel Subsystem previously attached to a Level 66 or DPS 8 to an IOP	- 5,:	000 300	111 NA	1,111 NA	745 NA
MXF8639	Power Sequence for FIPS channel/subsystem	3,:	200	5	200	149
		Monthly License Fee (\$)	Ex- panded Support Charge (\$)	Initial License Fee (\$)	License Orig. Fee (\$)	Annual Basic Support (\$)
GCOS 8 S	YSTEM RELEASE 3000					
	Operating System Executives:					
SVS8053 SVS8059 SVS8050	GCOS 8 Operating System Executive GCOS 8 OP Exec for DPS 8000 GCOS 8 Basic System	2,800 2,800 NSC	NSC NSC (1)		8,000 8,000 8,000	

NA—Not available. NSC—No separate charge. NC—No charge.

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		Monthly License Fee (\$)	Ex- panded Support Charge (\$)	Initial License Fee (\$)	License Orig. Fee (\$)	Annual Basic Support (\$)
	Operating System Extensions:					
SVE8000 SVE8001 SVE8028 SVE8038 SVE8039 SVE8040 SVJ8000* SVP8001 SVP8001 SVP8002 SVS8014 SVP8081	FMS Catalog Cache Facility FMS Test Access Facility Password Encryption Facility Console Manager NP Operator Console Console Journal Parametric JCL System Maintenance Facility System Maintenance Facility System Performance Analysis Six Processor Support Composition System Maintenance Facility; DPS 8000	81 82 68 235 112 165 44 107 93 343 73 683	16 10 6 30 6 12 7 53 15 31 14 126			
SNU0471 SNU0472* SNU0473* SVU8000* SVU8001 SVU802* SVU8012* SVU8018 SVU8025 SVU8026	PPS Utilities PPS Off-line PSS On-line System Utilities File Generation Facility Sort/Merge Facility File Management System Utility VIDEO Comp System Utility Facility UTL8 System Administration:	31 NSC 63 59 131 386 25 185 175	5 7 5 21 56 5 30 			
SVE8043T SVE8044T SVU8041T SVP8012* SVU8016 SVU8017 SVU8022 SVU8023 SVU8023 SVU8024	HAPS 8 Basic System HAPS 8 On-Line Tape Management System PARS Mass Store I/O Analyzer CAPSUL FACTS SARA TRS Time Sharing Facilities:	850 250 675 354 329 	37 37 	20,000 5,500 22,000 2,500 14,500 14,800 6,950	 1,045	3,000 2,600 — — — —
SVE8008 SVE8010 SVE8010 SVE8013 SVE8014 SVE8015 SVE8016• SVE8017 SVE8017 SVE8018 SVE8019 SVE8020 SVS8005	TSS File Management Option TSS Adv. Application Support Option TSS Media Input Option TEX TSS Editing Option TSS Document Format Option TSS Document Format Option TSS Electronic Mail Option TSS Sort Interface Option TSS Administration Option TSS Administration Option Multicopy TSS Support Option TSS Facility	133 197 65 383 119 60 200 83 96 139 656 98	13 39 13 76 26 13 39 13 16 19 129 26			
SEL6012 SEL6013 SEL6014* SVD8004 SVE8011 SVE8012 SVE8022 SVL8000 SVL8001 SVL8003 SVL8003 SVL8003 SVL8007 SVL8008 SVL8010 SVL8011 SVL8012 SVL8013	Lisp Pascal Compiler B TSS Databasic TSS Cobol-74 TSS Fortran-66 TSS Fortran-66 TSS Fortran-77 Cobol-74 C and R Facility Fortran-66 Compiler PL/1 C and R Facility RPG-II Facility TSS Basic Cobol-68 Compiler Fortran-77 C and R Facility Fortran-77 Hex Exp. Option Fortran-76 Compatibility Cobol-74 RQ		28 13 13 13 33 59 59 59 59 54 49 18 	3,825 8,609 5,739 — — — — — — — — — — — — — — — — — — —		

NA—Not available. NSC—No separate charge. NC—No charge.

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Bull DPS 8000 Series

		Monthly License Fee (\$)	Ex- panded Support Charge (\$)	Initial License Fee (\$)	License Orig. Fee {\$)	Annual Basic Support (\$)
SVL8015 SVL8016 SVR8000 SVR8002 SVR8004 SVR8005 SVL8024 SVL8030T	C Programming Language Fortran-77 ESV C and R Cobol-74 Runtime Facility PL/1 Runtime Facility Fortran-77 Runtime Facility Fortran-77 ESV Runtime Facility DPS 8000 Cobol-85 Compiler and Runtime Facility DPS 8000 Ada Compiler and Runtime Facility Data Management Facilities:	340 534 106 92 71 161 358	40 11 13 5 17 54 1,815	8,000 — — — 716 40,000		800
SVD8006* SVD8007* SVD8000 SVD8001 SVD8002 SVD8003 SVD8011 SVD8067 SVU8004	DD/DS Basic Facility DD/DS On-line Option DM-IV Standard Facility DM-IV Fortran Subschema Trans. I-D-S/I Facility Index Sequential Processing Facility Multicopy DM-IV/TP Concurrency Interel for DPS 8000 Comprehensive Archival System End-User Facilities:	343 153 1,225 141 1,341 366 1,500 665	43 19 215 12 137 8 37 210 1,920	9,800 16,000		
SVE8025 SNM7804 SVH8053 SVH8053 SVH8053 SVH8058 SNU8000 SVU8035 SVU8036 SVU8037 SVU8038 SVU8040 AFF0001* SNM7800 SNM7803 SVP8004 SVH8001* SVH8001* SVH8001* SVH8001* SVH8003* SVH8004 SVH8003 SVP8006 SVP8006 SVP8007 SVP8020 SVU8020* SVU8027* SVU8028	Mail 8 Server Personal Computer Interconnect Inforedge for DPS 8000 Inforedge-Reporter for DPS 8000 Inforedge-Example Query for DPS 8000 TGRAF-05 Dr TEK 4010 Dr TEK 4105 Dr TEK 4107/4109 Dr TEK 4107/4109 Dr TEK 4107/4109 Dr TEK 4107/4109 Dr TEK 4115/4125 Dr Matrix QCC Dr HP 7475/Sweet-P Management Support Center; annual license fee, \$72,500 PC7800 Emulator PC7800 For Macintosh I-D-S/I DQ Forum 8 EDIT 8 PCF EQ IQ Credo Texto Logotel DM-IV QRP DM-IV QRP DM-IV QRP DM-IV QRP DM-IV V PLP MDQS/IV Syntax-Directed Editor Grafmaster Solution Center Menu DI-Textpro Programmer Productivity Facilities:	105 	15 495 100 40 50 			
SVH8006 SVP8009 SVP8010 SVP8014 SVP8015 SVP8016 SVP8021 SVP8021 SVP8023 SVU8029 SVU8021* SVU8050	Magna 8 Debug Support System Cobol-74 Debug Support Fortran-77 Debug Support Softool Cobol Softool CCC Complete Softool System-80 Cobol Auditec System-80 DM-IV Optional System-80 Cobol with DM-IV Optional Business-Graf Scientific/Engineer UW Tools	124 219 268 1,400 1,500 2,500 780 500 1,280 1,280	 24 32 1,575 	120,000 		14,400

NA—Not available. NSC—No separate charge. NC—No charge.

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Bull DPS 8000 Series

		Monthly License	Ex- panded Support	Initial License	License Orig.	Annual Basic
		Fee (\$)	Charge (\$)	Fee (\$)	Fee (\$)	Support (\$)
	Transaction Processing:					
SVD8015	СТР	321	32	_		
SVE8033	DM-IV/TP ELQ	275	55	—		_
SVP8013	TATS	500		10,800		1,620
SVP8017	TSM	980		24,000	—	1,920
SVS8002	DM-IV/TP Facility	1,714	207	—		
SVS8006	TDS	2,025	266			
SVS8007	TPE	710	70			—
SVS8017	TP8	2,140	243		_	
SVU8003	DM-IV/TP Forms Facility	339	64	_		_
	Special Packages:					
SVD8024	DDE Basic System	1,828	290		—	
SVD8028	DDE Comp package for TDS	4,208	596		—	
SVD8031	DDE Comp package for TPE	3,955	551		—	
SVD8033	DDE Management Facility for TDS	1,746	287	_		
SVD8034	DDE Management Facility for TPE	1,632	269			
SVS8003	DM-IV/TP Comp Facility	3,395	476			
SVS8009	DM-IV/TP System Management Facility	1,260	259			
	Other Software:					
SVS8012	GCOS 8 SR2000 P	45			_	
SVS8013	GCOS 8 SR2000 SER DB	38	_	·····	—	
SVS8015	GCOS 8 SR2300 P	57				
SVS8016	GCOS 8 SR2300 SER DB	45				
SVS8019	GCOS 8 SR2500 P	62				
SVS8020	GCOS 8 SR2500 SER DB	52			—	
SVS8057	GCOS 8 SR3000 P	70				
SVS8058	GCOS 8 KP and C	60				
	GCOS 8 Communications Facilities:					
SVC8000*	GRTS-II Facility	345	58	_		
SVC8001*	GRTS-II HDLC Support	166	14			
SVC8002*	NPS Facility	1,232	265			—
SVC8003*	NPS HDLC Support	166	14			
SVC8004	Extended NP Support	176	36			
SVC8006	F1F66	21	7			
SVC8040*	Inter Bisync-GK I S-II	97	18			
SVC8048*	UTID	569	139			-
SVC8049	Interchannel Support	135	∠0 10			_
SVC8051		200	70			
SVC8052	DSANET	125	20			
0.0002	BORNE I	120	20			

		Monthly License Fee (\$)	Optional Monthly Support Charge (\$)
DATANET	8 Distributed Network Supervisor Software		
SNC8120	Distributed Network Supervisor 300	560	99
SNC8121	Network Operator Interface	11	5
SNC8123	Host Connect Support for DPS 8, 88, and 90	60	11
SNC8131	HDLC Data Link Control point to point	95	18
SNC8122	Value Added Network Support, X.25, PAD, PBX, and LAN	185	33
SNC8126	Primary Network Support, X.21	185	33
SNC8127	Interactive Binary Synchronous Terminal Support (3270)	90	17
SNC8128	Remote Batch Binary Synchronous (2780/3780) Workstation Support	80	14
SNC8129	Remote Computer Interface (RCI) Terminal Support	50	9
SNC8130	Logical High-Level Data Link Control	125	22
SNC8190	Host Administrative Facilities	155	28
SNC8193	Network Administration Facility	35	6

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NA—Not available. NSC—No separate charge. NC—No charge.

		Monthly License Fee (\$)	Optional Monthly Support Charge (\$)
SVC8051	Unified File Transfer 8	200	72
SNC8195	Distributed Network Supervisor/Entry GCOS 8	450	80
SNC8197	Remote Switch/Concentrator	375	66
SNC8118	Eight-Inch Diskette Software Updates for DN8	10	NC
SCC3209	Remote Batch Facility/6 (RBF/6)	32	6
SCC3210	Distributed Job Processing	67	14
SCC3210	Distributed Job Processing	67	14

*Not available for new orders. (1) Charge varies relative to hardware central power. NA—Not available. NSC—No separate charge. NC—No charge.■