# Electronic Design 19 

See $\mu \mathrm{P}$ memory accesses in unique formats. A new kind of analyzer not only catches data, it displays information in four ways. Use the memory mode to
get the big picture of read-write activity. Then go into the page mode to look at details. Thumb through program listings or word sequences. Sort it out: p. 129.


## here today at no extra cost in every Trimpot ${ }^{\oplus}$ Potentiometer

Historically, pin-to-element termination problems have been one of the primary causes of trimmer failure ... especially during handling and PC board process operations. Bourns exclusive Swage-Bond ${ }^{\text {TM }}$ process virtually eliminates pin termination failure . . . truly a revolution in trimmer reliability. Furthermore, SwageBonding results in a marked improvement in temperature coefficient consistency.
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Bourns trimmers stay sealed when others fail. We know. We've tested them all. Bourns uses a chevron-type sealing technique, that seals without 0 -rings . . . eliminating the windup and springback that frequently occurs with such seals. The result is faster and more precise adjustability . . . with a seal that really works.

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Our complete line of TO-5 relays includes military and commercial/industrial types, with virtually all military versions qualified to established reliability MIL specs. For complete data, contact Teledyne Relays - the people who pioneered the TO-5 relay.


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- Centigrid ${ }^{\text {® }}$ Series

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applications (NASA qualified)

- High Environment Series

Hi-temperature, Hi -shock, and
Hi -vibration types

## תヘ TELEDYNE RELAYS

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# Introducing SpinGuard. 



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## Across the Desk

## A funny thing happened to us at the printer

Somehow the wrong photo got attached to a story. Which wouldn't have been so bad if that photo hadn't already received its due in the right story. So if that "pulse generator" on page 161 of Electronic Design Vol. 24, No. 15 , July 19, 1976 looks strange, you're right. It's a power supply, not a pulse generator. Sorry HP 8080 .


This is a power supply.


This is a pulse generator.

## Parasitic oscillation may have a simple cure

Regarding your article on parasitic oscillation in emitter followers (ED No. 13, June 21, 1976, p. 110), I have often observed such
problems with modern transistors, but for different reasons. Such oscillations can occur with simple resistive-emitter loads. In that case, one should check the path, however devious, from base to collector.

If it is some inches in length and has no unbypassed resistors or inductors, there is sufficient lead inductance for a respectable tank circuit somewhere in the frequency range mentioned in your article. The capacitors to complete the equivalent of a Colpits oscillator are formed from the transistor's base-emitter and collector-emitter capacities.
There were even oscillators made intentionally this way in the vacuum tube days; I think they were called "ultra audion." This form of parasitic oscillation is not much affected by adding emitter resistance, but a resistor in either base or collector spoils the Q of the leadformed inductor. The longer the path from base to collector, the larger the resistor that will be needed.

John O. G. Darrow
Westinghouse Air Brake Co.
Pittsburgh, PA 15218

## Random Thoughts

Your editorial sounds like my last job and my reasons for leav-ing.-R. B. White, Kaiser Aerospace, San Jose, CA. (Editorial: Using Resources," ED 5, Mar. 1, 1976).

This is about the third time you described our company president. You mean there's more than one? -Name withheld, Austin, TX. (continued on page 10)

Electronic Design welcomes the opinions of its readers on the issues raised in the magazine's editorial columns. Address letters to Managing Editor, Electronic Design, 50 Essex St. Rochelle Park, N.J. 07662. Try to keep letters under 200 words. Letters must be signed. Names will be withheld on request.

## OPTRON OFFERS IMMEDIATE DELIVERY OF NEW, LOW COST SERIES

OPTRON's new, low cost optically coupled interrupter module series combines non-contact switching and solid state reliability for applications requiring sensing of position or motion of an opaque object such as motion limit, paper edge or shaft encoding.

The new OPB 813, OPB 814 and OPB 815 consist of a gallium arsenide infrared LED coupled with a silicon phototransistor in an economical molded plastic housing. With a LED input of 20 mA , the OPB 813 and OPB 815 have typical unblocked current outputs of 2.0 mA and 3.0 mA , respectively. Typical output of the OPB 814 is 3.0 mA with a 10 mA input. The entire series is available from stock.

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| OPB 813 | H13A2 |
| OPB 814 | H13B1 |
| OPB 814 | H13B2 |

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## ACROSS THE DESK

(continued from page 7)
(Editorial: "The Star," ED 12, June 7, 1976).

Perhaps Teitaro Hiraga of TDK will come and speak to my boss.Name withheld, Haifa, Israel. (Challenges to the Engineer who Manages, ED 9, April 26, 1976).

## Misplaced Caption Dept.



I'll never work overtime again.

Sorry. That's Jan Vermeer van Delft's "Young Women Reading a Letter," which hangs in the Rijksmuseum in Amsterdam.

## Here's Tuit

We are deeply grateful to Walter Skowron of Hewlett-Packard for his most useful gift, pictured here. This gift should prove invaluable in helping us with those thousands of jobs we were going to complete as soon as we got a round tuit.


## 'My Crime' not serious enough for businessmen

I read your editorial of June 21st with some interest. The whole subject of business ethics is certainly high in everybody's mind these days.

I'm not sure that your "tongue-in-cheek" approach to the subject, if I understand your editorial, goes far enough in really bringing the message of the importance of honesty and integrity in business dealings.

In my mind, it is an extremely serious subject that needs straightforward talk and complete communication to all of the employees of a business enterprise.

William E. Terry
Vice-president
Instrument Group
Hewlett-Packard Co. 1501 Page Mill Road Palo Alto, CA 94304

## Wideband rf design raises some questions

I found Mr. Nagle's article on wideband rf transformers (ED No. 3) quite worthwhile. The information presented is generally not widely publicized and is often very useful in rf circuit design. I think a couple of comments can be made, however.

First, Figure 16 appears to be all fouled up (or is it down?). The insertion loss of the transformer is shown to rise below 1 MHz , but above 100 MHz it is shown to go down from the $-19.5-\mathrm{dB}$ reference. That's strange, because the insertion loss of the transformers that I've build goes up at either band limit.

Second, the equation for finding the required primary inductance to obtain the lower $3-\mathrm{dB}$ frequency desired was over-simplified. His equation is accurate only if the signal source is a current source and no parallel resistance shunts the primary (i.e., a high Q core). Ferrites are usually low-Q materials, which makes them useful for filter beads. And rf circuits typically employ broadband transformers with matched primary and secondary.

For the more general case, the
equation for insertion loss in $d B$ is
$10 \log \left(1+\left(R / N^{2} X_{p}\right)^{2}\right.$ $\left(1+R_{L} / 2 N^{2} R_{p}\right)^{2}$
where
$\mathrm{X}_{\mathrm{p}}=$ inductive reactance of one turn at frequency of interest
$R_{p}=$ parallel resistance of one turn at frequency of interest
$\mathrm{R}_{\mathrm{L}}=$ load resistance
$\mathrm{N}=$ total number of turns from $\mathrm{R}_{\mathrm{L}}$ to ground
and

$$
\mathrm{R}=\frac{1}{2 / \mathrm{R}_{\mathrm{L}}+1 / \mathrm{N}^{2} \mathrm{R}_{\mathrm{p}}}
$$

Although this equation is considerably more invloved, it provides accurate results and is easily programmed on a programmable pocket calculator. With this form of the equation, the effect of changing the number of turns is quickly evaluated. The values of $X_{p}$ and $R_{p}$ are often given by the manufacturer of the core. For those cores specified with $A_{L}$ and $Q$, or $A_{L}$ and loss factor, similar equations can be written.
Ken Wetzel

Naval Weapons Center
China Lake, CA 93555

## The author replies

I appreciate Mr. Wetzel's taking the time and trouble to write about my article.

Concerning the insertion loss being up (or down): I ascribe the performance of the transformers to the fact that, at low frequencies, the primary reactance falls-so the transformer becomes more and more of a short circuit. At the high frequency end, the voltage rise is due to resonance.

Mr. Wetzel is quite correct in pointing out that my equation for the primary inductance considers only the effect of the load resistance. However, the transformer losses and generator impedance will shunt the transformed load impedance, thereby lowering the required primary inductance. Therefore, use of my equation will usually result in a conservative design. While ferrites generally yield low A, compared with other core materials, the energy loss in a well-designed ferrite core should still be low compared to the energy dissipated in the load.
(continued on page 15)

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There's a saying in Italy, where we come from, that goes something like this: "between saying and doing there's a sea in the middle". OK, may be it


The TDA 2002 chip on its Pentawatt ${ }^{\circledR}$ frame. doesn't sound too great in English, but in Italian it does and it rhymes as well!

Now, the
particular "sea" in our case is just this: TDA 2002 was invented and patented by SGS-ATES; we were the first to
produce it and we're already delivering it - in fact, we've been mass-producing the TDA 2002 since the beginning of 1976. Now we hear that somebody else is going to launch a similar product on the market... in 1977. That's good, but for the moment let's talk about


Electronic Design 19, September 13, 1976

## 2002 weit. they do.



TDA 2002: 8 W integrated audio amplifier.
our TDA 2002, since it's the only one...
Worldwide patents for both the circuit and the package have been granted, or have been applied for. All of SGS-ATES' years of experience in the design and production of power linears are behind this product.

The package (Pentawatt ${ }^{\circ}$ ) and type of assembly guarantee more than 10,000 cycles of thermal fatigue with $\Delta \mathrm{T}_{\mathrm{C}}=100^{\circ}$, and that means long life. What's more, it's highly protected against short circuits; thermal overrange; supply overvoltages, including spikes; open ground; polarity inversion - which ensure the same trouble free, long life, even under exceptional conditions.

With a 14.4 V supply it gives 8 W on $2 \Omega$. It is ideal for car radios and CB transceivers: it saves $50 \%$ on external components and even more on space.

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## Some people just cant leave well enough alone.

Last March, while the other guys were making promises, Advanced Micro Devices was making Am9130's and Am9140's - the first family of 4K static RAM's.

Terrific. Everyone loved them. But were we satisfied? Did we stop there? No.

Advanced Micro Devices announces the $1 \mathrm{Kx} 4 / 4 \mathrm{Kx} 1$ Am91L30/L40, low-power versions of our Am9130/40.

These new circuits provide access times down to 250 nanoseconds with power dissipation of only 367 mW . That's half the power of the original. (And that's the same power
as the industry standard 1 K static RAM. You're getting four times the memory for the same power. Wow!)

There's more. These beauties do everything on a 5 -volt power supply. The logic levels are identical to TTL. You get all the features of the original, including full military temperature range availability and, as always, MIL-STD-883 for free. Plus: a freshly minted set of application notes and data sheets awaiting your call, wire or letter.

Some people just can't leave well enough alone.

# Advanced MOS/LSI 



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## Tek map found faulty

Though I have never been there, I have in my mind's eye a vision of the Tektronix facility in Wilsonville, OR. So I feel that the map on page 31 of your June 7, 1976 issue omits some important landmarks.

For example, I recall the lovely olde church nearby, the Fielde Mission. And a vegetable farmer, Planck, had a cornstand nearby.

In the bit stream, I recall seesome half waves and there was a conTROLL gate just on the other side of the Wheatstone bridge. And I recall, with some annoyance, that in a lovely wooded area of the grounds, some careless workmen had left a light pipe. Or was it a heavy pipe?

Dan Sheingold
Analog Devices
Norwood, MA 02062

## More connectors —and a note on radio

Ordinarily I don't take the time to write to editors of magazines. My business of servicing CB transceivers keeps me pretty busy, but I had to take time to make a couple of small corrections.

1. In your article on connectors ("Focus on Connectors," ED No. 11, May 24, 1976, p. 60.) I noted that Automatic Connector, Inc., of Commack, NY was not mentioned and they make a very nice connector, one much easier to install in the field than others. They deserve a mention, at least.
2. In response to the letter in the same issue by H. E. Fairman (p. 11), I would suggest that he look up the call sign of W2XMN Alpine, NJ and I think that he will find that it was issued to Maj. E. H. Armstrong. "K" call-signs were generally issued WEST of the Mississippi River. It was on W2XMN that I heard of the attack on Pearl Harbor!!

Alfred Kohlberg Jr.
570684 Ave.
New Carrollton, MD 20784

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[^1]
## Rental Electronics,Inc. 留

## GM laser crystals make better pollution monitors



Laser crystals made of lead-tin-telluride are being grown at General Motors for use in pollution monitoring systems.

Large, lead-tin-telluride laser crystals that will contribute to improved identification and measurement of air pollutants have been grown, for the first time, by researchers at the General Motors Research Laboratories, Warren, MI.

These crystals are used to make semiconductor-diode lasers that emit IR radiation in the 8 -to- $9-\mu \mathrm{m}$ wavelength range, a spectrum important to air pollution research. The lead-tin-telluride laser diodes will be incorporated in pollutionmonitoring equipment also under development at GM.

The GM crystals produce a laser output of $750 \mu \mathrm{~W}$, in contrast to only $10 \mu \mathrm{~W}$ produced by the best of the previously grown semiconductor lasers in the same wavelength range.

The lead-tin-telluride crystals $\left(\mathrm{Pb}_{1-\mathrm{x}} \mathrm{Sn}_{\mathrm{x}} \mathrm{Te}_{0.06}\right.$ to 0.08$)$ were grown by Dr. Wayne Lo, senior research engineer at the Laboratories, by nucleating them on a source ingot enriched with $0.01 \%$ tellurium. The ingot was sealed in an ampoule 15cm long and maintained at a critical temperature of 825 C for $\mathrm{sev}-$
eral days. This temperature markedly reduced crystal defects.

While conventional IR spectrometers and monochromators are available for pollutant monitoring, the broad spectrum emitted by their IR sources makes it difficult to distinguish pollutant molecules that have absorption bands close to each other.
The telluride laser, on the other hand, is much better suited for observing hydrocarbons present in exhaust gases and air, according to Dr. John C. Hill, senior research physicist at the Laboratories. The GM laser, Hill points out, has a resolution bandwidth $10^{4}$ times narrower than that of regular IR equipment. In addition, the GM laser power-per-unit bandwidth is about $10^{8}$ times higher than that of conventional IR instrumentation.

## Fuel-cell power plant to generate 27 MW . . .

A $\$ 40$-million project to develop a pollution-free, fuel-cell power plant for electric utilities is under
way at United Technologies, South Windsor, CT. The company, which is funding the project jointly with the Energy Research and Development Administration (ERDA) and the Electric Power Research Institute, is developing a 4.8 -MW fuelcell module that can be applied in multiples to produce up to 27.8 MW of power for a single installation.

Fuel for the cell will be hydrogen. An inverter plus a line transformer will be included in the module to convert the cell's de output of ac power.
Development of the first module is expected to take two years, and the full program is expected to eventually produce 56 27-MW units. Thirty-two units have already been contracted for by utility companies.

## while sodium-sulfur battery stores 10 MWh

Special hollow glass fibers that can conduct sodium ions are the key elements of a sodium-sulfur cell being developed by Dow Chemical for use in two different batteries: a 10 -MWh battery for storing utility power during off-peak-demand hours, and a 60 kWh battery for powering electric vehicles.

The Dow cell uses molten sodium for its positive electrode and molten sulfur for the negative, says William Brown, associate scientist at the Western Division in Walnut Creek, CA, where the cell is being developed. The temperature of the cell (which is about one-eighth the size and weight of its lead-acid equivalent) is maintained at 300 C , largely by the heat of the cell produced during the charge-discharge cycles.

Efficiency of the charge and discharge at high rates- $95 \%$ for voltage and $100 \%$ for current-is high compared to that of the conventional lead-acid battery. Voltage across a fully charged cell is 2.08 V .

Banks of 5-Ah laboratory cells have successfully undergone more than 2000 rapid-charge, deep-discharge cycles, more cycles than would be encountered in over three years of energy-storage use.

The hollow glass fibers of the sodium-sulfur battery-fiber diam-
eter is 0.003 in., and wall thickness 0.0005 in.-are filled with hot liquid sodium. The outside of the fibers is surrounded by the molten sulfur solute.

As the cell charges and discharges, the sodium remains chemically unchanged. But the sulfur is converted back and forth from sulfur to sodium sulfide $\left(\mathrm{NaS}_{\mathrm{x}}\right)$ as the sodium ions migrate away from the back to the interior of the hollow fiber during charge-discharge cycles.

The ultimate energy storage configuration for kilowatt and megawatt capacity will consist of banks of hundreds of series and parallelconnected cells.

## Race is on to convert 23-channel CBs to 40

On January 1, 1977, the Federal Communications Commission (FCC) will permit users of citizens band radios to operate on 40 channels instead of on the currently allowed, but already overcrowded, 23.

This announcement has touched off the beginning of a scramble among CB radio manufacturers to come up with an inexpensive (and government-acceptable) means of modifying existing sets to accommodate the additional channels. These sets include not only those transceivers already in the hands of consumers, but also the large number stored in warehouses.

The impetus behind this rush of activity is readily apparent in view of the enormous increase in sales of these two-way communications units over the past few years. More than 10 million CB 23-channel sets are already in use throughout the nation, with 4.2 million units produced and sold in 1975 alone. The estimated market for 1976 is 10 million sets.

For $\$ 25$, Hy-Gain Electronics Corp., Lincoln, NE, will remanufacture any of its present 23channel CB sets into 40-channel units, says Andrew Andros, HyGain's chief executive officer. HyGain's sets contain Phase-Locked Loop (PLL) oscillator circuits which can be programmed for "virtually any number of channels." The remanufacture process consists of reprogramming the PLL oscillator and replacing the 23 -channel
selector with a 40 -channel switch. Andros cautions, however, that this remanufacture procedure is applicable only to CB sets operating on the PLL principle, and cannot be applied to older, crystal-synthesizer-type sets.

In addition to PLL reprogramming for 40 -channel operation, an antenna filter might have to be added to conform to the new FCC specifications on harmonic radiation Andros adds.

Remanufacturing CB transceivers from 23 to 40 channels may not be all that simple, warns Ted Danhouser, Sales Manager of Motorola Communications, Schaumberg, IL.

The FCC will not allow the CB manufacturers to retrofit a 23 channel set with simple add-on devices-hence, the significance of the word "remanufacture." The word implies, for example, meeting not only the more stringent FCC limitations on spurious harmonics that may be radiated from the transmitter, but also on spurious radiation from the receiver. Such radiation-which can cause interference to television receptioncan originate in the local oscillator of the CB receiver.

## Holographic ROM stores 200 Mb on microfiche

A $4 \times 6$-in. piece of microfiche can be used to store up to 200 Mb of data in a holographic memory
system designed by TRW Systems, Redondo Beach, CA, for Holofile Industries of Woodland Hills, CA.

The microfiche (renamed holofiche) contains data stored in an array of $2 \times 2-\mathrm{mm}$ holograms. Each laser-written hologram contains 2500 bits of data. Since all of the data in a hologram can be read by illuminating any single point on the hologram, the data are virtually indestructible. In fact, should most of a given hologram be destroyed by, for example, punching a hole through the holofiche, all of the data can be reconstructed.

Holofile has produced a relatively inexpensive (under $\$ 500$ ) reader for holofiche. The reader uses a 2-mW Spectra-Physics $\mathrm{He}-\mathrm{Ne}$ laser to illuminate the holofiche and a self-scanning, Si-photodiode array from Reticon, Sunnyvale, CA, to detect the resulting image. The reader can be either connected to a computer terminal as a massstorage peripheral or used with an embossed card reader as a credit verification terminal.

When the reader is used with a computer terminal, an address is fed to the reader, and the laser is positioned to illuminate the proper hologram. Because of the mechanical positioning, the access time is from 1 to 10 s . Data can be read out, however, at up to 5 MHz .
"The reader is about 6 months to one year away from commercial availability," says James Case, vice president of operations at Holofile.

## News Briefs

UOP Inc. of Des Plaines, IL, is developing an oxygen sensor based on electrolyte technology to measure the concentration of oxygen in aircraft fuel tanks. The program is sponsored by the Air Force Aero Propulsion Laboratory as part of a safety system to measure explosive fuel-air mixtures in tanks.

A General Accounting Office review of the Navy F-14 fighter, which figured in two fatal crashes near San Diego earlier this year, has cited low avionics reliability as a possible cause of the fighter's poor performance. Reliability of some major flight systems
ranged from 6-to- $14 \%$ of the desired objective. The Air Force Avionics Laboratory is seeking industry sources to do research on laser communications at data rates of 2-10 gigabits per second. Primary applications are in space.

The Navy plans to buy 204 Light Airborne Multi-Purpose System (LAMPS) helicoptersplus six prototypes-with production to begin in 1981. These heavy-on-electronics helicopters will be used for anti-submarine warfare. A contractor will be selected next spring; Boeing and United Technologies Corp. are considered the leading contenders.


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## At Wescon/76

## Charge transfer technology used in sonic imager and signal correlator

A basic review of charge-transfer device technology and a look at some new applications for it will be featured in Session 19, "Application of Charge Transfer Devices to Sampled-Data Signal Processing."

Employing charge-coupled device (CCD) as a lens for ultrasonic imaging applications will be described by Roger D. Melen, a researcher at Stanford University's Electronics Laboratory, Stanford, CA. According to Melen, the CCD lens promises to reduce the weight, volume, power consumption and cost of ultrasonic imaging systems by as much as an order of magnitude. In addition, the CCD lens permits improvements in system resolution, field of view, range and frame rate.
Reviewing past efforts in ultrasonic imaging systems, Melen notes that developing electronic focusing techniques has always been a problem due to the need for a high performance, low-cost, electronically variable, analog delay line. With CCDs, this variable delay is now possible.

## Standard processing used

The cascade charge-coupled device (C3D) is an array of CCD delay lines interconnected on a single IC substrate. According to Melen, the array can be fabricated with present-day MOS manufacturing techniques on a 120 -by- 120 -mil chip.

The delay lines of the lens consist of multiple sections, each clocked by independent clocks of differing frequencies. When these separately clocked sections are combined with multiple-input taps, the result is a device that can perform the high-speed Fourier and


An ultrasonic lens composed of a CCD chip can reduce the size and power needed by ultrasonic imaging systems. It is described in Session 19.

Fresnel transformations required for the imaging task.

The CCD lens is the equivalent of an optical, wedge-shaped lens cascaded with a quadratically shaped lens. Unlike the optical counterpart, however, the thickness of the CCD lens is electronically variable so that the imaging system can be focused and steered to different targets.

A large-scale integrated circuit that can perform binary-analog correlation will be described in "A Programmable Binary-Analog Correlator," by Udo Strasilla of Reticon Corp., Sunnyvale, CA.

According to Strasilla, a 32stage, bucket-brigade, tapped delay line is used to perform correlation or convolution between an analog
signal and a programmable binary function. The new correlator duplicates the complex signal-processing task normally handled by digital computers. The digital method involves $a / d$ conversion, storage in memory, a multitude of multiplications and a d/a conversion. With the BBD correlator, however, all these operations are replaced by a sequence of sample, store, shift and multiply functions, that are performed by a single IC chip.

Explaining how it works, Strasilla notes that discrete-time samples of the analog signal are shifted into the BBD register. After every clock period, the temporarily stored analog samples are multiplied with a binary pattern residing in a static shift register. - $\quad$


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## At Wescon/76

# Semis invade medical transducers; $\mu$ Ps monitor EKG and blood pressure 

Field-effect transistors as physiological transducers and microprocessors as electrocardiograph and blood-pressure monitors are two of the new developments to be discussed at Wescon in Session 22, "Needs and Trends in Medical Electronics -1976."

In "Application of FET Semiconductor Technology to Biochemically Specific Transducers," Stanley Moss, a researcher at the University of Utah's Department of Bioengineering, will describe the invasion of semis into the physiological transducer area and will report on a new device called an ISFET. Developed under an NIH grant, the ISFET is used in miniaturized transducers that produce an output when exposed to a specific type of ion.

The ISFET, says Moss, is basically a MOSFET that has either a bare gate or a polymer ion-selective gate membrane. Because the membrane is in direct contact with the gate insulator of the FET, the voltage (Nernst Potential) developed at the membrane solution interface is transmitted to the gate.

When the chip is used as a transducer, notes Moss, the part containing the ISFET gate is exposed to the electrolyte solution, while the rest of the device is covered with epoxy. To date, transducers sensitive to calcium, potassium and hydrogen ions have been fabricated.

The transducer has an anion liquid ion exchanger immobilized in a matrix material. As a cation approaches the membrane, it becomes attracted to the membrane. The positive charge of the cation adds to the already existing charge on the membrane, thereby changing its value. This change is then reflected as a change in gate charge.

Moss predicts that FET tech-


A microprocessor controlled medical monitor automatically keeps track of a patient's EKG and blood pressure. The system processes the EKG data and stores it only when significant deviations in the EKG are detected.
nology will also be used to develop transducers sensitive to enzyme reactions as well as to immunological reactions. During the latter reactions, an antigen-a substance that stimulates the body's defense mechanism to produce antibodiesis covalently bonded to a surface connected to the gate of a FET. The antigen and the antibody with which it reacts have specific charges associated with them. When these two combine, they produce a third charge. Like the charges of the ion transducers, this new charge will affect the amount of charge under the gate of the FET.

## $\mu \mathrm{P}$ checks EKG and pressure

A portable electrocardiogram and blood pressure signal monitor will be described at the same session by Martin Graham, professor of electrical engineering of the University of California at Berkeley. According to Graham, the system provides a day-long analysis of EKG signals and periodic recordings of blood pressure. He further notes that rather than record sig-
nals all the time, the system processes the information and reduces it, extracting and storing data only when it detects deviations.

To minimize the amount of memory needed, the micro-based monitor waits for an irregular or premature heartbeat and then records it and several subsequent beats. The EKG part of the system is similar to commercially available EKG recorders, notes Graham, but smarter. It can distinguish and store abnormal behavior separately.

Blood pressure measurements can be taken and recorded throughout the day or upon request. A measurement algorithm uses a Fast Fourier Transform to detect shifts in the spectral content of the Korotokov sounds characterizing the systolic and diastolic pressures. Comparing the frequency spectrum of sounds associated with previous heart beats to the spectrum of the current beat can determine the blood pressure. The systolic and diastolic points can be identified by a change in specific-frequency components that exceed a fixed threshold.

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CIRCLE NUMBER 19

# Electronic fingerprints compete with ID cards for access control 

For years, law enforcers have identified people by their finger-prints-personal "signatures" that can neither be duplicated or forged.

Now, private industry has become interested in putting this identification technique to use, especially in light of the proliferation of stolen and forged identification cards.

The most common use of fingerprint identification outside law enforcement agencies is screening entries into restricted areas. Benefits of screening by fingerprint include:

- Positive identification (rather than by a potentially misleading photograph).
- Cost reduction (machines are generally cheaper than guards).
- Automatic recording of entry and exit times.

Operating systems are already in use at banks and at military and industrial institutions across the country. One such unit, "Fingerscan," a complete access control system designed by Calspan Corp., Buffalo, NY, is being employed by the Morgan Guaranty Trust Co., NY, to identify employees entering its vault, tellers' area, and securities room:

## The right fingerprint opens doors

In the "Fingerscan" system a data terminal is installed at each point of entry to a restricted building or area. Anyone wishing to gain access to the area keys his ID number (or any other pre-assigned code) into the terminal keyboard. Upon receipt of a return signal, the person then places one (preassigned) finger on a glass plate. An electromechanical photosensor scans

[^2]

Calspan's Fingerscan terminal accepts an individual's ID number and fingerprint for transmission to a central computer. After comparison against its stored-data file, the computer decides whether or not to grant access.
the finger's surface, converts the analog optical signals to digital format, and transmits the resultant signal to a central control station located at a protected site within the installation.

At the control station, a Texas Instruments minicomputer, the 980 B, automatically searches its disc file (capacity 4000 individuals) for that particular ID location in memory. Stored at that address in digitized format is the essential information on the employee's fingerprint-basically a set of numbers representing the fingerprint's ridge endings, in particular, the location and direction of these ridge endings. These particular data, called minutiae, are the key to positive identification because they are unique.

Simultaneously with the disc file search, a hard-wired special-purpose computer examines, in real time, the fingerprint of the person seeking entry, selects the important fingerprint minutiae and transmits this information to the 980 B , which compares the infor-
mation to the data stored in the disc-file memory. Should there be positive correlation, a signal is generated to permit entry. The entire identification process reportedly takes two seconds.

The Calspan system also records such vital security information as who entered and left and when, and which entrances were used.

A great deal of design effort has gone into making the system tamper proof, according to Claron Swonger, vice president of Calspan Technology Products (a subsidiary of the Calspan Corp.)

Suspicious activity at any entrance or exit will automatically set off an alarm. For example, if a person attempts to key in an unassigned number, the system allows him to try a fixed number of times -to allow for any honest errors. But once he has exceeded this number, a warning alarm signal is sent to the security office.

Similarly, the system will try to read a fingerprint a fixed number of times. If unsuccessful, the machine assumes the worst and trig-

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gers an alarm. Fingerscan will also "view with suspicion" an employee's ID number showing up at different entrances, all within a short time.

Swonger points out that the system can deal with many other possibilities, such as:

- Persons tampering with the terminal.
- Persons keying in invalid (nonexistent) ID numbers.
- Failure of a file storage device.
- Injury to the designated finger of an individual.
- Attempts to "forge" a fingerprint.
- Data communication errors.

On occasion, the Fingerscan will err by either accepting an individual who should be rejected or rejecting an individual with valid credentials. The error rate for both cases, according to Swonger, is approximately $1 \%$.

## Another one will use TV

Currently in the final design stages at Sperry Rand Corp., Great Neck, NY, is a similar access-control fingerprint identification system. Using a TV-like scanning technique, the Sperry system samples thousands of bits of information from a single finger, encodes them, and compares them with similar fingerprint information previously taken from that employee and stored in a computer.
"For a high security device, more than just a simple 'yes' or 'no' signal is wanted (at the entrance or gate)," says Thomas Garrigan, manager of marketing for security systems at Sperry.

The reason, he explains, is that a false signal can easily be inserted by someone entering or breaking the line and feeding in a string of "yes" signals. That is, if all the signal processing is done at a central computer, and only the final result, a "yes" or "no" signal is returned to the terminal at the entrance gate, then this signal can possibly be duplicated.

For this reason, the information linking the central computer with the remote terminal at the access point is sent in complex form. A microprocessor at the remote terminal decodes this information to provide the necessary signals for the person seeking entry.


A single fingerprint is scanned to provide data on positions and angles of discrete points. These are matched against a similar set stored in memory. Verification is granted at a certain degree of match.


Rockwell's Printrak 250 verifies identity of a person based on a fingerprint read directly from a finger.

An access control module in the Sperry system provides an additional "surprise" to anyone tampering with the system. An employee keys in an ID number and supplies a fingerprint at a computer terminal. If the central computer decides that all is in order, it will transmit an appropriate signal to open the second door, which permits the employee to enter the restricted area.

Should the computer sense, however, that something is wrong at the terminal, it will not only refuse to open the inner door, but will also automatically lock the first door behind the unsuspecting individual. An alarm will then alert the security force.

The system's error rates for anyone keying in a valid ID or personnel code number are approximately $0.1 \%$ for false acceptances and $1 \%$ for false rejections.

Sperry's system, expected to be available in product form in approximately six months, will be able to replace up to 50 guards.

## More units on the way

Printrak 250 , a direct-fingerprint reader developed by the Autonetics group of Rockwell International, Anaheim, CA, will soon be on the market, too. Like the Calspan and Sperry systems, the Rockwell system requires the print of only one finger.
"A prototype has already been built," reports Richard Snyder, director of ID systems at Autonetics. The first versions of this system will be aimed at the banking industry for automatic deposits and withdrawals but other applications envisaged include immigration control as well as a variety of areas where credit cards are now in use.

Ultimately, the over-all impact of fingerprint ID schemes on banking and credit card operations will depend on the cost. Present applications are limited to individual banks, single buildings, or single installations where the number of personnel involved is at most in the thousands.

Expansion of such systems to large-scale credit card replacement or to national banking systems would require an enormous increase in computer storage, coupled with a corresponding large increase in the number of input fingerprint-reading terminals.

Autonetics' engineers are seriously considering the use of charge-coupled devices (CCDs) in production units in an attempt to drive down the cost per terminal.

One aspect of the expanding technology of personnel identification via fingerprints that cannot be overlooked is the psychological consideration. Some individuals, or groups, for one reason or another, object to a record of their fingerprints (even one finger) being permanently stored in some remote and inaccessible computer file. This objection has prompted the investigation of other biometrictype personnel sensors.

## Hand reader used, too

Identimat Corp. of New York City has developed one solution-


Only one finger is scanned for its fingerprint at a terminal of Calspan's Fingerscan. The system uniquely identifies an individual.
the measurement of certain parameters of the entire hand. The Identimat system measures hand geometry parameters, such as finger length, curvature of finger tip, and translucency. The combination of these measurements is unique to any one individual, according to Chet Cohen, Identimat's vice president.

Three hundred such devices are now in operation in a variety of installations ranging from schools to financial institutions. "Unions won't touch fingerprint machines," says Cohen, "and a large number of other organizations also object to this method of personnel indentification."

Garrigan of Sperry Rand, who has encountered few objections to the fingerprint method, disagrees. Fingerprint devices that are used expressly for access control do not record an actual fingerprint. What they record are digital data on certain discrete characteristics (the minutiae) of a fingertip pattern. The entire print is not available.

The controversy over whether fingerprint access control machines do or do not invade personal privacy has yet to be resolved. Nevertheless, with the costs for maintaining staffs of guards, keeping accurate entry records, and attempting to outwit forgers, ever rising, while at the same time the costs of technology go down, biometric security devices of nearly all forms would seem to have a secure future. ■ $\quad$

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[^3]
# Satellite enables scientists to track the movements of bears 

Two hundred miles north of Point Barrow, Alaska, a 500-pound female polar bear has been outfitted with a battery pack on her chest and a 1 -watt radio transmitter on her back. To advise the Nimbus 6/RAM satellite of her whereabouts, she transmits 15 bursts of energy-once a minute for 15 minutes when the satellite is overhead. To conserve battery power, the transmitter shuts down for four-day intervals.

Nimbus receives the signals, measures their rf frequency for Doppler shift, decodes the data and sends the information to earth.

## Bear's movements monitored

The purpose of this invasion of the privacy of polar bears, being carried out by the Dept. of Fish and Wildlife, is to determine if, and how, the pipeline now under construction is interfering with the hibernation patterns of Arctic bears.

The equipment being used includes a transmitter built by Handar Co., Santa Clara, CA, which was originally designed for use on buoys at sea, and an antenna by Transco Products, Venice, CA. Since the satellite is "visible" to the bear for 15 to 20 minutes of each orbit, the biologists have 15 to 20 data points from which to determine Doppler shift, and consequently, the bear's movement.
"NASA knows exactly where the satellite is at any given moment, so any Doppler shift is due to movements of the telemetry unit on the bear," says Hank Fallek, president of Handar, who went to Alaska himself to put the transmitter on the bear. So far, Fallek says, the bear's recorded location has been accurate to 1 km .

The battery pack, worn on the


A miniaturized telemetry package that is strapped on to polar bears in Alaska relays a bear's location to researchers via the Nimbus weather satellite. Scien tists are studying the effect of the Alaskan pipeline on hibernation patterns.
bear's chest, is a 3.2 -pound unit made with organic lithium. With its energy capacity of nearly 1 kW hour, Fallek expects the charge to last about 18 months.

## Miniature antenna used

Transmitter size was kept down by Transco's small, circularly-polarized antenna. "The operating frequency of 401.2 MHz corresponds to a wavelength of 29.4 inches," observes Dr. John Greiser, director of engineering at Transco. "But we made use of a high dielectric constant material and coplanar stripline to produce an antenna with only a 6 -inch square radiating aperture."

The antenna is built on an epoxy-fiberglass board, which has cutouts from Emerson and Cuming High-K material. Greiser explains why: "The stripline gap is filled
with this material, which has a dielectric constant of about 10 , resulting in dramatic size reduction." The telemetry unit on the bear measures $8.5 \times 12 \times 3 \mathrm{in}$.

The antenna is placed in the lid of the Lexan transmitter package and heat-welded in place. Hemispheric patterns are produced with a peak gain of +4 dBic .

The designer feels that the antenna can be made even smaller by loading the gaps with titanium dioxide. The powdered chemical. which is used in the manufacture of paint, is mixed with a resin and applied as a paste. Depending on the mix, the dielectric constant can range from 10 to 20 .

Handar's Fallek is confident that Transco's work will enable him to reduce the size of the telemetry unit to $5 \times 5 \times 2$ in. "Future programs may include dolphins," he explains. - $\quad$

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4. Nine section Unidex switch with PC terminals at opposite sides for attachment to parallel PC boards.
5. Connector and harness assembly attached to four section, 24 position Multidex switch.
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| $\mu$ PD5101-E | 1 K STATIC RAM |
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[^4]
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## New legislation aimed at overseas bribes

Moving to curb the recent scandals in overseas arms sales, the Administration has submitted to Congress legislation that would require U.S. citizens to report payments made to foreign government officials. Known as the Foreign Payments Disclosure Act, the proposal would require disclosures of payments made to a foreign official in connection with an official action of his government or in connection with sales to, or contracts with, a foreign government.

The reports would be submitted initially to the Commerce Dept., then transmitted to the Departments of Justice and State, the Internal Revenue Service and the Securities and Exchange Commission. After a year, they would be made public. Penalties for failing to report, or for making false reports, would carry fines up to $\$ 100,000$ and prison terms up to three years.
The proposal follows recent revelations that such U.S. firms as Grumman and Lockheed paid questionable commissions to foreign sales agents on transactions involving aircraft exports.

## Army field radio contract to E-Systems criticized

The Army "apparently steered an \$11-million contract for mobile field radios to a favored company, E-Systems Inc. . . . despite lower bids from a competitor (Bristol Electronics Inc.)," according to a staff report by the House Government Operations Committee.

E-Systems won the contract after it was underbid three times by Bristol, the report noted. On the fourth submission, E-Systems had the low bid of $\$ 528$ per unit, but, the report added, "through contract modifications (the firm) was permitted to supply 10,030 additional radios at unit costs ranging from $\$ 780$ to $\$ 934$." The radios were intended for foreign military sales.

The report also criticized Congress's own General Accounting Office for its role in the procurement. After a protest by Bristol, GAO first declared the contract improperly awarded but later reversed itself when asked by the Army to reconsider. The committee urged that GAO hereafter stick by its original decision in a contract dispute unless "gross error or clear damage to the public interest is shown."

## EIA, WEMA oppose tariff legislation

The two major trade associations representing the electronics industry, the Electronic Industries Assn. and WEMA, have testified against proposed tariff legislation during hearings conducted by the House Ways and Means Committee.

The committee is considering a bill, H.R. 9220 (also known as the Customs Modernization Act), that would streamline the existing tariff regulations. As part of the process, the committee is considering changes to Section 592, which permits the government to assess penalties against an importer for the entire value of a shipment rather than for the actual revenue loss to the government.

Both associations focused their opposition on Section 592. WEMA called it an outmoded and poorly written law that causes honest U.S. businessmen to be treated like criminals. EIA urged the committee to amend the Section to establish grades of penalties for fraud, negligence and error; to postpone any penalty or seizure of goods, until the penalty is agreed upon; and to establish the right of any importer facing a penalty to seek judicial review of his own action.

WEMA, which represents many semiconductor producers, also complained that the existing tariff law hits particularly hard at firms that depend on overseas assembly. WEMA suggested that the law be changed to permit firms that think they may have made a mistake to come forward voluntarily and pay up any duties owed without being exposed to criminal penalties.

## Hughes, Westinghouse divide airborne radar market

Hughes and Westinghouse, the two traditional rivals in the military airborne radar business, appear to be dividing up the market for the new generation of tactical fighters.

In the latest competition between the two, Hughes has been selected by McDonnell Douglas to subcontract the radar on the Navy's new F-18 fighter. The contract not only calls for Hughes to supply three engineering and 16 pre-production model radars under a $\$ 63.9$-million development agreement, but also contains options to permit the firm to supply radars to all 800 aircraft in the proposed Navy procurement. At a reported production price of $\$ 325,000$ each, plus development costs, that works out to nearly $\$ 300$-million's worth of new business.

Earlier, Westinghouse was selected by General Dynamics to produce a somewhat smaller radar (approximately $\$ 250,000$ each in production) for the Air Force's new F-16. The Air Force plans to buy 650 of these aircraft. Westinghouse will also co-produce with European firms the radars for the 348 F-16s ordered by NATO. Westinghouse even has a chance to supply the radars for an export version of the F-18, known as the F-18L, being developed by Northrop and McDonnell Douglas.

The F-16 and F-18 radars are similar in that they operate at X-band for air-to-air applications and up in the K-band region for higher resolutions in air-to-ground missions. The more powerful F-18 radar puts out $400-450$ watts, compared to $150-250$ watts by the F-16 radar.

Capital Capsules: The Federal Aviation Administration is replacing the 100 -words-per-minute teletypewriters used for the past two decades at its flightservice stations with cathode-ray tube displays that can operate up to 3000 wpm. Applied Devices Corp., Hauppauge, NY, is supplying $325 \mu \mathrm{P}$ based terminal controller units, 800 keyboard/displays and 400 high-speed printers. . . . The Air Force Avionics Laboratory is seeking industry sources to do research on laser communications at data rates of 2 to 10 gigabits per second. Primary applications are in space.


The failure. A 16 W overload causes this $1 / 2 \mathrm{~W}$ carbon film resistor to burst into flame. The initial failure mode is a short circuit, causing even more current to be drawn as shown on the meter.

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[^5]
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You can get these synchros in the following Bu/weps types:

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| TYPE NO. | CASE | $\mathrm{V}_{\text {CEO }}$ | $V_{\text {EBO }}$ | $\mathrm{V}_{\text {CE(sat) }}$ | ${ }^{1} \mathrm{C}($ max $)$ | $h_{\text {fe }}$ | $\mathrm{f}_{\text {(typ) }}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SDT 96301 | T0-3 | 60 V | 10 V | 1.8 V max @ 70A, 7A | 120A | 10 min @ 70A, 5V | 10 MHZ |
| SDT 96302 | TO-3 | 100 V | 10 V | 1.8 V max @ 70A, 7A | 120A | 10 min @ 70A, 5V | 10 MHZ |
| SDT 96303 | T0-3 | 140 V | 10 V | 1.8 V max @ 70A, 7A | 120A | 10 min @ 70A, 5V | 10 MHZ |
| SDT 96304 | T0-3 | 200 V | 10 V | 2.0V max @ 40A, 8A | 70 A | 8-40 @ 40A, 10 V | 10 MHZ |
| SDT 96305 | TO-3 | 250 V | 10 V | 2.0V max @ 40A. 8A | 70A | 8-40 @ 40A, 10V | 10 MHZ |
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| Type \# | $\begin{aligned} & \text { (pk.) } \\ & \mathbf{I}_{6} \\ & \hline \end{aligned}$ | $\mathrm{V}_{\text {ceo }}$ | $\begin{gathered} \left(h_{\mathrm{fe}}=10\right) \\ \mathrm{V}_{\mathrm{ce}}(\mathrm{sat}) @ \mathrm{I}_{\mathrm{l}} \end{gathered}$ |
| :---: | :---: | :---: | :---: |
| JAN-TX 2N5926 | 90A | 120 V | 0.6V @ 50A |
| PT-7511 | 90A | 200 V | 0.6V@ 50A |
| $\begin{aligned} & \text { JAN-TX } \\ & \text { 2N5927 } \end{aligned}$ | 120A | 120 V | 0.6V @ 70A |
| PT-6502 | 200A | 80 V | 0.7V @ 100A |
| PT-9502* | 500A | 80 V | 0.5 V @ 300A |

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## Silicon transistors to 500 amps with lowest $V_{G E}$ [sat]



## Microprocessor Design

## 1977 automobile to conserve fuel with a $\mu \mathrm{P}$-based ignition system

While everyone has been talking about microprocessors not being installed in cars before 1979 or later, General Motors has scooped the industry by installing a $\mu \mathrm{P}$-based system in their 1977 Toronado. The lucky micro maker who will be supplying the automotive brain is Rockwell International, Anaheim, CA.

That system will be used to control engine spark advance and it will increase the mpg by 10 percent. While that's only a limited use of micros in a limited number of cars, the GM application promises to be the first of many.
"I don't know of anything else in the automobile that has as much effect on improving fuel economy while simultaneously lowering emissions," says Frank Jaumont of GM's Delco Electronics Div.

Once a $\mu \mathrm{P}$ system doing this task has proven itself, other possibilities include control of fuel injection, car cruising speed, automatic transmisson, anti-skid braking and chassis suspension. It can also provide a digital read-out of car speed, total mileage and trip mileage while monitoring possible failure conditions. It can even prevent an intoxicated driver from starting his car.

## Why will they be used?

Microprocessors are going to be used in cars for several pressing reasons. Both stringent

DELCO-REMY MISAR* SYSTEM

*General motors registered trademark
Federal laws and public pressure have called for greater fuel economy. Environmental considerations have additionally mandated low pollutant emissions. Simultaneously, microprocessor and related electronic hardware costs have come down.

The American automobile manufacturers have designed their systems differently. The first task of a microprocessor system will be control of spark advance and fuel injection. The necessary algorithms and equations have already been developed by each of the auto makers.
(continued on page 54)

## $\mu \mathrm{C}$ card uses EA9002 $\mu \mathrm{P}$; no RAM is needed for small systems



A single printed card, called the PLS-891, uses the Electronic Arrays 9002 microprocessor. Because the $\mu \mathrm{P}$ contains 64 bytes of scratchpad RAM, no separate RAM chips need be added. The card, manufactured by Pro-Log, contains to the $\mu \mathrm{P}$, sockets for 2 k of PROM and 2 k of ROM, and several I/O ports.
The I/O consists of three latched 8 -bit output ports and two 8 -bit input ports. Output drive is 10 TTL loads and input loading is one TTL load. The $\mu \mathrm{P}$ has instruction execution times of $3.2 \mu \mathrm{~s}$ for one-byte and $6.4 \mu \mathrm{~s}$ for two-byte instructions, with the clock on the card.
Power requirements are 5 V at 1.8 A , and -10 V at 0.5 A .
Pro-Log Corp., 2411A Garden Rd., Monterey, CA 93940, (408) 372-4593.

## MICROPROCESSOR DESIGN

Controlling both spark advance and fuel requires measuring such variables as starting condition, engine speed, intake manifold vacuum, engine and coolant temperature, and flywheel angle. These must be digitized and fed to the $\mu \mathrm{P}$. In contrast, a conventional nonelectronic ignition system responds only to engine speed and manifold vacuum, and all other variables must be compromised.

Ford's approach to the control problem uses a 12 -bit NMOS chip having integral multiply and divide instructions. Eight-bit output accuracy was determined to be sufficient. To ensure this accuracy, the intermediate results are calculated to the 12 -bit precision.
"The control functions are obtained by fitting semi-imperical functional forms to engine performance and emissions data obtained from dynamometer and vehicle tests," says Shawn Devlin of Ford's Advanced Engine Engineering Dept.

GM plans to store the points and access them in a look-up table in a ROM. "You put a discrete-point engine map into the memory," Jaumot says, "then, between these points you interpolate with additional factors to find the actual control point. If the temperature is rising, for example, you might want to interpolate up. Otherwise, you will interpolate down."

Earl Meyer of Chrysler Corp. says, "We have produced a spark computer that works pretty much the same as our analog system, and we are satisfied with it. We are working with Texas Instruments and RCA in a program that will develop both spark and fuel systems.
"Our philosophy in design is to minimize hardware and maximize software control of

engine functions. That way control can be adjusted from year to year and model to model, and we won't have to retool a custom circuit each time."

## What about the sensors?

Since these systems monitor so many variables, there is a wide range of sensor types. The GM system contains a solid-state manifold-vacuum sensor mounted right inside the controller. An input and output vacuum line is connected to it. Such sensors might be thermistors for temperature sensing or coils with ferrite cores for position information. Bimetallic switches are also used for go/no-gotemperature indication.

## Where will it be mounted?

The physical location of the electronics involves a tradeoff. The $\mu \mathrm{P}$ system can be
(continued on page 56)


## HUGHES

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To learn how we can serve your interconnection needs, contact Jack Maranto or Dave Cianciulli: Hughes Connecting Devices, 17150 Von Karman Ave., Irvine, CA 92714.

Or call (714) 549-5701.
mounted in the passenger compartment, where the temperature and vibration is not severe. Or it may be mounted under the hood, close to the sensors and actuators.

If the unit is mounted too far from the engine, the signal wires pick up and radiate noise, because production line economies dictate the use of unshielded wire. Noise is, of course, a more severe problem in a digital system than in an analog system.

GM's controller is mounted in the passenger area near the glove compartment. The wires must go from one end of the engine compartment, through the firewall and across the instrument panel. Despite this wire length, "most of our filtering and noise suppression is taken care of in the basic design," comments Charles Dusenberg of GM. "We have also had no
problem with digital signals radiating into the car radio."

On the other hand, when the electronics is mounted in the engine compartment the package must withstand engine-generated heat, plus dust and vibration that are not present in the passenger compartment.

The Chrysler digital system, for example, will be mounted the same way as their present analog system. That unit is located in a plastic encapsulated enclosure on the air cleaner.

Engine compartment temperature can reach 121 C on a hot day. The operating temperature rise of the Chrysler analog spark-control computer is limited to 17 C by means of cool air reaching it from the outside. Thus, even on a hot day, the spark computer operates at just 49 C.

## Microcomputer accessory board combines memory and interfaces



The MI accessory board can easily expand E\&L Instruments MMD-1 microcomputer breadboarding system (see Electronic Design, No. 14, July 5, 1976, Vol. 24, p. 37 ). On the $6 \times 12-\mathrm{in}$. MI board is enough room for 2-k bytes of RAM workspace (1-k byte supplied), a teletypewriter interface, a paper-tape-reader control and an audio-cassette interface.
If you have an MMD-1, the MI board mounts directly onto the main circuit board, connects to the I/O port and can be powered by the main unit's supply. The MI board,
though, can work with any 8080 -based system and require 5 V at $1 \mathrm{~A},+12 \mathrm{~V}$ at 100 mA and -12 V at 250 mA . Only $1-\mathrm{k}$ byte of RAM is supplied on the board but there are additional sockets to hold another 1-k of RAM and 1-k byte of PROM or ROM.

The ROM sockets can hold memories that contain the bootstrap load program for the cassette interface or a special debug program that provides an interactive interface between the microcomputer and a teletypewriter.

For the cassette interface a listing of the bootstrap loader program is supplied, or you can optionally purchase a ROM that holds the program. You must write your own software to control the paper-tape or TTY inputs. With the Debug program you can set breakpoints, examine, and modify registers.

The fully assembled and tested accessory board costs $\$ 200$, but if you want it in kit form you'll pay only $\$ 150$. The set of ROMs holding the Debug program cost $\$ 150$, but a papertape listing is also available for $\$ 50$.
E \& L Instruments, 61 First St., Derby, CT 06418. (203) 735-8874.
Booth No. 714-716
CIRCLE NO. 225
Two software packages available for 8080 processor
A disc operating system and a compiled Basic language system for the 8080 micro are being offered by Intelligent Computer Systems. Included in the software are debug capability, an assembler, text editor, a Basic compiler and a Basic interpreter.

The software package includes such features as sequential and random-access file manipulation, print editing, assembly-routine linking, subroutine linking and program chaining through console control. Also included are binary and logic operations, 7 -digit variables, 255 character string variables and two-dimensional arrays.
Intelligent Computer Systems, 777 Middlefield Rd., Suite 40, Mountain View, CA 94043. (415) 961-8941.

## New snap-in rockers with Cutler-Hammer reliability.

Here's a completely new line of snap-ins, each engineered with the kind of solid dependability you expect in Cutler-Hammer Rockette ${ }^{\circ}$ switches. Bright metal bezels, illuminated and non-illuminated, A-c and D-c capabilities up to 20 amps .

Sub-panel rockers in a variety of colors, rocker or paddle designs in standard, special, or proprietary models.

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# quality digital meter a cheap analog meter." 


"At Siliconix, we call the LD130 'everyman's digital voltmeter chip.'
"Almost any high volume product that requires a meter can be made more attractive and useful with this sophisticated, low power, 3-digit DVM chip. In addition to the visibility and style of digital readout, the LD130 provides 10 to 20 times the accuracy and much higher resolution than analog meters.
"Moreover, the LD130 imposes none of the design restrictions of 'cheap' analog meters. Its full 3-digit range of $\pm 999$ allows almost any variable, such as very high and very low temperatures, to be measured. In contrast, a person can at best resolve one part in 100 using an inexpensive analog meter.
"Automatic zeroing, automatic polarity outputs, and auto-ranging outputs eliminate zero adjust potentiometers, switch the $\pm$ sign, and allow automatic control of decimal points and range scaling resistors.
$" \pm 5 \mathrm{~V}$ CMOS construction enables the LD130 to operate efficiently with any standard CMOS or TTL logic. The multiplexed BCD outputs are easily interfaced with 'intelligent' microprocessor-based systems. This format is also ideal for the widest range of digital displays.
"Accuracy is $\pm 0.1 \%$ of reading $\pm 1$ count. Accuracy is maintained automatically by Siliconix' exclusive quantized feedback design (patent applied for) which continuously corrects for zero drift. This same feature has made the Siliconix $\pm 3-1 / 2$ digit LD110/LD111 DVM set the most widely used by designers of professional instruments and control systems.
"Just as important, the LD130 is easy and inexpensive to use. Unlike most DVMs, it requires no precision resistors and capacitors, no external temperature compensation, no dual tracking references, no operational amplifiers or input buffers. These have been eliminated by state-of-the-art chip design.
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To order the LD130CJ ( $\$ 8.75$ at 100 lot), contact our franchised distributors: Components Plus, Elmar, Hamilton/Avnet, Pioneer, Quality Components, Semiconductor Specialists, or R. A. E. For more details, call or write Siliconix, 2201 Laurelwood Road, Santa Clara, California 95054, (408) 246-8000.

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Control Data Corporation, 4000 NW 39th Street, Oklahoma City, OK 73112

## The smallest graphic recording system is also the biggest in flexibility and convenience.



## Most compact.

The Honeywell Model 1858 Oscillograph is an unbelievable $83 / 4$ inches short, including plug-in signal conditioning and internal paper take-up. The 65 -pound-light 1858 is easy to take anywhere as well; it can be used in a rack, on a table, on the seat of a car or plane.

## Most flexible.

You get up to 18 channels (expandable to 32), each with dc to $5,000 \mathrm{~Hz}$ response, and a choice of 42 discrete paper speeds . . . up to $120 \mathrm{inch} / \mathrm{sec}$. You also get a choice of seven plug-in signal conditioning modules, each of which was designed to perform a specific function. Taken together, these modules cover a wide range of input signals, including those from transducer sources. Its new 14 -channel add-on housing permits up to 32-channel capacity, but adds only $51 / 4$ inches to height. And our new 1887 plug-in signal conditioning module (one of seven)
provides simultaneous input signal conditioning for magnetic tape recording and the Model 1858, or serial record and playback from tape to the Model 1858.

## Most convenient.

Because the Model 1858 uses a cathode ray tube and plug-in signal conditioning modules, it is easier to use and set up than any other graphic recorder. You never have to waste time with pens . . . there are no inks, chemicals or toners to worry about. Also, our signal conditioning is calibrated in volts/division and have both trace position and trace-off controls, so that you can position any trace at any point across the record. The trace, if desired, may traverse the full record width even at maximum frequency. All these features add up to making the 1858 the most in graphic recording.

For complete technical specifications, call or write Lloyd Moyer, Honeywell Test Instruments Division, P.O. Box 5227, Denver, CO 80217, (303) 771-4700.

## Mriltell you why this OEM picked PDP-8. Hisgood engincerins andit's good businers:"



The man in the picture is Dr. David Kemper, biochemist and product manager in charge of development, production, installation and field support for an amazing analytical device called Rotochem IIa, from American Instrument Co.

The computer inside his product is a PDP-8A from Digital. Dr. Kemper is buying scores of them. Why?
"They're inexpensive in a market that's cost sensitive. They're incredibly reliable in a
market that's reliability oriented.
And our customers can get
service anywhere in the world. The PDP-8A gives us the performance we need at a price we can't beat. We can offer the capability to run 50 tests on each of 250 patients."

Sensitive but tough. High in performance, low in cost. "Good engineering. Good business." It's the same story you hear from OEMs around the world. And that's why PDP-8's the most
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ELECTRONIC DESIGN is deeply honored to have received official recognition as a participant in the American Revolution Bicentennial Celebration, with authority to display the Bicentennial Symbol.

A computerized supermarket system in Tokyo had just everything. It made life easier for the shopper, for the checkout clerks and for supermarket management. And it saved time and money.

A shopper could stroll down the aisles and use a magnetic card to select products displayed behind glass windows. A computer would keep a running tally of her purchases and instantly provide a totaled receipt after she brought her goods to the checkout counter.

Beautiful. The concept was great and the system worked just as it was supposed to work.
 The supermarket manager must have been thrilled with the crowds of shoppers who took advantage of this super-modern system. Briefly. Then, after the novelty wore off, they left in droves. The system had to be scrapped and the supermarket was forced to return to the "old-fashioned way."

The system designer obviously tackled some important shopping problems. He saw shoppers handle, damage and misplace merchandise. He saw them wait on long checkout lines as clerks packed each item, checked the price and punched cash registers. And the designer overcame most of the problems. But the system was a failure. It neglected some key points.

Shoppers wanted to touch the merchandise. They wanted to examine labels. They wanted to compare one can of food with another. But the system didn't let them. Once they selected something it was theirs, and they were charged for it unless they went through an annoying procedure to return it. Further, shopping was a social occasion-not to be rushed. People wanted to chat and socialize. And the new system interfered with those pleasures.

So it was a great idea, but a failure-like many of the things we do. Too often, we design what it pleases us to design, rather than what's needed in the marketplace. We get a brilliant idea for solving some design problem; we devote time and effort to that problem; and we win. But nobody needed that problem solved. Lab shelves throughout the world are littered with prototypes of brilliantly conceived products that customers didn't need. Their designers did a job right. But they did the wrong job.


GEORGE Rostiky Editor-in-Chief


Series R is the economical answer for an electronic switch input componnt to use with standard dual-in-line packages (DIP's), ideally designed for incorporation into a broad variety of electronic devices including advanced microprocessor controlled equipment. Series $R$ is compatible with TTL, CMOS Logic. Redundant contacts insure required reliability.

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Schadow's RE Series offers both SPST and SPDT contact arrangements in an extremely compact design. Available with or without buttons, and can be mounted on PC boards.
A crisp, consistent tactile feel is achieved due to the constant tension of the formed inner contact arrangement.

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CIRCLE NUMBER 45

Series RE contacts are installed on a customerdesigned printed circuit board (edge connections or pin connections). Because of the modular, low profile design, you can place the series RE at any location on the PC board that best suits your custom application.

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| Wattage Capacity | 200 mW |
| :---: | :---: |
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| Contacts | Silver or Gold |
| Contact Resistance | 100m $\Omega-1 \Omega$ (Series REK) |
|  | $300 \mathrm{~m} \Omega-1 \Omega$ (Series RE) |
| Contact Bounce | . 1 Milisec. Max. |
| Operating Force | 100 Grams |
| Travel. | . 03 in. |
| Life | . $10^{6}$ Cycles @ 10mA |

Intel is now shipping high speed, low cost memory for two of the hottest new minicomputers, DEC's PDP-11/04 and PDP-11/34.

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# We guarantee our crystal oscillators for one full year. Which is a year longer than any of our major competitors will guarantee theirs. 

If you're in the business of making or marketing computers and information systems, you know how important a guarantee is. It's your way of letting your customer know you're so confident about your product's performance that, for a specified amount

## The IIIFGuarante <br> MF Electronics warrants this molded crystal

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Well, at MF Electronics, we feel the same way about our products. Especially the crystal clock oscillators that are our specialty. We make them the best way we -or anyone-know how. So we're able to guarantee them for a full year.

## The difference between oscillators.

What separates good oscillators from bad is space. Space is one thing there shouldn't be any of inside an oscillator. But the way most oscillators are made, space is one thing you
 can count on.

## The hollow truth about crystal oscillators.

Look at this oscillator. The fact that we can lift the cap to show you what's wrong with it shows you what's wrong with it.


After the crystal and other parts are attached to the base, the cap is glued on, creating a bond that's tenuous at best.

Air seeps through this bond, allowing dirt and moisture to collect. You've got a leaky oscillator, one that's prone to loose parts and electrical shorting. That's how deadly a breath of fresh air can be to the inside of an oscillator.

## The un-holey oscillator:

It's molded. What a blessing.


A molded oscillator, on the other hand, has no holes, no open spaces, nothing to hide and nowhere to hide it. Its crystal is hermetically sealed and set in a monolithic block of solid black plastic. There are no spaces for air to penetrate, no room for dirt or moisture to accumulate. Wave soldering can't even deteriorate the unit, so there's no danger of loose pins or joints.

## A test that rings true.

One of our customers, who is also one of the country's largest users of crystal clock oscillators, tested the performance of
various oscillators. He found that MF's molded oscillator lasted 3000 hours in an $85^{\circ} \mathrm{C} / 85 \%$ relative humidity test. If you've ever done any oscillator testing yourself you know how remarkable that is.

## Two more solid reasons to use MF Crystal Oscillators.

3rd overtone crystals are used in MF's molded oscillators to provide greater electrical and mechanical stability in frequencies exceeding and including 20 MHz .

And an MF molded oscillator is the only one made that meets the UL oxygen index guideline of $28 \%$.

## Because we're solid,

your product is more solid.
So when you use MF molded crystal oscillators, you're giving your product a more efficient component, a heart that will beat longer. And your customers will be giving you fewer complaints and service calls.

MF Electronics is the only company that makes molded crystal oscillators. We invented them.

We make what we think are the best crystal oscillators you can buy. And we guarantee them, so your product can be "the best you can buy."

And that's the solid truth.

## THE MF GUARANTEE

MF Electronics warrants this molded crystal oscillator to be free from defects for one year from date of purchase.

Any oscillator found to be defective during this period may be returned to the factory, postage paid, for repair or, at our option, replacement without charge.

MF Electronics limits its liability to the repair and/or replacement of the returned MF oscillator.



Our new series of male and female "D" connectors offer you a cost effective external mass termination cable and connector system second to none. Its uniqueness begins with a one-piece " $D$ " connector package that meets industry standards for size, pin spacing, and contact reliability. With no loose parts to match up, positive cable-to-contact alignment is assured. Conductors are mass terminated in seconds with our standard BLUE MACSTM hand or bench tools. The results? Faster installation, higher reliability.

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The Ansley BLUE MACSTM jacketed cable is U.L. listed for external interconnection of electronic equipment. Electrically, it outperforms standard jacketed twisted pairs in typical 1/O applications. And there's no special zipper lock tubing required - reducing the need for an extra cable accessory. Installation is faster, easier. And like all Ansley connectors, you can daisy chain our "D" types anywhere in the cable - along with our DIP socket, card edge, or pc board connectors.

Cable alignment and high contact reliability is assured - because both cable and connector are grooved for absolute alignment. Our patented TULIPTM 4-point in-sulation-displacing contacts are permanently fixed and sealed-in to provide a reliable, gas-tight, corrosion-free mass termination.

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Working with either ana-
log or digital specs
can be quite a hassle.
When you select $a / d$ or d/a converters-which span both worlds-expect double the trouble.

Also, a lack of standard definitions for the specs multiplies the problems. Common converter specs such as accuracy, resolution, linearity, temperature coefficients, settling time and conversion speed are often defined to make a vendor's product look good, not necessarily to help you.

Even worse, each of the converter familiesthe analog-to-digital, voltage-to-frequency and resolver (synchro)-to-digital, along with their opposites-has unique specifications that cause additional headaches.

## Don't confuse accuracy with resolution

When you compare converter specs from various manufacturers, you'll find that accuracy and resolution are often used interchangeably. However, these two specs are only loosely related. Resolution defines the size of the smallest output increment (sometimes referred to as step size), while accuracy tells you how exact the output level is, in terms of a known reference.

Most converters have their resolution stated in bits-6, 8,10 or more. But that really doesn't define the smallest increment. To get the resolution in millivolts or milliamps, you must first take the full-scale output and divide it by $2^{n}$, where n is the resolution in bits.

When you compute a converter's resolution, carefully separate the terms "full-scale range," "full-scale voltage" and "full scale." In bipolar units that handle, say a $\pm 10-\mathrm{V}$ signal, the "fullscale range" is 20 V , the "full-scale voltage" is 10 V and "full scale" is whatever the manufac-

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These recent $a / d$ and $d / a$ converter additions to Beckman's line are available in glass-epoxy packages for as little as $\$ 20$ and in metal hermetic packages for only $\$ 70$ (both in 100 -unit quantities).
turer decides to put on the data sheet.
Accuracy (really inaccuracy) is a combination of many error sources. Some of the more important error sources include: quantization, nonlinearity, scale factor (gain), offsets, temperature coefficients and power-supply changes. Of these, some are defined in terms of LSBs, others in percentages-and vendors often leave it to you to fit the numbers together.

The quantization error of a converter specifies the inherent uncertainty that occurs each time a conversion takes place. Since there are only $2^{\text {n }}$ possible codes (for an n-bit converter) to represent all values from zero to full scale, each code covers a range of analog values that is 1 LSB wide. Usually each code represents the midpoint of the step and thus uncertainty of the value is 0.5 LSB. And, this error is over and above any other errors that contribute to the inaccuracy of
the data converter.
If the manufacturer gives the converter accuracy with respect to a known standard, preferably one traceable to the National Bureau of Standards, the figure is usually called the absolute accuracy.

In many cases, though, all the vendor supplies is the relative accuracy-the maximum deviation of the output from a straight line drawn between the zero and full-scale output points. Some vendors call this deviation the linearity error, but other manufacturers object because it includes the additional error caused by offset.

True linearity is determined by measuring the maximum deviation from a straight line drawn between the value of the converter's offset and the full-scale value.

Often omitted from data sheets is the differ-ential-linearity error. When available, it tells you the maximum variation the step size can have


The wide variety of modular, hybrid and monolithic converters available from Datel can fill almost any a/d or d/a converter need. A universal voltage-to-frequency/ frequency-to-voltage converter is also available.
for an incremental code change. Ideally, the dif-ferential-linearity error will be zero-but imperfections in resistor networks and active components create an error that can usually be held to within $\pm 0.5$ LSB.

If the error increases beyond 0.5 LSB , the converter could skip a code and become nonmonotonic. When a converter is monotonic, its output will change in proportion to every input change-increasing when the input increases, and vice-versa.

## Temperature changes ruin every spec

Some vendors can't maintain, over a wide oper-ating-temperature range, an error of less than 0.5 LSB. As a result, they guarantee the converter's
monotonicity only at room temperature-though this temperature limitation may be buried in a footnote.

Heat, of course, is the scourge of most electronic circuits, and converters are no exception. The effects of drifts in component values show up as increased nonlinearity, decreased accuracy, decreased resolution, loss of monotonicity and more.

Few data sheets give all converter tempcos. In fact, you'll be lucky if the vendor gives you the most important three: those for linearity, offset and scale factor. Most companies supply tempcos in percent per degree centigrade, while others make it harder for you by giving the tempcos in parts per million per degrea centigrade. Converting from ppm $/{ }^{\circ} \mathrm{C}$ to $\% /{ }^{\circ} \mathrm{C}$ is easy enough, but trying to add up several terms with mixed units can raise your temperature.

Because many vendors only guarantee specifi-


High-speed modular a/d and d/a converters as well as a wide selection of $\mathrm{v} / \mathrm{f}$ and $\mathrm{f} / \mathrm{v}$ converters are available from Teledyne-Philbrick. MIL-temperature-range performance and 883 level-B processing are available.
cations at 25 C , make sure the converter can still meet the demands of your application when the tempcos are added in.

Find out how the vendor derived his tempco values. Does he take just a couple of endpoint values and average them over the entire range, or are several points over the range tested to check for changes in direction of the curve and to make sure that the rate of change doesn't vary too drastically?

Another point to check is the way the tempoo errors are summed, if at all. Many vendors use an rms summation that "averages" all the drifts so one high number gets buried with several low ones.

Although a worst-case arithmetic drift summation might be an ideal spec to get, it's not too realistic. Very rarely do all the tempcos change
in the same direction. Manufacturers don't like to give you this spec either-in most cases it makes their converters look much worse than they really are.

You can often trim converter offiset voltages to zero at room temperature with external potentiometers. But don't forget to add the extra tempco of the trimmers to the overall tempco of the converter. And remember, the manufacturer won't be responsible for the tempco after you've added external components-his specs were for the converter as it was shipped.

## Settle the argument about settling time

When you start to look at the dynamic performance specs, you'll run into a lot of different definitions for output settling time. Often you'll find it defined as the time required for the converter output to arrive at and stay within 1 LSB of its new stable state.

For d/a converters, vendors may give you only the settling time for a minor transition (a change of only 1 bit). Don't accept it. Settling times for most 1-bit changes are very short.

You'll be better off if you get one of the morecritical bit-change conditions specified: zero to full scale, minus full scale to plus full scale, or the major carry. The major carry, of course, occurs when the maximum number of bit changes takes place for a single bit increment-for instance, when 011 . . . 1 changes to 100 . . 0 .

Other factors that affect the settling time include the type of load and whether the converter delivers a voltage or a current.

Current-output units usually settle much faster than voltage-output converters. And, of the different loads, resistive types have the least effect on settling time. Reactive loads-such as coaxial cables, motors and cathode-ray tubes-can cause ringing and thus increase the settling time. Get


Commercial and MIL-grade d/a converters, in both voltage and current-output models, as well as a/d converters, are made by Burr-Brown. Prices for the DAC-80 series start at $\$ 20$ in hundreds for glass-package units.
your vendor to spell out the settling time for your expected load.
$D / a$ and $d / r$ converters have glitches on their outputs-a major problem. Glitches are very narrow transient pulses that are generated whenever digital inputs change. The largest glitch occurs at the major carry or borrow, when all the switches are changing.

If you're planning to use a $\mathrm{d} /$ a converter for CRT display generation, a glitch spec on the data sheet is an absolute must. Make sure you get both the amplitude and width of the glitch. Ei-ther-by itself-tells you nothing since, for most applications must know the total energy of the glitch.

F/v converters don't have glitches, but they do have settling-time problems when their input frequencies change. Settling time can be defined in several ways. Some vendors spec it in milliseconds, others list several milliseconds plus several cycles of the new input, and still others just use several cycles of the new input.

In all but the first case, the settling time depends on the input frequency. Of course, if you know the expected frequency range, the way the spec's defined doesn't matter too much; it's quite easy to pick the converter that responds fastest.

## How fast is fast?

With a/d converters, instead of settling time you get the conversion time. But a nice small number doesn't necessarily mean a high throughput rate. Most manufacturers "inadvertently" omit the fact that another 10 to 500 ns might be needed for logic housekeeping before or after each conversion. Thus the actual throughput of a unit with a $1-\mu$ s conversion time may be only 800,000 words per second, instead of the expected one million.

For a d/a converter, throughput depends on


Precision hybrid a/d converters like the MN5210-with guaranteed performance over the MIL temp range-and low-cost hybrid d/a converters like the MN3013 are off-the-shelf products from Micro Networks.
both settling and slew times. Sometimes the converter settles to its new value rapidly, but it may require a long time to reach the vicinity of the new level. Not all settling specs include the slew time.
$R / d$ and $d / r$ converters have a step-response spec that gives a worst-case conversion time to an arbitrary accuracy (in most cases 1 LSB). But the worst-case step responses would be for a 0 to 179 -degree angular change-the maximum deviation the converter could theoretically face. Fortunately, such abrupt transitions rarely occur in resolver applications. Slewing is usually the major problem.

Instead of having a slew-rate spec, angular converters are usually characterized in terms of their tracking-rate, in degrees/second. If the listed rate is exceeded, internal counters in the converter can't keep up with the resolver (synchro) position.
Commonly omitted from a tracking-converter's spec sheet is the maximum acceleration rate and the error at this acceleration. Of course, the faster the acceleration, the greater the error.

## Resolution and noise don't mix

As converters deliver more precise outputs, the distinction between a signal and accompanying noise gets harder to make. Output noise stems from two major sources: internally generated, caused by active and passive components; and externally generated, from power and signal sources.

Internal noise you really can't do much about; just make sure the manufacturer tells you how much there is and how it affects the converter output. For analog outputs, manufacturers often spec the noise as an rms voltage, measured over an arbitrary bandwidth. In this way, large spikes and high-frequency components can easily. be camouflaged from your scrutiny.

Peak values of some spikes can exceed the rms rating by 10 times or more so get the manufacturer to define the noise in peak or peak-to-peak terms, and over the widest bandwidth possible. Also, have the vendor specify the input conditions at which the noise was measured. You can get a low, but misleading, noise figure for a zero input, but a very high noise figure once the internal circuits start to operate.

Noise often shows up as an instability of the digital word, for a/d converters, or as a slight shift of frequency for $\mathrm{v} / \mathrm{f}$ converters.

External noise coming in on the signal or power lines causes false triggering of internal circuits and results in output errors. Take a hard look at those exceptional performance specs some manufacturers provide. You'll find most vendors assume zero input noise-a condition


These speedy a/d converter modules, from an 8 -bit, $800-$ ns unit to a 16 -bit, $8 \cdot \mu$ s unit, complement Intech's line of $d / a, v / f$ and $f / v$ converter modules.


MIL-quality converters, such as this DAC-347 hybrid d/a converter, have been added to the line of modules already available from Hybrid Systems.
almost impossible to meet.
Some manufacturers include extra bypass capacitors on the power pins to help eliminate the incoming supply noise. (A good power-supply rejection spec will also help.) Other vendors separate the analog and digital grounds to improve isolation and reduce noise feedthrough. In most cases, though, the manufacturer leaves it up to you to minimize power-supply effects.

The power-supply specs for converters are often vague. You'll usually find a statement like "supplies are $\pm 15$ and +5 V , nominal," along with some specified range of variation. For a dual supply, the spec normally assumes the voltages track. But what happens when they don't? Asymmetrical supply operation is often needed, so get the vendor to spec it.

Most d/a converters are available with either current or voltage-output options. For current-
output units, you'll often have to pry out-directly from the manufacturers-the effects of a changing output voltage. Few converters have a true current output that is unaffected by changing load conditions. Voltage output units have a similar spec problem; few have a true voltage output that is unaffected by changing load conditions.

A specialized form of $\mathrm{d} / \mathrm{a}$ converter-the multiplying $\mathrm{d} / \mathrm{a}$-has no reference, but can be set up to handle all the conventional applications plus some other important ones.

The converter's output is the analog equivalent of the product of an external reference level and the digital input word. When you go out to pick a multiplying $\mathrm{d} / \mathrm{a}$ converter, make sure the data sheet defines the number of operating quadrants the converter has; you can get one, two or fourquadrant operation.

Signal feedthrough in multiplying converters can also keep you going in circles. When the digital input or reference is zero, you want a zero output; this is rarely the case. If either the signal or the reference input changes rapidly, capacitive leakage paths, especially in IC products, may let part of the input signal get through to the output.

Vendors play a lot of tricks with the feedthrough spec if you don't watch them carefully. For instance, they may define feedthrough only for low frequencies-where capacitive leakage is insignificant. Check the frequency over which feedthrough applies; a good maximum frequency should be at least 10 kHz , with many companies now giving you numbers closer to 100 kHz .

Another problem with feedthrough specs is the way the vendors define the amount of feed-


Two-chip a/d converters that use multiple-slope techniques help keep the cost of instruments low by providing BCD outputs. The $8052 / 53$ A is from Intersil.
through. Some will give you the value in LSBs at a specific frequency, others may give you a maximum voltage and still others will give it to you as a percentage. A maximum voltage would be the ideal definition, but the conditions that exist when that number is given should also be specified-for example, temperature, frequency and input conditions.

## A/d converter specs abound

Whether you build or buy an a/d converter, the first decision you have to make is a tough onewhich conversion method to use? Parallel conversion offers the highest speed, but is the most expensive approach. Ramp-comparison and multi-ple-slope methods are the slowest (conversion times of milliseconds or more) but can be made very accurate. The multiple-slope technique also offers noise immunity to line frequencies if the integration period is made equal to $1 / f$. Succes-sive-approximation a/d converters offer a good combination of all features-they're fairly fast ( 800 ns for 8 bits) and are reasonably priced ( $\$ 25$ and up).
Interface requirements and logic compatibility are two major areas that manufacturers have been lax in defining. Until recently, converters have had TTL and DTL-compatible inputs and outputs, but newer converters have CMOS and even three-state control and data lines.

Some of these circuits claim TTL compatibility, but beware. Often, these circuits have a very limited fanout-typically one or two normalized TTL loads-instead of the large fanouts that are typical of TTL circuits. Vendors often bury the drive restrictions in one of the footnotes.


A speedy successive-approximation a/d converter developed by National Semiconductor-the MM4357-delivers an 8 -bit word in $20 \mu \mathrm{~s}$. It costs $\$ 7.95$ in hundreds.

A/d converter input conditions are usually vaguely defined on data sheets. Input impedance may not be specified. If it isn't, you'll usually find an input current listed. From the input current you can find the input impedance. But why do it the hard way? The manufacturer should spell it out.

When an a/d converts a ramp input, you may find it skips several codes. This occurs if the codes on either side of the missing one get too close together (a large differential nonlinearity). As the converter tries to pick a representative code, it sees only two choices instead of three.

Many multiple-slope converters are used in instruments that deliver numerical readouts and BCD data. The converters are designed to provide outputs in BCD form, usually specified as $2-1 / 2,3-1 / 2$, or $4-1 / 2$ digits, with maximum counts of 199, 1999 or 19999. However this is not always the case.

Not all manufacturers agree about the definition of the half digit. Many use 1 as the maximum reading for the extra (first) digit. But several others use a 2 and some even use a 4 and call it a $3 / 4$ digit. Make sure you understand what the vendor really means.

Some integrating units also come with an autozero capability. If the one you pick does, check out the amount of external support circuitry needed to make it function. Also, make sure the auto-zero will work for your particular circuit layout; sometimes voltage pickup and offsets can wipe out its advantage.

Monolithic multiple-slope converters present a complex problem for interfacing with a processor. You'll have to come up with a software routine or some specialized hardware to demultiplex the BCD-coded lines.

## For high accuracy consider $\mathbf{v} / \mathrm{f}$ converters

The $\mathrm{v} / \mathrm{f}$ converter can form a slow, but very accurate a/d converter. And it can transmit data over a two-wire serial-data link. But, with most $\mathrm{v} / \mathrm{f}$ converters the output frequencies are not very exact. They're fine when you have a matching set-a $v / f$ and $f / v$ converter-or when you go directly to a counter, but when you compare two $\mathrm{v} / \mathrm{f}$ or $\mathrm{f} / \mathrm{v}$ converter outputs, don't expect an exact match.

What is the shape of the output signal from the $v / f$ converter? Most manufacturers don't tell you if its a square wave, a pulse, a sine wave or a triangle wave. If they tell you it's square or pulsed, do you also know its duration? How about the on/off ratio or the output levels? There are many gaps the vendors can leave on the data sheet; don't get caught in them.

V/f converter linearity, probably the most important spec for this family, appears on data
sheets in several forms. Some vendors define linearity as a percentage of full scale, others as a percentage of full scale plus a percentage of signal. Still others provide just a percentage of signal. Whichever looks better on the data sheet is what you will be given. If you know the signal range, a converter with its nonlinearity based on signal might prove easier to specify.
F/v units suffer many of the same problems as $\mathrm{v} / \mathrm{f}$ 's, but you must give extra care to their input parameters. For instance: What pulse widths are acceptable, what pulse separations are permissible, how much noise can be tolerated and what rise times can be handled? Many manufacturers leave these questions unanswered; don't let them.

You can often adjust (scale) the output ranges of $v / f$ and $f / v$ converters so that when the signals feed into a display or processor you don't have to readjust the units being measured. Find out how much of an adjustment range is per-missible-and what happens to the other converter specs when the converter is readjusted?

## Synchro converters present unique specs

Once past their input circuitry, electronic resolver and synchro-to-digital converters look pretty much alike. This is also true for the outputs of digital-to-resolver (synchro) converters. These circuits use Scott-T transformers to con-


Voltage and current-output d/a converters are available from Zeltex with resolution from 8 to 14 bits. A/d converters with comparable resolution are also available.


Packaging 15-bit resolution on a circuit card that's 0.3 in. high, Natel's tracking s/d converter-Model SD542 -can handle input velocities of up to $3600^{\circ} / \mathrm{s}$.
vert the 3 -wire synchro signals into 4 -wire resolver equivalents and vice-versa. But the Scott-T does not necessarily have to be a transformer; some companies offer an electronic equivalent, which offers small size but sacrifices high-voltage isolation.

The sampling and tracking $\mathrm{r} / \mathrm{d}$ converters form the two basic families. Sampling units take a sample of their sinusoidal inputs and digitize the signals into a binary or BCD-coded angle. Tracking units do one complete conversion when they're turned on, and then continuously change their value as the input signals change. Multichannel systems use sampling converters because a completely new conversion must be done each time a channel is sampled.
Most r/d systems operate on 60 or $400-\mathrm{Hz}$ references, so response times are usually in tens of milliseconds. However, if the mechanical input angle changes by $180^{\circ}$ a tracking converter can lose its count and require up to 300 ms to recover. Manufacturers are loath to tell you this, and in many cases omit the step response for a


The Model M6000 s/d converter delivers a 12-bit natur-al-binary angle output. It is only one of a wide line of $\mathrm{s} / \mathrm{d}$ and $\mathrm{d} / \mathrm{s}$ converter modules from Astrosystems.


You can get true 16-bit performance from the MP1916TC d/a converter designed by Analogic. Built into it are data latches and a temperature-controlled oven.

180 degree change.
If resolvers change angles faster than the unit can track, the $\mathrm{r} / \mathrm{d}$ converter will have a lag error as part of its dynamic response characteristic. Sampling converters are also prone to this error because they require several milliseconds to do a conversion. The conversion delay makes possible errors of 40 to 50 arc-seconds.

Converter accuracy specs are often given in arc minutes plus an LSB. This can let the manufacturer imply better accuracy than is really available. Get the vendor to state accuracy in absolute terms.

## Power output is important for $\mathrm{d} / \mathrm{r}$ converters

Digital-to-resolver converters often have underspecified output capabilities. Many converters are protected from overload only for short periods of time, say 2 to 5 minutes, and can burn out their power amplifiers. Data sheets don't care if the bearing got stuck in the motor mount, so make sure you have a rating for continuous overload protection. Also, load ratings should be given for reactive loads-not resistive. Synchros and resolvers are inductive loads, with typical power-factor angles of 70 to 80 degrees.

Inductosyn-to-digital converters, a special class of $\mathbf{r} / \mathrm{d}$ developed by Farrand, offer some rather unique problems. These converters use two air-coupled moveable linear windings to form a high-resolution control transformer. They usually work in an industrial environment (with plenty of electromagnetic and radio-frequency interference). Therefore, susceptibility to switching and radiated noise should be specified.

Even though vendors claim Inductosyns will work with carrier frequencies ranging from 2 to 10 kHz , if you choose too low a frequency, the carrier won't be coupled efficiently from the primary to secondary. And, with too high a frequency, leakage capacitances cause interference and signal loss.

Inductosyn resolution can be influenced by two factors usually not listed on data sheets.

- The precision to which the induced voltages in the two windings follow the sine and cosine laws with angle.
- The degree of control smoothness required. The more precise the resolution, the harder to meet the maximum velocity and acceleration requirements.


## Monolithic converters: a growing field

Monolithic d/a converters such as those developed by Precision Monolithics, Analog Devices and Motorola, have already found their way into many applications. Their low cost-typically $\$ 2$ to $\$ 10$ for commercial temperature range prod-
ucts-opens many doors. But beware of low-cost "bargains." Many vendors achieve the low cost by omitting the reference and sometimes the output amplifier too.

Until now, monolithic d/a's have been able to resolve up to 12 bits with relative accuracies to $0.01 \%$. Recent developments are boosting performance still further.

The most accurate, highest-resolution monolithic $d /$ a converter will soon be available from Intersil. The 6202 will accept a 16 -bit digital input and resolve the analog output to a true 16bit accuracy of $30 \mu \mathrm{~V}$. The converter, built with CMOS technology, uses an all-digital scheme of time-division multiplying to do the conversion. This unusual scheme eliminates the precision resistors and switches normally needed.

However, only 30 conversions per second are possible. And, the slow speed may restrict the 6202 to low-frequency-measurement, display and controls applications.

Recently announced by Precision Monolithics is an 8 -bit companding $\mathrm{d} /$ a converter (DAC-76). It


Monolithic and modular $a / d$ and $d / a$ converters as well as $\mathrm{r} / \mathrm{d}$ and $\mathrm{d} / \mathrm{r}$ converters are available from Analog Devices with resolutions from 8 to 16 bits.
has either logarithmic or antilog weighting that is logic-level selectable. The $\log$ compression or expansion gives the converter the small-signal relative accuracy of a 12 -bit converter-at a cost of $\$ 19$ in hundreds. The DAC-76 can also be used as a multiplying $\mathrm{d} /$ a converter. (See story ED Vol. 24, No. 9, April 26, 1976, p. 111.)

Analog Devices has a new monolithic d/a con-verter-the AD7522-that accepts 10 -bit data words and has two sets of input-data latches on the same chip as all the other circuitry. The extra latches simplify interface requirements for microprocessor-based systems and eliminate the outboard latches normally required. Intersil also
offers the AD7522 as a second-source product. Just out of Motorola's ovens are the MC3510/ 3410,10 -bit d/a converters. These laser-trimmed multiplying converters have a maximum error of $0.05 \%$ (relative to full-scale output current). Also coming from Motorola is an ECL-compatible converter that accepts 8 bits and settles in 20 ns .

Several other companies-including Advanced Micro Devices, National Semiconductor, RCA and Signetics-are jumping onto the d/a converter bandwagon with both second-source and proprietary products.

## You can't get a monolithic a/d converter

There really aren't any monolithic $a / d$ converters available that include all the necessary circuitry. At the very least you'll have to add a reference source and several scaling resistors. And, for some models you'll even have to add a comparator or clock. Two of the most complete monolithics, though, are the 8702 from Teledyne Semiconductor and the MM4357 from National Semiconductor. Both of these just need a reference and a couple of resistors.

The $8702 \mathrm{a} / \mathrm{d}$ converter delivers 12 bits and has a relative accuracy of $\pm 0.5$ LSB. It uses a change-balancing integration technique to convert signals and can thus deliver a word only every 20 ms . National's unit delivers an 8 -bit word every $80 \mu$ s (maximum) with its successive-approximation circuitry. Its full-scale error is $\pm 2$ LSB for the lowest cost model ( $\$ 7.95$ in hundreds) and $\pm 1$ LSB for higher accuracy versions. Threestate outputs simplify interface requirements.

TRW and Precision Monolithics have recently announced monolithic 8-bit and larger successiveapproximation a/d converters. The TRW units use ECL technology and have conversion times of 140 ns for 8 bits, 200 ns for 10 bits and 500 ns for 12 bits. PMI's 8 -bit converter should be available by the end of the year and is expected to have a conversion time of under $10 \mu \mathrm{~s}$.

Most a/d converters are one or two-chip integrating units that require external comparators or resistor-capacitor networks. Some popular manufacturers of monolithic a/d's are Analog Devices, Integrated Photomatrix, Intersil, Motorola and Siliconix.

The AD7550 made by Analog Devices uses a four-slope integration technique and delivers a 13 -bit data word. It is a CMOS circuit and requires a reference and an external R-C network, and can deliver about 40 words per second. Three-state outputs permit it to be connected directly to data busses.

Analogic has had a single-chip integrating unit available for about a year, the MN2301, that delivers $3-1 / 2 \mathrm{BCD}$ digits (1999 max count) and can do 10 conversions per second. It costs about
$\$ 24$ in 100-unit lots.
Siliconix has also announced a single-chip CMOS unit with a 3 -digit BCD output-the LD130. Just add a reference and a few capacitors and it's ready to go. It costs $\$ 8.75$ in 100 -unit lots.

Two-chip, multiple-slope converters with 3-1/2 digit, 4-1/2 digit and up to 16-bit binary outputs are available from Integrated Photomatrix, Intersil, Motorola and Siliconix, with prices ranging from about $\$ 12.50$ per set in 100 -piece lots.

Monolithic v/f or f/v converters are few and far between, unless you include "function-generator" chips such as the 8038, or phase-locked loops. The only three "monolithic" $\mathrm{v} / \mathrm{f}$ (or $\mathrm{f} / \mathrm{v}$ ) units are available from Raytheon (the RM4151), Intech/FMI (the A-8400) and Analog Devices (the AD537). A single-supply version of the A-8400 (the A-8402) will be available from Intech, shortly.

Both the Raytheon and Intech units can be used as either a v/f or f/v converter, just by simple external pin connections but need from 4 to 15 external components. They can operate at frequencies of up to 10 kHz . The AD537 from Analog Devices operates only as a v/f converter and delivers frequencies of up to 100 kHz .

There are currently no monolithic $\mathrm{r} / \mathrm{d}$ or $\mathrm{d} / \mathrm{r}$ converters available at any price, however hybrid circuits are now starting to fill the need.

## Hybrids: the best of two worlds

Hybrid microelectronic converters combine the best of the monolithic and discrete-component worlds whenever space is at a premium. $\mathrm{D} / \mathrm{r}$ and $r / d$ converters have always been pretty bulkyin modular form a $2.5 \times 4 \times 0.8 \mathrm{in}$. package is common-but in many of the new all digital systems much of the bulk can be eliminated.

ILC Data Device has developed a complete 10 bit $\mathrm{s} / \mathrm{d}$ converter in a 36 -pin double DIP that costs just $\$ 345$ in single-unit quantities. It has a $\pm 30$ minute worst-case error and can track at speeds of up to 100 rps for a 400 Hz reference (see story p. 140 of this issue). Aside from this unit, ILC Data Device offers two series-the H and HMSDC-of hybrid $\mathrm{s} / \mathrm{d}$ and $\mathrm{r} / \mathrm{d}$ converters and has a 14-bit hybrid unit in development.

Most activity in hybrids comes from manufacturers of $\mathrm{a} / \mathrm{d}$ and $\mathrm{d} / \mathrm{a}$ converters. Some of the manufacturers-Beckman, Burr-Brown, Datel, Hybrid Systems, ILC Data Device, Micro Networks and Teledyne Philbrick-offer hybrid converters with resolutions ranging from 8 to 16 bits at speeds as short as 50 ns for d/a's and several hundred nanoseconds for $\mathrm{a} / \mathrm{d}$ 's.

A low-cost d/a converter like the DAC-80 12bit unit introduced by Burr-Brown and second sourced by Micro Networks and others costs less than $\$ 20$ when purchased in quantities of 100 or
more. The converters come in 24 -pin DIPs and have either binary or BCD input coding. Both current or voltage-output models are available, with settling times of 300 ns or $3 \mu \mathrm{~s}$, respectively, for a full-scale input change.

Beckman has just announced a series of lowcost d/a units. The 877-80 and 877-85 12-bit units cost $\$ 20$ and $\$ 40$ respectively in 100 -up quantities. They operate over a 0 to 85 or -25 to $+85-\mathrm{C}$ range, have a linearity error of $\pm 1 / 2$ LSB and settle in 300 ns (for the current output models and a $100-\Omega$ load).

Datel and Hybrid Systems also offer hybrid units with up to 12 -bit resolution and accuracy. Datel's units, the DAC-HZ series, are pin-compatible with the DAC-85 series from Burr-Brown and the 877-85 units from Beckman. They have prices starting at $\$ 49$ in singles and require only $\pm 15$ - V instead of $\pm 15-\mathrm{V}$ and +5 -V supplies.

Inexpensive a/d's are also available from many


Bus-compatible a/d and d/a converters, with three-state logic outputs or inputs, are available from SGR Corp. Both successive-approximation and multi-slope a/d converters are available.
of the same firms; Burr-Brown's ADC-85 and ADC-80 series units have been second-sourced by Beckman, Datel and Micro Networks. These converters come in 32-pin packages and include a clock, comparator and reference. The ADC-85 also has an input-buffer amplifier. Conversion times are $25 \mu \mathrm{~s}$ for $\mathrm{ADC80}$ and $10 \mu \mathrm{~s}$ for the 85. Prices start at under $\$ 50$ in 100 -unit quantities.

ILC Data Device offers some ECL-compatible d/a and a/d converters in its ADH series that are about the best you can get in hybrid form. The ADH-030 12-bit d/a converter settles in under 50 ns for a full-scale change, has an internal reference and a linearity of $0.05 \%$.
The a/d converter, Model ADH-010 requires several hybrid packages, because of power dissipation restrictions, but can be mounted on a single circuit board. It can deliver ECL-compatible 10 -bit words at a 1 MHz rate. Prices for these speedy units start at $\$ 295$ for the ADH-030
and go to $\$ 895$ for the ADH-010. A 12 -bit version of the a/d will be available shortly.

Single-package hybrid converters with full MIL-temperature-range performance and hermetic packages are available from companies like Burr-Brown, Datel, Micro Networks and Teledyne Philbrick. Teledyne's units-such as its 4058-83 12-bit d/a which has a settling time of 200 ns (current output) -are some of the fastest available with TTL output levels. They are screened to MIL-STD-883, Level B.

## Modular and rack converters offer the tops

As the space you have available increases, or the performance requirements become more exacting, modular and rack-mounted converters can fill most needs. For very-high-speed applications, companies like Computer Labs, Datel, ILC Data Device, Intech, M. S. Kennedy and TeledynePhilbrick offer a/d converters with conversion times of under $1 \mu \mathrm{~s}$ for 8,10 and 12 -bit units.

The fastest include the VADC $8 / 17$ from ILC Data Device, which delivers 8 -bit words at a 17 MHz word rate; the MOD-4100 from Computer Labs that operates at a $100-\mathrm{MHz}$ word rate and delivers 4 -bit data and the ADC-UH8B from Datel, an 8 -bit unit with a 10 MHz sampling rate. The DDC unit is made up of three circuit cards and costs $\$ 4350$. The MOD- 4100 costs $\$ 2120$ and is an open-frame converter and the Datel unit costs $\$ 995$ and is a $3 \times 5 \times 1.15-\mathrm{in}$. module.

At slightly slower speeds the field opens up to over a hundred module manufacturers and about half as many manufacturers of rack-mounted circuits. Some of the well-known manufacturers of off-the-shelf modules include Analog Devices, Analogic, Burr-Brown, Datel, Dynamic Measurements, Hybrid Systems, ILC Data Device Corp., Intech, Phoenix Data, Teledyne-Philbrick and Zeltex. Rack units for very high precision (12-bit resolution and tighter) are available from companies such as Phoenix Data, Preston Scientific, Princeton Applied Research and Tustin Electronics.

Prices for modular converters range from under $\$ 15$ for simple 8 -bit current-output versions to over $\$ 1000$ for high-speed, high-precision a/d converters such as the MP8016 series of 14, 15 or 16 -bit units from Analogic. They have conversion times of 0.6 to $2.5 \mu \mathrm{~s}$ per bit. SGR Corp. has recently announced a line of microprocessor-compatible converters-double-buffered $d / a$ converters and a/d converters with three-state outputs.

Modular v/f and f/v converters are available from about a dozen manufacturers with full scale frequency ranges of $10 \mathrm{kHz}, 100 \mathrm{kHz}$ and 1 MHz commonly offered. Teledyne-Philbrick has topped even those ranges with its $4707,5-\mathrm{MHz}$ voltage-
to frcquency converter.
Many interface options-from differential or instrumentation-amplifier front ends, to optical-ly-coupled inputs and outputs-are available from most vendors. Intech even offers a "super" unit with a built-in crystal to improve frequency stability. Wide ranges of performance specs are available from companies like Analog Devices, Datel, Dynamic Measurements, Intech and Tele-dyne-Philbrick. Basic $f / v$ and $v / f$ prices start at $\$ 25$ for units with nonlinearities of under $1 \%$.

Another group of converters that use a deltasigma technique are somewhere in between the $a / d$ and $v / f$ converter ( $o r d / a$ and f/v). Hybrid Systems offers these converters under the trade name of Deltaverters. Just released circuits include the DV620 encoder and DV621 decoder that can be used at frequencies of up to 20 kHz and cost only $\$ 59$ each in singles. (See story p. 138 this issue.)

The angular-converter area has about a dozen


A simple 3-digit voltmeter can be built using the LD130 single-chip integrating a/d converter made by Siliconix. In addition to the chip all you need is a display driver, display, four transistors, some resistors and a capacitor.
manufacturers vying for the market. Some of the largest include Analog Devices, Astrosystems, Computer Conversions, Control Science, ILC Data Device, Natel, North Atlantic Industries and Transmagnetics. Both Analog Devices and ILC Data Device have introduced Inducto-syn-to-digital converters-the IDC 1701, 1703 and IDC-101, 102, respectively. One version from each company delivers a count of 4000 per pitch cycle and the other a count of 4096. The units from Analog Devices come without the input amplifiers necessary to sense the Inductosyn output and cost $\$ 350$. The DDC units include the amplifiers and isolation transformers and cost $\$ 495$.

Within the last year, North Atlantic Industries has developed a custom LSI circuit for use within its LSI/ 85 and LSI/ 90 series of $s / d$ converters. The circuit permits a $35 \%$ reduction in module size and an increase in relability. Accuracy of the LSI/85 units is better than $\pm 3$ minutes


Monolithic $\mathrm{d} / \mathrm{a}$ converters, ranging from 4 to 12 -bit units, are the prime product line from Precision Monolithics. Its newest unit is the Comdac, a companding 8 -bit d/a.
up to angular change rates of $1440^{\circ} / \mathrm{s}$. Prices start at under $\$ 500$.

Natel Engineering's r/d and d/r converter line offers a wide choice of capabilities-from its SD552 16 -bit $\mathrm{r} / \mathrm{d}$ converter in a 0.42 -in.-high package to the SD402/602 converter that require only $2.13 \times 2.13 \times 0.42 \mathrm{in}$. and operate from a single $+5-\mathrm{V}$ supply instead of the $\pm 15$ and $+5-\mathrm{V}$ supplies usually needed. Prices for the SD402/ 602 start at $\$ 199$ for the commercial version. A soon to be released dual-speed converter, the SD402, spans 50 to 400 Hz in one version or 400 Hz and up in another. It has 16-bit resolution, $0.01 \%$ accuracy and fits into about 7 cubic inches. The converter runs from a $+5-\mathrm{V}$ supply and costs $\$ 995$ or $\$ 1095$, depending upon the frequency selected.

High power-drive d/s converters that can power up to three size-11 torque receivers have been introduced by Computer Conversions. The units accept a 14 -bit input and can deliver 90 or 11.8-V, rms, line-to-line at 60 or 400 Hz . Prices for these units are under $\$ 500$.

Converters from Astrosystems use tapped toroidal transformers that are said to make the converters insensitive to temperature and aging. S/d converters from Astrosystems have an infinite range of scale factors just by a simple change in an RC time constant. Full scale is determined by the ratio between the integrator's time constant and a digital clock frequency.

Transmagnetics and Control Sciences both offer high-speed tracking $s / d$ converters with rates of $5760^{\circ} / \mathrm{s}$ and $3600^{\circ} / \mathrm{s}$ for their 14 -bit units. For fast tracking you'll pay dearly, though. The Transmagnetics unit (Model 1651), for instance, costs $\$ 640$ in singles.

## Need more information?

For manufacturers' literature, detailed specs and other information helpful in selecting and applying converters this list of major OEM suppliers is provided. Code letters A, B and C indicate whether the vendor offers $a / d$ or $d / a$ converters, $\mathrm{f} / \mathrm{v}$ or $\mathrm{v} / \mathrm{f}$ converters, or $\mathrm{r}(\mathrm{s}) / \mathrm{d}$ or $\mathrm{d} / \mathrm{r}(\mathrm{s})$ converters, respectively. For additional manufacturers' listings see Electronic Design's GOLD BOOK.

Acromag Inc., 30765 Wixom Rd., Wixom, MI 40896. (313) 624. 1541. (S. Kresch). B

Circle No. 381
Action Instruments Co., Inc., 7969 Engineer Rd., San Diego, CA 92111. (714) 279-5726. (R. Britton). B Circle No. 382
ADAC Corp., 118 Cummings Park, Woburn, MA 01801. (617) AD Data Systems, 830 Linden Ave., Rochester, NY 14625 AD Data Systems, 830 Linden Ave., Rochester, NY 14625.
(716) $381-2370$. (H. Turner). A Americian Astrionics, 291 Kalmus Dr., Costa Mesa, CA 92626. (714) 540-4330. A, C. Circle No. 385

American Electronic Labs., Inc., P.O. Box 552, Lansdale, PA 19446. (215) 822-2929. (T. Keffer). A Circle No. 386 American Vector Inc., 50 Culver Rd., Dayton, NJ 08810. (201) 329-4683. (T. Gordon). A
Anadex Instruments Inc., 7833 Haskell Ave., Van Nuys, CA

91046 . (213) 782-9527. (K. Mathews). B Circle No. 388 | 91046. (213) 782-9527.' (K. Mathews). B Circle No. 388 |
| :--- |

Analog Devices, Inc., P.O. Box 280, Norwood, MA 02062. (617)
$329-4700$. (L. Wickersham). A, B, C $\quad$ Circle No. 389
Analogic Corp., 1 Audubon Rd., Wakefield, MA 01880. (617) 246-0300. (D. Chase). A Circle No. 390
Astrosystems Inc., 6 Nevadia Dr., Lake Success, NY 11040. (516) 328-1600. (G. Steinberg). C Circle No. 391

Automated Industrial Measurements, P.O. Box 125, Wayland, MA 01778. (617) 653-8602. (E. Hilton). B Circle No. 392
Aydin Vector Div., P.O. Box 328, Friends Lane, Newton, PA 18940. (215) 968-4271. (J. Thomason). A Circle No. 393 Baldwin Elecs. Inc., 1101 McAlmont St., Little Rock, AR 72203. (501) 375-7351. (S. Berg). A Circle No. 394

Barber-Colman Co., Industrial Instruments Div., 1317 Rock St., Rockford, IL 61101. (815) 877-0241. (W. Paetz). A
Base Ten Systems Inc., 3828 Quakerbridge Rd.. Trenton, NJ 08619. (609) 586-7010. (W. Errickson). A Circle No. 396 Beckman Instruments, 2500 Harbor Blvd., Fullerton, CA 92634. (714) 871-4848. (D. Snyder). A Circle No. 397 Bell \& Howell Control Prods. Div., 706 Bostwick Ave., Bridgeport, CT 06605. (203) 368-6751. (S. Rose). B

Circle No. 398 9500. (D. 10411 Bubb Rd., Cupertino, CA 95014. (408) 255 Burr-Brown Research Corp., 6730 S. Tucson Blvd., Tucson, AZ 85734. (602) 294-1431. (J. Santen). A, B Círcle No. 406

Cal Tek Engineering, 29 Pemberton Rd., Wayland, MA 01778. (617) 653-0355. (D. Sheehan). B Circle No. 407

Cambridge Thermionic, 445 Concord Ave., Cambridge, MA 02138. (617) 491-5400. (L. Wilkes). A Circle No. 408

Canberra Ind., 45 Gracey Ave., Meridan, CT 06450. (203) 238 2351. (B. Compo). A Circle No. 409 C \& A Products Inc., 37-12 58 St., Woodside, NY 11377 (212)
$779-4303$. (R. Bogen). A, C
Clifton Precision, Div. Litton Systems Inc., Marple At Broadway, Clifton Heights, PA 19018. (215) 622-1000. (W. Runiewicz). C Circle Nu. 411
CMP Ind. Inc., 23660 Research Dr., Farmington Hills, MI 48024. (313) 477-1750. (A. Carter). A Farmington Hircle No. 412

Coded Communications Corp., 1620 Linda Vista Dr., San Marcos, CA 92069. (714) 744-3710. (J. Tyler).A

Computer Central, P.O. Box 804, Gaithersburg. (301) 948-5557. (A. O'Hara). A, C Gaithersburg, MD 20760. Computer Conversions Corp., 6 Dunton Ct E Norcle No. 414 11731. (516) 261-3300. (S., Renard). C ${ }^{\text {E., }}$ Circle No. 415 Computer Labs Inc., 505 Edwardia Dr., Greensboro, NC 27409. (919) 292-6427. (J. Poitras). A Circle No. 416 Conrac Corp. NJ Div., 32 Fairfield PI. W., Caldwell, NJ Control Sciences Inc., 8399 Topanga Canyon Blvd., Suite 303, Canoga Park, CA 91304. (213) 887-7344. (H. Ericsson). C Circle No. 418
Coulbourn Instruments Inc., P.O. Box 2551, Lehigh Valley,
PA $18100,(215) 395-3771$ (P. Johnson). B Circle No. 419 PA 18100. (215) 395-3771. (P. Johnson). B Circle No. 419 Cycon Inc., 1080 E. Duane Ave., Sunnyvale, CA 94086. (408) 732-8311. A Circle No. 420
Data General Corp., Rte. 9, Southboro, MA 01772. (617) 485.
Data Tech, 2700 S. Fairview Rd., Santa Ana, CA 92704. (714) 546-7160. (W. Miller). A Circle No. 422
Data Translation Inc., 109 Concord St., Framington, MA
01701 . (617) 879-3595. (F. Molinari). A $\quad$ Circle No. 423

Datawest Corp., 7333 E. Helm Dr., Scottsdale, AZ 85260. (602) 948-3280. (D. Bozich). A. C Circle No. 424 Datel Systerns Inc., 1020 Turnpike St., Canton, MA 02021 (617) 828-8000. (E. Zuch). A, B Circle No. 425 Datex Div., Conrac Corp., 1600 S . Mountain Ave., Duarte, CA
$91010 .(213) 359-9141$. (W. ShevelI). A Circle No. 426 Datum Inc., 1363 State College Blvd., Anaheim, CA 92806 (714) 533-6333. (N. Dawirs). A Circle No. 427 Digital Equipment Corp., 146 Main St., Maynard, MA 01754. (617) 897-5111. (T. Johnson). A Circle No. 428 Dynamic Measurements, 6 Lowell Ave., Winchester, MA 01890 (617) 729-7870. (J. Gendreau). A, B Circle No. 429 Elographics, Inc. P.O. Box 388, Oak Ridge, TN (615)
$482-4039$. (R. Evans). A EPSCO Inc., 411 Providence Hwy., Westwood, MA 02090. (617) 329-1500. (H. Gershanoff). A, C Circle No. 432 E-Systems Inc., P.O. Box 6030, Dallas, TX 75222. (214) 742 9471. (D. Dirks). A

Circle No. 433 Farrand Controls Inc., 99 Wall St.. Valhalla, NY 10595. (914) 761-2600. (C. Farrand Jr.). A, C Fluidyne Instrumentation, 1631 San Pablo Ave., Oakland, CA 94612. (415) 444-2376. (R. Jennings). A Circle No. 435 FX Systerns Co., 5070 Kings Hwy., Saugerties, NY 12477 Gap Instrument Corp., 110 Marcus Blvd., Hauppauge, NY 11787. (516) 273-0909. (W. Soboleski). A, C Circle No. 437 General Atronics Corp., 1201 E. Mermaid Lane, Philadelphia eneral Instrument Corp., 600 W. John St., Hicksville, NY 11802. (516) 733-3333. (G. Cacavio). A, B, C Circle No. 439 Hewlett-Packard, 175 Wyman St., Waltham, MA 02154. (617)
890-6300. (R. Hanna). A 890-6300. (R. Hanna). A
Hybrid Systems Corp., 22 Third Ave., Burlington, MA 01803. (617) 272-1522. (D. Misno). A Circle No. 441

Hy Comp Inc., 146 Main St., Maynard, MA 01754 . (617) 897.
4578. (N. Palazzini). A
ILC Data Device Corp., 105 Wilbur PI., Bohemia, NY 11716 (516) 567-5600. (A. Carbone). A, C Circle No. 443

Intech Inc., 1220 Coleman Ave., Santa Clara, CA 95050 (408) 244-0500. (P. Pinter). A, B Circle No. 444 NJ 07092. (201) 233-6010. (P. Noble). A Circle No. 445 Inter-Computer Electronics Inc., P.O. Box 507, Lansdale, PA 19446. (215) 855-0922. (A. Rosset). A CIrcle No. 446 Interface Engineering Inc., 386 Lindelof Ave., Stoughton, MA 02072. (617) 344-7383. (R. Eastman). C Circle No. 447

Intersil, 10900 N. Tantau Ave., Cupertino, CA 95014. (408) 257-5450. (J. Corser). A Circle No. 448
Intronics Inc., 57 Chapel St., Newton, MA 02158. (617) 332 -
7350 . (A. Pfaelzer). A, B
Jorway Corp., 27 Bond St., Westbury, NY 11590. (516) 997 8120. (T. Radway). A Circle No. 450

Kennedy, M.S., Pickard Dr., Syracuse, NY 13211. (315) 455. 7077. (B. Lesjack). A Circle No. 45 (213) 287-973i. (H. Halverson). A Circle No. 452 Kinetic Systems Corp., Maryknoll Dr., Lockport, IL 60441 (815) 838-0005. (L. Klaisner). A Circle No. 453

Lancer Elecs. Corp., 3950 German Town Pike, Collegeville PA 19426. (215) 489-2265. (Mr. Amstey). A. Circle No. 454
LRC Inc., 101 Digital Dr., Hudson, NH 03051. (603) 883 - 8 Circle No. 455
8001 . (F. Solla). A
Macrodyne Inc., 1900 Maxon Rd., Schenectady, NY 12301 (518) 372-5619. (D. Antos). A Circle No. 456

Martin-Decker Co., 1928 S. Grand Ave., Santa Ana, CA 92705. Media III, 2259 A Via Burton, Anaheim, CA 92806. (714) 870 7660. (G. Salley). A

Circle No. 458
Metric Systems Corp., 736 N. Beal St., Fort Walton Beach FL 32548. (904) 242-2111. (R. Zlata). A Circle No. 459 Micro Networks Corp., 324 Clark St., Worcester, MA 01606.
$\begin{aligned} & \text { (617) } \\ & \text { 852-5400. (R. Jay). A }\end{aligned}$
Motorola Semiconductor Products, 5005 E. McDowell Rd. Phoenix, AZ 85008. (602) 962-2994. (S. Faulkner). A Circle No. 461
Natel Engineering Co., Inc., 8954 Mason Ave., Canoga Park, CA 91036. (213) 882-9620. (J. Naster). C Circle No. 462 Clara, CA 95051. (408) 732-5000. (D. Coleman). A Circle No. 463
Nationwide Elec. Systems Inc., 1536 Brandy Pkwy., Stream wood, IL 60103. (312) 289-8820. (H. Hanson). A

Newport Labs, 630 E. Young St., Santa Ana, CA 92705. (714 540-4914. (C. Hasley). A Circle No. 430 Non-Linear Systems Inc., Box N, Del Mar, CA 92014. (714) 755-1134. (H. Lesser). A Circle No. 465 North Atlantic Industries, 200 Terminal Dr., Plainview, NY 11803. (516) 681-8600. (K. Salz). C Circle No. 466 North Hills Elecs. Inc. Alexander PI., Glen Cove, NY 11542.
$(516) 671-5700$. (H. Marx). B

Nuclear Data Inc., Golf \& Meacham Rd., Schaumburg, IL 60196. (312) 884-3621. (J. Heraty). A Circle No. 468 Optical Elecs. Inc., Box 11140 , Tucson, AZ 85734. (602) 624 Orbit Instrument, 131 Eileen Way, Syosset, NY 11791. (516) 921-6310. (J. Kilkenny). A Circle No. 470 Ortec Inc., 100 Midland Rd., Oak Ridge, TN 37830. (615) 482. 4411. (R. Welch). A

Circle No. 471 Pacific Electro Dynamics, 14220 Sunset Hwy., Bellevue, WA 98007. (206) 747-7400. (W. Maesner). C Circle No. 472 Perkin-Elmer Corp., Main Ave., Norwalk, CT 06856. (203) 762-4786. (P. Hutchinson). A, C Circle No. 473 Phoenix Data, Inc., 3384 W. Osborn Rd., Phoenix, AZ 85017.
(602) $278-8528$. (J. Hartigan). A Pioneer Magnetics Inc., 1745 Berkeley St., Santa Monica, CA 90404. (213) 829-3305. (G. Held). B Circle No. 475 Plessey Environmental Systems, 3939 Ruffin Rd., San Diego,
CA 92123. (714) 278-6500. (G. Clausen). A Circle No. 476 Precision Monolithics, 1500 Space Park Dr., Santa Clara, CA 95050. (408) 246-9222. (D. Soderquist). A Circle No. 477 Preston Scientific, 805 E. Cerritos, Anaheim, CA 92805 . (714)
$776-6400$. (B. Spear). A Prime Computer Inc., 145 Pennsylvania Ave., Framingham, MA 01701. (617) 879-2960. (N. Stover). A Circle No. 479 Princeton Applied Research Co., P.O. Box 2565, Princeton, NJ 08540. (609) 452-2111. (S. Letzler). A Circle No. 480 Process Computers Systems Inc., 5467 Hill 23 Dr., Flint, MI 48507. (313) 744-0225. (G. Johnson). A Circle No. 481 Quindar Elecs., 60 Fadem Rd., Springfield, NJ 07081 (201) Radar Technology Inc., 91 Merrimack St., Haverhill, MA Ragen Diata Systems Inc., 125 Schmitt Blvd., Farmingdale, NY 11735. (516) 293-1333. (M. Richardson). A

Renco Corp., 26 Coromar Dr., P.O. Box 246, Goleta (805) 968-1525. (R. Hotchin). A
leta, CA 93017.
Circle No. 485 RFL Ind. Inc., Coms Div., Powerville Rd. 23, Boonton, NJ 07005. (201) 334-3100. (R. Gilman). B ${ }^{\text {(R) }}$ Circle No. 486 Richard-Lee Co., Box 724, New Providence, NJ 07974. (201) 665-1333. (L, Gintella). B

Circle No. 487 Scanivalve Inc., P.O. Box 20005, San Diego, CA 92120. (714)
283-0010. (H. Hunt). A Sea Data Corp., 153 California St., Newton, MA 02138. (617) 244-3216. (W.' Hill). A

Circle No. 489
SGR Corp., Neponset Valley Industrial Park, P.O. Box 391, Canton, MA 02021. (617) 828-7773. (S. Radler). A, B

Signetics, 811 E. Arques Ave., Sunnyvale, CA 94086. (408) Siliconix Inc., 2201 Laurelwood Rd., Santa Clara, CA 95054 (408) 246-8000. (S. Bolger). A ara, CA 95054.
Circle No. 492
Singer Co., Kearfott Div., 1150 McBride Ave., Little Falls, NJ
$07424 .(201)$
$256-4000$. (C. Wheatley). A C Circle No. 493 07424. (201) 256-4000. (C. Wheatley). A, C' Circle No. 493 Singer Instrumentation, 5340 Alla Rd., Los Angeles, CA Solid State Elecs., 15321 Rayen St., Sepulveda, CA 91343.
$(213) 894-2271$. (E. Politi). A, B Spacetac Inc., Crosby Dr., Bedford, MA 01730. (617) 275 1710. (R. Reed). A

Circle No. 496
Tauber-Dreyer Corp., 10533 Topeka Dr., Northridge, CA 91324. (213) 368-4011. C

Circle No. 497
Teledyne Geotech, 3401 Shiloh Rd., Garland, TX 75041. (214) Teledyne-Philbrick, Allied Dr. at Rte. 128, Dedham, MA 02026. (617) 329-1600. (F. Goodenough). A, B

Tennelec Inc. (615) 483-8404. (B. Hebenstreit). A Oak Ridge, TN 37830. Theta Instrument Corp., 24 Dwight PI., Fairfield, NJ 07006 (201) 227-1700. (T. Villano). C

Three Rivers Computer Corp., P.O. Box 235, Pittsburgh PA 15213. (412) 621-6250. (B. Rosen). A Circle No. 502 Transmagnetics Inc., 210 Adams Blvd., Farmingdale, NY 11735. (516) 293-3100. (A. Talbot). C Circle No. 503 TRW Defense and Space Systems Group, 1 Space Park, Redondo Beach, CA 90278. (213) 536-1977. (H. DiMond). A

Tucker Elecs. Co., P.O. Box 1050, Garland, TX 75040. (214 Tustin Elecs. Co. 1431 E. St. Andrews PI., Santa Ana CA Tustin Elecs. Co., 1431 E. St. Andrews Pl., Santa Ana, CA
92705 . (714) 549.0391 . (W. Buchanan). A Circle No. 505 Varian Assoc., 611 Hansen Way, Palo Alto, CA $94303 .{ }_{\text {Circle No. }} \mathbf{5 0 6}$
$493-4000$. (J. Heldack). A Vega Precision Labs Inc., 800 Follin Lane, Vienna, VA 22180. (703) 938-6300. (W. Howells), A, B, C Circle No. 507 Vernitech Div., Vernitron Corp., 300 Marcus Blvd., Deer Park NY 11729. (516) 586-5100. (J. Moonelis). C Circle No. 508 Xincorm Corp., 8944 Mason Ave., Box 648, Chatsworth, CA
91311 . (213) $341-5040$. (B. Sear). A Circle No. 509
Zeltex Inc., 940 Detroit Ave., Concord, CA 94518. (415) 686. 6660. (W. Peacock). A Circle No. 510

New 32D permanent magnet fhp motors and 32D-5F right angle gearmotors join the growing Bodine PM family.

Perfectly matched with Bodine's PM controls, 32 's offer the same performance characteristics as the 42's-in a smaller package. Only $3.55^{\prime \prime}$ in diameter. Built for high starting torque, low speed opera-
tion, and a high degree of control, the new systems have continuous duty motor ratings of $1 / 12,1 / 10$ and $1 / 8 \mathrm{Hp}$ at 2500 Rpm . New right angle gearmotors are available in six standard ratios.

All PM systems have grounding provisions for safe operation. Bodine PM motors are totally-enclosed, nonventilated and reversible.

Optional "L" bracket mounting kits, terminal endshield kits, and other accessories are available. Bodine also offers D-C motors for use with your own controls. See your Bodine Distributor or write for Catalog CDC-PM.

Contact Bodine Electric Company, 2500 W. Bradley Pl., Chicago, IL 60618. Phone (312) 478-3515.

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# Design single-section delay equalizers rapidly with normalized tables and graphs. Practical circuits provide parabolic and negative or positive-slope equalization. 

In spite of the impact of computers on design work, normalized tables for designing such oftenused networks as delay equalizers, still remain one of the most useful and powerful of engineering aids. However, tables for delay-equalizer design have lagged behind those for other networks, such as filters and attenuators, largely because of the large number of possible and desirable variations. Nevertheless, if we restrict our interest to single-section all-pass equalizers, which command wide usage, the number of different versions falls to practical limits.

Some published delay-equalizer tables ${ }^{1}$ are fairly widely used, but they are directly applicable only to parabolic equalizers. Also, they offer little insight into the equalizer's delay shape when the bandwidth is normalized. In addition, these published tables are not applicable where the shape factor (d value, Figs. 1 and 2 and related value, $\theta$ ) is very small, and where equivalent bridged-T circuits (Fig. 1) require mutual inductance.

The new normalized table (Table 1) overcomes these handicaps. In addition to shape factors, it provides the normalized frequencies, X , of the inflection points and peak delays for all three types of equalizers-parabolic, positive and nega-tive-shown in Figs. 3b, 3c and 3d.

For a parabolic equalizer (Fig. 3b) the table's columns A and C contain the X values corresponding to positive and negative-slope inflection points, respectively. Column B provides the X of the maximum-delay.

For a positive-slope equalizer (Fig. 3c), the same column A provides the frequency of its single inflection point, and column B provides its maximum-delay point.

Similarly, for a negative-slope equalizer (Fig. $3 d)$ column C provides the frequency of the single inflection point, and column B is its maximumdelay point.

Beyond the points of inflection, equalizer shapes quickly depart from the assumed theo-

[^7]

1. Delay-equalizer circuits with components normalized as a function of the shape-factor, $d$, from Table 1 may be predistorted (to compensate for dissipation losses) by the inclusion of the dotted components. $\mathrm{R}_{\mathrm{dc}}$ is the dc resistance of the inductor in the parallel-tuned tank, and $E$ is the loss ratio at resonance ( $E>1$ ).
retical shapes. For parabolic equalizers, you can use the peak-delay frequency and the inflection points to estimate quickly how much the equalizer's shape departs from the ideal shape.

For slope equalizers, you can quickly determine the maximum percentage bandwidth by using the frequency of the peak delay taken from column B as one band edge, and by using the point of inflection as the center frequency. Thus the performance limits can be determined rapidly without either a computer or a detailed error analysis.

## Steps for using the tables

Using the table to design delay equalizers is a straightforward, step-by-step procedure.

Step 1. Specify the passband required. If a parabolic delay equalizer's center doesn't coincide with the center of the desired passband, use

Step. 3. Calculate a quantity, M, from the appropriate equation.

$$
\begin{array}{ll}
\text { Parabolic Delay: } & M=\frac{T \Delta f_{c}}{B^{2}} \\
\text { Slope Delay: } & M=\frac{T \Delta f_{c}}{B}
\end{array}
$$

$\mathrm{T} \Delta=$ differential delay (seconds)
$\mathrm{f}_{\mathrm{c}}=$ center frequency of bandwidth specified in Step $1(\mathrm{~Hz})$
$B=$ ratio of the bandwidth to $\mathrm{f}_{\mathrm{c}}$
For the example in Step 1

$$
\mathrm{M}=\frac{\left(4 \times 10^{-9}\right)\left(65 \times 10^{6}\right)}{\left(\frac{30}{65}\right)^{2}}=1.22
$$

Step 4. Look up the values of $d$ and $X$ in Table 1. With the value M , look up the normalized-frequency variable, X , in the column appropriate for the desired equalizer type and also obtain the value of the shape factor, $d$. The relationships between the shape factor, $d$, and the other shape factor, $\theta$, are explained later. In the previous example (Step 1), the closest value in the table to M is 1.212 , and

$$
\begin{aligned}
& \mathrm{d}=1.80 \\
& \mathrm{X}_{\mathrm{c}}=0.925 \\
& \Theta=68.12^{\circ} .
\end{aligned}
$$

Also, the columns labeled Positive slope and Negative slope provide the inflection points.

$$
\begin{aligned}
& \mathrm{X}_{\text {neg }}=1.142 \\
& \mathrm{X}_{\text {pos }}=0.715 .
\end{aligned}
$$

Note: The terms positive and negative slope refer to the slope of the equalizer's delay, not to the delay of the circuit to be corrected. Interpolation may be used to establish the values for d, X and $\theta$.

Step 5. Determine the normalized component values. Choose an equalizer circuit from Figs. 1 or 2 and calculate the normalized' component values using the value of d obtained in Step 4.

Step 6. Calculate the actual circuit values. Introduce the desired impedance and frequency into the relationships

$$
\begin{aligned}
& \mathrm{C}=\mathrm{C}_{\mathrm{n}} \frac{\mathrm{X}}{\mathrm{R}_{\mathrm{o}} \omega_{\mathrm{c}}} \\
& \mathrm{~L}=\mathrm{L}_{\mathrm{n}} \frac{\mathrm{X} R_{0}}{\omega_{\mathrm{c}}},
\end{aligned}
$$

where the subscript n identifies the normalized values (from Step 5) and
$\mathrm{R}_{\mathrm{o}}=$ terminating impedance (ohms)
$\omega_{\mathrm{c}}=$ center frequency of the equalized bandwidth (in radians)
$\mathrm{X}=\omega / \omega_{0}$, the normalized-frequency variable from the table in Step 4.
Note: Normalized-component circuits resonate at the normalized frequency, $\mathrm{X}=1$. However, the frequency of peak delay occurs at $\mathrm{X}=1$ only for $\theta=90^{\circ}$ (Fig. 3a).

Let's equalize a system having a negative-delay slope of 5 ns and a $60-$ to -80 MHz passband.

3. Normalized delay when plotted against normalized frequency, $X$, for different shape factors, $\theta$, provide a quick overview of an equalizer's characteristics (a). The tables for both parabolic and positive or negative sloping equalizers, because of approximations, include the errors plotted in Fig. 4. These errors increase with increasing bandwidth.

First calculate

$$
\mathrm{M}=\frac{\mathrm{T} \Delta \mathrm{f}_{\mathrm{c}}}{\mathrm{~B}}=\frac{\left(5 \times 10^{-9}\right)\left(70 \times 10^{6}\right)}{\left(\frac{20}{70}\right)}=1.225 .
$$

Because the system's delay has a negative slope-one that decreases delay as frequency in-creases-we must use a positive-slope equalizer. We find the nearest value, $\mathrm{M}=1.235$, in Table 1, where

$$
\begin{aligned}
& \mathrm{X}=0.77 \\
& \mathrm{\theta}=71.80^{\circ} \\
& \mathrm{d}=2.56 .
\end{aligned}
$$

If you select the circuit shown in Fig. 2a, calculation of the normalized component values yields

$$
\begin{aligned}
& \mathrm{C}_{\mathrm{na}}=\frac{\mathrm{d}-1}{2 \sqrt{\mathrm{~d}}}=\frac{2.56-1}{2 \sqrt{2.56}}=0.4875 \\
& \mathrm{C}_{\mathrm{nb}}=\frac{1}{\sqrt{\mathrm{~d}}}=\frac{1}{\sqrt{2.56}}=0.6250 \\
& \mathrm{~L}_{\mathrm{na}}=\frac{2}{\sqrt{\mathrm{~d}}}=\frac{2}{\sqrt{2.56}}=1.250
\end{aligned}
$$


4. Equalizer errors are plotted for parabolic (a), positiveslope (b) and negative-slope (c) equalizers.

$$
\mathrm{L}_{\mathrm{nb}}=\frac{\sqrt{\mathrm{d}}}{2}=\frac{\sqrt{2.56}}{2}=0.800
$$

For a terminating impedance of $75 \Omega$, the actual values are

$$
\begin{aligned}
& \mathrm{C}_{\mathrm{A}}=\frac{(0.4775)(0.770)}{(2 \pi)(75)\left(70 \times 10^{6}\right)}=11.38 \mathrm{pF} \\
& \mathrm{C}_{\mathrm{B}}=\frac{(0.6250)(0.770)}{(2 \pi)(75)\left(70 \times 10^{6}\right)}=14.59 \mathrm{pF} \\
& \mathrm{~L}_{\mathrm{A}}=\frac{(1.250)(75)(0.770)}{(2 \pi)\left(70 \times 10^{6}\right)}=0.164 \mu \mathrm{H} \\
& \mathrm{~L}_{\mathrm{B}}=\frac{(0.800)(75)(0.770)}{(2 \pi)\left(70 \times 10^{6}\right)}=0.105 \mu \mathrm{H}
\end{aligned}
$$

## Determining the errors

Because of approximations employed in deriving the tables, the design procedure introduces some errors (Figs. 3b, 3c and 3d). Figs. 4a, 4b and $4 c$ are plots of these errors. In the previous example, we would go to Fig. 4b for a positiveslope equalizer. With a $\theta 71.8$ and a B equal to 0.256 the error at the lower-band edge ( 60 MHz ) is

## Table 1．Normalized delay equalizer data

| A |  | B |  | C |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{array}{r} \text { POSTIV } \\ \text { EQUA } \end{array}$ | $\begin{aligned} & \text { SLOP } \\ & \text { ZERS } \end{aligned}$ | PAR ¿QU | IREFS | NECAT fou | $\begin{aligned} & \text { SLCPE } \\ & \text { IZEFS } \end{aligned}$ | SHAP |  |
| $\times$ | $N$ | $x$ | N | X | N | $\theta$ | d |
| .903 | －nys | ． 29.3 | － 60 | －S $\angle^{\text {a }}$ | －32） | 30.60 | ． 333 |
| ． 235 | ． 298 | ． 237 | － 200 | ． 555 | ． 344 | 31.87 | ． 347 |
| － 134 | －） 01 | － 328 | －$\lambda^{2}$ | ． 979 | ． 367 | 33.62 | ． 361 |
| ． 219 | －0uz | ． 393 | ${ }^{4}$ | 1．C0 | ． 390 | 35.26 | ． 375 |
| ． 247 | ． 014 | ． 445 | － 0 | $\therefore . C 19$ | .413 | 36.80 | ． 390 |
| ． 271 | ． $0: 27$ | ． 439 | － 13 | 1． $\mathrm{C}^{2} 5$ | ． 433 | 39.27 | ． 406 |
| ． 292 | ． 029 | ． 326 | ． 218 | 1．$: 49$ | －4う9 | 39.66 | ． 422 |
| ． 311 | －114 | ． 558 | －． 24 | 1． C 62 | ． $4 \times 3$ | 40.98 | ． 439 |
| ． 329 | － 7 ！ 7 | ． 387 | － 31 | 1．C73 | － 677 | 42.24 | ． 456 |
| ． 346 | － 027 | ．6：3 | － 39 | 1．C83 | － 531 | 43.46 | .474 |
| ． 362 | ． 029 | ． 636 | ． 48 | 1． 692 | － 556 | $44 . t 2$ | ． 493 |
| ． 377 | ． $03=$ | ． 657 | － 58 | 1.1510 | － 41 | 45.73 | ． 513 |
| － 292 | － 0142 | ． 677 | － 583 | 1.107 | － 657 | 46.81 | ． 534 |
| .425 | ． 044 | ． 695 | $\therefore 77$ | 1.114 | .634 | 47.84 | ． 555 |
| ． 42 | ． 057 | ． 711 | － 91 | 1.119 | －CS1 | 48.84 | ． 577 |
| ． 434 | ．065 | ． 727 | ． 25 | 1.124 | －689 | 49.81 | ． 600 |
| ． 447 | ． 075 | ． 741 | ． 119 | 1．129 | ． 718 | 5.3 .74 | ． 524 |
| ． 46 ？ | ． 085 | ． 754 | ． 134 | 1.133 | ． 748 | 51.65 | ． 649 |
| ． 473 | ． 296 | ． 708 | － 32 | ？．13h | ． 778 | 52.52 | .675 |
| ． 485 | ． $10^{\circ}$ | ． 778 | － 58 | 1.139 | － 009 | 53.37 | ． 702 |
| ． 497 | ． $12^{\circ}$ | ． 789 | .136 | 1.142 | ． 842 | 54.19 | .730 |
| －567 | .134 | ． 799 | ． 2.26 | 1.144 | ． 475 | 54.59 | ． 760 |
| ． 221 | ． $14^{\circ}$ | ． 3 ， 8 | ． 223 | 1.145 | ． 310 | 55.77 | ． 790 |
| ． 533 | － 104 | ． 317 | －251 | $\therefore .147$ | ． 945 | 56.52 | ． 822 |
| － 544 | ． $18{ }^{7}$ | ． 826 | ． 275 | 1.149 | － 682 | 57.25 | ． 854 |
| － 353 | － $9^{7}$ | ． 334 | －32： | 1.157 | $\therefore$－？ 0 | 57.57 | ． 889 |
| ． 566 | －2：4 | ． 342 | － 327 | ．． 151 | ． 59 | 58．66 | ． 924 |
| ． 376 | ． $23^{\circ}$ | ． 349 | ． 359 | 1．15！ | 1． 20 | 59.34 | ． 961 |
| ． 587 | ． 256 | ． 856 | －$=91$ | －．15？ | $\therefore 4$ ？ | 59.59 | 1.063 |
| ． 397 | ． 278 | ． 862 | ． 425 | 1.152 | ． 25 | 63.63 | 1.240 |
| ． 607 | － 30 \％ | ． 368 | ． 451 | ．．152 | －． 237 | 62.26 | 1.381 |
| ． 616 | ． 325 | ． 874 | .499 | 1．152 | $\therefore .876$ | 61.87 | 1.124 |
| ． 0 こと | ． 351 | ． 479 | － $4^{\circ}$ ） | 1．152 | －． 324 | 62.46 | $\therefore .169$ |
| ．035 | ． 379 | ． 385 | － 54 | －． 151 | $\therefore .373$ | 63.04 | 1.216 |
| ． 644 | ． 4.56 | ． 890 | － 532 | $1.15 i$ | 1．425 | 63.60 | 1.265 |
| .653 | ． 435 | ． 394 | － 681 | 1.150 | 1.473 | 64.15 | 1.315 |
| .661 | ． 407 | ． 399 | ． 733 | 1.149 | －．523 | 64.69 | 1.368 |
| ． 669 | ． 530 | ． 393 | ． 7 73 | 1.149 | －．59） | 65.22 | 1.423 |
| ． 677 | － $53=$ | ． 907 | －号う | 1.143 | －． 648 | 65.73 | 1．486 |
| ．685 | － 57 ？ | ． 911 | －9：3 | 1.147 | －．759 | 66.23 | 1.539 |
| ． 093 | －6）9 | ． 915 | －981 | i．145 | $\therefore .772$ | 66.72 | 1.6 CC |
| ． 703 | ． 649 | －919 | 1． 553 | 1.145 | $\therefore .838$ | 67.20 | 1.664 |
| ． 7,7 | ． 691 | ． 722 | 1.132 | 1.144 | －．9．6 | 67.66 | 1.731 |
| .715 | ． 735 | ． 925 | 1.122 | 1.142 | ． 2.976 | 63.12 | 1.800 |
| ． $72 i$ | ． 73 ？ | ． 723 | 2．299 | 1.141 | 2． 43 | 6 6． 57 | 1.872 |
| ． 723 | － 827 | ． 931 | 1.391 | 1.147 | 2．：？${ }^{\text {a }}$ | 69.50 | 1.947 |
| ． 735 | ． $88{ }^{\circ}$ | ． 934 | 1.493 | 1.139 |  | 69.43 | 2.325 |
| ． 74 ： | .933 | ． 937 | 1.394 | 1.137 | $\therefore .292$ | 67.85 | 2.106 |
| ． 747 | ． 938 | ． 939 | 1．7，5 | 1.136 | 2． 66 | 70.25 | 2.198 |
| ． 753 | $\therefore .046$ | ． 942 | 1．823 | 1.134 | 2.453 | 75.65 | 2.278 |
| ． 759 | $\therefore .126$ | ． 944 | 1.949 | 1．133 | 2．543 | 71．C4 | 2.369 |
| ． 765 | 1.159 | ． 946 | 2． 82 | ¢．131 | 2.636 | 71.42 | 2.464 |
| ． 775 | ． $23{ }^{\circ}$ | ． 949 | 2． 324 | 1．130 | 2.733 | 71.80 | 2.562 |
| ． 776 | 1.334 | ． 931 | 2． 275 | 1.129 | ．． 033 | 72.16 | 2.665 |
| ． 78. | －．377 | ． 953 | 2．535 | 1.127 | 2.937 | 72.52 | 2.771 |
| ． 786 | －． 452 | ． 755 | 2.705 | 1.125 | 3.144 | 72.87 | 2.882 |
| .791 | 1.531 | ． 956 | 2.986 | 1.124 | 3.150 | 73.21 | 2.997 |
| ． 796 | 1．5：3 | ． 958 | 3.78 | 1.122 | 3.272 | 73.55 | 3.117 |
| － 5.21 | －． 679 | ． 960 | 3．：82 | 1．122 | 3.392 | 73.88 | 3.242 |
| ． 805 | $\therefore .789$ | ． 761 | 3.499 | 1.119 | 3．5：6 | 74.20 | 3.372 |
| － 81 \％ | $\therefore .883$ | ． 963 | 3.729 | 1.119 | 3.546 | 74.51 | 3.507 |
| ． 814 | 1.98 － | ． 964 | 3.974 | 1.115 | 3.779 | 74.82 | 3.647 |
| ． 318 | 2.983 | ． 766 | 4．233 | 2.115 | 3.919 | 75.12 | 3.793 |
| ． 823 | 2．189 | ． 967 | 4.599 | ：． 113 | 4.363 | 75.42 | 3.944 |
| .827 | 2.301 | ． 969 | $4.8 ง 2$ | 1.111 | 4． 112 | 75.71 | 4.102 |
| ． 53 ： | 2.417 | ． 970 | 5.213 | 2．11． | 4.367 | 75.99 | 4.266 |
| ． 835 | 2.53 P | ． 971 | 5.443 | 1．10त | 4.528 | 76.27 | 4.437 |
| ． 838 | 2.605 | ． 972 | 5.794 | 1.107 | 4.695 | 76.54 | 4.614 |
| ． 64 ？ | $2.79^{7}$ | ． 973 | 6.166 | 1． 105 | 4.858 | 70.81 | 4.799 |
| ． 443 | 2.934 | ． 974 | 6.562 | 1.104 | $\therefore . .48$ | 77.67 | 4.991 |
| ． 849 | $3.07^{\circ}$ | ． 975 | 6.781 | 1．10？ | 5． 234 | 77.32 | 5.191 |
| ． 852 | 3.223 | ． 976 | 7.427 | 1.101 | 5.427 | 77.57 | 5.398 |
| －E56 | 3.334 | ． 977 | 7.902 | 1．C99 | 5.628 | 77.82 | 5.614 |

$6 \%$ and about $12 \%$ at the upper－band edge（ 80 MHz ）．This means that the original 5 －ns differ－ ential delay is corrected and flat in the band center，but there is a $0.3-\mathrm{ns}$ roll－up on the low end and a $0.6-\mathrm{ns}$ roll－down on the high end．

## Losses can be determined

Generalized dissipation－loss curves are plotted in Figs． 5 and 6．Fig． 6 is a plot of the equations of Table 2 for the cases where $Q \gg 1$ ．
postive stope


| DARABOLIC EQUALIZERS |  | NEGATIVE SLQPE EOHALILERS |  | SHAPING <br> FACTORS |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $x$ | ， | X | n | $\theta$ | d |
| 778 | 8.452 | 1．C98 | 2． 235 | 78． 56 | ． 335 |
| 79 | 8.534 | $\therefore .696$ | －． 53 | 78.89 | 6.372 |
| $98 \%$ | 9． $3>5$ | 1.695 | 6． 277 | 78.52 | 6.315 |
| 81 | 20．150 | 1.193 | 6.512 | 78.75 | 6.568 |
| 781 | i）． 738 | 1.892 | －． 751 | 7\％．97 | 6.936 |
| 82 | 11.414 | －．c91 | 7． 02 | 77.15 | 7.164 |
| 783 | 12．132 | ？．C69 | 7． 6 ？ | 79.40 | 7.388 |
| 83 | 12.894 | 1． 688 | 7．533 | 79.61 | 7.683 |
| 784 | 13.732 | 1．C83 | 7.413 | 79.81 | 7.951 |
| 785 | 14.360 | 1．CP5 | $8 .: 24$ | $8) . C 1$ | 8．31\％ |
| 935 | 15.471 | 2．C84 | $3 .+C 7$ | 80.21 | 9． 543 |
| 36 | 16.437 | 1．C9\％ | 9.721 | 89.40 | 8．988 |
| ． 986 | 17.453 | 1．C81 | 9． 46 | 8．）． 59 | S． 348 |
| 987 | 18．55： | 1．c8 ${ }^{\text {a }}$ | 9.335 | 8.7 .77 | 9.722 |
| 787 | 19.755 | 1．C．78 | 9.736 | 8．1． 55 | 20．111 |
| 938 | 20.931 | i． .177 | ：cu | 81.13 | 1 C .515 |
| 788 | 22．231 | $\therefore .675$ | ． 479 | 81.39 | ic． 936 |
| 39 | 23． 510 | 1．c75 | ¢ 72 | 81.47 | 11.373 |
| 739 | 25．74 | 1.073 | 25 | 81.64 | 11.828 |
| 990 | 26.627 | 1.072 | $7) 4$ | 8i．e0 | 12.301 |
| 93 | 23．：7j | 1．C7？ | 44 | 81．96 | 12.793 |
| 771 | 30．523 | 1.67 | 12 | 32.12 | 12.305 |
| 91 | 31.278 | 1．c69 | 75 | 82.28 | 1？．837 |
| 791 | 33.046 | 1.167 | 563 | 82.43 | 14.391 |
| 992 | 35.734 | 1．C60 | －32 | 82.57 | 24.766 |
| 992 | 32．449 | $\therefore$ С6， | 4.613 | 82.72 | 15．565 |
| 792 | 49.477 | 1． $\mathrm{C}_{64}$ | －5．265 | 82.86 | 16． 187 |
| 93 | 42．393 | 1．C63 | ． 737 | 83．0c | 15．335 |
| 793 | 45.638 | 1．C6？ | －0．335 | 83.14 | 17.508 |
| 93 | 48.445 | 1．C61 | 15．954 | 83.27 | 18． 209 |
| 793 | 51.422 | 1．ct． | ：7． 296 | 83． 40 | 18.937 |
| 794 | 54．इ80 | 1．C59 | 1．3． 264 | 83.53 | 19.695 |
| 794 | 57.531 | ：．C5 | ． 458 | 83.66 | 29.482 |
| ． 994 | 61.486 | 1．C57 | 17．678 | 83.78 | 21.3 C 2 |
| ．94 | 65.357 | 1．05t | 420 | $83.5 C$ | 22.154 |
| 95 | 69．こう7 | 1．655 |  | 84.02 | 23.040 |
| 5 | 73.300 | i．C54 | 22．1．1i | 84.14 | 23．961 |
| 95 | 78．： 52 | 1.653 | －．．erj3 | 84.25 | 24.920 |
| 95 | 82.777 | 1．CE2 | $\therefore 3.722$ | 84.36 | 25.917 |
| 5 | 87.243 | 1．C51 | －4．428 | 84.47 | 26．953 |
| ． 996 | 93．2：7 | 1．C55 | 25.563 | 84.58 | 28． 231 |
| 996 | 98.317 | 1.647 | 20.545 | 84.69 | 25.153 |
| 996 | ： 94.963 | 1．643 | － 61 | 84.79 | 36． 319 |
| 96 | －1．1．178 | 1．647 | 24.215 | 84.89 | 31.532 |
| 6 | $\because 8.181$ | 1.047 | ． 713 | 84.59 | 32.753 |
| 96 | 125．379 | 1．C46 | ． 51 | 85.69 | 34.105 |
| 6 | ：33．： 34 | $1.64=$ | 2． 35 | 85.18 | 35.469 |
| ． 397 | 141．：175 | 1.544 | 33.265 | 35．28 | 36．827 |
| ． 997 | ：49．787 | 1．$C 43$ | －4．54？ | 85.37 | 38.363 |
| 997 | 158．326 | 1．$(43$ | 3.2870 | 85.46 | 39．898 |
| 997 | 263．618 | 1．C4？ | 37.250 | 85.55 | 41.493 |
| 97 | ¢78．898 | $1.64 ?$ | 32.683 | 85.63 | 43.153 |
| ． 997 | 189.403 | $1.64{ }^{\circ}$ | 4．． 273 | 85.72 | 44.879 |
| 97 | 391.370 | 1.04 | $4: .771$ | 85.80 | 46.674 |
| ． 797 | 223．539 | 1.039 | 43.329 | 85.88 | 48．541 |
| 8 | 226.651 | $1 . C 38$ | 45． 31 | 95．56 | 50.483 |
| ． 998 | 249.455 | $1 . C 37$ | 46.733 | 86.14 | 52．5C2 |
| B | 255．597 | 1． 637 | 4．3． 344 | R6． 12 | 54.603 |
| ． 998 | 270．526 | 1．c30 | 5.420 | $86.2 n$ | 56.787 |
| 8 | 287．．99 | $1 . C 35$ | 52.375 | 86.27 | 55.058 |
| ． 998 | 304． 71 | 1.235 | 54.396 | 36.34 | 61.420 |
| 8 | 323.104 | 1．C34 | 56．5．j2 | 86.41 | 63.877 |
| －998 | 342.160 | 1.034 | 53.590 | P6． 48 | 66.432 |
|  | 363.609 | 1．c33 | $6 . .965$ | 80.55 | 69．190 |
| ． 998 | 385.725 | 1.032 | 63． 229 | 86．6？ | 71.853 |
| 8 | $499 .: 78$ | 1.632 | 63.736 | 85.68 | 74.727 |
| ． 998 | 434.259 | 1.031 | 63.343 | 85.75 | 77.716 |
| 98 | 460.447 | 1.031 | $7 . .995$ | 86.81 | 80.325 |
| － 979 | 488.437 | －． 63 | ？ 3.753 | 36.87 | 84． 258 |
| 9 | 518.120 | ！．$C 29$ | 75.822 | 86.93 | 87.420 |
| 9 | 549.508 | ：．$¢ .29$ | 79.6 J？ | 86.99 | 90.917 |
| 9 | 583．：35 | 1.628 | 82.698 | 87.65 | 94． 554 |

The normalized curves in Fig． 5 are applicable to cases where the capacitor $Q$ is much larger than the inductor $Q$ ．Although the plot is exact only for $Q_{\mathrm{C}}=\infty$ and $Q_{L}=100$ ，errors are very small where $Q_{\mathrm{c}} \gg \mathrm{Q}_{\mathrm{L}}$ ，which is almost always the case．
To illustrate the use of the loss curves，we con－ tinue with the previously discussed example： Note that the equalization network has a $\theta$ of $71.8^{\circ}(\operatorname{Cos} \theta=0.312)$ and a normalized center frequency， $\mathrm{X}=0.77$（actual center frequency is 70 MHz ）．We obtain from Fig． 5 a normalized

Table 2. Equations for dissipative losses

|  | Loss in inductors only $\left(Q_{C}=\infty\right)$ <br> Loss in capacitors only $\left(Q_{1}=\infty\right)$ | Loss in both inductors and capacitors |  |
| :---: | :---: | :---: | :---: |
|  |  | $\mathrm{Q}_{\text {L }}=\mathrm{Q}_{\mathrm{C}}$ | $\mathrm{Q}_{\mathrm{L}} \neq \mathrm{Q}_{\text {c }}$ |
| Solution at $\omega=\omega_{\omega}$ if $Q \gg 1$ | $=20 \log \left[\frac{\operatorname{Cos} \theta-\frac{1}{2 Q}}{\operatorname{Cos} \theta+\frac{1}{2 Q}}\right]$ | $=20 \log \left[\frac{\operatorname{Cos} \theta-\frac{1}{Q}}{\operatorname{Cos} \theta+\frac{1}{Q}}\right]$ | $\simeq 20 \log \left[\frac{\operatorname{Cos} \theta-\frac{1}{\mathrm{Q}_{\mathrm{L}^{\prime}}}-\frac{1}{\mathrm{Q}_{\mathrm{c}}}}{\operatorname{Cos} \theta+\frac{1}{\mathrm{Q}_{\mathrm{L}}}+\frac{1}{\mathrm{Q}_{\mathrm{c}}}}\right]$ |
| Exact solution at $\omega=\omega_{0}$ | $=10 \log \left[\frac{\mathrm{Q}^{4}+\mathrm{Q}^{2}\left(1-\frac{1}{2 \operatorname{Cos}^{2} \theta}\right)+\frac{1}{16 \operatorname{Cos}^{4} \theta}}{\left(\mathrm{Q}^{2}+\frac{\mathrm{Q}}{\operatorname{Cos} \theta}+\frac{1}{4 \operatorname{Cos}^{2} \theta}\right)\left(\mathrm{Q}^{2}+\frac{\mathrm{Q}}{\operatorname{Cos} \theta}+\frac{1}{4 \operatorname{Cos}^{2} \theta}+1\right)}\right]$ | $=10 \log \left[\frac{Q^{2}-\frac{2 Q+1}{\cos \theta}+\frac{4+\frac{1}{\mathrm{Q}^{2}}}{4 \cos ^{2} \theta}}{1+1} \mathrm{Q}^{2}+\frac{2 \mathrm{Q}+1}{\cos \theta}+\frac{1+\frac{\mathrm{Q}^{2}}{}}{4 \cos ^{2} \theta}+1\right]$ | Unwieldy |
| General solution | $=10 \log \left[\frac{A^{2}+B^{2}}{C^{2}+D^{2}}\right]$ <br> where: $\begin{aligned} & \mathrm{A}=\mathrm{X}^{4}\left(\varepsilon^{2}-\delta^{2}\right)-2 \mathrm{X}^{2} \varepsilon(1-2 \operatorname{Cos} \\ & \mathrm{B}=2 \mathrm{X}^{+} \varepsilon \delta-2 \mathrm{X}^{2} \delta\left(1-2 \operatorname{Cos}^{2} \theta\right) \\ & \mathrm{C}=\mathrm{X}^{+}\left(\varepsilon^{2}-\delta^{2}\right)-2 \mathrm{X}^{3} \delta(2+\varepsilon) \operatorname{Cos} \\ & \mathrm{D}=2 \mathrm{X}^{1} \varepsilon \delta+2 \mathrm{X}^{3}\left(2 \varepsilon-\delta^{2}\right) \cos \theta- \\ & \mathrm{X}=\frac{\omega}{\omega_{0}} \\ & \varepsilon=1-\frac{1}{\mathrm{Q}_{\mathrm{L}} \mathrm{Q}_{\mathrm{O}}} \\ & \delta=\frac{1}{\mathrm{Q}_{\mathrm{L}}}+\frac{1}{\mathrm{Q}_{\mathrm{O}}} \end{aligned}$ | $\theta)+1$ $\begin{aligned} & -2 \mathrm{X}^{2} \varepsilon\left(1+2 \operatorname{Cos}^{2} \theta\right)+2 \mathrm{X} \delta \operatorname{Cos} \theta+1 \\ & 2 \delta \mathrm{X}^{2}\left(1+2 \operatorname{Cos}^{2} \theta\right)-4 \mathrm{X} \operatorname{Cos} \theta \end{aligned}$ <br> $Q_{L}=\frac{\omega L}{R}$ (Series L-R) <br> Qi. $=\omega$ RC (Parallel R.C) |  |


5. Normalized insertion loss is plotted as a function of $\omega / \omega_{0}$ for an inductor Q of 100 (capacitor $\mathrm{Q}=\infty$ ). However, a change of Q from 10 to 1000 has little effect.
loss at 80 MHz of about 0.83 , since the normalized high-end frequency is

$$
\frac{80 \times 10^{6}}{70 \times 10^{6}} \times 0.77=0.88
$$

At the low end the normalized loss is about 0.34 , since the low-end normalized frequency is

$$
\frac{60 \times 10^{6}}{70 \times 10^{6}} \times 0.77=0.66
$$

To obtain the losses in dB use Fig. 6. For a Q of, say, 100 , the denormalization factor is found to be 0.28 . This makes the losses
$0.28 \times 0.83=0.23-\mathrm{dB}$ loss at 80 MHz, and $0.28 \times 0.34=0.095-\mathrm{dB}$ at 60 MHz .

## Tables are derived from prior work

The procedure to transform the general circuit equations for Figs. 1 and 2, so that Table 1 could be compiled, was published by Skwirzynski and Dunlop in 1964. ${ }^{2}$ In compiling the table, approximations were required. The region near the peakdelay frequency is assumed to be parabolic, and the region near the inflection points is assumed to be linear (Figs. 3b, 3c and 3d).

Errors that result from these approximations increase with increasing bandwidth. However, for the value of $\theta$ and for bandwidths normally encountered, the error is not usually a serious problem. Skwirzynski and Dunlop also treated these errors in detail. Modified curves are presented graphically in Fig. 4.
Where $\theta$ is related to $d$ by
$\mathrm{d}=1 / 4 \cos ^{2} \theta$
$\omega_{\mathrm{o}}=$ resonant frequency of the network
$\mathrm{X}=\omega / \omega_{0}$ (normalized frequency variable) The absolute delay of the all-pass delay-correcting networks is

$$
T=\frac{\frac{4 \cos \theta}{\omega_{0}}\left(\mathrm{X}^{2}+1\right)}{\mathrm{X}^{4}-2 \mathrm{X}^{2}\left(1-2 \cos ^{2} \theta\right)+1}
$$


6. This set of curves is used to denormalize the equalizer loss. If inductor and capacitor Qs are equal, use the dB values in the parentheses; when the Qs are very different (the usual case) use the first values listed. Note that infinite loss can occur for some values of Q and $\theta$.
and the slope of the delay curve is

$$
S=\frac{d T}{d X}=\frac{2 X\left[\left(3-4 \cos ^{2} \theta\right)-2 X^{2}-X^{4}\right]}{\left[X^{4}-2\left(1-2 \cos ^{2} \theta\right) X^{2}+1\right]^{2}}
$$

The infiection points are the real positive roots of the equation

$$
3 X^{8}+4\left(3-\cos ^{2} \theta\right) X^{6}-6\left(5-6 \cos ^{2} \theta\right) X^{4}
$$

$+12\left(1-5 \cos ^{2} \theta+4 \cos ^{4} \theta\right) X^{2}+\left(3-4 \cos ^{2} \theta\right)=0$. And the peak-delay frequency is

$$
\omega_{\mathrm{P}}=\omega_{0} \sqrt{2 \sin ^{2} \theta-1}
$$

The problem of response deviation due to dissipative losses in circuit elements, has been treated by several authors, ${ }^{3-6}$ but usually only at the resonant frequency, and often only for special conditions.

Table 2 lists the applicable equations for loss calculations. Usually, losses result from the inductor Qs; capacitor loss is insignificant. For Qs much larger than 1 , the usual case, the simplified forms in row 1 of Table 2 may be used.

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VOLUME PRODUCTION: Miller-Stephenson - Our principal raw materials come direct from Du Pont tankers into our 5500 gallon storage tanks through a closed system direct to container.
Other Aerosol Cleaners - Low volume suppliers often load from open 55 -gallon drums thereby introducing possibility of contamination.
CONTAINER:
Miller-Stephenson - Our new seamless cans further reduce the possibility of contamination.
Other Aerosol Cleaners - Cans with soldered seams may introduce residual contaminants. SAFETY IN SHIPPING:
Miller-Stephenson - Most of our "Freon" aerosol solvents are non-regulated items, exempt from all Federal Regulations "Restricted Articles". May be Shipped Air Transport.
Other Aerosol Cleaners - Do not meet Air Transport Regulations.

MS aerosol solvents have the lowest residual contamination in the industry - some approaching 5-7 ppm. The general range for the industry is $50-130 \mathrm{ppm}$.
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# 90-degree phase-difference networks are simply designed with a program in Basic. Only a few parameters need to be known to start; the rest are calculated. 

A simple calculator program can overcome one of the major problems in designing 90-degree phase-difference networks. Because the calculator rapidly performs many iterations of the problem, you can slash the time required for trial and error calculations, if some parameters are unknown.

These networks are used in single-sideband modulators, frequency-shift keying systems, and display generators of CRTs. The networks split a sinusoidal voltage, usually in the audio-frequency range, into two outputs with a nearly constant phase difference; in this article, 90 degrees.

The major stumbling block is calculating the network zero/pole frequencies from a given set of performance specifications (Figs. 1 and 2).

## Older solutions proved cumbersome

The mathematical solution used here requires solving elliptic functions that have no closedform. That is, results cannot be obtained by substitution of variables, such as center frequency and number of poles, into a finite set of equations.

Published solutions have taken the form of tables and graphs, approximations, and, for special cases, exact solutions. Tabular and graphic solutions ${ }^{1,2}$ tend to be inexact, and often require awkward and time-consuming interpolation.

Some of the input parameters that are needed for the design of these networks: the total number of poles (N); the ratio of the low to high frequency points, called range ( R ) ; the center frequency (FO), and the phase-difference tolerance over the frequency band (TOL). Table 1 shows the complete list.

The closed-form approximation routine for one design of these networks applies only where the required number of poles equals an integer power of two. ${ }^{5,6}$ Another technique works well for small

[^8]values of $N$ and $R$, but diverges widely for larger values. ${ }^{4}$

The program given here works for any number of poles from one to 30 . I've called it Glory-poles-for "Glory-osky Zero, I've got the pole frequencies!" The name is appropriate, because the pole frequencies are needed in the design equations of any network to get the circuit values ${ }^{9}$ (Fig. 5).


1. Phase-difference networks are constructed from two separate networks. The relative phase difference of the outputs will be 90 degrees over a given frequency band. The output-voltage difference is ignored.

2. For a $\mathbf{9 0}$ degree phase-difference network having three or four zero/pole pairs, the phase difference $(\Delta \theta)$ oscillates around the designed phase difference with a total amplitude of twice the tolerance.

While Glorypoles is written HP Basic, you can use the program listing to modify it for other calculators. It may be necessary to change some of the logic statements for use on other computers. For instance, in line 100, "If A and B" means "If $(A \neq 0)$ and $(B \neq 0)$ " in another language.
Table 1, together with Fig. 2, defines the input variables and symbols. The program solves for all possible relations between the first seven listed variables, both forward and inverse.

This forward-backward mode of solution is useful as a preliminary design aid when nothing
the input " 0 " in a branching test since no variable ever has an actual value of zero.

All variables are entered via one or two INPUT statements (Fig. 4 and Table 2). The three variables, F1, F2 and N, are sufficient for a complete design. The remaining variables are then calculated and printed out. If one or more of F1, F 2 or N is entered as a " 0 ," it is taken as an unknown, and branching occurs-to an auxiliary input-statement (lines 110 and 820).

The program now needs four (or two) more inputs; each is either specified, or entered as a

Table 1. Variable symbols and definitions used in Glorypoles program

| Variable Symbols |  | Input Priority | Definition | Units | Equals |
| :---: | :---: | :---: | :---: | :---: | :---: |
| External | Internal |  |  |  |  |
| F1 | $A^{\ddagger}$ | 1 | Low freq. limit | Hz |  |
| F2 | $B^{\ddagger}$ | 2 | Hi freq. limit | Hz |  |
| N | N | 3 | Combined poles | - |  |
| R | R | 4 | Range | - | F2/F1 |
| FO | F | 5 | Center freq. | Hz | $(\mathrm{F} 1 \cdot \mathrm{~F} 2)^{1 / 2}$ |
| TOL | T | 6 | Phase tolerance | Deg. |  |
| DB | D | 7 | Sideband supression | DB | (Fig. 3) |
| - | $P(J)$ | - | Normalized poles | - | ( $\mathrm{J}=1$ to N ) |
| - | W(J) | - | Scaled poles | Hz | FO P P(J) |

\# Do not confuse the internal program variable $A$ and $B$ with the $A$ and $B$ network labels of Fig. 1 .
is known in advance concerning expected performance or the required degree of network complexity.

## See how the program runs

First, Glorypoles calculates a satisfactory set of parameters from the first set of inputs. Then, the program calculates and prints A and B-network pole frequencies.

The program accepts all 128 possible combinations of the seven variables F1, F2, N, R, FO, TOL and dB as inputs, known or unknown. Computational priority is assigned in this order. If you try to specify inconsistent or unnecessary values, those values will be ignored, and consistent values will be calculated and printed out as replacements. A variable is specified by inputting its value. If it is unknown, a " 0 " (zero) must be entered in its place. The program uses
" 0 ." Glorypoles calculates and prints out the remaining unknowns, using the combined data from both input statements. Program-error messages are "INSUFFICIENT INPUT DATA," "OUT OF RANGE" (lines 200 and 600) or "IMPROPER FREQ DATA" (line 540) after which the program restarts to input statement 1.

After these preliminary calculations, the user is presented with a five-way branch $\mathrm{DO}=$ ? (line 1100), which calls for the input of one of the following digits (or $S$ for stop).

1 - complete restart at F1, F2, N
2 - partial restart at R, F0, TOL, DB
3 - continue, print scaled poles in Hz.
4 - continue, print normalized poles
5 - continue, print both $3 \& 4$.
The program yields normalized poles in addition to those scaled in frequency for the specific application. Poles are first calculated in normalized form (lines 1160 through 1300) and then

3. Single-sideband modulation of a carrier frequency, $f_{\mathrm{e}}$, uses a 90 -degree phase difference network of Fig. 1 to split the modulating input for two balanced modulators.

4. A simplified flow chart for the Glorypoles program shows how input data are entered and how the unknowns are calculated.

5. Circuits can be designed directly from the program outputs once zero/pole pairs are known.
multiplied by F0 (line 1290) to yield the scaled values.

When the calculator prints the normalized values, the column headed by " $\mathrm{P}(1) \times \mathrm{P}(\mathrm{N})$ " is also printed. Mathematically, normalized poles $P(J)$ are reciprocally related such that $P(1) \times$ $\mathrm{P}(\mathrm{N})=1, \mathrm{P}(2) \times \mathrm{P}(\mathrm{N}-1)=1$, etc. For odd numbers of poles the middle pole, PC, will equal one. All poles are actually computed independently, and not by reciprocals, so that their paired product, which should be unity, gives a running check on computer accuracy.

Using Glorypoles, the A and B networks can be designed directly from the bandwidth and sideband-suppression specification, in dB , for an SSB transmitter/receiver (Fig. 3).

Sideband rejection in dB is given by: Minimum rejection $=$

$$
20 \log _{10}\left[\tan \left(\frac{\pi \times \mathrm{TOL}}{360}\right)\right], \mathrm{dB} .
$$

Table 3 shows printout for the following runs:

1. For the case of $\mathrm{F} 1-250, \mathrm{~F} 2=2500$, and $\mathrm{N}=4$ (Fig.5). ${ }^{8}$
2. For the trivial (but useful) case, $\mathrm{N}=1$. Here the B network becomes unity and $\mathrm{E}_{\mathrm{b}}=\mathrm{E}_{\mathrm{in}}$.
3. For the case of $\mathrm{N}=5$ ( $=$ odd). The A network has three poles, and the B network has two. The starting frequency, F1, is specified and the other frequencies are calculated from N and TOL.
4. For the case of a $100-\mathrm{Hz}$ to $10-\mathrm{kHz}$ network having a specified 40 dB of SSB suppression. Since the program quantizes the calculated value of N to the next higher integer value $(\mathrm{N}=7)$, the resulting final value of -44 dB is more than the specified 40 dB .
5. The same as No. 4, except that N and DB

## Table 2．Glorypoles program listing

```
DN: 2t 3n],p[3x],N[3x]
PRIUT
PRIVT "AT DC=?: 1,2-PEPEAT, 3-PRINT HZ, 4-VORMALIZED, 5-BOTH"
PRIUT
PRINT
LET T=R=T=D=N=CI=a
MNN=R=T=D=NN=C1=a
MRINT M-IM
LNPUT A,B,N
LE!. V= INT(ABS(N)+.5)
PNIणT "2-INPUT: R, FO, TOL, DB";
INPUT P,F,T,D
LET C1=1
LET D=-ABS(D)
IF T THEV 200
IF N1 THFN 85%
PRIYT "INSUFFICIENT INPUT DATA"
GOTO 50
IF P>1 AND R<1.GOQO1E+09 THEN 243
PRINT "OUTT OF RANSE"
DEINT
gOTO 119
IF A THE: 28%
IF 3 THEV 318
IF F THEN 34a
GOTO 639
LET D=7*A
アロI\T "F2=";B;
FOTC 550
LET n= B/N
PNIUT "Fl=";A;
6070 580
LFT A=F/SOR(R)
วคIMT "%1=";A;"F2=";B;
GOTO 630
IF A THE: 413
IF B THEM
IF A<F TIIEV 430
OOTO 540
LET B=F,?/A
LET R=E/A
PRIMT "M='";
GOTC 699
IF D>F THE:\ 500
SOT0 540
LET A=F+ 2/3
SNIMT "F1=";n;
30%6 45%
IF A<B THEN 560
PNINT "IMPROPET FRFR EATA"
#CTO 50
LET R=B/A
PRIMT "?=ツ;口;
LET F=SOR(A*B)
PNINT "FO=";F;
PRI:IT "OUT OF PANGE"
GCTO 40
LET K1=1/P
LET Kム= K1'2
IF K1>.7 THEN }74
LET v=a
FO? J=12 TO 1 STEP - - 
LET Y=(J-1.25)*M4*(I+Y)/J
VEYT J
LET L=-v/(4+2*V)
IF L>1.aganaze-af THEv 7ea
LET 91=L
30T0 78方
LET K:2= (1-K4), (1/4)
LET L=.5*(1-%?)/(1+1%2
LE- M= L.4
LET O1=L*(1+M*(2+15*1))
LET O1=L*(1+M*(2+15**))
LET O=EXP(O.8696תOS(01))
MFIN
IF N THEN 1078
IF C1 THEI 850
IMPITT T,D
LET D=-ABS(D)
IF T THE: 88%
IF D THEV 1928
ICTO 180
IF THEN! 1240
LET n=(T/229.183)+(1/N)
LET Y З= Y4=.5
LET C=g
FCR J=1 T0 2g
LET C=C+2*J-1
LET F=?+C
LFT }\because3=v3+
LET Y4 = V 4+E*(-1) \J
IF E<1.00902F-\7*\vee4 THEI 990
NEXT J
LET R= (Y3/Y4) 12
?1:1T "?=";?
GOTO 240
LET T=114.592*ATV(EYO(D/8.\epsilonB580))
GOTC 880
LET N1=LOG(0.1)*LOG(T/229.183)/9.8698
LET N=INT(N1+.099999)
P\OmegaINT "N=";N;"(";N1;")";
LET T=229.183*2 +N
```


## 1088 <br> 1908

100 PRINT $" T O L=" ; T ; " D B=\cdots$ ；；
100 PRINT＂$\quad$ DC＝$=$＂
1110 IIMPUT
1123
1130
1130 IF $\gamma=1$ THEV $\in O$
1143 IF $Y=2$ AND C1 THEN 118

1150 IF $Y=2$ AND C1
1160 LET $s=1.5708$
1160 LET $S=1.5798$
1179 FOR $J=1.00$
1179 FOR $J=1$ TC
$1189 \quad$ LET $\mathrm{U}=\mathrm{t}=\mathrm{g}$
1180 LET $U=\mathrm{t}=\mathrm{g}$
190 LET $Z[J]=(-1) *(J-1) * 3 \cdot 14159 *(2 * J-1) /(4 * リ)$
12 120 FOR K＝TO $2 \emptyset$
1210 LET $\quad \mathrm{O}=(2+(K+1))+K$
1220 IF $\mathrm{K}<1$. Øøøø日E－2の THEN 1260
1230 LET $\mathrm{U}=\mathrm{U}+\cdots * \cos ((2 * K+1) * 2(J 1)$
1230
1240
1240
1250
1258
1260
127 LET $\mathrm{P}=\mathrm{S}+2 *=(-1)+\mathrm{N} * \mathrm{U}$ N N
$127 \mathrm{LET} S=S+2 * A T N(S Q R(K 1) / P[J])$
1280 LET P［J］＝ABS（P［J］）
1290 LET $\quad$ U［J］＝F＊P［J］
1306 NEXT J
1310 LET $\mathrm{T}=57.2958 * \mathrm{~S}$
1320 LET $D=8.68589 * \operatorname{LOG}($ TAN $(5 / 2))$
1330 PRINT
1348 IF $F=\varnothing$ OR $X=4$ THEN $137 \varnothing$
1358 PRINT＂A－NETHORK－HZ＂，＂B－NETMORK－HZ＂，
1369 IF $\mathrm{K}=3$ THD： 1390
1370 PRINT＂A＝NCRMALIZED＂，＂B＝NORMALIZED＂，＂P（1）＊P（V）＂
1380 GOTO 1430
1398 PRINT
1409 PRINT
1410 FOR $J=1$ TO N STEP 2
1420 IF $F=\sigma$ OR $X=4$ THEN 1480
1430 IF $J=N$ THEN 1460
1440 PRINT U［J］，v［J＋1］，
1450 GOTO 1478
1458 GOTO 1478
1460 PRINT V［JJ，＂＂，
1460 PRINT＂［JJ，＂＂＂，
1470 IF $X=3$ THEN 1530
$\begin{array}{llll}1470 & \text { IF } & X=3 & \text { THEN } \\ 1480 & \text { IF } & J=N \\ 1400 & \text { THEN } & 1510\end{array}$
1480 IF J＝N THEN 1510
1490 PRINT P［J］，P［J＋1］，P［（J＋1）／2］＊P［N＋1－（J＋1）／2］
1500 GOTO 1540
1510 PRINT P［J］
1520 GOTO 1540
1530 PRINT
1540 NEKT J
1550 PRINT
1560 PRINT
1570 PRINT
1570 PRINT
1580 PRINT
$\begin{array}{ll}1590 & \text { PRINT } \\ 1690 & \text { GOTO }\end{array}$
1690 GOTO
1610 END

| Line | Value |  | Formula |
| ---: | :---: | :--- | :--- |
| 780 | 9.8696 | $(\pi)^{2}$ |  |
| 890 | 229.183 | $720 / \pi$ |  |
| 1020 | 114.592 | $360 / \pi$ |  |
| 1020 | 8.68589 | $20 / \ln (10)$ |  |
| 1160 | 1.5708 | $\pi / 2$ |  |
| 1190 | 3.14159 | $\pi$ |  |
| 1220 | $1 \mathrm{E}-20$ | （Arbitrary） | Prevents Underflow |
| 1310 | 57.2958 | $180 / \pi$ |  |

are specified，and $R$ is calculated．
6．The same as Reference．${ }^{9}$ Note the slight dif－ ference in pole frequencies and TOL values．

7．For the maximum allowable $\mathrm{N}=30$ and $R=1 \mathrm{E} 9$ ．Note that $\mathrm{P}(1) \times \mathrm{P}(\mathrm{N})$ is starting to diverge，and the final TOL calculation is off by 0.38 degrees．Since the TOL is calculated as the sum and difference of 30 arctan calculations， an error is generated．The program is accurate enough for most practical problems．

## ＇$Q$＇means something else

Program＇Q＇（line 790）is the elliptic function ＂nome＂＂and has nothing to do with the usual circuit quality factor，Q．It is calculated for $\mathrm{K} 1>$

Table 3. Problems run on Glorypoles

0.7 (lines 740-780), or approximated by Horner's method (lines 660-730) for $\mathrm{K} 1 \leq 0.7$.

The two methods give the same value of Q to 10 places at the branching value of $\mathrm{K} 1=0.7$ (line 650). Q really serves no useful purpose as output; it remains solely for program analysis.

Normalized poles were left in as an optional output because they can still be calculated where there is not enough input data to frequency scale them-for example, when inputting N and R only. The program outputs two values each for TOL and dB . The first is calculated from a theoretical value,

$$
\mathrm{TOL}=720 \mathrm{Q}^{\mathrm{N}} / \pi \text { degrees (line 1070) }
$$

The second TOL output is obtained by summing the individual-pole phase shifts at F1 over the entire network after all the pole frequencies are calculated ${ }^{6}$. This provides another check on computer accuracy because these two values should agree. The theoretical, non-integer value of N is output (when N is not specified) in parentheses to show what the nearest integer value actually is. The program assumes the next higher integer value for N and continues calculations.

There are some "idiot" lines ( $90,410,480$, 530) that check for improper input format. The entire sequence from lines 80 to 620 is computationally simple and could easily be done on a hand calculator, but at a loss of convenience.

The pole-calculating algorithm at 1160 through 1300 stores the poles in arrays $\mathrm{P}(\mathrm{J})$ and $\mathrm{W}(\mathrm{J})$. The inside K-loop ( $1200-1250$ ) uses a maximum of 20 terms of lines 1230 and 1240 to calculate each pole, except that the program usually jumps from line 1220 to 1260 much sooner.

Program length could be reduced by using a higher level language and leaving out the frills, but the routine runs very nicely with 8 k of core in HP minicomputers.

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# Use transconductance amplifiers to make programmable active filters. The OTAs offer a 10-octave tuning range by using predictable $\mathrm{V}_{\mathrm{BE}}$-versus-IC relationships. 

Operational transconductance amplifiers (OTAs) can provide control of many active filter parameters over a $60-\mathrm{dB}$ dynamic range. Ranges this wide are possible because the controlling signals are currents and not voltages. And currents can represent filter variables-such as corner frequency, center frequency, Q or bandwidthbetter than a voltage, since the relationship between the collector current and the base-to-emitter voltage drop of a silicon transistor is predictable over extremely wide ranges.

Over the last few years state-variable and biquad active filter circuits have been adopted by many companies as universal filter building blocks. Analog multipliers scale the impedance elements of these filters, thereby achieving parameter control. ${ }^{1}$ Commercially available multipliers, however, are limited when it comes to voltage range, temperature drift and linearity. Because of these drawbacks, programmable filters based on analog voltage multipliers are generally restricted to operation over a 3 -to-6-octave range. But by exploiting the transistor's $\mathrm{V}_{\mathrm{BE}}$ vs $\mathrm{I}_{\mathrm{C}}$ relationship in an OTA, you can use current signals to provide filter control over many decades.
The operational transconductance amplifier consists of a matched pair of npn transistors, connected as a variable transconductance multiplier, and a matched pair of pnp transistors, connected as a current mirror (Fig. 1). The smallsignal relationship between the collector difference currents $i_{1}-i_{2}$ and the base-emitter difference voltages $\mathrm{v}_{1}-\mathrm{v}_{2}$ can be expressed by

$$
i_{1}-i_{2}=\left(i_{\mathrm{E}} / 2 \mathrm{~V}_{\mathrm{T}}\right)\left(\mathrm{v}_{1}-\mathrm{v}_{2}\right),
$$

in which $\mathrm{V}_{\mathrm{T}}$ is the thermal voltage and is typically 26 mV at room temperature, and $\mathrm{i}_{\mathrm{E}}$ is the combined emitter current.

Due to the current-mirror action of the pnp pair, the collector current of $Q_{4}$ equals $i_{1}$, the collector current of $\mathrm{Q}_{1}$. The difference current, $i_{1}-i_{2}$-shown as $i_{0}$-is proportional to the product of the emitter current and the differential voltage applied to the npn pair. This product

[^9]

1. The basic operational transconductance amplifier uses two pnp transistors and two npn's to provide a linear conversion of voltage to current.
holds up over a very wide range of common-emitter current values-a property that makes the OTA very powerful in programmable filter synthesis and wide-range gain control. ${ }^{2,3}$ Let's see how this can be put to use by examining an inte-grator-the basic building block of active filters.

## Programmable integrators from OTAs

A programmable integrator can be built with an op amp and a transistor array (Fig. 2). To ensure the validity of a small-signal approximation for $Q_{1}$ and $Q_{2}$, the differential input signal is scaled down with resistors $R_{1}$ and $R_{2}$. For the values given in Fig. 2, the difference voltage becomes

$$
\begin{aligned}
\mathrm{v}_{1}-\mathrm{v}_{2} & =\left(\mathrm{V}^{-}-\mathrm{V}^{+}\right) \mathrm{R}_{2} /\left(\mathrm{R}_{1}+\mathrm{R}_{2}\right) \\
& =3.2 \times 10^{-3}\left(\mathrm{~V}^{-}-\mathrm{V}^{+}\right)
\end{aligned}
$$

Thus an input signal of $\pm 5 \mathrm{~V}, \mathrm{pk}-\mathrm{pk}$, is attenuated into a $\pm 16-\mathrm{mV}$ swing at the bases of $\mathrm{Q}_{1}$ and $\mathrm{Q}_{2}$. Transistor $\mathrm{Q}_{5}$, connected in diode form, provides emitter bias for the current-mirror transistors, while the $100-\mathrm{k} \Omega$ potentiometer compensates for the over-all voltage offset of the OTA. The pot should be adjusted so that the difference current is zero for $\mathrm{V}^{-}=\mathrm{V}^{+}=0$.

The transfer function of the circuit can easily be found if you use Laplace transforms:

$$
\mathrm{V}_{\mathrm{o}}=-\mathrm{I}_{\mathrm{o}} / \mathrm{sC}=\left(\mathrm{i}_{\mathrm{E}} / 2 \mathrm{~V}_{\mathrm{T}}\right)\left(\mathrm{v}_{2}-\mathrm{v}_{1}\right) / \mathrm{sC},
$$

which can be simplified to

$$
\mathrm{V}_{\mathrm{o}}=\left(\mathrm{V}^{+}-\mathrm{V}^{-}\right) / \mathrm{s} \tau
$$

where

$$
\tau=\left[2 \mathrm{~V}_{\mathrm{T}} \mathrm{C}\left(\mathrm{R}_{1}+\mathrm{R}_{2}\right) / \mathrm{R}_{2}\right] / \mathrm{i}_{\mathrm{E}}
$$

These equations show that the circuit integrates the differential input signal and that the constant of integration, $\tau$, is inversely proportional to $i_{E}$. The constant, $\tau$, can be programmed externally if $i_{\mathrm{E}}$ is controlled with a variable current generator. When this is all put together, you have a programmable differential integrator.

2. To build a programmable integrator, the OTA can be combined with an op amp and some resistors to form the basic building block of an active filter.

Of course, if one input is grounded, the circuit is an inverting or noninverting integrator. To minimize any error introduced by the op amp at low current levels, an op amp with very low input bias current, such as one with a FET input or super-beta transistor input, should be used.

## Combined integrators make a filter

Two integrators can be combined to make a programmable filter that exhibits simultaneous second-order, low-pass and bandpass responses (Fig. 3). To build a circuit using this block diagram, you also need a dual-output current generator to control both integrators. The actual circuit is shown in Fig. 4 (offset-adjustment pots have not been included, for the sake of simplicity). Now let's see how it works.

If we start with the block diagram of Fig. 3 and use Laplace transforms to find the bandpass and low-pass outputs, we get:

$$
\begin{aligned}
& \mathrm{V}_{\mathrm{BP}}=\left(\mathrm{V}_{\mathrm{i}}-\mathrm{V}_{\mathrm{LP}}-\mathrm{V}_{\mathrm{BP}} / \mathrm{Q}\right) / \mathrm{s} \tau \text { and } \\
& \mathrm{V}_{\mathrm{LP}}=\mathrm{V}_{\mathrm{BP}} / \mathrm{s} \tau,
\end{aligned}
$$

where $1 / Q$ represents the fraction of $V_{B P}$ fed back to the input of the first integrator. After combining these equations and rearranging terms, we get:

$$
\begin{aligned}
& \mathrm{V}_{\mathrm{BP}} / \mathrm{V}_{\mathrm{i}}=\mathrm{s} \omega_{\mathrm{C}} /\left(\mathrm{s}^{2}+\mathrm{s} \omega_{\mathrm{C}} / \mathrm{Q}+\omega_{\mathrm{C}}{ }^{2}\right) \text { and } \\
& \mathrm{V}_{\mathrm{LP}} / \mathrm{V}_{\mathrm{i}}=\omega_{\mathrm{C}}^{2} /\left(\mathrm{s}^{2}+\mathrm{s} \omega_{\mathrm{C}} / \mathrm{Q}+\omega_{\mathrm{C}}{ }^{2}\right),
\end{aligned}
$$


3. This block diagram of a second-order integrator shows how you can obtain both bandpass and low-pass outputs from a single programmable filter.
where

$$
\omega_{\mathrm{C}}=1 / \tau \approx 62 \times 10^{6}\left(\mathrm{i}_{\mathrm{E}}\right) .
$$

The center frequency of the bandpass can now be calculated:

$$
\mathrm{f}_{\mathrm{c}}=\omega_{\mathrm{C}} / 2 \pi \approx 10^{7}\left(\mathrm{i}_{\mathrm{E}}\right) .
$$

This shows that $f_{c}$ is directly proportional to the common-emitter current that feeds the OTAs. Thus if $i_{E}$ changes from $1 \mu \mathrm{~A}$ to $1 \mathrm{~mA}, \mathrm{f}_{\mathrm{c}}$ varies accordingly from 10 Hz to 10 kHz -a threedecade or 10 -octave range.

When you have ranges this wide, exponential parameter control is usually more convenient than linear control, since changes can then be expressed in decibels rather than linear steps. The exponential control over a $10-\mathrm{Hz}-\mathrm{to}-10-\mathrm{kHz}$ range can be accomplished by use of a dual-output exponential current generator. Let's specify the circuit parameters so that a $1-\mathrm{V}$ increment in the control voltage, $\mathrm{V}_{\mathrm{c}}$, results in a one-octave change in $i_{E}$ and hence in $f_{c}$. The relationship between $f_{c}$ and $V_{C}$ can then be written as

$$
\mathrm{f}_{\mathrm{c}}=10 \times 2^{\mathrm{v}_{\mathrm{c}}} \mathrm{~Hz}\left(0 \leq \mathrm{V}_{\mathrm{c}} \leq 10\right)
$$

The circuit of Fig. 4 provides this wide range. Its Q factor is set by $\mathrm{R}_{1}$ and can range from 0.5 to several hundred. As the Q increases, the circuit will eventually break into oscillation at a frequency of $f_{c}$. When used in this mode, the circuit can operate as a voltage-controlled quadrature oscillator, since the low-pass output lags the

4. The complete schematic of the programmable active filter uses but three transistor arrays and three op amps,
yet offers a 10 -octave tuning range. The filter delivers bandpass and low-pass outputs.
bandpass output by $90^{\circ}$. For good sine purity, though, a nonlinear limiting element should be added in the feedback loop to ensure output amplitude stabilization. As an added bonus, the $Q$ and $f_{c}$ are totally independent, and any adjustment of either has no effect on the other.

## The circuit works like this . . .

A common problem with state-variable circuits is the so-called $Q$ enhancement at high frequencies, which results from the phase lag of finite bandwidth op amps. This effect can be reduced if you use high-speed op amps and counteract the phase lag of the amps with some intentionally inserted phase lead in the interstage coupling circuitry. This is exactly what the $15-\mathrm{pF}$ capacitors do when connected in parallel with $16-\mathrm{k} \Omega$ coupling resistors.

Since the amount of phase lag is likely to vary from one op-amp type to another, the capacitor values shown in Fig. 4 are only indicative of the order of magnitude involved. The optimum capacitor values can be determined experimentally.

The dual-output current generator used in Fig. 4 consists of three matched transistors, $\mathrm{Q}_{1}, \mathrm{Q}_{2}$ and $Q_{5}$, a level shifter, $Q_{6}$, and a regulator op amp. The op amp keeps the current through $Q_{5}$ constant, and thus develops a temperature-tracking reference voltage at the emitter of $Q_{5}$ for the emitters of $\mathrm{Q}_{1}$ and $\mathrm{Q}_{2}$.

Level shifter $Q_{6}$ scales the input voltage range into a nominal swing of 180 mV at the common
bases of $Q_{1}$ and $Q_{2}$. This permits the collector currents of these transistors to undergo a threedecade change as $\mathrm{V}_{\mathrm{C}}$ is swept from 0 to +10 V . Diode-connected transistors $Q_{3}$ and $Q_{4}$ translate the reference voltage of the entire exponential circuit to below ground, so that the collectors of $Q_{1}$ and $Q_{2}$ can feed directly to the common emitters of the OTAs.

Potentiometer $R_{2}$ adjusts the width of the exponential range, and $R_{4}$ shifts the entire range up or down and, in turn, controls the scale factor of the exponential function. The $50-\mathrm{pF}$ capacitor in the feedback circuit of the regulator, together with $R_{6}$, prevents high-frequency oscillation of the op amp. Resistor $R_{5}$ compensates for any error introduced by the emitter bulk resistances, $r_{\mathrm{E}}$, of $\mathrm{Q}_{1}$ and $\mathrm{Q}_{2}$. These resistances could cause the relationship between the collector current and the base-emitter voltage drop to be no longer exponential in the upper current range. The value of $R_{5}$ is calculated by the formula:

$$
\mathrm{R}_{5} \approx 2 \times 130 \Omega \times \mathrm{R}_{6} / \mathrm{r}_{\mathrm{E}}
$$

Since $\mathrm{r}_{\mathrm{E}}$ is only about $10 \Omega$ or so, the value of $R_{5}$ easily compensates for the bulk-resistance error.

Nonperfect matching between $\mathrm{Q}_{1}$ and $\mathrm{Q}_{2}$ means that the collector currents of these transistors are likely to differ, and this will affect both $f_{c}$ and Q. However, this doesn't create a problem, since the mismatch is automatically compensated for during the initial calibration adjustments of $R_{1}$, $R_{2}$ and $R_{4}$. The performance of the filter for a $Q$ setting of about 50 is shown in Fig. 5.

5. The response of the programmable filter is linear (in decibels) over a $10-\mathrm{V}$ tuning range. It has a corner or center frequency within a $10 \cdot \mathrm{~Hz} \cdot \mathrm{to}-10 \cdot \mathrm{kHz}$ range.

The transfer characteristics of the OTAs, as well as those of the dual-output current generator, depend on $\mathrm{V}_{\mathrm{T}}$, the thermal voltage. The filter thus has a certain amount of temperature sensitivity, and for applications where this is critical, you can minimize it. All you have to do is replace the $51-\Omega$ input resistors ( $\mathrm{R}_{2}$ in Fig. 2) and the $180-\Omega$ current-generator input resistor ( $R_{3}$ in Fig. 4) with thermistor elements that have compensating characteristics.

## Automatic $\mathbf{Q}$ control for the filter

In the circuit of Fig. 4, Q is determined by the setting of $\mathrm{R}_{1}$. However, the Q can also be made programmable if you add a transconductance multiplier and associated current generator. This modification is shown in Fig. 6, where, for clarity, only the circuit components directly involved in the change are shown schematically; the rest of the circuit is shown in block diagram form. For optimum performance, make provisions to zero the voltage offset between $\mathrm{Q}_{1}$ and $\mathrm{Q}_{2}$.

The response of this circuit to input-signal levels is the same as for the circuit shown in Fig. 4 , except that the value of Q is now determined by a control voltage, $\mathrm{V}_{\mathrm{Q}}$. The gain of the circuit at $f_{c}$ equals $Q$, and this may be a problem for large values of Q . This is because the amplifiers in the filter will saturate for large voltage inputs, unless the input is intentionally maintained below a predetermined value. To eliminate this possible saturation problem, an additional input to the

6. You can program the $\mathbf{Q}$ of the tunable filter by adding a transconductance multiplier in the input stage and an exponential current generator to control it.

7. The low-pass response for the modified circuit of Fig. 6 shows that for the $v_{\mathrm{a}}$ input, gains will reach values higher than one (a), while for the $\mathrm{v}_{\mathrm{b}}$ input, gains are adjusted for a maximum of one (b).
filter, $\mathrm{V}_{\mathrm{B}}$, can be designed in.
Since the signal $V_{B}$ is applied to the filter through the multiplier that controls the Q , it undergoes some attenuation. This attenuation is inversely proportional to the value of Q . Thus the gain of the filter at $f_{c}$ is unity, regardless of the value of Q . The typical low-pass responses to the two input lines are shown in Fig. 7. -

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# Get around op amp limitations in active filter design. Simple derived expressions show how to compensate the amplifier and explain second-order effects. 

Avoid the problems caused by limited op amp gain-bandwidth products in active filter designs. You can obtain near-ideal response by modifying the filter's quality factor and natural frequency.

The limited gain-bandwidth product of the amplifier creates several problems in second-order filter responses:

- Increased gain near the natural frequency of the filter.
- A downshift of the natural frequency due to the excess phase shift of the amplifier.
- A maximized $\mathbf{Q}$ at low frequencies due to the finite amplifier gain.

To analyze the problems, and compensate the circuit for them, let's use one of the most common filter forms, the state-variable active filter, as a model (Fig. 1). ${ }^{1,2}$ This filter circuit has a low-sensitivity to component variations, an adjustable circuit $Q$, and adjustable natural frequency. ${ }^{3,4}$ It provides simultaneous low-pass, bandpass and high-pass responses.

## Start with the filter transfer function

Before we derive the filter transfer function, let's model the amplifiers used inside. This firstorder model should be sufficient for the forthcoming analysis:

$$
\begin{equation*}
\mathrm{a}(\mathrm{~s})=\mathrm{a}_{\mathrm{o}} /\left(1+\mathrm{s} / \omega_{\mathrm{a}}\right)=\mathrm{RGB} /\left(\mathrm{s}+\omega_{\mathrm{a}}\right), \tag{1}
\end{equation*}
$$

where the radian gain bandwidth, RGB, equals $\mathrm{a}_{\mathrm{o}} \omega_{\mathrm{a}}$. Now the transfer function for the entire filter can be written as:
$\mathrm{V}_{\mathrm{o}} / \mathrm{V}_{\mathrm{s}}=(\mathrm{s}+\mathrm{X})\left(\mathrm{G}_{\mathrm{s}} \mathrm{RGB} / \mathrm{G}_{\mathrm{T}}(\mathrm{RC})^{2}\right)$ $\times\left[\mathrm{s}^{4}+\mathrm{s}^{3}\left(\omega_{\mathrm{a}}+\mathrm{X}\right)+\mathrm{s}^{2}\left(\omega_{\mathrm{a}} \mathrm{X}+\mathrm{Y}+\mathrm{G}_{2} \mathrm{RGB} / \mathrm{C}_{\mathrm{i}}\right)\right.$

$$
\begin{equation*}
\left.+s(X Y)+G_{1} R G B / C_{i}(R C)^{2}\right]^{-1} \tag{2}
\end{equation*}
$$

where $\mathrm{C}_{\mathrm{i}}$ is the input capacitance,

$$
\mathrm{X}=\left(\mathrm{G}_{1}+\mathrm{G}_{2}\right) / \mathrm{C}_{1}, \mathrm{Y}=\mathrm{G}_{\mathrm{F}} \mathrm{RGB} / \mathrm{G}_{\mathrm{T}} \mathrm{RC}
$$

and $G_{T}=G_{F}+G_{s}+G_{2}$.
For the ideal amplifier and zero input capacitance, Eq. 2 reduces to :

$$
\begin{equation*}
\mathrm{V}_{\mathrm{o}} / \mathrm{V}_{\mathrm{s}}=\mathrm{G}(0) \omega_{\mathrm{n}}{ }^{2} /\left(\mathrm{s}^{2}+\omega_{\mathrm{ns}} / \mathrm{Q}+\omega_{\mathrm{n}}{ }^{2}\right), \tag{3}
\end{equation*}
$$

where

$$
\omega_{\mathrm{n}}{ }^{2}=\mathrm{G}_{1} / \mathrm{G}_{2}(\mathrm{RC})^{2}=\mathrm{R}_{2} / \mathrm{R}_{1}(\mathrm{RC})^{2},
$$

[^10]

1. A minimum of three op amps are needed to build the state-variable active filter. This filter delivers simultaneous low-pass, bandpass and high-pass outputs.

2. The integrator stage of the state variable filter can be modeled by this simple circuit. Each filter has two integrator stages.

$$
\begin{aligned}
& \omega_{\mathrm{n}} / \mathrm{Q}=\mathrm{G}_{\mathrm{F}}\left(\mathrm{G}_{1}+\mathrm{G}_{2}\right) / \mathrm{G}_{\mathrm{T}} \mathrm{G}_{2} \mathrm{RC} \\
& \text { and } \mathrm{G}(0)=\mathrm{G}_{\mathrm{s}}\left(\mathrm{G}_{1}+\mathrm{G}_{2}\right) / \mathrm{G}_{\mathrm{T}} \mathrm{G}_{1} .
\end{aligned}
$$

In these equations $\omega_{\mathrm{n}}$ is the natural frequency in radians, $Q$ the quality factor and $G(0)$ the dc gain.

The definitions of $\omega_{\mathrm{n}}$ and Q can be written accurately if you work back from the ideal transfer function and substitute frequency values into Eq. 2. For example, in Eq. 3, when $s=j \omega_{n}$, the gain magnitude is Q and the phase is $-90^{\circ}$. Likewise from Eq. 2, when $s=j \omega_{n}^{\prime}$, the gain magnitude is $\mathrm{Q}^{\prime}$ and the phase is $-90^{\circ}$. (The primed variables denote modification from the ideal.)

The transmission zero in Eq. 2 is much greater than $\omega_{\mathrm{n}}$. Thus, if we set $\mathrm{s}=\mathrm{j} \omega_{\mathrm{n}}^{\prime}$ in Eq. 2 we can compute the gain:

$$
\begin{align*}
G\left(j \omega_{\mathrm{n}}^{\prime}\right) & \cong \text { WXZ }\left[\omega_{\mathrm{n}}^{\prime}{ }^{4}-\omega_{\mathrm{n}}{ }^{2}\left(\omega_{\mathrm{a}} X+\mathrm{G}_{2} \text { RGB } / \mathrm{C}_{\mathrm{i}}\right.\right. \\
& +\omega_{\mathrm{n}} G_{\mathrm{F}} R G B \sqrt{\left.R_{1} / R_{2} / G_{\mathrm{T}}\right)}+\mathrm{Z} \omega_{\mathrm{n}} / \mathrm{C}_{\mathrm{i}} \\
& \left.+j \omega_{\mathrm{n}} X Y-\omega_{\mathrm{n}}^{\prime 2}\left(\omega_{\mathrm{a}}+X\right)\right]^{-1}, \tag{4}
\end{align*}
$$

where X and Y are the same as noted in Eq. 2 and $W=\mathrm{G}_{\mathrm{s}} \omega_{\mathrm{n}} / \mathrm{G}_{\mathrm{T}} \mathrm{G}_{1}$ and $\mathrm{Z}=\mathrm{RGB} \omega_{\mathrm{n}} \mathrm{G}_{2}$.

By setting the real part of the denominator equal to zero you can find the natural frequency of the filter, $\omega_{n}^{\prime} \approx \omega_{n}$, and the gain
$\mathrm{G}\left(\mathrm{j} \omega_{\mathrm{n}}^{\prime}\right)=(\mathrm{WZX})\left\{\mathrm{j} \omega_{\mathrm{n}}^{\prime}\left[\mathrm{Z} / \mathrm{QC}_{\mathrm{i}}-\omega_{\mathrm{n}}{ }^{2}\left(\omega_{\mathrm{a}}+\mathrm{X}\right)\right]\right\}^{-1}$
where W, X and Z are defined in Eq. 3 and 4.
From this result and $\omega_{n}^{\prime} \approx \omega_{\mathrm{n}}$ you will find that Eq. 5 becomes
$\mathrm{G}\left(\mathrm{j} \omega_{\mathrm{n}}^{\prime}\right) \approx\left[\mathrm{G}_{\mathrm{s}}\left(\mathrm{G}_{1}+\mathrm{G}_{2}\right) / \mathrm{G}_{\mathrm{T}} \mathrm{G}_{1}\right] \mathrm{Q} /-90^{\circ}$.
causes a decrease in natural frequency. Let's examine the frequency change by use of the idealized transfer function of Eq. 3, modified as follows:

$$
\begin{equation*}
\mathrm{G}(\mathrm{~s})=\mathrm{G}(0) /\left[1+\omega_{\mathrm{n}} / \mathrm{sQ}+\left(\omega_{\mathrm{n}} / \mathrm{s}\right)^{2}\right] . \tag{8}
\end{equation*}
$$

The middle coefficient of $s^{-1}$ in the denominator of this equation represents the left-most integrator in the inner Q loop of Fig. 1. In the last term of the denominator, the $\mathrm{s}^{-2}$ coefficient represents the cascade combination of both integrators in the outer $\omega_{\mathrm{n}}$ loop. Let's look at the effect of these

3. The plot of $\mathbf{Q}$ multiplicative factors versus normalized $f_{n} Q$ product (a) and of percent natural frequency down-

$$
\begin{align*}
& \times\left[1-\omega_{\mathrm{n}} \mathrm{Q}\left(1+\mathrm{R}_{2} / \mathrm{R}_{1}\right) / \mathrm{RGB}\right]^{-1}  \tag{6}\\
& =\mathrm{G}(0) \mathrm{Q}^{\prime} \angle-90^{\circ},
\end{align*}
$$

for $\omega_{\mathrm{a}} \mathrm{C}_{1} \ll \mathrm{G}_{1}+\mathrm{G}_{2}$.
The effective $Q$, or $Q^{\prime}$, is therefore enhanced above the ideal Q by the factor

$$
\begin{align*}
\mathbf{M}_{\mathrm{Q}^{\prime}} & =\left[1-\omega_{\mathrm{n}} \mathrm{Q}\left(1+\mathrm{R}_{2} / \mathrm{R}_{1}\right) / \mathrm{RGB}^{-1}\right.  \tag{7}\\
& =\left[1-\left(1+\alpha^{2}\right) \beta\right]^{-1},
\end{align*}
$$

where $\alpha=\sqrt{\mathrm{R}_{2} / \mathrm{R}_{1}}$ and $\beta=\omega_{\mathrm{n}} \mathrm{Q} / \mathrm{RGB}$.
Thus, when you design the filter, don't use the desired Q . Use a lower value, $\mathrm{Q}^{\prime}=\mathrm{Q} / \mathrm{M}_{\mathrm{Q}^{\prime}}$. Alternately, you can add a small lead-compensation capacitor, C, in parallel with $R$, to offset $Q$ enhancement.

## Downshift in frequency also compensates

While the gain-bandwidth limitation of the difference amplifier causes Q enhancement (peaking), the integrator's gain-bandwidth limitation
shift versus normalized $f_{\mathrm{n}} \mathrm{Q}$ product (b) can be used to interpret the derived results of the equations.
integrators on the filter's frequency response by using the simplified integrator model of Fig. 2.5,6

Finite input and output impedances have been omitted from the model for clarity, but can be included without significantly changing the results. The gain expression for the integrator model is then:

$$
\begin{equation*}
\mathrm{V}_{\mathrm{o}} / \mathrm{V}_{\mathrm{s}}=-\mathrm{a}(\mathrm{~s}) /[1+\mathrm{sRC}(1+\mathrm{a}(\mathrm{~s}))] \tag{9}
\end{equation*}
$$

As a (s) goes to $\infty$ this equation reduces to $-1 /$ $\mathrm{s} \tau$, where $\tau=\mathrm{RC}$. This indicates that the integrator sets the filter's frequency. For a more accurrate frequency determination, Eq. 9 can be expressed as

$$
\begin{align*}
\mathrm{V}_{\mathrm{o}} / \mathrm{V}_{\mathrm{s}} & =-\mathrm{a}(\mathrm{~s}) /[1+\mathrm{s} \tau \mathrm{a}(\mathrm{~s})] \\
& =-[\mathrm{s}(\tau+1 / \mathrm{RGB})]^{-1}  \tag{10}\\
& =-1 / \mathrm{s} \tau^{\prime}\left(\text { for } \omega_{\mathrm{a}} \ll \omega\right),
\end{align*}
$$

where $\tau^{\prime}=\tau+1 /$ RGB is the modified time constant. Since the time constant has increased, it follows that the modified natural frequency will

4. The General Instrument Model ACF7092C active filter (a) when tested on the low-pass output with a design
be decreased to the value $\omega_{\mathrm{n}}{ }^{\prime \prime}=\alpha / \tau^{\prime}=\alpha /(\tau+1 / \mathrm{RGB})=\omega_{\mathrm{n}} \tau /(\tau+1 / \mathrm{RGB})$.

Since the time constant has changed there is a deviation in natural frequency that can be calculated by:

$$
\begin{equation*}
\gamma=\left(\omega_{\mathrm{n}}-\omega_{\mathrm{n}}^{\prime \prime}\right) / \omega_{\mathrm{n}} \cong 1 / \mathrm{RGB} \tau=\beta / \alpha \mathrm{Q} \tag{11}
\end{equation*}
$$

if $\tau$ RGB $\gg 1$
To find out how much of a decrease in Q is caused by the change in frequency, substitute the amplifier gain from Eq. 1 into Eq. 10. The resulting equation becomes:

$$
\begin{align*}
\mathrm{V}_{\mathrm{o}} / \mathrm{V}_{\mathrm{s}}= & -\left[\mathrm{RGB} /\left(\mathrm{s}+\omega_{\mathrm{a}}\right)\right] / \\
& {\left[1+\mathrm{s} \tau \operatorname{RGB} /\left(\mathrm{s}+\omega_{\mathrm{a}}\right)\right] } \\
= & -1 / \tau \mathrm{s}^{\prime}(\text { for } \mathrm{RGB} \gg 1 / \tau) \tag{12}
\end{align*}
$$

where $\mathrm{s}^{\prime}=\mathrm{s}+\omega_{\mathrm{a}} / \tau \mathrm{RGB}=\mathrm{s}+\beta \omega_{\mathrm{a}} / \alpha \mathrm{Q}$.
Eq. 12 implies that the finite gain of the integrators creates a frequency shift in $s$ that causes the real part of the second-order pole-pair to be increased by $\beta \omega_{\mathrm{a}} / \alpha \mathrm{Q}$. This increase is equivalent to lowering $Q$ to $Q^{\prime \prime}$, and is found from the increase in the real part of the complex pole-pair:

$$
\begin{aligned}
\omega_{\mathrm{n}} / 2 \mathrm{Q}^{\prime \prime} & =\omega_{\mathrm{n}}+\beta \omega_{\mathrm{a}} / \alpha \mathbf{Q} \\
& =\left(\omega_{\mathrm{n}} / 2 \mathrm{Q}\right)\left(1+2 \beta \omega_{\mathrm{a}} / \alpha \omega_{\mathrm{n}}\right)
\end{aligned}
$$

Now $Q^{\prime \prime}$ can be written as $M_{Q}{ }^{\prime \prime} Q$ where

$$
\begin{align*}
\mathbf{M}_{Q}^{\prime \prime} & =1 /\left(1+2 \beta \omega_{\mathrm{a}} / \alpha \omega_{\mathrm{n}}\right) \\
& =1 /\left(1+2 \mathrm{Q} / \alpha \mathrm{a}_{\mathrm{o}}\right) \tag{13}
\end{align*}
$$

the Q multiplicative factor.
A plot of the $Q$ multiplicative factor $M_{Q}{ }^{\prime}$ and $M_{Q}{ }^{\prime \prime}$ versus the normalized $f_{n} Q$ product or $\beta$ can help interpret the results we've just derived

Q of 50 produces output curves that coincide with the predicted results from the equation (b).
(Fig. 3). The upper family of curves corresponds to the Q-enhancement factor $\mathbf{M}_{Q}{ }^{\prime}$ and is shown for various values of $\alpha$. The lower family of curves corresponds to the Q-degradation factor $\mathbf{M}_{Q}{ }^{\prime \prime}$ and is shown for various values of $\omega_{\mathrm{a}} / \alpha \omega_{\mathrm{n}}$.

## Interpret the results carefully

From these graphs you can see that decreasing values of $\alpha$ imply a reduced $M_{Q}{ }^{\prime}$ ( Q enhancement) because as $R_{2}$ is made less than $R_{1}$ more openloop gain-bandwidth is obtained from the difference amplifier. And, for $\beta>0.2 \mathrm{Q}$, enhancement rapidly becomes excessive. Thus when you select op amps for a filter, make sure their gain-bandwidth products are at least 10 times the $f_{n} Q$ product to minimize $Q$ sensitivity.

The $M_{Q}{ }^{\prime \prime}$ curves show that $Q$ degradation is only important when $\omega_{\mathrm{n}}<0.1 \omega_{\mathrm{a}} / \alpha$. Typical op amps have an $f_{a}$ of 10 Hz , and thus $Q$ degradation will only be significant below 10 Hz when $\alpha \geqslant 0.1$. In general, Q enhancement should be considered for frequencies above 1 kHz when Q is greater than 100 .

Fig. 3b shows the percentage of frequency downshift versus $\beta$, for different values of $\alpha \mathrm{Q}$. You can see that circuits with higher Qs can be designed more accurately for natural frequency than can low-Q circuits.

To verify these results, a commercially ayail-

Comparison of measured and calculated natural frequency and $\mathbf{Q}$

| Case | Idealized design values $f_{n}$ in kHz | Calculated values |  | Measured values |
| :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{gathered} (\mathrm{GB}=1 \mathrm{MHz}) \\ \text { (Typical) } \end{gathered}$ | $\begin{gathered} (\mathrm{GB}=875 \mathrm{kHz}) \\ \text { (Actual) } \end{gathered}$ |  |
| 1 | $\begin{aligned} & f_{n}=1 \\ & Q=50 \end{aligned}$ | $\begin{gathered} 996.84 \mathrm{~Hz} \\ 52.91 \end{gathered}$ | $\begin{gathered} 996.39 \mathrm{~Hz} \\ 53.35 \end{gathered}$ | $\begin{aligned} & 993.3 \mathrm{~Hz} \\ & 56.43 \end{aligned}$ |
| 2 | $\begin{aligned} & \mathrm{f}_{\mathrm{n}}=2.5 \\ & \mathrm{Q}=40 \end{aligned}$ | $\begin{gathered} 2.480 \mathrm{kHz} \\ 44.94 \end{gathered}$ | $\begin{aligned} & 2.477 \mathrm{kHz} \\ & 45.75 \end{aligned}$ | $\begin{aligned} & 2.477 \mathrm{kHz} \\ & 48.53 \end{aligned}$ |
| 3 | $\begin{gathered} f_{\mathrm{n}}=5 \\ \mathrm{Q}=40 \end{gathered}$ | $\begin{gathered} 4.921 \mathrm{kHz} \\ 51.28 \end{gathered}$ | $\begin{aligned} & 4.910 \mathrm{kHz} \\ & 53.44 \end{aligned}$ | $\begin{aligned} & 4.935 \mathrm{kHz} \\ & 58.21 \end{aligned}$ |
| 4 | $\begin{aligned} & \mathrm{f}_{\mathrm{n}}=10 \\ & \mathrm{Q}=30 \end{aligned}$ | $\begin{gathered} 9.683 \mathrm{kHz} \\ 44.78 \end{gathered}$ | $\begin{aligned} & 9.639 \mathrm{kHz} \\ & 48.17 \end{aligned}$ | $\begin{aligned} & 9.807 \mathrm{kHz} \\ & 50.87 \end{aligned}$ |
| 5 | $\begin{aligned} & f_{n}=10 \\ & \mathrm{Q}=40 \end{aligned}$ | $\begin{aligned} & 9.684 \mathrm{kHz} \\ & 71.43 \end{aligned}$ | $\begin{aligned} & 9.639 \mathrm{kHz} \\ & 80.46 \end{aligned}$ | $\begin{aligned} & 9.809 \mathrm{kHz} \\ & 81.66 \end{aligned}$ |
| 6 | $\begin{aligned} & f_{n}=10 \\ & Q=50 \end{aligned}$ | $\begin{aligned} & 9.684 \mathrm{kHz} \\ & 111.11 \end{aligned}$ | $\begin{aligned} & 9.639 \mathrm{kHz} \\ & 134.62 \end{aligned}$ | $\begin{aligned} & 9.813 \mathrm{kHz} \\ & 135.67 \end{aligned}$ |

able filter, the ACF-7092C manufactured by General Instrument, was selected for testing (Fig. 4a). Typical responses for a $Q$ of 50 at an $f_{n}$ equal to 1 and 10 kHz were measured and displayed on a scope (Figs. 4b and 4c). Q enhancement at 10 kHz increased above its value at 1 kHz , as can easily be seen. Some of the points shown on the scope traces are tabulated in the table and also compare favorably with the calculated results. - :

## Acknowledgement

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# Use slew-rate filtering <br> to discriminate against noise spikes. Pass the desired signals without phase or amplitude distortion. 

The usual methods of reducing the adverse effects of noise spikes in electronic control circuits also disturb the desired signals. At best they are inadequate. One solution is to use a filtering method based upon limiting the slew rate of an amplifier. With slew-rate limiting "normal" control signals can pass faithfully without timing or amplitude distortion, and yet fast spikes are severely attenuated (Fig. 1).

Amplitude-limiting techniques clip the top of an intefering spike, but the rest of the spike remains. Worse, the signal of interest might be clipped too.

Although a low-pass RC filter can reduce the amplitude of noise strikes, the filter then takes several RC time constants to recover. And the desired signal is considerably altered in amplitude and phase, even without noise spikes.

Both of these common methods cause trouble, particularly with amplitude comparators for detecting signal levels: amplitude limiters set high to avoid distorting the desired signal can't keep noise spikes from triggering comparators; and phase delay in low-pass filters, delays the operation of comparators.

## Slew-rate limiting to the rescue

Limit the slew rate of an op amp, and the output from the circuit can still faithfully follow the desired input-control signals. But noise spikes, because they require a very high slew rate to reproduce, are severly discriminated against. The circuit's output can slew only at a predetermined maximum rate; only a small triangular pip results from a formidibly high fast spike. Slower, normal signals pass through unaffected.

The slew rate of an op amp can be controlled as in Fig. 2a. Output voltage fed back to the op amp's inverting terminal tends to keep the voltage between inverting and noninverting inputs near zero, because of the op amp's very-high open-loop gain.

[^11]

1. A slew-rate filter can pass the desired signal without phase or amplitude distortion, but it will severely attenuate sharp noise spikes.

For the rates of change ( $\mathrm{dV} / \mathrm{dt}$ ) in the usual control signal, the output of the op amp readily provides enough charge into $\mathrm{C}_{1}$ through $\mathrm{R}_{1}$ to closely follow the input signal. However, for signals with high steeply rising wavefronts, the opamp output saturates and limits the output slew rate of the circuit; the maximum rate of charge is limited to $\mathrm{I}_{\mathrm{C}}=\mathrm{V}_{\mathrm{cc}} / \mathrm{R}_{1}$.

When driven into such a current-limited mode, the circuit changes from a voltage follower with a gain of one to a constant-slew-rate filter. The circuit limits the output voltage's rate of change until the input signal's dV /dt falls below the circuit's slew-rate limit, and until the circuit's maximum negative slew rate can restore the capacitor voltage to correspond to the input signal.

The circuit's slew-rate (SR) capability is

$$
\begin{aligned}
\mathrm{SR}=(\mathrm{dV} / \mathrm{dt})_{\max } & = \pm \mathrm{I}_{\mathrm{c}} / \mathrm{C}_{1} \\
& = \pm \mathrm{V}_{\mathrm{cc}} / \mathrm{R}_{1} \mathrm{C}_{1} .
\end{aligned}
$$

For the components shown in Fig. 2a

$$
\begin{aligned}
\mathrm{SR} & = \pm 15 /\left(30 \times 10^{3} \times 10^{-6}\right) \\
& = \pm 500 \mathrm{~V} / \mathrm{s} .
\end{aligned}
$$

Resistor $\mathrm{R}_{2}$ provides some decoupling between the input and output, and helps stabilize the circuit. As a rule of thumb, the ratio of $R_{1} / R_{2}$ is made equal to 300 ; thus,

$$
\mathrm{R}_{2}=30 \times 10^{3} / 300=100 \Omega .
$$

In practice, set the slew rate to about twice the maximum rate of change you expect in the signal of interest. This 2:1 ratio ensures rapid circuit recovery following a disturbance.

In one application, a proportionate voltage represents the instantaneous value of current flowing in a de traction motor. A thyristor controls the power input to the motor by alternately connecting and disconnecting the motor from the traction battery. Motor current ramps in the positive direction during the on period, and falls during the off period. The maximum rate at which the current rises or falls is given by

$$
\mathrm{L} \cdot \frac{\mathrm{dI}_{\mathrm{m}}}{\mathrm{dt}}= \pm \mathrm{E}
$$

where
$\mathrm{I}_{\mathrm{m}}=$ motor current,
$\mathrm{E}=200 \mathrm{~V}$ (traction battery),
$\mathrm{L}=4 \mathrm{mH}$ (motor inductance).
Therefore,

$$
\frac{\mathrm{dI}_{\mathrm{m}}}{\mathrm{dt}}= \pm 50 \times 10^{3} \mathrm{~A} / \mathrm{s}
$$

The proportionate voltage that represent a motor current of 400 A has a maximum value of $\pm 2 \mathrm{~V}$. Therefore, the maximum rate of signalvoltage change is

$$
\frac{d V_{m}}{d t}=\frac{d I_{m}}{d t} \times \frac{2}{400}= \pm 250 \mathrm{~V} / \mathrm{s}
$$

Thus the values of $\mathrm{R}_{1}$ and $\mathrm{C}_{1}$ in Fig. 2a, which provide $\mathrm{SR}=500 \mathrm{~V} / \mathrm{s}$, should be able to adequately handle these motor-control signals, in accordance with the $2: 1$ criterion.

Small variations in the basic op-amp slewlimiting circuit can provide improved noise-pulse discrimination.
(continued on next page)

2. The basic circuit of the slew-rate filter (a) can be improved to provide increased discrimination against noise spikes for specific spike polarities (b), or with a few additional parts provide fast recovery following negative spikes or transients (c).

3. A squelch output can be taken from the output terminal (S in Fig. 2) of the op amp to cut off data reading when the signal's rate of change rises above a desired level.

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When an engineer works on a job it should be a real job. The fruit of his work, if he is successful, should be brought to the marketplace.

I've spoken to many engineers who have worked in the laboratory and found that nothing ever gets to the marketplace. They can't point to something and say, "I did that." And that's the most frustrating thing in the world.

## You've got to realize that there's a bit of an engineer's personality in any product you see. An engineer wants to see his stamp on some piece of work.

There are probably all kinds of reasons for this, but it relates to the fact that productive activity is a basic human trait. If you're a manager, you must pay attention to that.

Let me give you a personal example. My dad was a builder and I can remember riding through San Francisco with him. He would point to a house or a store and say, "I built that."

Well, I worked with my father sometimes. I had the job of assigning the location of the Westlake Market. My role was insignificant but I relate to that market, even now. There's a piece of me there.

I had similar experiences in my days as a salesman with Fairchild. I never designed any of the Apollo equipment but I did the application engineering for many parts that went into Apollo. So I identified with that program.

Our guys who design, say, the MOS LSI chips that go into a calculator feel they're part of that product. They get warm feelings whenever they see it in a shop window, whether the calculator is marketed by National or by a customer.

If a company is not committed to marketing a product, it should not get an engineer working on that product. Sure, you're going to make mistakes. X percent of the time you're going to decide later that you ought to have rejected one project in favor of another. You have to examine such things on a regular basis.

But a company basically should be prepared to follow through with what it starts. That sounds like a basic thing. Every company, you would think, wants its projects to fly. But that's not aiways true.

You find companies who start six projects, then kill five and select one for the market. Or you get: "We have to dabble in this in case there's something there. Competition is talking about something in this area so we had better assign a coup'e of guys to follow it." I think that's a bad idea.

> If you're going to get into a thing, define the technology you want to use, then assign it to a product.

# Floyd Kvamme of Speaks On Stimulating 

## Your Engineers

There's a good example in $I^{2} L$. People started discussing integrated injection logic about two years ago. When we looked at it, we said, "Hey, that looks interesting. Maybe we ought to do something in that area." So we got some guys together and said "OK, where is its greatest chance of being competitive in the marketplace?"

We concluded that the best home for $\mathrm{I}^{2} \mathrm{~L}$ was in watch circuits. It had several things going for it-on-chip drivers, very low power consumption, and smaller chips than needed with CMOS.

So we gave some guys the job of developing $I^{2} \mathrm{~L}$ technology and a circuit technique-applied to a watch circuit. In other words, our mission was not to explore $I^{2} L$, but rather, to explore $I^{2} L$ watch circuits. We wanted to tie our research to a product so that, if the guys were successful, we could take something to the marketplace.

There's an interesting fallout to that $I^{2} L$ project. We had been building a lot of watch circuits before we developed $I^{2} L$ circuits. We had been very successfu! with CMOS. Well, our CMOS guys, recognizing the benefits of the on-chip driver, finally developed on-chip drivers in CMOS circuits. That seemed impossible a year earlier.

Now you may wonder if we're neglecting research in favor of development. That depends on your definition; the line between them becomes very fuzzy.

At the extremes it's easy to distinguish research and development. The fellow who developed the first MOS circuit was surely conducting research. And the fellow who made a minor modification of an old process was doing development. But someplace between these two, there's something hard to define.

What's really important is that you must con-

## National Semiconductor


tinue to push that forefront of technology, whether you call it research or development. Pushing that forefront makes it possible to do something you couldn't do before. You can't exist in this business without that.

Now basic research is certainly necessary in many areas. But in the semiconductor field, we have not begun to tap what research has put into our hands. And we have a responsibility to tap what's already there.

I think that research for the sake of research itself can be frustrating to people who work in the real world. We want to use any technology we're using as extensively as possible. But we also want to bring something to the party. We want to add something so that next time we work
in that technology we'll have a broader base of knowledge to work with.

## You can't just be a me-too manufacturer. You may use 90 or $95 \%$ of old knowledge on a new product, but you must plumb another $5 \%$ or so of new depths. And that's exciting.

This approach to real-product design has an added benefit. You can give useful work to the new engineer. Too often, when industry hires an engineer out of school, he's not used productively for a long time. He's given make-work projects. We turn him into a leaf-raker or gopher. You know. "Go for this, go for that." So we waste
the company's money while we demoralize the young engineer.

But with a real-product philosophy, we put a new engineer to work immediately. We can give him a project that involves perhaps $99 \%$ of existing technology, and he plumbs $1 \%$ of new territory. The more experienced engineer can take a project that looks for $40 \%$ new technology. In both cases, you start with a technology you haven't fully exploited. Yet a part of what you do shouid become a new contribution.

To maximize your contributions you have to remove barriers that prevent things from getting done. Engineers want to see things accomplished. They don't want to goof off all day. They want to work. It's our job to help them do that. We try to remove things that prevent their being creative.
It's unfortunate that so much of business runs on paperwork. It really bothers me when a guy needs five signatures to get something done. It's very important that the manager clear things out of the engineer's path so that he can spend most of his time in creative activity.

## Who is Floyd Kvamme?

When he took his BEE from the University of California at Berkeley in 1959, 21-year-old Floyd Kvamme had never met an engineer and didn't know what engineers did for a living. So he worked as an engineer with Electronic Systems Development Corp. in Ventura, CA before going to Syracuse. There he worked for GE (and wrote seven chapters of the GE Transistor Manual) and studied at Syracuse University, where he earned his Master's in EE in 1962.
After returning to California, he did postMaster's work in semiconductor electronics and computer design at the University of California at Los Angeles while working at Space Technology Laboratories. There he learned that military circuit design held little fascination. He had been bitten by the semiconductor bug.
So he eagerly accepted an offer from Mel Phelps of a job in custom-microcircuit marketing for Fairchild in 1964. In February, 1967 he left the position of Microcircuit Marketing Manager to join Charlie Sporck at National Semiconductor. And in 1974 he was appointed vicepresident and general manager of the Semiconductor Division.
It's a job he loves because he finds tremendous challenge in the semiconductor business. He loves the excitement of a wild new application of semiconductors, and loves the fact that there are always 20 things going on at the same time.

He's an ardent tennis player, and he skis often with his wife, Jean, and their three sons, Mark,

## There's something else you can do to stimulate your engineers; give them as much responsibility as possible.

We do that by dividing our company into some 15 smaller businesses. The fellow in charge of each is like a president of his own company.

Take the example of Jeff Kalb, who runs our memory components. He has all the engineering and marketing responsibility, all the process-deve'opment responsibilty, and he has his fabrication lines. He really runs that business. He has his own capital requirements, his own capital budget, his own profit-and-oss statement. He has a P\&L center, not just a cost center.

Of course Jeff has to report to corporate management. But we let him make his own decisions. And this ties back to what I said in the beginning. By giving a man his own company, so to speak, we're putting him closer to his end product.


Damon and Todd. He enjoys languages, too. He once boasted that he could say "thank you" in 20 languages.

He reads a great deal, especially philosophy and history. His reading is often related to his strong belief in the bible and its messages. This belief is at the heart of his deep involvement in a small fellowship of people who believe that religion should not be merely a Sunday morning exercise. Kvamme tries to live his beliefs.

This also help us attract top people. Look at all our people who used to be presidents or senior executives of their own companies-Bob Lloyd, Dick Baeder, Marty Oudewaal-and others.

This is the reverse of what you usually find. As companies get larger in our industry, they tend to lose their entrepreneurial individuals. In our case, despite our size-and we're operating at an annual rate of about $\$ 300$ million-we're attracting entreprenuers. We try to make the entrepreneur feel comfortable.

Of course, we do have some centralized function. We have a single advertising department, for example, and a single advertising budget. The entrepreneurs compete for the ad manager's time and budget, just as they compete for our salesmen's time.

At some point you must bring some decisions to a higher management level. A higher official would make decisions, for example, on how to apportion the advertising budget. He would decide how much to devote to discretes, to LEDs, to memories, and so on.

There are other cases where corporate people might have to step in. I had to intervene recently and tell one guy to hire more engineers and spend more money on R\&D. You must make sure people invest adequately in the future.

You also have to make sure that your entrepreneurs participate in the growth of the entire corporation. It's bad to set up a situation where they don't care about other groups. You want to encourage people to make suggestions that can help other parts of the company.

Encouraging people is important for many reasons, so you sometimes have to take special steps to get people to talk.

We have an open-door policy because we want people to come in and tell us things like, "Hey, something ain't working." As a manager you can make extremely dumb moves if you have people who are afraid to tell you things.

But an open-door policy is easier said than done. You need sensitivity. If I see a guy walk by my office three or four times and glance at me, I must be sensitive to the fact that he may be looking for an opening. He may be nervous. He wants to say something but it's not convenient. In such a case, I've got to push myself and say, "Hey, let's have a cup of coffee." I've got to help that man open my door.

Unfortunately; people who are good at expressing themselves aren't always good listeners. It's important to learn to listen. I used to have a motto in my office that said: "Listen to learn, and learn to listen." That's a key to good management. -

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| 256 | $32 \times 8$ | 6330/1-1 | OC/TS | 16 | com mil | $\begin{aligned} & 50 \\ & 60 \end{aligned}$ | $\begin{array}{r} \$ 2.55 \\ 5.00 \end{array}$ |
|  |  | 5330/1-1 | OC/TS | 16 |  |  |  |
| 1024 | $256 \times 4$ | 10149 | OE | 16 | com | 30 | 17.50 |
| 1024 | $256 \times 4$ | 6300/1-1 | 0C/TS | 16 | com mil | $\begin{aligned} & 55 \\ & 75 \end{aligned}$ | $\begin{aligned} & 3.25 \\ & 7.90 \end{aligned}$ |
|  |  | 5300/1-1 | OC/TS | 16 |  |  |  |
| 2048 | $256 \times 8$ | **6308/9-1 | 0C/TS | 20 | com mil | $\begin{aligned} & 65 \\ & 85 \end{aligned}$ | $\begin{aligned} & 15.95 \\ & 33.50 \end{aligned}$ |
|  |  | **5308/9-1 | OC/TS | 20 |  |  |  |
| 2048 | $512 \times 4$ | 6305/6-1 | 0C/TS | 16 | com mil | $\begin{aligned} & 60 \\ & 75 \end{aligned}$ | $\begin{array}{r} 7.00 \\ 15.95 \end{array}$ |
|  |  | 5305/6-1 | OC/TS | 16 |  |  |  |
| 4096 | $512 \times 8$ | **6348/9-1 | OC/TS | 20 | com <br> mil | $\begin{aligned} & 65 \\ & 85 \end{aligned}$ | $\begin{aligned} & 15.95 \\ & 33.50 \end{aligned}$ |
|  |  | **5348/9-1 | OC/TS | 20 |  |  |  |
| 4096 | $512 \times 8$ | 6340/1-1 | OC/TS | 24 | com mil | $\begin{array}{r} 90 \\ 120 \end{array}$ | $\begin{aligned} & 15.95 \\ & 33.50 \end{aligned}$ |
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| 4096 | $1024 \times 4$ | 6350/1-1 | OC/TS | 18 | com mil | $\begin{aligned} & 60 \\ & 75 \end{aligned}$ | $\begin{aligned} & 15.95 \\ & 33.50 \end{aligned}$ |
|  |  | 5350/1-1 | OC/TS | 18 |  |  |  |
| 4096 | $1024 \times 4$ | 6352/3-1 | OC/TS | 18 | com mil | $\begin{aligned} & 60 \\ & 75 \end{aligned}$ | $\begin{aligned} & 15.95 \\ & 33.50 \end{aligned}$ |
|  |  | 5352/3-1 | OC/TS | 18 |  |  |  |
| 8192 | $1024 \times 8$ | **6386/7-1 | OC/TS | 22 | com mil | $\begin{array}{r} 90 \\ 125 \end{array}$ | Consult Factory |
|  |  | * 5386/7-1 | OC/TS | 22 |  |  |  |
| 8192 | $1024 \times 8$ | **6380/1-1 | OC/TS | 24 | com | 90 | Consult Factory |
|  |  | **5380/1-1 | OC/TS | 24 | mil | 125 |  |

*max access time is guaranteed over the complete voltage and temperature variation.

- available October 1976.


## Introducing Model 9300 Quality that's Quick \& Quiet

Model 9300 Vacuum Column digital tape transport has characteristics common to all Kennedy recorders - and a few new ones. It's quick (125ips); quiet (noise level of less than 70 db ), and it has the built-in quality of all Kennedy products.

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Model 9300 is not only quick and quiet - it's very competitive. That's quite a lot, considering the Kennedy quality.

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## Age reference zeners to achieve stable long-term performance

Compensated zener diodes having voltage temperature coefficients (TC) better than $50 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ and voltage time stabilities better than 500 $\mathrm{ppm} / 1000 \mathrm{~h}$ are classified as voltage-reference diodes. But commercially available very stable reference diodes ( $2 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ and $10 \mathrm{ppm} / 1000 \mathrm{~h}$ ) are expensive.

A measuring and aging technique makes it possible to improve both the TC and the voltagetime stability of standard low-performance reference zeners and achieve TCs as low as $2 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ and long-term stabilities of about $20 \mathrm{ppm} / 1000 \mathrm{~h}$. Thus a $\$ 40$ precision reference zener may be replaced by a $\$ 1.35$ diode and about $\$ 1.40$ worth of testing.

A curve typical of the change in temperature coefficient with respect to operating current ( $\mathrm{I}_{z}$ ) is shown in Fig. 1. From the curve, it is apparent that the TC can be either negative or positive, and that at a particular bias current, the TC passes through the zero value.

The data for a graph like Fig. 1 may be generated by operating a diode at several bias currents and a given temperature, say 25 C , and recording the diode voltage at each current. Then, if you raise the temperature a given amount, say 10 C , the voltage changes for the same bias currents are obtained as follows:

$$
\Delta \mathrm{V} /{ }^{\circ} \mathrm{C}=\frac{\mathrm{V}_{35 \mathrm{C}}-\mathrm{V}_{25 \mathrm{C}}}{10 \mathrm{C}}\left(\text { at a given } \mathrm{I}_{z}\right)
$$

Straight-line approximations may be made for interpolating between currents.

From the plot, determine the bias current of the zero-TC point of the diode. To age the diode, bias it at this current and place it in an oven at an elevated temperature of about 70 C .

Fig. 2 is a plot of the accelerated aging characteristic of a typical zener diode. The zener diode was placed in an oven at $70 \pm 1 \mathrm{C}$ for seven weeks. Once each week the diode was allowed to cool to room temperature ( $25 \pm 0.1 \mathrm{C}$ ) and its voltage measured at the previously established "zero-TC" current.

Fig. 2 shows that most of the voltage drift takes place during the first 168 h (1 week). If a baseline is drawn starting after the first 168 h (heavy line), the excursions of $\Delta \mathrm{V}$ are seen to
be much smaller than the initial change. Another baseline drawn starting after 336 h (dotted line) shows an improvement that is not nearly as pronounced as the initial change.

In general, aging the diode for 1 week results in about 2-to-1 improvement in voltage drift with time. After aging, operation of the diode at or near its zero-TC point can provide a $10: 1 \mathrm{im}$ provement over operation elsewhere.

Kenneth E. Panck, Electrical Design Engineer, Lab Instrument Div., Tektronix Inc., P.O. Box 500, MS 39-135, Beaverton, OR 97077.

Circle No. 311


1. A plot of $\Delta V$ vs $I_{z}$ for a standard zener diode shows that the TC value goes through zero at a particular bias current. Operation at this point is recommended when using the diode as a reference.

2. A plot of a zener diode's aging characteristics demonstrates improved voltage drift after 1 week of accelerated aging.

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## Handle 100-V analog signals with 10-V ICs by using a divide/multiply technique

Linear-IC building blocks that are low in cost, reliable and small usually can't handle linear voltage swings greater than $\pm 10 \mathrm{~V}$.

However, a simple level-changing technique allows the handling of high signal levels to 145 V (Fig. 1) with such readily available $10-\mathrm{V}$ ICs as multiplexers, op amps, multiplying digital-to-analog converters, analog multipliers and solid-state switches.

The method employs a precision divide-by-ten amplifier followed by a precision multiply-by-ten amplifier at the system's input and output, respectively. The signal processing is performed between these amplifiers at $10-\mathrm{V}$ levels.

Divide-down and multiply-up precision resistors used in the amplifiers have the same ohmic values, as determined by the equation

$$
\frac{\mathrm{e}_{\mathrm{IN}}}{\mathrm{e}_{1}}=10=\frac{\mathrm{e}_{\mathrm{o}}}{\mathrm{e}_{2}}=\left(1+\frac{\mathrm{R}_{1}}{\mathrm{R}_{2}}\right) .
$$

The resistor ratios may be changed to handle other signal levels. Resistor $\mathrm{R}_{1}$ must be physically large to avoid internal self-heating, and hence, resistance change with applied voltage.

Performance achieved with $\mathrm{e}_{1}$ connected directly to $\mathrm{e}_{2}$ includes a bandwidth of over 1 MHz , a dc gain error of less than $0.01 \%$, a zero-error offset adjustable to closer than $\pm 200 \mu \mathrm{~V}$ and temperature coefficient of less than $\pm 150 \mu \mathrm{~V} / 10 \mathrm{C}$.

The output of the system uses a $15-\mathrm{mA}, \pm 100$ V output op amp. The modified Burr-Brown BB 3582, for example, together with its feedback network, provides a precise gain of 10 . The amplifier is compensated for optimum gain-bandwidth product at a gain of 10 with an external RC network to shape its open-loop gain-vs-frequency characteristics. The BB 3582 also includes thermal protection and current limiting and is housed in a TO-3 case.

Unfortunately, the divide/multiply system's signal-to-noise ratio is poorer than in a system employing only $100-\mathrm{V}$ signal levels. Although the signal is attenuated by the input-divider circuit, noise and dc drift are not.

Ronald W. Embley, Staff Engineer, Electronic Associates, Inc., West Long Branch, NJ 07764.

Circle No. 312

2. Four channels of output multipliers (a) and eight channels of input dividers (b) each fit on separate PC boards.


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 SUPPLIES ${ }^{\text {CATION }}$
## Use an IC voltage reference to control crystal-oven temperature accurately

Simple, accurate crystal ovens can be constructed very inexpensively by taking advantage of the temperature-dependent characteristics of a for-ward-biased silicon junction.

Discrete transistors can be used to sense and regulate temperature using the drift of a tran-sistor-junction's $\mathrm{V}_{\mathrm{BE}}-\mathrm{I}_{\mathrm{C}}$ curve with temperature (about $2.2 \mathrm{mV} /{ }^{\circ} \mathrm{C}$ ). However, an IC with a built-in temperature stabilizer, the LM399H, can be more easily used. This device, though designed for use as a voltage reference, can be altered to sense and regulate an external temperature. It also contains a stable zener that can be used as a precise noise-free voltage reference, or as a source to power low-level circuitry within the oven.

For oven-temperature control, only the stabilizer portion of the LM399H precision reference is used. Ordinarily, the stabilizer senses and maintains an internal constant 85-C die temperature. Its temperature-dependent current source varies from about 20 mA at 25 C to 1 or 2 mA at 85 C .

For crystal-oven use, the IC's plastic thermal shield is removed, and the device is firmly mounted to a heat sink that is attached to a copper-clad

PC board within the oven. The relatively large copper surface serves as a good averager of the oven's over-all internal temperature.

When all oven-control components are at room temperature and power is applied to the circuit (Fig. 1), the current drawn by the stabilizer is sufficient to saturate $Q_{1}$ and apply full power to oven heater $R_{1}$. As the oven and LM399 temperatures approach 85 C (approximately), the stabilizer draws less and less current through $Q_{1}$ and $\mathrm{R}_{1}$.

The oven reaches equilibrium at a temperature between 82 and 84 C . At equilibrium the heat generated by combining stabilizer, $\mathrm{R}_{1}$, and $\mathrm{Q}_{1}$ equals the heat lost through the walls of the oven. Between 12 and 15 min ofter turn-on, the temperature is within $1 \%$ of its final value. A few minutes more stabilizes it within $\pm 0.1 \mathrm{C}$ (Fig. 2).

In a prototype oven, $\mathrm{R}_{1}$ was made from a length of nichrome wire wound around the outside of the oven core. For an oven with a core volume of $3 \mathrm{in}^{3}$, a $10-\mathrm{W}$ heater was found to be sufficient.

Forrest Sass and Brent Welling, National Semiconductor Corp., 2900 Semiconductor Dr., Santa Clara, CA 95051.

Circle No. 313

2. In 10 to $\mathbf{1 5}$ minutes after turn-on, the temperature reaches about $1 \%$ of its final value.

# OHLV USHPY CIUES YOU ALL SH TOP 5PECS III RESISTOR PRECISIOI. 

## TCR

No other precision resistor comes even close to our $0 \pm 1 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ throughout the temperature range $0^{\circ} \mathrm{C}$ to $+60^{\circ} \mathrm{C}$.


## HIGH-FREQUENCY CAPABILITY

Our Bulk Metal ${ }^{n}$ resistive elements are inherently non-inductive, with rise and settling times as low as 1 nsec (no ringing)necessary characteristics in pulsed-data and highfrequency applications.


## STABILITY

After documented 10,000-hr load-life testing, our new S444 mil-spec resistor showed $\triangle \mathrm{R}$ 's of no more than 0.02\% (200 ppm).
 training program. Vishay Resistive Systems Group, 63 Lincoln Highway, Malvern, PA 19355; phone (215) 644-1300; TWX 510-688-8944.

## TOLERANCE

$\pm .01 \%$ is easy, and our hermetics even go to $\pm .001 \%$. While wirewounds can trim this tight too, can your circuits tolerate their inductance, ringing, and higher TCR?

## TC TRACKING

So close that all the resistors we've ever made will still track each other within $3 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$, and we can also match and track resistors to within $1 / 2 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$, regardless of resistance value and tolerance-good to know for ladder networks and bridge circuits.

## NO NOISE

-at least none that can be detected by the best noisemeasuring equipment on the market ( -40 dB low-end resolution)

Learn to make custom precision resistors in your prototype design lab, or even for production in your plant. Call or write for information on our popular


# Build a voltage-controlled oscillator with only one TTL-inverter package 

A low-cost (about $\$ 15$ for parts) voltage-controlled crystal oscillator (VCXO) can be built with only one TTL inverter logic package. Although not highly linear or extremely stable, the configuration is easily adapted to many applications, including phase-lock loops.

Three inverters ( $\mathrm{G}_{1}, \mathrm{G}_{2}$ and $\mathrm{G}_{3}$ ) connected in a series loop form an unstable logic configuration that ensures oscillator start-up; the smallsignal ac gain of a TTL gate is greater than one in the frequency range of interest. The consistency of this circuit's start-up characteristics contrasts strongly with start-up problems found in many other oscillator circuits built around TTL gates and inverters.

The crystal operates slightly below its series resonance, which, together with the components in the loop, provides a $360^{\circ}$ phase shift around the loop. An input control voltage of 0 to 5 V can vary the capacitances of the varactors, $\mathrm{CR}_{1}$ and $\mathrm{CR}_{2}$, to provide a small frequency change.
The values of $R_{1}$ and $R_{2}$ and the capacitances of $\mathrm{CR}_{1}$ and $\mathrm{CR}_{2}$ are not critical. They are selected so that the circuit oscillates at between 70 and $90 \%$ of the crystal frequency, without the crystal in place. Setting the "natural" frequency of the circuit in this manner prevents spurious operation of the oscillator at its higher harmonics.

The usable center-frequency range of the VCXO, is approximately 1 to 20 MHz . Of course, the frequency depends on the crystal and the type of TTL inverter used. A low-power 54L04 is useful for 1-to-2-MHz operation; a standard 5404 can cover 2 to 6 MHz ; a high-speed unit, such as the 54 H 04 or 54 S 04 , is required for higher frequencies.

A useful amount of frequency control can be achieved within the TTL-supply range of 0 to +5 V . Actual test results at 25 C for a $6-\mathrm{MHz}$ crystal using a 54S04 TTL inverter and 1N5463 A varactors provided the following results:

Control Voltage

| rol Voltage | Frequency |
| :--- | :--- |
| 0 V | $5,993,810 \mathrm{~Hz}$ |
| 1 | $5,999,508$ |
| 2 | $5,999,591$ |
| 3 | $5,999,620$ |
| 4 | $5,999,652$ |
| 5 | $5,999,667$ |

J. E. Buchanan, Westinghouse Electric Corp., Defense \& Electronics Systems Center, Friendship International Airport, P.O. Box 746, Baltimore, MD 21203.

Circle No. 314


## IFD Winner of May 10, 1976

Gordon L. Wong, Project Engineer, Data Tech, Div. of Penril Corp., 2700 S. Fairview Ave., Santa Ana, CA 92704. His idea "CMOS Analog Switches Can Precisely Control An Op Amp's Gain" has been voted the Most Valuable of Issue Award.
Vote for the Best Idea in this issue by circling the number for your selection on the Reader Service Card at the back of this issue.

SEND US YOUR IDEAS FOR DESIGN. You may win a grand total of $\$ 1050$ (cash)! Here's how. Submit your IFD describing a new or important circuit or design technique, the clever use of a new component or test equipment, packaging tips, cost-saving ideas to our Ideas for Design editor. Ideas can only be considered for publication if they are submitted exclusively to ELECTRONIC DESIGN. You will receive $\$ 20$ for each published idea, $\$ 30$ more if it is voted best of issue by our readers. The best-of-issue winners become eligible for the Idea of the Year award of $\$ 1000$.

ELECTRONIC DESIGN cannot assume responsibility for circuits shown nor represent freedom from patent infringement.

# To be precise about it, you need a wide choice of ultra precision resistors. 



## MAR Non-inductive Metal Film

Resistance range 20-1 meg $\Omega$ in 4 molded, axial lead sizes. All the advantages of evaporated metal film resistors with precision tolerances to $.01 \%$ and temperature coefficients to $5 \mathrm{PPM}{ }^{\circ} \mathrm{C}$.

The inherent low inductance and low capacitance of this design make it ideally suited to high-speed applications requiring a high degree of stability.

AR Resistor Networks Provide the best performance and maximum flexibility where multiple, interdependent resistors are required. Resistance ranges from $20 \Omega$ to $10 \mathrm{meg} \Omega$. TC and tolerance matching to $\pm 1 \mathrm{PPM} /{ }^{\circ} \mathrm{C}$ and $\pm .01 \%$ respectively.



AR-40 Metal Film Designed to satisfy critical design requirements where minimum T.C., stability and absolute accuracy are needed. Temperature coefficients as low as $\pm 1$ PPM $/{ }^{\circ} \mathrm{C}$ and purchase tolerances to $.01 \%$ are standard.


TaNFilm Resistor Networks
These feature inherent passivation, ratio tolerances to $.05 \%$ and TCR tracking to 3ppm. Standard packages are DIP sandwich from 4 pins to 20 pins and flatpack from 10 to 24 pins. Standard products include R-2R ladders in both package types, in R values from $2 \mathrm{~K} \Omega$ to $50 \mathrm{~K} \Omega$ and resolution from 8 Bits to 12 Bits. Other standard and custom networks also available.

For more technical information on these products call 319-754-8491. Or, for the broadest choice in resistors for all types of applications, write or call TRW/IRC Resistors, an Electronic Components Division of TRW Inc., 401 N. Broad St., Phila., Pa. 19108. Tel. 215-922-8900.

# We've improved our Mini-Mag magnetic circuit breakers. <br> Broader line. Improved design. Easier to order. And more available. 

New code designations,
easy to understand
and use in specifying
and ordering.

New positive latching mechanism, greater repeatability, more positive feeling toggle action.

New broader line of
standard models.

Mini-Mag yesterday and Mini-Mag today look alike outside. We wouldn't want to change that. Inside is where the big difference is; design changes provide greater dependability and improved performance. Latching, repeatability and toggle action have all been improved for reliable, low cost circuit protection.

And, don't overlook the intriguing possibilities for protecting semiconductor devices. Mini-Mag circuit breakers may be used to protect solid-state switches such as SCRs, triacs, power diodes, power transistors, and solid state relays. Circuit clearing time is as fast as 10 milliseconds.

Did we say the Mini-Mag is more available? You bet! One hundred authorized distributors stock Wood Electric Mini-Mag circuit breakers nationally. Sound good? Works out even better when you set up a local stocking program for delivery of exactly what
you need on a pre-arranged schedule.
Mini-Mag circuit breakers, UL and CSA Recognized, can now be specified in 330 standard distributor stock models, thousands more when you order direct from the factory.

Best of all, our new code designators give you an easier-to-use, more error free part numbering system for specifying and ordering improved Mini-Mag circuit breakers.

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European address: Electrical Products Group,
AMF International Limited, AMF House, Whitby Road, Bristol BS4 4AZ, England. Telephone: (0272) 778383, Telex: 449481, AMMAFOCO, BRISTL.

CIRCLE NUMBER 71

## New Products

## Microprocessor analyzer rearranges captured data into unusual displays



Biomation Corp., 10411 Bubb Rd., Cupertino, CA 95014. (408) 2559500. See text.

Biomation's 168-D $\mu \mathrm{P}$ analyzer is the first to rearrange captured memory transactions into various display formats. The unit digests up to 256 memory accesses, beginning and ending with specified start and stop events, then spits the information out on its CRT in one of four possible arrangements.
Two of the formats can be termed memory maps, and the other two called time listings (see display photos at upper right). With a moveable cursor and the panel controls, you can transfer one display to another and examine any desired data.

In the top photo, showing the Memory Display Mode, four blocks form the data field of the CRT, with each block composed of an $8 \times 8$ hexadecimal matrix. This mode shows the "big" picture of system activity-that is, in memory mode, the most significant eight
bits (pages) of a 16 -bit address word are mapped on the CRT field.

In the top photo, asterisks show that pages $\phi 3$ and $\phi \mathrm{C}$ were accessed. Alphanumeric readouts indicate that a total of 256 accesses were recorded, and that the blinking cursor (the line under $\phi \mathrm{C}$ ) is positioned at memory location 239. The notation, " 215 AT CURSOR," indicates that 215 accesses on page $\phi \mathrm{C}$ were recorded.

You can look at all 215 accesses by decrementing the cursor position. Other readouts show the start and stop events and the information at the cursor position.

In the second display mode, the Page Mode (center photo), you can take a more detailed look at the recorded data. The cursor position remains constant when you switch from one mode to the next, so that you look at the same access from mode to mode.

In the Page Mode, the data field maps the least significant eight bits of the address. Select which

page you want to view, and all addresses associated with that page are mapped.

Thus, in the center photo, page $\phi \mathrm{C}$ was selected, the cursor is at address $\phi 6$, and 24 accesses occurred at $\phi \mathrm{C} \phi 6$. The cursor displays the data at the 24th access. To look at others, you just decrement the cursor.

The bottom photo shows the (continued on page 130)

## INSTRUMENTATION

(continued from page 129)
168-D's Sequential Display Mode, Here, 256 data-field locationseach representing a memory loca-tion-are displayed with the accesses arranged according to time. The letters $R$ and $W$ indicate whether memory activity was a read or write. Note that the cursor again occupies position 237.

Not shown is the Biomation unit's List Mode, which shows 20 accesses at a time-the earliest at the top of the screen and the latest at the bottom. The listing can be in either hexadecimal or binary, as you wish. The cursor is displayed at the center of the list and its position determines which 20 accesses are displayed at any one time.

To hook up the 168-D to your system, you unplug your $\mu \mathrm{P}$, then replug it into a personality module. The module's interface plug then goes into the $\mu \mathrm{P}$ socket in the system under development.

Price of the 168 -D wasn't definite at press time, but the unit is expected to sell in the neighborhood of $\$ 5400$. Delivery is 60 days. Booth No. 483 to 489 Circle No. 305

## Unit computes digital averages



Monitor Labs Inc., 4202 Sorrento Valley Blvd., San Diego, CA 92121. (714) 453-6260. \$750.

With the Model 8640 signal aveager, the output of a single-pen strip-chart recorder can be used to display true digital averages for any time period from 1 to 99 min . The averager has a time shared output that allows the operator to preserve the original instantaneous output trace. During the averaging period, the strip chart displays the current reading as usual.

CIRCLE NO. 250


Triplett Corp., 286 Harmon Rd., Bluffton, $O H$ 45817. (419) 3585015. \$130.

Model 64 solid-state FET VOM is said to be drop-proof, burnoutproof and super-safe. Six lowpower ohms ranges on the portable, battery-operated model have an open circuit voltage of only 90 mV . Circuitry permits continuous meter operation with a carbon battery life in excess of one year even with long usage. The 29 -range tester has only a single range selector switch.
Booth No. 604-606 Circle No. 251

## Two inexpensive DPMs make their debut



Simpson Electric, 853 Dundee Ave., Elgin, IL 60120. (312) 697-2260. See text.

Two new 3-1/2-digit DPMs, Models 2860 and 2861, sell for under $\$ 80$. The 2860 ( $120 / 220-\mathrm{V}$ ac line operation) is priced at $\$ 79.00$ and the 2861 ( $5-\mathrm{V}$ dc operation) is priced at $\$ 69.00$. Readout is on 7 segment 0.43 -in.-high red LEDs with automatic polarity and out-ofrange indication, and fixed decimal. Accuracy is $\pm(0.1 \%$ of reading +1 digit). Stock ranges include 200 mV to $200 \mathrm{~V}, 20 \mu \mathrm{~A}$ to 200 mA , with special ranges to order. Case dimensions are $3.9 \times 1.93 \times$ 4.27 in. Panel cut out is $3.622 \times$ 1.680 in .

Booth No. 476-478 Circle No. 252


Here's a family of small, solid-tantalum capacitors with a per-unit substitution factor as high as one for four. To give you savings all the way in space, weight and cost.

Mallory THFs are specially designed for low impedance to ripple current at frequencies above 1 kHz through 100 kHz . Which makes them ideal for high-frequency power supply switching, or for regulator switching. Or for bypassing or filtering unwanted ripple currents.

ESR is low, so power losses are low. With the solid electrolyte and hermetic seal, long life is inherent. And electrical characteristics are very stable over a temperature range of $-80^{\circ} \mathrm{C}$ through $125^{\circ} \mathrm{C}$.

Mallory THF Spirit of '76 Capacitors come in a wide range of ratings; 5.6 to $330 \mu \mathrm{~F}, 6$ to 50 VDC . They're the result of our engineering program that's producing new high-performance types at less cost to you. Just ask your Mallory representative.

## Blend your own digital patterns \& timing signals

Interface Technology, 852 N . Cummings Rd., Covina, CA 91724. (213) 966-1718. Basic unit, \$5985; 10-12 weeks.

With microprocessor-controlled data and timing generator, Model RS-432, you can generate unique
digital patterns and special control timing signals simultaneously. Formats include serial or parallel data of specified output period; data from selected blocks of the word memory; data contingent upon special inputs such as levels, pulses or sense switches, as well as generation of data continuously or single shot.
Booth No. 873 Circle No. 253

## digital data printer does the most ${ }^{\text {s.n }} 495$

Newport built the Model 810 Printer with you in mind...with maximum interface potential, reliability and performance characteristics. We invite you to take a look inside and compare these outstanding features before you buy another printer.

- 9 columns, Std: expandable to 18
- Small size $41 / 2^{\prime \prime} H \times 81 / 2^{\prime \prime}$ W
- Programmable two-color printing
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- Convenient paper and ribbon loading
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NNEWPORT

## Low-cost pulse gen delivers $5-\mathrm{MHz}$ signal



Continental Specialties, 44 Kendall St., Box 1942, New Haven, CT 06509. (203) 624-3103. \$124.95.

Design Mate 4 pulse generator costs just $\$ 124.95$ and generates symmetrical and unsymmetrical pulses from 0.5 Hz to 5 MHz . The positive output ranges from 100 mV to 10 V , with a rise and fall time of less than 30 ns . The unit offers an independently controlled pulse width and spacing from 100 ns to 1 s in seven overlapping ranges, as well as independent variable amplitude CMOS, and fixed amplitude TTL outputs.

CIRCLE NO. 254

## DMM lets you set nulls with analog meter



Viz Test Instruments Group., 335 E. Price St., Philadelphia, PA 19144. (215) 844-2626. \$267 (dealer optional).

The WD-750A offers such features as a 3-1/2-digit, 3/4-in. LED display, and a built-in analog meter. The unit is powered by either 120 V or by its own built-in rechargeable nickel-cadmium battery. The multimeter has six dc and six ac voltage ranges covering 1 mV to 1200 V (rms for ac). There are five current ranges from $1 \mu \mathrm{~A}$ to 1 A and six resistance ranges from $1 \Omega$ to $10 \mathrm{M} \Omega$.

CIRCLE NO. 255

# Announcing the 1740A... a new 100MHz scope with fresh measurement ideas. 

In the time domain-Push the third channel trigger display button, release, and you have a simultaneous display of the trigger waveform plus channel A and B traces. Now you can make accurate timing measurements from the trigger signal to events on either or both channels.

A X5 vertical magnifier provides $1 \mathrm{mV} /$ div sensitivity on both channels to 40 MHz , without cascading, so you can monitor low-level signals directly. Signals such as the output of read/write heads of disc or mag tape units, low-level ripple on power supplies, or medical sensor and electro-mechanical transducer outputs.

In the data domain - You can combine the 1740A with HP's 1607A Logic State Analyzer and use the analyzer's pattern trigger or
 delayed trigger output for external scope triggering. Add the "Gold Button" (an optional logic-state pushbutton in lieu of A versus B) for just $\$ 128^{*}$ and (with the 1607 A ) you have the convenience of logic-flow display or real-time display at the push of a button. That means you can view the logic states of operational circuitry directly for pinpointing a program problem. Then - with a push of a button - take a look at the waveforms you've selected at that specific point in time.

Add to all this, features such as selectable input impedance ( 1 megohm or 50 ohms ) and the time-tested $8 \times 10 \mathrm{~cm}$ CRT used in our 180 System lab scopes for bright, easy-toread displays. Priced at just $\$ 2,095^{*}$, the 1740A with its new ideas, simplifies both real-time and data-domain measurements. When

## STATE

STPL you get your hands on this
scope - you'll know you're working with a quality instrument. Give your local

HP field engineer
a call today.

* Domestic U.S.A. price only.


## Data/Time Domain Oscilloscopes

 better than $\pm 0.0025 \%$. With gain and offset errors trimmed out, the absolute accuracy of the Model 109 makes it compatible with resolutions of $\pm 16$ bits binary or $\pm 51 / 2$ digits BCD.

## SUPER STABLE

Gain TC $< \pm 1 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ Offset TC $< \pm 0.2 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$

## ECONOMIC $\$ 99^{\circ \circ}$

*Requires external counter and clock.

282 Brokaw Rd., Santa Clara, CA 95050 Tel. (408) 244-0500, TWX 910 338-0254

John Fluke, P.O. Box 43210, Mountlake Terrace, WA 98043. Mountlake Terrace, WA 98043 .
(206) 774-2211. 1953A with -15 , $\$ 1595$; 120 days.

An internal IEEE-488 bus interface option ( -15 ) is now available for Model 1953A universal counter/timers. The interface, available as a factory-installed option at time of purchase, permits interconnection between the 1953A and other compatible instruments. Option 15 uses the recommended ASCII code format and permits ASCII code format and permits
full remote operation of range and function selection, control of the A and B trigger slopes, ac-dc coupling, attenuation selection and the separate-common input status between channels A and B.

CIRCLE NO. 256

## INSTRUMENTATION

IEEE-488 interface controls counter-timer


## Sig gens offer

 improved specs

Boonton Electronics, Parsippany, NJ 07054. (201) 887-5110. 102C, $\$ 3575 ; 102 \mathrm{D}, \$ 4295 ; 4$ wks.

Two new FM-AM signal generators offer an extended frequency coverage from 450 kHz to 520 MHz , as well as harmonic distortion reduced to less than $0.5 \%$ for $100-\mathrm{kHz}$ deviation in the FM band, lower residual FM, and a typical SSB broadband noise floor of 135 $\mathrm{dB} / \mathrm{Hz}$. Model 102D provides a phase-locked digital display of frequency with $100-\mathrm{Hz}$ resolution and a stability of $0.5 \mathrm{ppm} / \mathrm{h}$ after warmup. Model 102C is identical to the 102D but has no phase lock.

CIRCLE NO. 257

10-MHz dual-trace scope carries $\$ 595$ price tag


Tektronix, P.O. Box 500, Beaverton, OR 97077. (503) 644-0161. $\$ 595$.
The D61a dual-trace oscilloscope attains bandwidths to 10 MHz . Fully transistorized, it weighs only 15 lbs . Operation is simplified by automatic selection of chopped and alternate modes and fully automatic triggering. In the TV trigger position, it automatically selects line or frame displays. With external X capabilities, this scope can be used in the X-Y mode. Vertical sensitivity is $10 \mathrm{mV} /$ div at bandwidth, and the $8 \times 10-\mathrm{cm}$ CRT provides high-contrast displays.

CIRCLE NO. 258

## Ac powered DPMs cost $\$ 60$ in quantity



Gralex Industries, 155 Marine St., Farmingdale, NY 11735. (516) 694-3607. \$94 (ac version).

Model 38 DPMs use LSI circuits and the latest in LED display technology. Available in both ac line and $5-\mathrm{V}$ de powered versions, Model 38 features 3-1/2 digits, bipolar operation with automatic minus display, 0.5 -in digit display and the proposed NEMA standard DPM case. The ac-line version is also pin compatible with most ac DPMs now in popular use.

## Rose enclosures. New. Beautiful. Quality, Standards.



Rose Enclosures, made specifically for electronic use, culminate years of design, engineering and production experience. Extra high quality, preci-sion-finished units provide functional protection, easy access, excellent esthetics. Available materials: Lexan, Aluminum, Polyester and ABS. Clear plastic covers with Lexan and ABS. A competi-tively-priced stock of Rose Enclosures is maintained in Belding,
Mich. for immediate shipment. Contact us at (616) 794-0700.

Visit Booth 653 - ISA-76 International Conference and Exhibit - Houston CIRCLE NUMBER 77

## Power Terminal Blocks Unlimited -A Full Selection From Our Stock

Reduce capital tie-up. Save storage space. Draw on our inventory to get the block you want when you want it. Full range of capacities, sizes and types for both copper and aluminum conductors. Most are UL recognized and CSA certified.

Request bulletin. If you have a special requirement, give us the specs. We'll design a block specifically for your needs.

## Underwriters Safety Device



## BergStik Straight and Right-Angle Headers... for production line or prototype.



Here's an inexpensive method of inserting . 025 square pins in boards for wire-wrapping or input/ output usage. Header consists of drawn wire pins ( $3 / 4$-hard phosphor bronze) molded in position in glass-filled nylon carriers. Result: A stronger pin with no rough edges. Headers are available on $.100^{\prime \prime}, .125^{\prime \prime}$ and $.150^{\prime \prime}$ centers. Carriers are notched to allow the user to break to length for any desired number of pins. BergStik Headers also have tongue-and-groove design to facilitate side-by-side stacking. Headers can be loaded like components and do not interrupt flow on the assembly line. Just plug 'em in! (Write for Bulletin 140)

BEPG
ELECTRONICS
New Cumberland, Pa. 17070 Phone: (717) 938-671

## CIRCLE NUMBER 79

## Minisert" Low-Profile P.C.Socket... provides repeated pluggability for I.C.'s, DIP's, etc.



For use where space is restricted $.030^{\prime \prime}$ above and $.088^{\prime \prime}$ below board

extends just socket fits wide range of hole sizes. leads of DIP's. LED's and LSI's. Elastromeric eads of DIP's, LED's and LSI's. Elastromeric sea keeps foreign matter out of socket. Free standing design can be used in any socket pattern. Semiautomatic machine inserts at rates up to 2000 per hour. Write for Bulletin 120 or call.
*Sockets sell for $\$ 20.00$ to $\$ 30.00$ per thousand in volume quantities.
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New Cumberland, Pa. 17070 Phone: (717) 938-6711

## Multi-unit stores, processes, analyzes



Princeton Applied Research, Box 2565, Princeton, NJ 08540. (609) 452-2111. \$2750.

Model 4101 scan recorder accommodates the varied requirements of recording, storing, displaying and transferring of analog data. It can serve as a digital $\mathrm{X}-\mathrm{Y}$, strip-chart or transient recorder. Analog input signals are digitized to 10 -bit accuracy and stored in 1024 words of memory or in two 512banks. The curves in memory are continuously available for scope display or hard-copy recording by conventional plotters. A front panel, 3-1/2-digit display under cursor control allows readout of word amplitude and address.

## Portable scope combines high bw and sensitivity



Telctronix, Inc., P.O. Box 500 , Beaverton, OR 97077. (503) 644 0161. \$3300; 4 wks.

A bandwidth of 250 MHz combined with a sensitivity of 5 $\mathrm{mV} /$ div characterizes the company's latest portable scope. The $250-\mathrm{MHz}$ bandwidth is specified at the probe tip. Sweep rate is to 1 $\mathrm{ns} / \mathrm{div}$ on an $8 \times 10-\mathrm{cm}$ CRT Other features: delayed sweep, variable trigger holdoff, trigger view, automatic scale-factor readout, and availability of clip-on battery power. The 475A also offers (as an option) direct numerical readout of displayed time intervals and a precision built-in DMM.

CIRCLE NO. 261

## Scanner handles up to 100 channels



Keithley Instruments, 28775 Aurora Rd., Cleveland, OH 44139. (216) 248-0400. \$1995.

Model 703 scanner mainframe can switch up to 100 channels. When used in the company's System 1, a calculator-controlled measuring system, the 703 can be remotely controlled to select one or more of 100 channels on a single mainframe. The 703 permits selection of multiple active channels simultaneously. Remote control of channels can be accomplished in any order. A three-digit readout on the front panel displays the most recently switched channel. The display also provides two "error" indicators.

CIRCLE NO. 262

## Ca", IOM COST REA RELAY ROEK HIBD EPOK EDIILD

## $\$ 1.85$ to 496

Depending on coil voltage, contact level and volume.
Blue Boy Reed Relays offer:

- Protection from handling, solvents, tough environments
- 1 pole normally open contacts up to 10 watts
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- Coil voltages 5 through 48 VDC; $.35^{\prime \prime} \mathrm{H} \times 1^{\prime \prime} \mathrm{L}$

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26477 Golden Valley Road • Saugus, California 91350 (805) 252-8330 - (213) 788-7292 . TWX 910-336-1556 IIEB-TROLIUC.

## DP Ris: RELNY 35 NODELS

 Your choice of:- SPST, DPST \& SPDT dry reed contacts, 3 to 10 watts.
- SPST mercury wetted reed contacts, 28 watts.
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interface your microcomputer.systems to the real world. From development through finished product, Burr-Brown has analog input and output systems that are totally hardware and software compatible with your microcomputer system.

# Introduce Your Microcomputer to the Analog Worldthe Easy Way. 

Using an Intel SBC80/10 (8080), Intellec ${ }^{\circledR}$ MDS800 (8080, 3000), Motorola EXORciser ${ }^{\circledR}$ (M6800)?

We now have analog I/O systems that are mechanically and plug compatible with each of these microcomputer
systems, as well as the IIO system we recently introduced for Intel's Intellec ${ }^{\circledR} 8$.

Our IIO systems can be configured to meet your exact needs, too. Choose 8-channel differential input, 16-channel single ended input, high-level
or low-level. The standard models have DC/DC converters which require only
+5 V power. In OEM applications where the necessary $\pm 15 \mathrm{~V}$ are available,
Burr-Brown analog Input/Output systems can be ordered without power supply, cable and connector. This brings the price down to just $\$ 295$ each, in hundreds.
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Thermistor Division St. Marys, PA 15857 814/781-1591 • Telex 91-4517

# Digital encoder/decoders enhance audio system sound 



Hybrid Systems, Crosby Dr., Bedford, MA 01730. (617) 275-1570. See text.

Using a novel conversion technique based on the delta-sigma process, Hybrid Systems has developed encoding and decoding circuits optimized for audio-signal manipulation. The two circuits, the DV620 analog-to-digital encoder and the DV621 digital to analog decoder, can be used to digitally delay and manipulate audio signals in a way not previously economically feasible.
Both units have a frequency range of 20 Hz to 10 kHz and require a clock signal of 250 to 500 kHz . The encoder accepts inputs of $1 \mathrm{~V} \mathrm{rms} \mathrm{maximum} \mathrm{(for} 0 \mathrm{~dB}$ ) and has an input impedance of $150 \mathrm{k} \Omega$. The decoder has an output offset of $\pm 50 \mathrm{mV}$ that is trimmable to zero and an output impedance of $500 \Omega$. However, since the units are usually ac coupled, the offsets can often be neglected.

When used as a set, with or without a digital delay interspersed, the $620 / 621$ has an overall gain of $0 \pm 2 \mathrm{~dB}$; a signal-to-noise ratio of 65 dB , minimum; and $1 \%$ maximum distortion (at 0 dB ).

Each circuit is housed in a $2 \times$ $2 \times 0.4-\mathrm{in}$. module and requires +15 at 30 mA and -15 V at 15
mA. All digital inputs and outputs are CMOS compatible, and the analog output of the 621 can deliver about 5 mA .

Basically, the encoder circuit samples the input at about $4-\mu \mathrm{s}$ intervals (depending upon the clock frequency) and encodes the analog value into digital pulses whose rate and width are directly proportional to input amplitudes. The decoder takes the pulse train and converts it back to analog form, smoothing out the jagged areas with a filter.

To improve on the basic deltasigma process, the modules use a "memory" circuit that maintains a running tally of the audio-signal variations and makes an educated prediction of the waveform slope for the next clock period.

The DV620 and DV621 modules, when used with shift registers, can recreate concert-hall realism by delaying the sound output that reaches a set of rear speakers. (The set supplements the main front speakers in a stereo system.) The AudioPulse Div. of Hybrid Systems has such a unit available for the audiophile, the Model 1.
Price for either the encoder or decoder circuit is $\$ 59$ for single unit purchases and delivery is from stock.



# Cut size, weight and cost with hybrid s/d converters 



ILC Data Device Corp., Airport International Plaza, Bohemia, NY 11716. (516) 567-5600. See text.

Whenever a manufacturer claims he can do a job in one-hundredth the volume, one-tenth the weight and at a lower cost than his closest competitors we usually say bull. Not so with the HSDC-10 hybrid synchro-to-digital converter made by ILC Data Device Corp. Its 10 -bit converters measure only $0.8 \times 1.9 \times 0.21 \mathrm{in}$. and offer performance equal to or better than the Model SDC1603 from Analog Devices (Norwood, MA), the LSI 90 from North Atlantic Industries (Plainview, NY) or the Model SDC410 from Computer Conversions (East Northport, NY).

The tracking rate of DDC's unit is 100 rps -more than five times that of the Analog Devices' and Computer Conversions' units. Even more important, the high acceleration constant ( $\mathrm{K}_{\mathrm{a}}=200,000$ for 400 Hz ) of the HSDC-10 means that the acceleration lag will be less than 1 LSB for speeds of up to $70,000^{\circ} / \mathrm{s}^{2}$. This compares with only $3500^{\circ} / \mathrm{s}^{2}$ for the LSI 90 from North Atlantic.

Converter settling time for a $179^{\circ}$ step (to within 1 LSB ) is a speedy 25 ms -eight times faster
than previously available. The fast settling of the HSDC-10 permits it to be used in systems with reference frequencies of up to 3000 Hz .

All of the three competing units require +5 and $\pm 15 \mathrm{~V}$ and dissipate more than 2 W . In comparison, the DDC unit needs only two supplies-a +15 V and a second +3 to +15 V supply. The converter dissipates only 0.2 W and when set to handle TTL can drive two standard TTL loads.

The HSDC-10 comes in all standard synchro and resolver formats and is fully transient protected. Transformer isolation can be obtained as an option. Operating temperature ranges of 0 to 70 and -55 to +125 C are available. All units are manufactured and processed at MIL-STD-883 Level C, with Level B processing available.

All three competing units do have built-in transformer isolation and cost from $\$ 100$ to $\$ 200$ more than the $\$ 345$ single-unit price of the HSDC-10. Delivery for the HSDC-10 is from stock.

| For DDC | CIRCLE NO. 301 |
| :--- | :--- |
| Analog Devices CIRCLE NO. 302 |  |
| Computer Conversions |  |
|  | CIRCLE NO. 303 |
| North Atlantic |  |
| CIRCLE NO. 304 |  |



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For your copy of our reliability report or complete resistor network data, write: CTS OF BERNE, INC., 406 Parr Road, Berne, Indiana 46711. Phone (219) 589-3111.


## Signal conditioner ckt has internal reference

Calex Mfg., 3305 Vincent Rd., Pleasant Hill, CA 94523. (415) 932-3911. From \$84; stock to 2 wks.

A complete signal-conditioner, the Model 165 bridgesensor, comes in a single $2 \times 2 \times 0.6 \mathrm{in}$. package. It's more than just an instrumentation amplifier since it also contains an internal reference circuit, a bridge supply and a comparator. The 165 A operates from a single $+15-\mathrm{V}$ supply and the Model 165B operates from unregulated +28 V dc. With single-supply operation, the instrumentation amplifier output has a range of +50 mV to +10 V . With the inputs shorted together, the amplifier section can be connected for an output of either +50 mV or +5 V . With a dual supply the instrumentation amplifier can also be connected for an output of $\pm 10 \mathrm{~V}$.

CIRCLE NO. 263

## Triple active filter has switchable cutoffs

Fogg System Company, Box 22226, Denver, CO 80222. (303) 758-2979 From $\$ 196$ (unit qty.); stock.

A triple high-pass filter, the Model 127, can be frequency programmed via switches built onto its circuit card. Each high-pass filter section consists of an active two-pole Butterworth fitler whose low-frequency $3-\mathrm{dB}$ point is user programmed with a DIP switch The high-frequency $3-\mathrm{dB}$ point is greater than 20 kHz for all units. The triple filter is housed on a 5.4 $\times 4.55 \times 0.7$ in. PC card. Four versions, that provide different ranges for the low-frequency cutoff point, are available. They are: 1 to $16 ; 10$ to 160,100 to 1600 and 500 to 8000 Hz . Roll-off is 40 dB per decade for all units. The three filters can also be combined to give three independently adjustable frequency responses from a single input or they can be connected in series for faster low-frequency roll-off.

CIRCLE NO. 264

## Reference junction takes 10 input channels

San Diego Instrument Laboratory, 7969 Engineer Rd., San Diego, CA 92111. (714) 292-0646. \$180 (5up) ; stock.

The SL100, a 10 -channel reference junction, receives 10 thermocouple wire pairs with guards and provides transition of the wires to copper conductors. It also electronically compensates the selected pair relative to 0 Celsius. Compensation for any wire type (types J, K, T, E, R, S, and B are standard) is available, as well as any reference temperature. The unit measures $6 \times 3.14 \times 1.06 \mathrm{in}$. and comes with a choice of $0.04-\mathrm{in}$. diameter solder pins or a ribboncable edge connector. Oversize truss-head screws and large binding posts can accommodate up to No. 14 AWG thermocouple wire. An outer metal cover, indicator light, and zero adjustment are also provided. Compensation is within $0.025 \mathrm{C} /{ }^{\circ} \mathrm{C}$ with a long-term stability of $0.002 \mathrm{C} /{ }^{\circ} \mathrm{C} /$ year.

CIRCLE NO. 265


The Ferranti Model ZN425E-an 8 bit dual mode analog to digital/digital to analog converter features:

- Single chip monolithic construction
- Typical settling time $1.0 \mu \mathrm{~S}$ for 1 L.S.B. step
- 8 bit binary counter, R-2R ladder network and switches

- On-chip precision voltage reference
- Self-contained, precision ramp generator
- TTL and CMOS compatible


*1000 piece price


## Temperature controller spans -50 to 550 F

PSG Industries, 1225 Tunnel Rd., Perkasie, PA 18944. (215) 2573621. $\$ 10.95$ (2500-up); stock.

The TC-series of solid-state temperature controllers will control temperatures over a range of -50 to 550 F . They can handle a current load of 1 A at 115 V ac and use zero crossover switching. Available features of the controllers include failsafe protection, 0.2 C differential or better and adjustable temperature control.

CIRCLE NO. 266

## Data-acquisition system handles $\pm 10 \mathrm{mV}$ inputs

Burr-Brown, International Airport Industrial Park, Tucson, AZ 85706. (602) 294-1431. \$170 (100-up); stock.

The SDM853 eight or 16-channel data-acquisition system can convert analog signals ranging from $\pm 10 \mathrm{mV}$ to $\pm 10 \mathrm{~V}$ into 12 -bit digital words. Inside the system is a low drift, unity-to-60-dB gain instrumentation amplifier, a 16-channel multiplexer, a sample/hold amplifier and a 12 -bit a/d converter. All blocks are not internally connected so the data acquisition system can be very flexible. The amplifier has a gain range from 1 to 1000 , and an offset voltage drift of $2 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ at $\mathrm{G}=1000$. Over-all the unit's accuracy is $\pm 0.025 \%$ of full-scale reading, with a maximum nonlinearity of $\pm 1 / 2$ LSB. Gain error and offset error are both adjustable to zero. System accuracy drift with temperature is $30 \mathrm{ppm} /$ ${ }^{\circ} \mathrm{C}$ of reading. The differential amplifier CMRR is $74-\mathrm{dB}$ down at 1 kHz and 65 dB down at 3 kHz . Channel cross-talk is 80 dB down at 2 kHz (OFF channel to ON channel). The modular data-acquisition has a 33 kHz max throughput rate. Maximum input voltage is $\pm 16 \mathrm{~V}$, and the complementary output coding can be unipolar straight binary, bipolar offset, or binary two's complement. Input impedance is $100 \mathrm{M} \Omega$, shunted by 10 pF (OFF channel); $100 \mathrm{M} \Omega$, 100 pF (ON channel). The SDM853 operates from $\pm 15-\mathrm{V}$ supplies over a 0 to 70 C range and measures $4.6 \times 3 \times 0.375 \mathrm{in}$.
Booth No. 503-505 Circle No. 267

N ow, a single integrated circuit, our TAD-32 (Tapped Analog Delay), can provide filtering with passband-to-stopband ratios of 40 DB or more per device. Simple variation of the clock sampling rates over 5 decades will accordingly shift a given filter characteristic. Transversal or recursive filters can be constructed with over 60DB dynamic range and linear phase. Tapped delays up to several hundred milliseconds are possible.

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RETICON ${ }^{\circ}$
910 Benicia Avenue, Sunnyvale, California 94086 PHONE: (408).738-4266 TWX: 910-339-9343 CIRCLE NUMBER 89

## Digital-to-resolver ckt has $\pm 6$-min accuracy

Singer, Kearfott Div., 1150 McBride Ave., Little Falls, NJ 07424. (201) 256-4000. $\$ 225$ (100-up); 30 days.

A digital-to-resolver converter (part number C70 4773 514) is suited for use in servo loops, CRT
displays, and other low power applications. The resolver output is derived from a reference voltage and digital inputs at sufficient power levels to drive loads as low as $2000 \Omega$. The unit measures $2.565 \times 2.565 \times 0.42 \mathrm{in}$. and has an accuracy of $\pm 6$ minutes over the operating temperature range of -55 to +85 C .

CIRCLE NO. 268

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CIRCLE NUMBER 92


Kurz-Kasch, Inc.
Electronics Division,
1501 Webster Street, P.O. Box 1246, Dayton, Ohio 45401 (513) 223-8161

## Compact s/d converters have 10 or 14 -bit inputs

North Atlantic Industries, Terminal Dr., Plainview, NY 11803. (516) 681-8600.

The LSI/90 synchro-to-digital converter measures only $2.12 \times 2.56$ $\times 0.82 \mathrm{in}$. It uses the company's LSI Trig/Logic processor chip to reduce the size. The $s / d$ converter is TTL compatible and can also be used in MOS interfaces without modification. Both 14 and 10 -bit resolution versions are available. The standard operating range is 0 to 70 C , and an optional "HT" version that operates between - 55 and +105 C is also available. Accuracy of the converter is $\pm 3$ minutes up to speeds of $1440^{\circ} / \mathrm{s}$. The LSI/90 converter series is electrically identical to the company's LSI/85 series.

Booth No. 495 Circle No. 271

## Hybrid converter series contains 18 models

Datel Systems, 1020 Turnpike St., Canton, MA 02021. (617) 828-8000. See text; 6 to 8 wks.

The HX and HZ series of 12 -bit hybrid data converters contain 12 different d/a converter models and six different a/d converter models. The models span operating ranges of 0 to $70,-25$ to $+85,-55$ to +100 , and -55 to +125 C . The converters are available in both glass or metal, hermetically sealed DIPs. There are two basic a/d converter lines with three models each: the ADC-HX series units have a conversion time of $20 \mu \mathrm{~s}$ and cost from $\$ 119$ to $\$ 185$; and the ADC-HZ series units have a conversion time of $8 \mu \mathrm{~s}$ and cost from $\$ 149$ to $\$ 215$. All have five programmable input ranges, 20 $\mathrm{ppm} /{ }^{\circ} \mathrm{C}$ maximum tempco, and $1 / 2$ LSB maximum nonlinearity. The 12 DAC-HZ converters have output settling times of $3 \mu \mathrm{~s}$ and cost from $\$ 49$ to $\$ 175$. The $\mathrm{d} / \mathrm{a}$ converters have five programmable voltage output ranges, $20 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ maximum tempco, and $1 / 2$ LSB maximum nonlinearity. Two models in the series have a tempco of 10 $\mathrm{ppm} /{ }^{\circ} \mathrm{C}$ maximum.

CIRCLE NO. 272

Sample/hold amplifier locks signal in 500 ns


Intech, 282 Brokaw Rd., Santa Clara, CA 95050. (408) 244-0500. $\$ 180$ (unit qty.); stock.

The A-881 high speed sample/ hold amplifier has a 500 ns maximum acquisition time to $0.01 \%$. Its maximum aperture is 5 ns and the circuit can aquire a 4 kHz input signal to within a $1 / 2$ LSB uncertainty for 12 -bit applications. Nonlinearity of the output is $0.005 \%$ maximum and the dielectric absorption of the circuit is less than $0.01 \%$ over a $5 \mu$ s hold time. The A-881 comes in a $2 \times 2$ $\times 0.5$ in. module and requires $\pm 15 \mathrm{~V}$ supplies. An operating range of 0 to 60 C is guaranteed and if derated specs can be tolerated, a range of -25 to 85 C is possible.

CIRCLE NO. 273

## Data-acquisition system does 30,000 samples/s

Analog Devices, P.O. Box 280, Route 1 Industrial Park, Norwood, MA 02062. (617) 329-4700. See text; stock.

A complete 12 -bit data-acquisition system is available in a $3 \times$ $4.6 \times 0.375 \mathrm{in} .(76.2 \times 116.8 \times$ 9.5 mm ) module. The system is a pin-compatible alternative to several popular competitive models and costs only $\$ 295$ in single quantities. The DAS 1128 includes a $15-\mu \mathrm{s}, 12$ bit a/d converter, sample-and-hold amplifier, precision reference, highstability buffer amplifier, and a 16 channel multiplexer. System features include a $2.5 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ nonlinearity tempco, an $8 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ gain tempco, $\pm 0.12 \%$ of FSR relative accuracy at a 33 kHz throughput rate. The DAS1128 requires 15 V at $40 \mathrm{~mA},-15 \mathrm{~V}$ at 70 mA , and 5 V at 250 mA .

CIRCLE NO. 274

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CIRCLE NUMBER 95


COMPONENTS

## Lighted pushbuttons available in LED versions



Illinois Tool Works, Inc., Licon Division, 6615 W. Irving Pk. Rd., Chicago, IL 60634. (312) 282-4040. From \$3.90: list; stock to 8 wks.
The new LED version of Licon's 05 lighted-pushbutton series is offered in seven different background button color with red, yellow and green LEDs. Centered in a $1 / 2$-in. button, a $0.1 \times 0.25-\mathrm{in}$. window displays the LED over a $150^{\circ}$ viewing angle. LEDs remain fixed during switch operation, which improves reliability. Life of the switch is two-million cycles at logic-level loads. Bifurcated contacts are used, and options of 1 or 2-pole NO momentary switching or indicator only is available. A maintained-latching logic circuit is also available.
Booth No. 504-506 Circle No. 275
Semiconductors provide overvoltage protection


Midwest Components, Inc., 1981 Port City Blvd., Muskegon, MI 49443. (616) 777-2605.

These semiconductor devices can provide low-cost, recyclable protection against overvoltages applied to ohms scale of digital meters. Under normal operation, the devices maintain a low resistance until a high voltage causes a sharp rise in resistance, which switches the voltage source out of the circuit. Maximum applied voltages ranging up to 1000 vdc can be handled.
Booth No. 1024 Circle No. 276

## Cermet trimmer mounts on PC boards



Allen-Bradley, 1201 S. Second St., Milwaukee, WI 53204. (414) 6712000. $\$ 0.55$ ( 1000 up ); 4 to 6 wks .

The new Type 90 , single-turn $1 / 2-\mathrm{W}$ cermet trimming potentiometer has terminals spaced on a 0.1in. grid for PC-board mounting. The trimmer has a resistance range of $100 \Omega$ to $2 \mathrm{M} \Omega$, and it features a metal actuator and sturdy terminals. It's offered in two versions: Type 90 V for top adjustment and 90 H for side adjustment.

CIRCLE NO. 277

## Ceramic cap trimmer covers audio to 500 MHz



Johanson Manufacturing Corp., 400 Rockaway Valley Rd., Boonton, NJ 07005. (201) 334-2676. \$0.75 ( 1000 up) ; stock.

The new 9371 series of ceramic trimmer capacitors are $50 \%$ smaller than most other trimmers of this type, but still provide high capacitance values, according to Johanson. They are available in four capacitance ranges from 1.5 to $4,3.0$ to $10,3.5$ to 18 and 5.0 to 25 pf with Qs $>300$ at 10 MHz . All units meet or exceed MIL-C81. They have an over-all diameter of 0.225 in . with a $0.215-\mathrm{in}$. above-board height. The 9371 series are designed for audio-to500 MHz use.
Booth No. 1003 Circle No. 278

## CMOS logic drives sensitive relays

Electrodyne, Inc., 1404 21st St., Milwaukie, OR 97222. (503) 6540711. \$6 (1000 up).

Electrodyne's DPDT sensitive (30 mW ) electromechanical DIP Series 10 relays can be switched directly by many IC devices, such as CMOS logic. Elimination of the need for interfacing reduces design cost and saves board space.

Also, no heat sinking is required. These rotary balanced-armature, environmentally sealed relays don't incorporate glass-capsule reed switches commonly used. The terminal grid spacing of $0.1 \times 0.3$ in. allows plugging directly into DIP sockets. The relay can be used to switch dry circuits to $2-\mathrm{A}, 28-\mathrm{V}-\mathrm{dc}$ loads. Low capacitance ( 0.3 pF $\max$ ) provides excellent rf-switching capabilities.

CIRCLE NO. 279

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COMPUTER DEVICES OF CALIFORNIA 11901 Burke St., Santa Fe Springs, Calif. 90670 (213) 696-2595 - TELEX: 69-6352 For local offices see EEM

## Solid-state relay has random turn on



Gordos/Grigsby-Barton, Inc., 1000 North Second St., Rogers, AR 72756. \$11.27 (1000 up); 2 to 3 whs.

A new series of random-turn-on, all-solid-state photocoupled relays, the Series 6B 16000, can control resistive, capacitive or inductive loads. Standard features include reverse-input-polarity protection, current-limited input and an RCsuppression network. Housed in molded packages, the UL-recognized units include output ratings of 2.5 to 25 A at 24 to 280 V ac, $50 / 60$ Hz . Their 4 -to-32-V-dc input is TTL-compatible. A constant-current feature provides improved LED life characteristics over the full input-voltage range. The relays are particularly well suited for control of loads with large voltage/ current phase shifts, or where zero-voltage turn-on is not required.

CIRCLE NO. 280

## DIP ceramic capacitors feature low inductances

Sprague Electric Co., 347 Marshall St., North Adams, MA 01247. (413) 664-4411.

Layer-built ceramic capacitors in four-pin bantam DIPs, Type 935C Monolythic, feature low inductance and low impedance with a very high self-resonant frequency. They offer a typical inductance of less than 4 nH , when used in the four-terminal mode. Alternate layers of ceramic dielectric material and metallic electrodes are fired into a rugged homogeneous block. Available with COG (NPO) and X7R temperature characteristics, these capacitors have preferred ratings of $0.01,0.022,0.047$, and $0.1 \mu \mathrm{~F}$ at 100 V dc.

## Optical limit switch is bounceless



Optron, Inc., 1201 Tappan Circle, Carrollton, TX 75006. (214) 2426571. \$2.75: OPS-200 (1000 up); stock.

The OPS-200 and OPS-200A solid-state switches feature a shutter, controlled by a snap-action mechanism, which interrupts a light path between a gallium-arsenide infrared LED and a silicon photosensor. There is no contact bounce or contact contamination. The switches interface high-speed logic circuitry without the buffering required with conventional switches. Operated at rated currents, the OPS 200 and OPS 200A still meet specifications after $100,000-\mathrm{h}$ of operation. The OPS 200 in the closed condition with a LED drive current of 30 mA provide a minimum output of 1.6 mA at 0.4 V . Maximum output with the light path blocked is $20 \mu \mathrm{~A}$ at 12 V. The OPS 200 A contains a photodiode sensor followed by a Schmitt trigger with $140-\mathrm{mA}$ output sink capability, eliminating the need for amplifiers in most applications. Power supply range is 5 to 9 V at a typical supply current of 4 mA .

## CIRCLE NO. 282

## Metal proximity switch senses to 40-k pulses/s

Electro Corp., 1845 57th St., Sarasota, FL 33580. (813) 355-8411. $\$ 55$ (unit qty); stock to 4 wks.

Three new noncontact metalsensing proximity switches, for 5, 12 and $24-\mathrm{V}$-dc operation, are designated Models 55527, 55525 and 55526, respectively. They are designed to provide a stable and fixed sensing distance, typically 0.375 in . for 4130 -steel and 0.175 in. for aluminum target. Switching speeds are 0 -to- 40,000 impulses-persecond. Output current is 10 mA dc for $5 \mathrm{~V}, 100 \mathrm{~mA}$ de for 12 V and 500 mA dc for $24-\mathrm{V}$ models.

CIRCLE NO. 283

Watch crystals made in tuning-fork form


Statek Corp., 1233 Alvarez Ave., Orange, CA 92668. (714) 639-7810. $\$ 1.50(100,000 u p)$.

A new package for watch crystals, Model WX-35, is the smallest and lowest-cost watch crystal in the industry, according to Statek. The crystal is a microminiature tuning fork made of quartz-a major departure from the XY-bar crystal usually employed by other US manufacturers. The crystal is $0.325-\mathrm{in}$. long by $0.15-\mathrm{in}$. wide and $0.054-\mathrm{in}$. thick. The thin leadless ceramic package allows watch module manufacturers to design thinner and smaller watches. Customers who are now using the WX2 CF are planning to phase in the new WX-3.

CIRCLE NO. 284

## Delay line to 255 ns programmed digitally

Engineered Components Co., 3580 Sacramento Dr., San Luis Obispo, CA 93401. (805) 544-3800. $\$ 30$ to $\$ 50$ (OEM qty) ; stock to 6 wks .

A $\mathrm{T}^{2} \mathrm{~L}$-compatible programmablelogic delay line allows for final delay adjustment during or after installation in a circuit. No additional external components are needed to obtain the desired delay. The delay line is offered in 15 models with time delays to a maximum of 255 ns and step resolutions of $1,2,3,5,10$ or 16 ns . Rise time for all modules is 4 ns maximum. The lines are digitally programmable in 16 delay steps with four digital inputs. Programming may be accomplished by remote switching, permanent termination of the appropriate programming pins to ground or automatically by com-puter-generated data.

CIRCLE NO. 285

# Stay current with small lamp data from General Electric. It's free. 

## Check these 6 halogen cycle lamps GE has added to its low-voltage line.



General Electric now offers over 27 halogen cycle lamps that pack high light output in small packages. (In addition, GE offers 8 sealed beam halogen lamps primarily for aircraft applications.) Bulb diameters range from $3 / 16^{\prime \prime}$ to $1 / 22^{\prime \prime}$. Lengths from $.520^{\prime \prime}$ to $2.25^{\prime \prime}$. Voltages from 3.5 to 28. O.V. And candlepower from 2.15 cd up to 250 cd .
They're ideal for you if you're designing applications such as optical systems, instrumentation, illuminators, fiber optics, card readers, displays and aircraft navigation. A variety of terminals are offered.
For updated technical information circle the number below or write GE for Bulletin \#3-5357.

## These GE wedge base miniature lamps offer you savings in time, money and space.

These lamps are ideal for applications such as indicators, markers and general illumination where space is at a premium. Their wedge-based construction makes them easy to insert and remove. They don't require bulky, complicated sockets. And because the filament is always positioned the same in relation to the base, you get consistent illumination from lamp to lamp.


You can choose from over 25 types of GE wedge base lamps. Voltages range from 6.3 V to 28 V . Candlepower from 0.03 to 12 cd . Bulb sizes range from subminiature at 6 mm to a heavy-duty bulb at 15 mm .
To send for updated wedge base lamp technical information, circle number below or write GE for Bulletin \#3-5259.

## These three free GE catalogs include important data changes that could affect your present design. Send for yours today.


\#3-5169
June '75 Miniature lamp catalog features 40 pages and 500 data changes for complete 500 -lamp line.

\#3-6252R1
Feb. '75 Sub-miniature lamp catalog features 24 pages and 91 changes for more than 210 lamps.

\#3-6254R
Dec. '74 Glow Lamp catalog features 8 pages and 50 changes for 83 Glow Lamp Indicator and Circuit Component lamps.

For up-to-date technical information on any of these items write: General Electric Company, Miniature Lamp Products Department \#3382- L, Nela Park, Cleveland, Ohio 44112.


## OR



The A-8400 is the world's first monolithic V/F/V with 12 bit accuracy, 100 kHz bandwidth and does both $\mathrm{V} / \mathrm{F}$ \& $\mathrm{F} / \mathrm{V}$ functions all in a 14-pin DIP which gives you the easiest buying decision in years.
NOW we've made things even easier with the A-8402; a single supply V/F/V with 11 bit accuracy, 100 kHz bandwidth and a supply range from +4 V to +18 V all in a 14 -pin DIP for $\$ 895$

282 Brokaw Rd., Santa Clara, CA 95050
Tel. (408) 244-0500, TWX 910 338-0254

# Flexible CRT terminals have many data handling features 



Hewlett-Packard, 1501 Page Mill Road, Palo Alto, CA 94304. (415) 493-1501. $P_{\&} \&$ : See text.

Combining the advantages of the company's earlier 2640 B and 2644 A display terminals, the 2645 A developed by Hewlett-Packard offers a wide range of data communications features. The 8080 -based CRT terminal operates in both local and distributed computing networks at switch-selectable datatransmission speeds from 110 to 9600 baud.

An optional capability of the terminal permits asynchronous or synchronous (BISYNC) multipoint polling of up to 32 terminals on the same line. The terminal has eight special-function keys. Each key can be set to issue a userdefined string of up to 80 characters or a control sequence, which can be stored in the terminal RAM.

The 2645 A permits off-line data preparation and editing, $\mu \mathrm{P}$-controlled memory allocation, self testing and both page and charactermode operation. Also included are: character wraparound to simplify text insertion, adjustable margins, back tab, memory lock, automatic data logging and field protection.

The CRT has a high-resolution $5 \times 10 \mathrm{in}$. display area that can show 24 lines of 80 characters. Up to 12,000 bytes of multipage dis-
play memory can be included in the terminal; 4000 bytes come as standard equipment. Information can be retrieved from the unit by scrolling forward or backward a line or page at a time.

There are four 128-character sets-switchable on a character-bycharacter basis-that can be used concurrently. Among the available sets are a Roman set with displayable control codes for program debugging, a line drawing set for forms, a mathematics set with frequently used symbols and a Greekcharacter set. Each character has a size of $0.097 \times 0.125$ in., but can be expanded to triple that size.

Two mini-3-M tape-cartridge transports are available as a builtin option. They provide up to 110,000 bytes each of mass data storage. This storage permits the 2645 A to batch information and to perform many operations on a stand-alone basis. An RS-232C interface is standard, but a 20 mA current loop can be ordered.

Special color-coded prefix keys provide access to the multiple data paths and permit information to be moved between the different peripherals available-display and keyboard, built-in cartridges, or external units such as a printer.

The 2645A can display either white-on-black or black-on-white
characters and has a blinking underline cursor. It can operate over a 5 -to- 40 -C range with a relative humidity of 5 to $95 \%$ (noncondensing). Over-all dimensions of the display are $17.5 \times 18 \times 13.5$ in. and the plug-in keyboard is $17.5 \times 8.5 \times 3.5 \mathrm{in}$. The unit requires 85 W and weighs a total of 50 lb .

The earlier 2640 B and 2644A terminals are based on the 8008 microprocessor and can communicate at rate of up to 2400 baud. The 2640 comes with only $1-\mathrm{k}$ words of memory and cannot be modified for the tape storage option. The 2644 A comes with $4-\mathrm{k}$ of memory but cannot be expanded any further. It does, though, have the dual mini tape drives built in for bulk storage. Prices for the 2640B start at $\$ 2600$ for a unit with $1-\mathrm{k}$ words and the additional $3-\mathrm{k}$ increases the price to $\$ 2900$. The 2644 A costs $\$ 5000$.

Prices for the 2645 start at $\$ 3500$ for the basic model with 4 -k words of RAM, and increase to $\$ 5100$ for the unit with dual tape transports. Other accessories and options range from $\$ 50$ to $\$ 500$ each. Delivery is from stock.
Booth No. 449-458 Circle No. 307

## Alter formats on a floppy-disc system

Remex, 1733 Alton St., Santa Ana, CA 92705. (714) 557-6860. \$2400, dual drive (OEM qty) ; 90 days.

The Model RFS 7500 floppy disc system features user-selectable sector size. By changing a DIP switch on the formatter PC board, the user can select a $1,2,4,8,16,26$ or 32 sector format. This formatting employs a soft "sectoring" technique similar to the IBM 3740 format, so the user can increase data capacity by decreasing sectors without changing software. The system incorporates formatter electronics, power supply and from one to four flexible disc drives-all enclosed in a 19 in . rack-mountable chassis. The formatter is $\mu \mathrm{P}$ based, performing many functions within the system which normally require main computer control.

CIRCLE NO. 286


## Problem:

Customer had a $1 / 3 \mathrm{hp}, 115$ volt single phase capacitor-run 3450 rpm motor. Running current was 4.6 amperes and starting current was 28 amperes with a start-up time of 1.6 seconds.
He needed overload protection that was faster than that supplied by the internal


Time vs Current
A - Motor Characteristics B - 15A Fuse B - UPG Delay 66, 5 A


Start-up Current Envelope Vertical $7.5 \mathrm{amps} \mathrm{rms} /$ div. Horizontal .2 sec./div. quick dropout in case of a jammed load or stalled rotor.

In order to start the motor without blowing a fuse, he was forced to use a 15 ampere rating. This gave him stalled rotor protection at about 10 seconds, but no overload protection.
The standard long delay magnetic protector required a $71 / 2 \mathrm{amp}$ rating to be able to turn his motor on reliably. This gave him his stalled rotor protection at about 5 seconds, and overload protection at $200 \%$. Still not good enough.

## Solution:

The Airpax UPG Delay 66, at 5 amp rating eliminated this problem with turn on ... locked rotor protection at about $41 / 2$ seconds . . . and overload protection at about $150 \%$ on nameplate in approximately 400 seconds. This allowed short periods of overload without nuisance tripping. His problem was solved. (Delay 66 is also available in larger Airpax Type 219 and 229 Molded


Stalled Rotor Trip Out Vertical $7.5 \mathrm{amps} \mathrm{rms} / \mathrm{div}$. Horizontal $.5 \mathrm{sec} . /$ div. Case units up through 100 amperes.)

If you have an application with a special protection problem, call Airpax Electronics at Cambridge, Maryland (301-228-4600) or write for literature on Airpax Circuit Protectors and Circuit Breakers.

## CAMBRIDGE DIVISION

Cambridge, Maryland 21613 • Telex 8-7715 • TwX: 710-865-9655 Other Airpax Divisions:
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C\&K's new Model 9201 miniature DPDT power switch meets all international electrical and dimensional specifications. It has an electrical life of 25,000 cycles at full load, a contact rating of 6 amps , and a dielectric strength rating of 1500 rms @ sea level. For more info on C\&K's international power switch, write or call today! 103 Morse St., Watertown, MA 02172 Tel: (617) 926-0800 TWX: 710-327-0460 Telex: 92-2546
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See C\&K at Wescon Booth 928-930 CIRCLE NUMBER 104

## MaX-Y line-up



Esterline Angus has the newest and best in $\mathrm{X}-\mathrm{Y}$ recorders, many models from high speed 2 pen to new stripped down OEM. Maximize your choices: rugged XYY' Model 540 zips along at up to 30 ips on each axis. Plots 2 independent variables against time or 3rd variable. Multirange with vernier on all axes. One millivolt/in. sensitivity.

OEM Model 575 contains a unique Y -axis pen shuttle with throwaway fiber-tip pen and pen lift in a single assembly. Slew speeds of 50 ips! Single ranges from 1 mV to $10 \mathrm{~V} / \mathrm{in}$. Add-on multirange module available for lab use. Full X-Y catalog: Esterline Angus Instrument Corporation, Box 24000, Indianapolis, Indiana 46224. Telephone 317/244-7611.


## Low cost, high performance 3A SPDT relay really saves PC board space

## Mounts on .69" centers... <br> satisfies thousands of application needs

Where size and space are important, the Series 27 relay can be just the low cost answer you've been looking for. It provides 3 amps of switching in a $0.526^{\prime \prime}$ cube and mounts on $.69^{\prime \prime}$ centers, assuring high density PC board mounting. The cost is $\$ 1.05$ each in 1,000 piece lots for 3,6 and 12 V dc units slightly higher for 24 V dc
You'll find the Series 27 relay suitable for hundreds of control applications. For instance: timing controls; gas pilot light controls; anti-theft devices for CB radios; automotive controls; emergency lighting equipment; and medical equipment, to name a few.

The relay has a 450 mW pick-up sensitivity $(180 \mathrm{~mW}$ available). Contact rating is 3 A res @ 28 V dc. 120 V ac. Contact resistance is 0.10 ohm.

Write for information today!

## NORTH AMERICAN PHILIPS CONTROLS CORP.

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CIRCLE NUMBER 107

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TWX (910) 322-1135

Microcomputer card has fully buffered lines


Pro-Log Corp., 2411 Garden Rd., Monterey, CA 93940. (408) 3724593. $\$ 350$ (single qty); stock.

The Model 8821 PC card contains an $8080 \mathrm{~A} \mu \mathrm{P}$ with TTL buffering of address and data lines. The card has address and memory control for DMA, a crystal clock, a power-on and external reset, 1-k bytes of RAM, capacity for $4-\mathrm{k}$ bytes of ROM, and external memory and I/O control. A separate I/O card is usually necessary to build a complete system. The 8821 card measures $4.5 \times 6.5 \mathrm{in}$. with a 56 -pin card-edge connector and fits into standard card racks. All parts used in card production are second sourced.
Booth No. 853, 855 Circle No. 287

## Digitizer connects to computers, calculators

Talos Systems, Inc., 7419 East Helm Dr., Scottsdale, AZ 85260. (602) 948-6540. See text.

A series of three PC boards connects the manufacturer's graphic digitizing tablets to a wide range of minicomputers and intelligent terminals. The Sequential model ( $\$ 350$ single qty) allows you to connect directly to the PDP-11. The GPIB module ( $\$ 450$ single qty) connects directly to the Tektronix 4051 smart terminal and most HP instruments. The Programmable module ( $\$ 450$ single qty) allows you to connect via an RS-232 port, to a teletypewriter. Also it provides several other formats for interfacing Tektronix, Monroe or Wang Calculators, modems, computers or recorders. These interfaces are in addition to the standard parallel-binary or BCD outputs. The appropriate board plugs into the digitizer's electronics box.

## Low-cost disc systems fit on DG's Nova CPUs

Tri-Star Computer Systems, Inc., 304 Harper Dr., Moorestown, NJ 08057. (609) 234-6661. \$17,000 (80 Mbyte).

Three disc subsystems for Data General's Nova minicomputers store 40,80 or 300 Mbyte. The TDS series of subsystems allow Data General's Nova users to add large-file disc storage to their minicomputer systems at lower cost than similar equipment from Data General. The disc subsystems include the controller, drives, power supply, interface, and all the necessary cables and mounting hardware. The systems feature $30-\mathrm{ms}$ average access time, $603-\mathrm{kHz}$ word transfer rate and $8.33-\mathrm{ms}$ average latency.

CIRCLE NO. 289

## Computer driven TV display is inexpensive

Grinnell Systems Corp., 2986 Scott Blvd., Santa Clara, CA 95050. (408) 988-2100. See text.

The GP-26, a graphic television imaging system, sells for about half the price of other systems. The system generates and displays complex line drawings and/or picture images on standard TV monitors. Users can choose black and white, grey scale, color, multilevel color in any combination. The system also generates alphanumeric characters, symbols and vectors. The GMR-26 interfaces to most popular computers, and any point on the display may bel accessed in $3.2 \mu \mathrm{~s}$. Other capabilities of the GMR-26 include line by line, up or down image scrolling, automatic incrementing of display entities and generation and display of a blinking cursor. Three resolutions are available: $512 \times 512$ elements, 256 $\times 512$ elements and $256 \times 256$ elements. The GMR-26 consists of a controller and one or more display channels. The controller includes the control logic, a power supply, a 19 -in. rack-mount chassis and a standard 16 -bit interface. Other interfaces can be furnished. A GMR-26 controller and two display channels with $256 \times 256$ element resolution costs $\$ 5700$.


The versatile new TEXTOOL "chip carrier" test socket series includes models capable of accepting all "carriers" with from 14 to 48 leads and body sizes up to and including .500 inch square.

In addition, only minor tooling changes allow the socket to also accept plastic "leaded" packages less than .500 inch square with two or four side, straight or formed leads.

A positive locking system enables loading and unloading literally with the
fingers. Different pressure pad thicknesses may be specified for the socket lid to adjust for those applications where packages are "too thick" or 'too thin.'
Other significant features of TEXTOOL's new "chip carrier" sockets include a lid design that eliminates shorting against contacts or P.C. board and which will not separate from the socket body under normal usage, integral mounting holes and minimum lid overhang at the back of the socket to permit maximum P.C. board mounting density.

All TEXTOOL "chip carrier" sockets are ideally suited for both test and burn-in applications and are available in a wide variety of materials to meet specific test requirements.

Detailed information on these and other products from TEXTOOL . . . IC, MSI and LSI sockets and carriers, power semiconductor test sockets, and custom versions is available from your nearest TEXTOOL sales representative or the factory direct.
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> New LaMarche solid state relays provide split second actuation in supervisory control circuits.

These new relays are offered in two basic versions. Model ERD has fast-acting, dc-voltage-sensing circuit with independently adjustable pull-in voltage and differential. High-reliability, zener-diodereferenced, electronic-time-gated switch drives relay. Model ERT is dc-voltagesensing unit having time-delayed operation on either "make" or "break", with independently adjustable "make" voltage and differential. Both types of relay have a nominal voltage of $6-250 \mathrm{vdc}$ and repeat accuracy of $0.1 \%$ over a temperature range of $0-60 \mathrm{C}$. Maximum reset time is 50 msec , and all models are field adjustable.
Typical applications include the operation of electromechanical relays in control systems for food and packaging machinery, machine tools, and a wide variety of manufacturing processes. They are also used to command valves, solenoids, protection devices and a variety of other resistive loads.
These units are offered in three enclosed-housing configurations, plus an open board version. Ac relay models also available. For full details, call your local LaMarche Sales Office, or contact factory on special applications. 106 Bradrock Drive
Des Plaines, Illinois 60018 Phone: 312/299-1188

DATA PROCESSING

## A telephone self-dials a predetermined number

Warren G-V Communications, 101 Okner . Pkwy., Livingston, NJ 07039. (201) 992-6200.

A telephone dials a programmed number of up to 11 digits, automatically, when the handset is lifted. Called the Courtesy Phone, the unit does this over the public switched network, not over leased or dedicated lines as previous systems did. The Courtesy Phone is self-contained and in a standard telephone assembly. The number to be dialed is determined by internal wire straps, and can be changed after installation. The circuitry is solid state and is powered by the central office line; no battery or other power source is required.

CIRCLE NO. 295

## Terminal has flexible data transmission

Omron Corp., Information Products Div., 440 East Middlefield Rd., Mountain View, CA 94043. (415) 964-2266; 30-60 days.

The 8025 C communications display terminal has an expanded data transmission capability. The terminal is controlled by a microprocessor and designed for stand-alone or on-line data entry. The 8025C will operate in both a normal and in a protected field mode. Protected fields cannot be altered by the terminal operator; only variable data may be inserted in specified areas of the display. The terminal permits identification of the beginning and end of data fields by transmission of control characters embedded in the protected field. This feature provides increased flexibility and control of data flow and improves data reliability. The 8025C features a 15 -in. diagonal, 1920character display. A full range of scrolling and editing features are included. In addition, the terminal will support RS-232 compatible peripheral devices. The unit is TTY compatible (ASR and KSR modes) with transmission rates to 2,400 baud asynchronous in full or halfduplex mode.

Hand-held calculator works with fractions


Casio, Inc., Consumer Products Div., 15 Gardner Rd., Fairfield, NJ 07006. (201) 575-7400. \$24.95.

The AL-8, 8-digit calculator adds, subtracts, multiplies and divides numbers expressed as fractions. The function-selector slide switch also selects the following instructions: to generate the remainder of a division between two numbers; to calculate standard deviation of a group of numbers; and to deal with numbers expressed as hours, minutes and seconds. The calculator comes with a one-year warranty on parts and labor. It operates on regular penlight batteries, included in the calculator price.

CIRCLE NO. 297

## Disc drives add memory to a desktop calculator

Hewlett-Packard, 1501 Page Mill Rd., Palo Alto, CA 94304. (415) 493-1501. 9885M: \$3900; 9885S: $\$ 2500$.

Two flexible-dise drives connect to the manufacturer's 9825 programmable desktop calculator. The drives; the 9885 M (master) and 9885 S (slave), have a memory capacity of 468 -k bytes per diskette. The 9825 calculator can direct up to eight 9885 M units. Each 9885 M. with its built-in controller, can in turn manage three 9885 S units.

CIRCLE NO. 298

CIRCLE NO. 296

## Thyristor/rectifiers operate at TV voltages

RCA/Solid State Div., Route 202, Somerville, NJ 08876. (201) 6856423. \$2.58 to \$3.22 (100 up); stock.

Eight integrated thyristor/rectifiers, the S3900 and S3901 series for color TV and the S3902DF and S3903MF for monochrome TV, can drive horizontal deflection circuits. They are all-diffused monolithic circuits incorporating a sili-con-controlled rectifier and a silicon rectifier on a common pellet. The S3900-series and S3902DF types are used as bipolar switches in television circuits to control horizontal yoke current during the beam-trace interval; the S3901series and S3903 MF types are used as commutating switches to initiate trace-retrace switching. Devices in the S 3900 series can supply 8 mJ of stored energy to the deflection yoke; this is sufficient for 29 and $35-\mathrm{mm}-$ neck color picture tubes operated at a nominal supply voltage of 31 kV . The S3902DF is capable of supplying 3 mJ of stored energy to the deflection yoke ; this is sufficient for $29-\mathrm{mm}$ neck black-and-white tubes operated at a nominal value of 19 kV .

## Optical sensor contains hybrid output circuit



HEI, Inc., Chaska, MN 55318, (612) 448-3510. $\$ 7.39$ (1000 up); stock.

A new single-point sensor, the HEI-115 solid-state optoelectronic assembly, contains a silicon phototransistor and waveshaping hybrid circuitry. This combination provides the user with a square-wave output that is TTL-compatible. The device has a broad-band spectral response, and an output capability of 10 standard TTL gates.

CIRCLE NO. 300

## Tuning-diode $\mathbf{Q}$ values increased two times

## Alpha Industries, Inc., 20 Sylvan

 Rd., Woburn, MA 01801. (617) 935-5150. See text; stock.Typical Q values of the DKV6520 and the DKV- 6530 series of hyperabrupt tuning diodes have been increased to over twice previous catalog values, according to Alpha. Guaranteed minimum Qs are 150 to $190 \%$ of previous mini-
mum values. There is no increase in cost. The DKV-6520 series, designed for hf to vhf applications, offers $\mathrm{C}_{\mathrm{T} 4}$ capacities of 20 to 200 pF with typical Q values of 420 to 125. For uhf applications, the DKV-6530 series offers $\mathrm{C}_{\text {T3 }}$ capacities of 11.5 and 28 pF with typical Q values of 700 and 400 . Both series offer tuning ratios of over $5: 1$ from 4 to 20 V , and exceptionally linear-frequency-vs-voltage tuning.

CIRCLE NO. 310


Every feature you'll ever conceivably want, including the ones that cost extra in other data consoles (rolled front edge, chrome legs and the like.) Every color from Burnt Orange to Sky Blue to Black; fourteen standard colors in all. Standard widths are $24^{\prime \prime}, 45^{\prime \prime}$ and $66^{\prime \prime}$, each in a choice of keyboard or desk heights. And the two styles you see here are just the beginning.

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Optima Enclosures, a division of Scientific-Atlanta, Inc. 2166 Mountain Industrial Blvd., Tucker, Georgia 30084 or call (404) 939-6340 CIRCLE NUMBER 111

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CIRCLE NUMBER 112

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KineticSystems Corporation
CAMAC Process Control Systems for Science and Industry

FET chips provide low noise at 10 GHz


Aertech Industries, 825 Stewart Dr., Sunnyvale, CA 94086. (408) 732-0880. \$95 (10-49); stock.

One-micron-gate GaAs FET chips, the AFT2000, are now available for low-noise applications to 15 GHz . Maximum available gain at 10 GHz is 10 to 12 dB and typical noise figure at 10 GHz is 3.5 $d B$. They offer freedom from shortterm drift. The AFT2000 require little or no circuit redesign when replacing similar competitive devices.

CIRCLE NO. 320

## JFETs available in low-noise versions

National Semiconductor Corp., 2900 Semiconductor Dr., Santa Clara, CA 95051. (408) 732-5000. $\$ 0.95$ to $\$ 2.50$ ( 100 up ) ; stock.

Ultra-low-noise JFETs are now available as standard parts. You no longer have to select them. The ncw series includes three TO-72 metal-can devices and three epoxyB, TO-92 devices, respectively identified as NF5101, NF5102 and NF5103 and PF-5101, PF5102 and PF5103. Key specifications for the new JFETs include an equivalent short-circuit input noise of $5 \mathrm{nV} / \sqrt{\mathrm{Hz}}$, which is typical with a drain current of 0.5 mA at 10 Hz ; the common-source transconductance is $4000 \mu$ mho (minimum). An important feature of the new series is tight control of the gatesource cut-off voltage. The $\mathrm{V}_{\mathrm{GS}}$ (OFF) of the NF/PF5101 ranges from 0.5 to 1 V ; the NF/PF5102, from 0.8 to 1.4 V ; and the NF/ P「5013, from 1.3 to 2.5 V .

## LEDs replace incandescent lamps



Data Display Products, P.O. Box 91072, Los Angeles, CA 90009. (213) 641-1232. $\$ 1.59$ (100 up); to 5 whs.

Model 2SBL LEDs are replacements for incandescent lamps in all popular sizes, including a special version of base $\# 2$. The extend-ed-length unit is designed to replace both the incandescent lamp as well as the lens on the front panel, thereby using the most of light output from the LED. Three colors are offered: red, amber and green. At 20 mA , typical, brightness is 50,32 and 20 mcd , respectively, with clear tinted encapsulation. Diffused versions are also available and are best suited where the LED is viewed without a secondary lens or panel. All types are available with built-in resistors for all popular voltages through 48 V . For ac operation over 6 V , a builtin rectifier also is available. Optimum current is 20 mA for maximum life and efficiency. The cathode is identified with a green dot. However, units with built-in resistors are not damaged if installed momentarily in the wrong direction. On most models, an integral secondary Fresnel lens is available for front panel applications.

CIRCLE NO. 322

## Power transistors rated 125 W at 400 V

International Rectifier, Semiconductor Div., 233 Kansas St., El Segundo, CA 90245. (213) 678-6281. \$11.41 (100-999); stock.

A new series of power transistors rated for $400-\mathrm{V}$ operation features a power-dissipation rating of 125 W. Designated 2N5241, the transistors employ hard-glass passivation of the silicon chip to provide stability at high operating junction temperatures. Collector current is rated at 5 A continuous, and dc-current gain is 35 max.

CIRCLE NO. 323

## Optoisolators have JDEC registration

Optron, Inc., 120 Tappan Circle, Carrollton, TX 75006. (214) 2426571. \$3.25:3N243, \$4.31:3N244, \$5.83:3N245 (1000 up); stock.

A new series of optically coupled isolators has the JEDEC registration $3 \mathrm{~N} 243,3 \mathrm{~N} 244$ and 3N245. When used as replacements for pulse transformers or mechanical relays, these isolators solve such problems as common-mode noise re-
jection, ground loops and voltagelevel translation. The devices consist of gallium-arsenide infrared emitters coupled with silicon phototransistors. They are similar to Optron's OPI 140 standard isolator, and they are available in a hermetically sealed TO-18 package. Guaranteed minimum current transfer ratios are $15 \%$ for the $3 \mathrm{~N} 243,30 \%$ for the 3 N 244 and $60 \%$ for the 3 N 245 . All devices feature $1-\mathrm{kV}$ isolation.

CIRCLE NO. 324


## Designing solid state telecommunication equipment?

 Let Tecnetics convert your 48VDC power source.Tecnetics high efficiency power converters are the reliable and cost effective way to convert 48VDC power sources into usable power for solid state devices.
Tecnetics offers a wide range of 48VDC input power converters with outputs ranging between 5 and 48 VDC and power up to 150 watts. All are super reliable, too, because Tecnetics is a high technology company that has been supplying the telecommunications industry with converters since 1958.

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| SERIES | POWER Watts | SINGLE (S) DOUBLE (D) TRIPLE (T) | VOLTAGE VDC | EFFICIENCY | $\begin{gathered} \mathrm{L} \\ \mathrm{IN} . \end{gathered}$ | $\begin{aligned} & \text { W } \\ & \text { IN. } \end{aligned}$ | H IN. | $\begin{aligned} & \text { RANGE } \\ & \text { U.S. } \$ \\ & 1 \text { to } 9 \end{aligned}$ |
| 3150-48 3100-48 3050-48 | $\begin{array}{r} 150 \\ 100 \\ 50 \\ \hline \end{array}$ | $\begin{aligned} & \hline \mathrm{S} \\ & \mathrm{~S} \\ & \mathrm{~S} \end{aligned}$ | $\begin{aligned} & 5-48 \\ & 5-48 \\ & 5-48 \end{aligned}$ | $\begin{aligned} & \text { High } \\ & \text { High } \\ & \text { High } \end{aligned}$ | $\begin{aligned} & 6 \\ & 6 \\ & 4 \end{aligned}$ | $\begin{aligned} & 4 \\ & 4 \\ & 4 \end{aligned}$ | $\begin{aligned} & 2.25 \\ & 2.25 \\ & 2.25 \end{aligned}$ | $\begin{aligned} & 500 \\ & 450 \\ & 425 \end{aligned}$ |
| $\begin{aligned} & \hline 3025-48 \\ & 9525-48 \\ & 1000 \\ & \hline \end{aligned}$ | $\begin{aligned} & 25 \\ & 25 \\ & 10 \\ & \hline \end{aligned}$ | $\begin{gathered} S \\ S, D, T \\ S, D, T \end{gathered}$ | $\begin{aligned} & 5-48 \\ & 5-24 \\ & 5-24 \end{aligned}$ | $\begin{aligned} & \text { High } \\ & \text { Std. } \\ & \text { Std. } \end{aligned}$ | $\begin{aligned} & \hline 4 \\ & 4 \\ & 3.5 \end{aligned}$ | $\begin{array}{\|l\|} \hline 4 \\ 4 \\ 2.5 \\ \hline \end{array}$ | $\begin{array}{\|l\|} \hline 2 \\ 1.5 \\ 0.96 \end{array}$ | $\begin{array}{c\|} \hline 395 \\ 250,295,365 \\ 115,125,140 \end{array}$ |
| $\begin{array}{\|l\|} \hline 1600 \\ 1300 \\ 1100 \\ \hline \end{array}$ | $\begin{aligned} & 6 \\ & 3 \\ & 1 \\ & \hline \end{aligned}$ | $\begin{aligned} & \text { S,D,T } \\ & \text { S,D,T } \\ & \text { S,D } \end{aligned}$ | $\begin{aligned} & 5-24 \\ & 5-24 \\ & 5-24 \\ & \hline \end{aligned}$ | Std Std Std | $\begin{aligned} & \hline 3.5 \\ & 2.35 \\ & 1.75 \\ & \hline \end{aligned}$ | $\begin{aligned} & \hline 2.5 \\ & 2.125 \\ & 1.50 \\ & \hline \end{aligned}$ | $\begin{aligned} & \hline 0.96 \\ & 0.84 \\ & 0.65 \\ & \hline \end{aligned}$ | $\begin{gathered} 89,99,109 \\ 79,89,99 \\ 49,55 \\ \hline \end{gathered}$ |

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## Floppy-disc control \& format circuit comes on single chip



Western Digital, 3128 Red Hill Ave., Box 2180, Newport Beach, CA 92663. (714) 557-3550. $P \& A$ : See text.

Building a controller or formatter to interface your computer to a floppy-disc drive need no longer be a complex design task.
The Western Digital FD1711 formatter/controller chip contains all the circuitry necessary to control a floppy-disc drive and format the data. It can replace about 100 to 150 assorted TTL circuits normally needed to do the job.

On the computer-interface side of the circuit, the processor can access and alter five on-chip 8-bit registers-the command, status, data, sector and track registersand sense the data request and interrupt signals.

The disc-interface side of the FD1771 provides write-gate and data outputs, a head-load output, stepping-motor outputs and a lowcurrent output. Inputs to the circuit include separated data and check lines, as well as lines for index, mark, track-0, write-protect, format-protect and write-fault.

As a formatter, the circuit can
arrange data in IBM 3740 format with $128,256,512$ or 1 -k byte sectors. In a non-IBM mode, sector lengths of up to $4-k$ bytes are possible.

The FD1771 accepts and executes 11 commands and has 8 -bit bidirectional busses for data, status and control registers. Also included on the chip is a circuit that generates a cyclic redundancy check to minimize data errors. A free-running 2 MHz clock with a $\pm 1 \%$ maximum variation is required to do the internal timing.

Both three-phase or step-and-direction control of a drive motor is possible, so either type of disc drive can be used with the chip. Stepping rates of 10,8 or 6 ms are possible.

The FD1771 operates over a 0 -to- $70-\mathrm{C}$ range and requires +12 and $\pm 5 \mathrm{~V}$. All input-output lines are TTL-compatible and can handle one $1.6-\mathrm{mA}$ load. The circuit is housed in a 40 -pin DIP.

Single-unit price of the controller/formatter is $\$ 80$, including a full documentation package. Delivery is from stock.

CIRCLE NO. 308

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## INTEGRATED CIRCUITS

## Low-cost a/d converter delivers words in $20 \mu \mathrm{~s}$

National Semiconductor, 2900 Semiconductor Dr., Santa Clara, CA 95051. (408) 732-5000. See text; stock.

An 8-bit a/d converter, the MM5357, costs only $\$ 7.95$ each in quantities of 100 . The converter has a linearity of $\pm 0.5 \mathrm{LSB}$, an input impedance of $100 \mathrm{M} \Omega$ and a conversion speed of $20 \mu \mathrm{~s}$. The MM5357 operates from +5 and $-12-\mathrm{V}$ supplies, dissipates about 170 mW and is housed in an 18-pin epoxy DIP. The converter contains a chain of 256 identical resistors connected in series, 255 analog switches, a high-impedance-input comparator, output latches and control $\log _{1} \mathrm{C}$ all on a single chip. A $10-\mathrm{V}$ reference applied across the resistor chain sets 256 precision voltages against which the unknown input voltage is compared by the analog switches under control of the on-chip logic.

CIRCLE NO. 325

## CMOS master slices hold up to 2000 CMOS pairs

International Microcircuits, 3000 Lawrence Expressway, Santa Clara, CA 95051. (408) 735-9370. See text.

Two additions to the Master MOS family of CMOS master slice chips have been announced. The largest of the new chips, called MasterMOS-XXL, contains over 2000 pairs of CMOS transistors, 64 output buffers and 88 interface pads. Typical development charges for a custom circuit using Master-MOS-XXL are less than $\$ 30,000$ and typical production prices are less than $\$ 75$ each for 1000 units. The other new chip called Master-MOS-XL, contains over 1000 pairs of CMOS transistors, 52 output buffers, and 66 interface pads. Typical development charges are less than $\$ 15,000$, and typical product costs are less than $\$ 35$ each for 1000 units. Both chips are fully compatible with the company's three existing MasterMOS chips. Typical development schedules are eight to 12 weeks from logic drawing to IC production.

Count extender widens range by factor of eight


Plessey Semiconductors, 1674 McGaw Ave., Irvine, CA 92714. (714) 540-9945. From $\$ 7.10$ (100-up); stock.

The SP8794, a $\div 8$ count extender increases the minimum division ratio of a 2 -modulus counter while retaining the same difference in division ratios. For instance, with the SP8794, $a \div 10$ or 11 counter becomes $\mathrm{a} \div 80$ or 81 counter, a $\div 5$ or 6 counter becomes a $\div 40$ or 41 counter. The SP8794 dissipates 40 mW and operates at frequencies from 0 to 120 MHz (minimum). Three operating temperature ranges are available: 0 to $+70 \mathrm{C}(\mathrm{SP} 8794 \mathrm{~B}) ;-40$ to $+85 \mathrm{C}(\mathrm{SP} 8794 \mathrm{M})$; and -55 to +125 C (SP8794A).

CIRCLE NO. 327

## IC comparator/counter is programmable

Motorola, 3501 Ed Bluestein Blvd., Austin, TX 78721. (512) 928-2600. $\$ 2.61$ (100-up); stock.

The MC14568B CMOS IC contains a phase comparator, a programmable reference divider (divide by $4,16,64$ or 100 ) and a presetable 4-bit counter (divide by 1 to 15). The device is designed for phase-locked-loop circuits. In synthesizer schemes, the circuit's 4-bit counter can be used in the feedback loop by itself or cascaded with other binary or BCD counters such as the MC14569B, MC14522B, and/or the MC14526B to increase the length of the count. The comparator/counter circuit has a 3 to $18-\mathrm{V}$ operating supply range. The MC14568B is available in two temperature range versions: -55 to +125 C ("AL" suffix in ceramic package) and -40 to +85 C ("CL" and "CP" suffix in ceramic or plastic, respectively). In addition to the two 16 -pin DIP versions, the circuit is available as tested die.

## Digital voltmeter chip drives 4-1/2 digits

EFICS, 85 X 38041 Grenoble Cedex, France. 100 francs (500-up); stock. The CAF p-channel, MOS digital voltmeter chip provides a $4-1 / 2$ digit readout. It delivers both multiplexed BCD outputs (4-bit parallel, in 5 serial blocks) and multiplexed seven-segment outputs that can directly drive liquid crystal displays. When connected with two op amps, a reference and a comparator, the CAF requires 160 ms to complete a reading. An external crystal controls the on-chip oscillator, which, in turn, controls all timing including automatic reset. The circuit requires +9 and -12 V supplies for a total operating power of 250 mW .

CIRCLE NO. 329

## Serial/parallel register completes filter set

Advanced Micro Devices, 901 Thompson Pl., Sunnyvale, CA 94086. (408) 732-2400. From $\$ 4.60$ (100up) ; stock.

The Am25LS22 8-bit serial/ parallel register includes a sign extend function. It is designed for use with the Am25LS14 multiplier and the Am25LS15 quad serial adder/subtractor. All three chips can be used to form a rudimentary digital filter. These circuits are available in molded or hermetic DIP packages and are specified for operation over the commercial or military temperature ranges.

CIRCLE NO. 330

## Speedy static RAM organized as $1 \mathrm{k} \times 4$

Electronic Memories \& Magnetics, 12621 Chadron Ave., Hawthorne, CA 90250. (213) 644-9881. \$18.75 (100-up) ; stock.
The SEMI 4104A static RAM offers an access time of 200 ns and a cycle time of 350 ns . It is organized as $1024 \times 4$. The 350 ns cycle time, or selected faster cycle times, permits application in data communications buffer memories. The SEMI 4104A has the same low standby power and "brownout" prolection as the SEMI $4200(4 \mathrm{k} \times$ 1) static RAM.

CIRCLE NO. 331

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POWER SOURCES
Portable dc standard offers $\pm 0.05 \%$ accuracy


Yokogawa Corp. of America, 5 Westchester Plaza, Elmsford, NY 10523. (914) 592-6767. \$995; stock.

A portable de voltage and current standard operates from its internal battery for three hours under full load. It can also operate from the $115-\mathrm{V}$-ac line. Features of the unit include: $\pm 0.05 \%$ accuracy from 0 to 12 V or 0 to 120 mA , and resolution of $1 \mu \mathrm{~V}$ and $0.1 \mu \mathrm{~A}$. The unit provides frontpanel display and selection of voltage or current: five decades for voltage ( 10 mV though 100 V ), three decades for current ( 1 through 100 mA ). Overvoltage and overcurrent protection and polarity selection are included. The internal battery charger repowers the batteries in three hours. The unit weighs 8 lb .
Booth No. 274 Circle No. 332

## Pulse-width modulation UPS efficiency upped

Cyberex, Inc., 7171 Industrial Park Blvd., Mentor, OH 44060. (216) 946-1783. \$350/kVA; 20 wk.

The Delta-Y uninterruptible power system's (UPS) overall-efficiency has been increased almost $10 \%$. In addition, reflected powerline distortions have been essentially eliminated and the system now presents almost unity powerfactor, thereby reducing kVA demand. Using pulse-width modulation, the Delta-Y is said to provide $25 \%$ greater reliability (MTBF) and more than $275 \%$ short-circuit current than step-wave designs. It is a three-phase solid-state system which can handle non-linear loads; and when operating with $100 \%$ phase-load imbalance, it still delivers balanced voltage. Single units pass from 15 to 250 kVA . Multiple units can operate in parallel, or redundantly.

## UPS protects self and load provides printout

General Electric, General Purpose Control Dept., P.O. Box 2913, Bloomington, IL 61701. Or call (203) 357-4088.

An uninterruptible power system (UPS) from GE is designed for operation in the 100 kVA -andup range. It features a solid-state annunciator that provides a printout of status and sequential events pertinent to automatic system operation. UPS multimodule systems have an intermodule datalink to coordinate individual control operation. An internal monitor and an output detector eliminate any possible common mode-control failures. This provides sustained power output even if one datalink is lost. A logicenter in each power module contains a self-diagnosing annunciator, which produces a time-stamped printout of status and sequential events of the automatic system operation. Indications are provided for more than 220 monitored items. Each power module has built-in "early warning" detectors that can be triggered by a module malfunction. The detectors are simultaneously linked to the sequential, selfdiagnosing annunciator. The printout of sequential events aids servicing. The logicenter continuously monitors and analyzes battery conditions. These "battery protector" functions are designed to prevent unnecessary battery cell stress and discharge.

CIRCLE NO. 334

## Rechargeable solid gel battery delivers 4 A-h

Elpower Corp., 2117 S. Anne St., Santa Ana, CA 92704. (714) 5406155. \$6.67 (100-up); last quarter, \% 6.

The Model EP640 solid-gel rechargeable batteries are the same physical size as common dry cells. They are fully rechargeable for as many as 150 cycles. The batteries are available with two different terminal configurations: the common spring terminal, with part number EP640-21, and a spade lug terminal, with part number EP-$640-8$. The $6-\mathrm{V}, 4$-Ah battery has an improved lead-acid design. It uses a fully gelled lead-electrolyte. Booth No. 922 Circle No. 335

## Two supplies targeted for $8080 \mu \mathrm{Ps}$ and RAMs

Semiconductor Circuits, 306 River St., Haverhill, MA 01830. (617) 373-9104. LCD5808, \$43 (10) ; CM5808, \$47.50 (10).

Two line-operated, encapsulated modular power supplies (LCD5808, CM5808) are designed to power $8080 \mu \mathrm{Ps}$ and several popular families of RAMs. Delivering short-
circuit protected output of +12 V dc at 300 mA and -5 V dc at -200 mA with $0.2 \%$ line and load regulation, the sources are electrically identical. But each is configured differently to accommodate a variety of power distribution techniques. Both models feature MTBFs in excess of $150,000 \mathrm{~h}$ at high line and full load and require no derating from -25 to +71 C .

CIRCLE NO. 336


For special electrical and/or mechanical power supply needs, Arnold Magnetics is hard to beat. Our unique modular concept allows us to mix our standard modules with specials to meet custom design requirements. The result is reduced engineering costs, fast deliveries and proven performance. And with over 1200 possible standard configurations a special from someone else may be just another standard for us.

- Single or dual inputs: 115 VAC, $47-500 \mathrm{~Hz}$, 12, 28, 48, 115, 150 VDC.
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- Efficiencies to $85 \%$.
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## POWER SOURCES

## Dual-output models join supply line



Deltron Inc., Wissahickon Ave., North Wales, PA 19454. (215) 6999261. \$35 to \$79; stock.

The "QD" Series of dual-output sources consists of three models. Model QD12/15-0.6 is rated at $\pm 12$ to $\pm 15 \mathrm{~V}$ dc at 0.6 A , Model QD. $12 / 15-1.7$ is rated at $\pm 12$ to $\pm 15 \mathrm{~V}$ dc at 1.7 A , and Model QD12/15-3.0 is rated at $\pm 12$ to $\pm 15 \mathrm{~V}$ dc at 3.0 A . The series are interchangeable with other competitive units. Specs include line and load regulation of $0.1 \%$, and ripple and noise of 1.5 mV rms . Tracking accuracy is $0.2 \%$ for line, load, and temperature. All units are protected against overloads and short circuits.

CIRCLE NO. 337

## 60-W dc/dc converters feature high MTBF

Etatech Inc., 187-MW Orangethorpe, Placentia, CA 92670. (714) 996-0981. \$165.

The company's "B" Series of power modules now includes 28 V input dc/dc converters. A fully regulated $5-\mathrm{V}, 10$-A module (Model B5D10) features a $70,000-\mathrm{h}$ MTBF at 80 -C baseplate, $70 \% \mathrm{~min}$ efficiency, size of $4-7 / 8 \times 1-3 / 4$ in., remote sensing, and complete overload/short circuit and overvoltage protection, all with automatic recovery.

Switching supplies work over wide input range
ACDC Electronics, 401 Jones Rd., Oceanside, CA 92054. (714) 7571880. $\$ 595$; stock to 4 wks.

The JP series of six switchingdc supplies operate from an input of 90 to 132 or 180 to 264 V ac, 48 to 63 Hz , single-phase. Standard outputs are: 2 V at $100 \mathrm{~A}, 5 \mathrm{~V}$ at $100 \mathrm{~A}, 6 \mathrm{~V}$ at $100 \mathrm{~A}, 12 \mathrm{~V}$ at 43 A , 15 V at 35 A and 24 V at 23 A . The output is floating-either the positive or negative terminal can be grounded. Additional features are: $70 \%$ efficiency; overload, overvoltage, reverse-voltage and thermal protection; remote-sensing; remote-shutdown; remotecontrol ; and parallel-operation. All units are $5 \times 7 \times 15 \mathrm{in}$. and weigh less than 20 lb . UL 478 recognition is pending.

## CIRCLE NO. 339

## Micro-based controller cuts electric power cost

Westinghouse Electric, Control Div., Tuscarawas Rd., Beaver, PA 15009. (412) 255-3693. From $\$ 3800$.

The Numa-Logic 600 is a programmable energy controller (PEC), which controls both demand and energy consumption. It is a microprocessor based system with capability for up to 16 electrical loads. The PEC contains a reprogrammable microcomputer which continuously monitors ongoing consumption of electricity and predicts energy demand for a given period of time. The controller automatically regulates, reduces and schedules the amount of power drawn from a utility company to reduce consumption and eliminate power peaks. No tie-in with utilities demand measuring equipment is needed. Its 16 individual electric loads include capacity for lighting, air conditioning, refrigeration equipment and most other electrical equipment. Information such as maximum kilowatt demand, time-of-day scheduling and minimum on/off times is entered by the user. The information is stored in the PEC's memory. The PEC is intended for small and medium-sized industrial plants, supermarkets, department stores, schools, hospitals, office and apartment buildings and hotels and motels.

CIRCLE NO. 340

## Calibrator simulates motion transducers



Trig-Tek Inc., 423 S. Brookhurst St., Anaheim, CA 92804. (714) 956-3593. \$425; stock.

The portable calibrator allows you to simulate the output of any accelerometer using the picocoulomb output, and any velocity pickup using the millivolt output. Picocoulomb outputs are in rms and peak from 000 to 999 . Ac millivolt outputs are in rms and peak from 000 to 999 with five precise frequencies of $100 \mathrm{~Hz}, 1000 \mathrm{~Hz}$ and the three frequencies associated with motion: 61.4 Hz where acceleration equals velocity; 139.9 Hz where acceleration equals displacement; and 318.9 Hz where velocity equals displacement. Dc millivolt outputs are available for checking tape recorders, strip chart or x-y recorders and oscilloscopes. The unit is operated by rechargeable batteries, is $3-1 / 2$ by 7 by 8 in ., and weighs 3 lb .

CIRCLE NO. 341

## Variable-dual, set-single outputs from small unit

Instant Instruments, Inc., 306 River St., Haverhill, MA 01830. (617) 373-9260. \$96-\$137; stock.

The LT series of miniature labo-ratory-supplies delivers variabledual tracking-voltages from $\pm 9$ to $\pm 18-\mathrm{V}$ de and a preset $5-\mathrm{V}$ de at currents from 100 to 1000 mA . Other voltage and current outputs may be obtained by special order. The power sources are cased in an unbreakable-epoxy enclosure and all components are American-made. A color-coded solid-aluminum knob and ring-reference gives visual voltage-control resolution to within $2 \%$ accuracy. Regulation is $0.05 \%$ for line, and $0.01 \%$ for load, ripple (PARD) is less than $1-\mathrm{mV}$ rms, and the preset-voltage accuracy is $1 \%$.

# Our carry-in recorder/reproducer will carry on for 32.8 hours! 

The 70-Ib. Sabre VI: It's a giant leap beyond any other small, high performance IRIG analog tape recorder/reproducer. Records at 8 electrically selectable speeds: 120 ips through 15/1.6 ips; reproduces at any 3 electrically selectable speeds; records from 15.3 minutes at the highest speed to 32.8 hours at the slowest on $14^{\prime \prime}$ reels. Remote speed selection and LED footage counter. LED bar data monitor. Let us give you full details.


## SANGAMO RECORDERS

THE INNOVATORS IN TAPE INSTRUMENTATION Sangamo Electric Company Data Systems
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(217) 544-6411

CIRCLE NUMBER 125

## Underwater connectors mate with the power on



Viking Industries, 9324 Topanga Canyon Blvd., Chatsworth, CA 91311 (213) 882-6275. P\&A: see text; 30 days.

Connecting and disconnecting cables underwater with the power on has not been possible until now. But Pisces connectors from Viking Industries can be used with up to $600-\mathrm{V}$ ac, at 20 A , on the pinseither underwater or in an explosive atmosphere.

Pisces can accomplish these feats because the pins mate in a chamber full of a dielectric fluid. The fluid squelches arcing and provides electrical insulation between the outside world and the mated or unmated connector pins.

To enter the receptacle, the pins on the plug pass through a flexible diaphragm that seals the dielectric fluid in its chamber. The diaphragm also ensures that pres-
sure differences between the outside and the fluid chamber are equalized. Then the connector can be mated and unmated at any pressure without contamination from outside, or loss of dielectric fluid. Because of the diaphragm's rapid compliance, fast changes in atmospheric pressure-such as those from explosions-won't damage the connector.

The connector's operating temperature range is -40 to +125 C and its insulation resistance is a good $5000 \mathrm{M} \Omega$. It withstands up to $20,000 \mathrm{lb} / \mathrm{in}^{2}$ of hydrostatic pressure, and has a dielectric withstanding voltage of 2 kV ac , regardless of pressure.

A thermoplastic is used for the shells and coupling nut; the insulator (plug) is polychloroprene. The shells will also be available in stainless steel or titanium.

Available pin configurations will range from 1 to 14 pins. The first version has four pins and sells for $\$ 125$ per mated pair, in single quantities. At the 100 -unit level, the mated pair price comes down to $\$ 85$.

CIRCLE NO. 306


That's why Shigoto can help make your ends meet. From hundreds of standard combinations to unusual characteristics that meet your own requirements. Even U.L. and C.S.A. approved. Our volume production for O.E.M.'s in electronics, communications, automotive, appliances, and practically every other industry, takes all the variables out of priceperformance differences. Shigoto's ability to consistently produce top quality assemblies, when combined with large quantities, always brings in the prices you need. Unmatched quality has become our standard, our reliability and delivery schedules will become yours. Call, write, or wire your specs. At Shigoto, you'll find we'll go to any ends, to any lengths, to satisfy you.
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## A snap-in PC board guide resists flames

Bivar, Inc., 1617 Edinger Ave., Santa Ana, CA 92705. (714) 5475832. $F R-650,6.5-\mathrm{in}$. length; 18 c (1000-up).

Printed-circuit board card guides are manufactured from $94 \mathrm{~V}-0$, a UL-rated material with fire retardant additives. Called Temp-OGides, they are brownish red, and rigid. They snap into $11 / 64 \mathrm{in}$. holes drilled into plates or channels that are from 0.47 to 0.090 in. thick. The card guides hold $1 / 16$ in. thick PC boards. Temp-OGides are stocked in 24 standard lengths from 2-1/2 through 14 in .
Booth No. 306 Circle No. 343

## Rf connector crimps onto cable

Kings Electronics Co., Inc., 40 Marbledale Rd., Tuckahoe, NY 10707. (914) 793-5000. \$18 (1-24).

Two crimp-type, push-on plugs are designed to mate with G-R leaf-spring coaxial cable receptacles. The rf connectors can be assembled to the cable with standard crimping tools. The No. 1935-1 plug fits on RG/U 58, 141, 142, 223, 303 and 400. The No. 1935-2 is for use with RG/U 8, 9, 213 and 214. Both are supplied with neoprene boots.

CIRCLE NO. 344

## Clear one-piece unit mounts LEDs to panels

Visual Communications Co., Box 986, El Segundo, CA 90245. 10¢ (high qty) ; stock-2 wh.

A LED mount called Cliplite improves LED viewing angle and brightness, and simplifies installation. Cliplite increases the angle of visibility and the brightness of standard LED displays by using striated lines and Fresnel rings in transparent butyrate plastic. It is a one-piece unit that mounts on a panel without tools; it snaps into a $1 / 4$-in. hole with finger pressure. The unit eliminates the grommet normally used to panel-mount LEDs. Cliplite is said to provide six-times the brilliance with a 180 degree viewing angle, using any standard narrow beam T-1 $3 / 4$ LED.

CIRCLE NO. 345

## Terminals secure wire leads to a PC board

Molex Inc., 2222 Wellington Ct., Lisle, IL 60532. (312) 969-4550. 2.35 ¢ (1000-up).

The 4706 series of terminals secures wire leads to a PC board prior to soldering. The terminals prevent the leads from falling out of the board holes during compo-
nent insertion and handling before wave soldering. Because the terminal fills the printed circuit board hole, solder voiding is minimized. The 4706 series terminals are inserted into the boards with a low insertion force, so adjacent components don't pop out of the PC board. After soldering, the insulation crimp creates a strain relief.

CIRCLE NO. 346

## You know our Capacitors, but have you seen our...

 EMI FILTERS applications. These include intermittent and continuous duty units rated to 500 amps , 5000 VDC and 600 VAC , and DC to 25 KHz . Single and multicircuit configurations (L, Pi and T ) are offered as low pass, electrical noise, line, screen room and heavy duty filters. Send us your circuit requirementsour extensive file of existing designs can probably provide the benefits of standardization to meet your non-standard needs. Get complete information today on our EMi Filters; write Electrocube, 1710 So. Del Mar Ave., San Gabriel, CA 91776; (213) 573-3300.

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CIRCLE NUMBER 146

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## BULLETIN 301

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279 Skidmore Rd., Deer Park, N.Y. 11729 Telephone: 516-586-5125

## PACKAGING \& MATERIALS

SMA-connectors require
no soldering


ITT, Cannon Electric Div., 666 E. Dyer Rd., Box 929, Santa Ana, CA 92702. (714) 557-4700. See text; 6 wks.

Three solderless SMA connectors for 0.141 -in. semi-rigid cables require no soldering of the center or outer conductors. The three versions are the Model 150500-5052, a straight plug costing $\$ 2.77$, in 100 -up quantities. The Model 150511-5051 is a bulkhead jack costing \$5.56, and the Model 150526-5052 is a right-angle plug costing $\$ 5.27$, each in 100 -up quantities. Each has a passivated stain-less-steel finish and features a separate, completely captivated female center contact that accepts the cable center conductor. The cable clamping mechanism is reusable and is capable of withstanding a pull test of 100 lb . The connectors are available with gold-plating or in stainless steel, and in other configurations.

CIRCLE NO. 347

## Coax connector provides VSWR of 1.08:1

Malco, 12 Progess Dr., Montgomeryville, PA 18936. (213) 799-9171. Model 141-0001-0001 jack, \$4.05 (1-9).

A microminiature coaxial connector is said to have the lowest voltage standing wave ratio in the connector industry. Lepra/Con coax connectors have a VSWR of only $1.08: 1$ at a frequency of 1 GHz . The connector is available in 180 different styles. Designed for RG178 and RG196, Lepra/Con also comes with an optional locking interface, which permits the user to lock the coaxial plug, when mated to its receptacle. This prevents any axial rotation.

CIRCLE NO. 348

## Instrument cases are made for portability



Buckeye Stamping Co., 555 Marion Rd., Columbus, OH 43207. (614) 445-8433. \$32.75 to \$99.50 (25-40); 2 wks.
Portable instrument cases, made of extruded aluminum shapes and aluminum panels, are designed for rugged applications. Dimensions are either $3-1 / 2$ or $5-1 / 4 \mathrm{in}$. high, and $8-1 / 2,11-1 / 4$ or 17 in . wide. The panels and feature stripes are either painted with a suede-type color finish or are vinyl-clad in blue, black or woodgrain. The cases include a dust cover on 5.25 in. high units to protect controls and meters. All sizes have a top with a flip-top piano-hinge at the back, and held securely with flushmounted slide locks. Silk-screening and custom piercing on front panels are also available.

CIRCLE NO. 349

## AC-line plug listed by UL as hospital grade

Pacific Electricord Co., Div. of Leviton Mfg. Co., P.O. Box 10, Gardena, CA 90247. (213) 5326600. Model C4700-010-GY 10-ft. cord, $\$ 3$ to $\$ 3.50$ ea. (1000-up).
A power-supply cord comes with a molded-on plug that is listed by Underwriters Laboratories as "hospital grade." Internal components are locked-in electrically, mechanically and physically. The construction restricts access and withstands cord pull out. The plug features a strain relief and blade assembly encapsulated in a PVC body. The assemblies are $100 \%$ tested for electrical integrity. In addition, torque, crush, drop, twist, and X-ray examination tests are performed on a statistical sampling basis. The standard color is light gray, and other wire sizes and jacket colors are available upon request.

CIRCLE NO. 350

## semioonductor

 overload protection... murata's posistors make it simpleMurata's PTH487A Posistors are designed to sense the case temperature of high power semiconductors and appropriately reduce power dissipation when dangerous power and/or current limits are approached. No other components are required, recycling is automatic and reliability is outstanding. Write for complete specifications.

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CIRCLE NUMBER 129

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## NEW LITERATURE ON ONE-PART EPOXY SYSTEMS



ECCOPRIME one-part products provide most of the properties of standard E\&C resins, adhesives, and coatings, and they can be used directly from the container, without weighing, measuring, or mixing. A new four-page illustrated folder lets you select from up to sixteen systems. Each offers improved convenience, accuracy, and speed when you use epoxies for casting, potting, bonding, sealing, or coating. Send for a free copy.

CIRCLE NO. 161

## NEW PRESENTATION DIELECTRIC MATERIALS CHART



This colorful chart is a standard reference for electronic engineers. Shows Dielectric Constant $\left(\kappa^{\prime}\right)$ and Loss Tangent $(\tan \sigma$ ) for many E\&C products and common materials plotted on $11^{\prime \prime} \times 16^{1 / 2 "}$ graph. For notebook or wall mounting.

CIRCLE NO. 162

## ELECTRICALLY CONDUCTIVE ADHESIVES AND COATINGS



ECCOAMP products offer high performance and savings for bonding, coating, sealing electrical/electronic components with conductive plastic. They include "cold" solders, anti-static, reflective and absorbtive coatings. Some have electrical and thermal conductivity equivalent to metals.

CIRCLE NO. 163
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## MICROWAVES \& LASERS

## Thermistor mounts work in mm range



Hughes Electron Dynamics Div., 3100 West Lomita Blvd., Torrance, CA 90509. (213) 534-2121. \$600 to $\$ 1525$.

A new series of millimeterwave, temperature-compensated thermistor mounts cover six waveguide bands in the 26.5 to $110-\mathrm{GHz}$ frequency range. The new mounts are designed to provide power measurements at millimeter-wave frequencies with the same convenience as those made in the lower microwave bands. All but the highest frequency range ( W -band) provide full waveguide bandwidth coverage. Each mount consists of an RF and compensating thermistor matched for tempco before being paired in the waveguide mount.

CIRCLE NO. 356
Directional couplers need less space


Sage Laboratories, 3 Huron Dr., Natick, MA 01760. (617) 6530844. \$250; 30 days.

Model FC1991 coaxial directional coupler covers the 3.7 -to- $4.2-\mathrm{GHz}$ communications band. Typical performance features are $50 \pm 0.2 \mathrm{~dB}$ nominal coupling, $0.25-\mathrm{dB}$ pk-pk coupling variation from 3.7 to 4.2 $\mathrm{GHz}, 22-\mathrm{dB}$ directivity, and $1.05: 1$ main line VSWR. Model FC1991 measures only $1-3 / 4 \times 1-1 / 2 \times$ $1-1 / 8$ in., excluding the connectors.

CIRCLE NO. 357

## 4-12 GHz detector has flat response



Systron-Donner, Advanced Component Div., 735 Palomar Ave., Sunnyvale, CA 94086. (408) 735-9660. $\$ 1050$; stock.

A calibrated directional detector is designed for use with microwave radio test sets in the com-mon-carrier bands from 3.6 to 11.7 GHz . The Model 1020-1 offers a previously unobtainable flat frequency response of $\pm 0.02 \mathrm{~dB}$ over any $30-\mathrm{MHz}$ window within the common-carrier bands. Over-all frequency response is $\pm 0.5 \mathrm{~dB}$ with $15-\mathrm{dB}$ directivity. Sensitivity is 10 mV per mW up to +13 dBm .

CIRCLE NO. 358

## Modulators feature low insertion loss



Hewlett-Packard, 1501 Page Mill Rd., Palo Alto, CA 94304. (415) 493-1501. \$495 to \$730; stock-8 wks.

Three new absorptive modulators for the frequency range 3.7 to 18 GHz feature at least $0.5-\mathrm{dB}$ lower insertion loss from 12 to 15 GHz , units. Models 33001E, 33001F and 33008 E are matched $\left(\mathrm{Z}_{0}=50 \Omega\right)$ at all attenuation levels. Each device covers more than one octave. The 33001 E is a $45-\mathrm{dB}$ isolation modulator with insertion loss of 2.5 dB from 8 to $12 \mathrm{GHz}, 3.0-\mathrm{dB}$ insertion loss from 12 to 15 GHz , and $3.5-\mathrm{dB}$ loss from 15 to 18 GHz . The 33001 F is an $80-\mathrm{dB}$ isolation modulator, and the 33008 E provides $45-\mathrm{dB}$ isolation from 3.7 to 8 GHz .

CIRCLE NO. 359

PHOTOELECTRIC CONTROLS APPLICATION HANDBOOK


## Photoelectric controls

Dozens of photoelectric applications for production and materialhandling control are described in a 48-page handbook. A glossary, as well as three appendices on control response time, wiring and selection of arc-suppression components, complete the publication. Micro Switch, Freeport. IL

CIRCLE NO. 360

## Electrode station hardware

A variety of electrode mounting station hardware for on-stream pH measuring requirements is described in a 12 -page booklet. Beckman Instruments, Fullerton, CA

CIRCLE NO. 361

## Circuit technology

Application notes for a 13-bit monolithic CMOS A/D converter, and new product descriptions for other devices are included in Vol. 10, No. 1 of Analog Dialogue. Analog Devices, Norwood, MA

CIRCLE NO. 362

## LSI test systems

Multi-user, multi-usage LSI test systems are described in a color brochure that includes a block diagram and descriptions of several software packages. Macrodata Corp., Woodland Hills, CA

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CIRCLE NUMBER 135


Prototype Magnetic Shields


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ER-4 COMPLETE PHOTO ETCH SET


CIRCLE NUMBER 143

## WHO

 MAKES WHAT \& WHERE TO FIND ITVolume 1 of Electronic Design's GOLD BOOK tells all. And, when you look up an item in its PRODUCT DIRECTORY you'll find each manufacturer listed COMPLETE WITH STREET ADDRESS, CITY, STATE, ZIP AND PHONE. Save time. There's no need to refer elsewhere to find missing information.

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## NEW LITERATURE

## Filter catalog

More than $550 \mathrm{RFI} /$ EMC filters and feedthrough capacitors are described in a 28 -page booklet published by RF Interonics, Bay Shore, NY.

CIRCLE NO. 364

## High-voltage supplies

A complete product line description of high-voltage power supplies, photomultiplier tube housings, pho-ton-counting and current-measuring instrumentation is contained in a new catalog. Pacific Photometric Instruments, Emeryville, CA

CIRCLE NO. 365

## Test instruments

A 40-page catalog describing new test instruments has just been released. New instruments described for the first time includes 5,15 and 30 MHz scopes and a 3.5 -digit voltmeter. B \& K Precision, Chicago, IL

CIRCLE NO. 366

## Mini reed relays

Bulletin MR-14.1 describes a new series of micromini reed relaysincluding what the company feels is the smallest form 2 A contact model on the market. Coto-Coil, Providence, RI

CIRCLE NO. 367

## Soldering and brazing

A comprehensive report on a modular approach to soldering and brazing systems is described-including case histories and data sheets-in a bulletin from LucasMilhaupt Div., Cudahy, WI.

CIRCLE NO. 368

## Pump-fluid recycling

Diffusion pump fluids can be cleaned and reused. Several methods are described in a new booklet from CVC Products, Rochester, NY

CIRCLE NO. 369

## Linear thermistors

A bulletin on linear thermistor networks is available for those who would like to investigate this alternative to thermocouples. Fenwal Electronics, Framingham, MA

CIRCLE NO. 370

## Coaxial connectors

A 64-page catalogue offering information on SMA, TM, TNC, and n-type rf connectors is available. Solitron/Microwave, Port Salerno, FL

CIRCLE NO. 371

## Magnetic cores

A 24-page catalog of tape-wound magnetic cores, including nine types of magnetic material is available. It features a list of 140 standard cores, their specs and magnetic path lengths. SGL Electronics, Westville, NJ

CIRCLE NO. 372

## Specialty tubing

Small-diameter tubing for industrial instrumentation is discussed in a new booklet from the Superior Tube Co., Norristown, PA.

CIRCLE NO. 373

## ICs for measuring control

A guide to $a / d$ and $d / a$ converters, to amplifiers and other all-IC devices, is available. Analog Devices, Norwood, MA.

CIRCLE NO. 374

## Function generator

A brochure that describes product features, specs, and 13 instrument configurations is available on the FG $50440-\mathrm{MHz}$ function generator. Tektronix, Beaverton, OR

CIRCLE NO. 375

## Hybrid microcircuits

A 30-page Hybrid Microcircuit Short Form Catalog that details new telephone tone receiver products, converters, power amplifiers and other devices is available from Beckman Instruments, Fullerton, CA.

CIRCLE NO. 376

## Pushbutton switches

Electrical and mechanical specifications, dimensional drawings, wiring diagrams and installation tips for standard pushbutton switches are found in a 16-page catalog. Centralab Distributor Products, Milwaukee, WI

CIRCLE NO. 377

# Design Aids 

## Steatite ceramics

Everything from water absorption through to power and loss factors are charted in a "Property Chart" of steatite, ceramic and lava insulators for electrical, electronic and chemical applications. Superior Steatite \& Ceramic Corp.

CIRCLE NO. 211

## IC interconnects

Design guidelines and application data for production sockets, burnin and test sockets as well as plugs, cables and headers are given in a four-page selection guide. Robin-son-Nugent.

CIRCLE NO. 212

## Microelectronic compounds

A microelectronic compounds selector chart provides a guide to the use of epoxy and urethane compounds for microelectronic applications. Hysol Div.

CIRCLE NO. 213

## Trimming potentiometers

A guide to MIL Spec trimmer potentiometers provides a handy reference to military spec numbers along with the equivalent type designations as well as MIL code resistance values. Weston Components.

CIRCLE NO. 214

## Display slide rules

Conrac's slide rule, useful for CRT display design, features a sine-wave MTF calculator on one side and a contrast ratio calculator on the other. The rule measures $9-1 / 4 \times 4 \mathrm{in}$. SSD.

CIRCLE NO. 215

## Buzz words

An expanded edition of "Sherry's Guide to Data Communication Buzz Words" is available from International Communications, Miami, FL

CIRCLE NO. 216

## Bulletin Board

Owens-Illinois has established a technical assistance program for customers of Digivue display panels, which can now be purchazed without the driving electronics.

CIRCLE NO. 217
Hewlett-Packard has cut prices $18 \%$ on its Model 5451B digital Fourier analysis system. There is also a new and less expensive $\mathbf{a} / \mathrm{d}$ converter with 2 channels, 12 bits and a 100 KHz maximum sampling rate.

CIRCLE NO. 218
EMI Gencom has acquired the vacuum photodiodes formerly manufactured by Tung Sol Division of Wagner Electric Co. The devices are used in analytical in struments such as turbidity me ters and UV monitors. They are low voltage (typically 9 V ) and fully compatible with solid-state circuitry, and can be battery operated.

CIRCLE NO. 219
Philips plans 94 different LOCMOS 400 digital ICs by the end of the year, of which over 70 are available now. They have a wide supply voltage range from 3 to 15 V and are pin compatible with other 4000 types.

CIRCLE NO. 220
Chomerics has developed a very low cost technique for the fabrication of printed circuit boards. Circuits made through this process can be molded to form 3-D features.

CIRCLE NO. 221
Fairchild Camera and Instruments has introduced 5 and $6-\mathrm{kV}$ couplers as direct replacements for most standard types of singlechannel couplers.

CIRCLE NO. 222
Honeywell has enhanced its $60 / 62$ small computer systems by doubling main memory and adding new data-management and communications features, among other changes.

CIRCLE NO. 223

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[^6]:    Intel Memory Systems
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[^12]:    Silicon photovoltaic cells analyze optical characteristics.

