This DMM keeps on working while it is being calibrated. A plug-in reference assembly goes to the cal lab, not the meter itself. Work for this unit means
more than just measurements. Key in numbers, and a $\mu$ P performs mathematical computations. Still another $\mu \mathrm{P}$ corrects for errors. Meet this multimeter on p. 105.



## Now in the only full line of super low profile SIP Resistor Networks.

If you haven't designed in Single In-line Package resistor networks because of their high profile, take another look. THE HEIGHT ON BOURNS SIPs IS ONLY . 190 INCH ! And that's standard for all 6,8 and 10 pin configurations with:

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For hand held radio transceivers, for example, where battery power drain as well as good RF performance are key factors, Teledyne's TO-5 relay is the logical choice for T-R switching.
Our complete line of TO-5 relays includes military and commercial/industrial types, with virtually all military versions qualified to established reliability MIL specs. For complete data, contact Teledyne Relays - the people who pioneered the TO-5 relay.


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SPDT \& DPDT types with internal transistor driver and suppression diode. Military and commercial/industrial versions.

- "D" and "DD" Series

With internal suppression and steering diodes. Military and commercial/industrial versions.

- Maglatch Series

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- Hi-Rel Series

Screened versions for space flight applications (NASA qualified)

- High Environment Series

Hi -temperature, Hi -shock, and
Hi -vibration types

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Cover: Photo bv Allen Howe, courtesy of Hewlett-Packard, Loveland Instrument Div.

[^0]
## ly

 four program instructions.$1 K$ bytes of high speed, low power static RAM.

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$70 \%$
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EFFICIENT


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Complete electrical specifications, size charts and prices for these units are listed in our new 60 page free catalog. Also listed are 12 additional line of power modules, including -

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400 to DC
DC to DC
DC to 60 -
DC to $400 \bumpeq$

For immediate complete information on Abbott Modules, see pages 1037-1056 Vol. 1 of your 1975-76 EEM Catalog or pages 612-620 Vol. 2 of your 1975-76 GOLD BOOK

## transistor

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## Editors

Editorial Offices
50 Essex St.
Rochelle Park, NJ 07662
(201) 843-0550

TWX: 710-990-5071
Cable: Haydenpubs Rochellepark
Editor-in-Chief George Rostky
Managing Editors:
Ralph Dobriner
Michael Elphick
Associate Editors:
Sid Adlerstein
Dave Bursky
Samuel Derman
Jules H. Gilder
Morris Grossman
John F. Mason
Stanley Runyon
Michael Shunfenthal
Contributing Editors:
Peter N. Budzilovich, John Kessler Alberto Socolovsky, Nathan Sussman

## Editorial Field Offices

East
Jim McDermott, Eastern Editor
P.O. Box 272

Easthampton, MA 01027
(413) 527-3632

## West

David N. Kaye, Senior Western Editor
8939 S. Sepulveda Blvd., Suite 510
Los Angeles, CA 90045
(213) 641-6544

TWX: 1-910-328-7240
Jim Gold, Western Editor
1537 Shasta Ave.
San Jose, CA 95128
(408) 279-8588

## Editorial Production

Marjorie A. Duffy, Production Editor
Tom Collins, Copy Editor

## Art

Art Director, William Kelly
Richard Luce, Anthony J. Fischetto

## Production

Manager, Dollie S. Viebig
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## Circulation

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## Asposs the Desk

## Ceramic package looks OK, but for how long?

We are considering ceramiccovered thick-film hybrid circuits in which the ceramic cap is cemented in place by an epoxy compound. We are evaluating this approach for use in commercial telecommunications equipment, against the use of a standard metal hermetic package.

Although our tests so far show no discernible difference between the two sealing methods we are worried about degradation of the epoxy seal during the long service life of the units ( 5 to 10 years).

We would be grateful to receive pertinent information that you might have handy, or at least, indication where such data may be obtained.

## H. Livni <br> Reliability Engineer Development Dept.

Tadiran, Telecommunications Div. P.O. Box 500

Petah-Tivka 49100
Israel
Ed Note:
Can anyone out there help Mr. Livni?
one of the most important ways of performing the useful conversion of solar energy, there seems to be reasonable ground for optimism that with the efforts beginning to be devoted to this field, significant advances will be achieved.

The substantial work on a Satellite Solar Power Station that is being carried out by NASA and industry has been directed to resolve the technical challenges. As the article pointed out, the reduction of transportation costs is one of the most significant areas of activity, and one that is already receiving attention by Boeing and Grumman, working in behalf of NASA/JSC.

The environmental effects are also being identified. So far we have found that the environmental impact could be minimized by appropriate choices of technology. For example, interactions with the ozone layer can be reduced to acceptable levels by substituting liquid propellants for the solid propellants to be used in the space shuttle.

Peter E. Glaser
Vice President
Arthur D. Little, Inc.
Acorn Park
Cambridge, MA 02140

## Solar-cell progress is ground for optimism

Samuel Derman's article (ED No. 6, March 15, 1976, p. 24 ) is an excellent survey of the state of the art of photovoltaic conversion. It skillfully presents both the advantages and the problems that will have to be overcome before it can be applied on a large scale.

Because this method represents

## Wrong name

We really blew it in our Advertiser's Index for the June issues. We failed properly to identify two ads as being from NEC Microcomputers, Inc., of Lexington, MA. The company is a wholly owned subsidiary of Nippon Electric Co., Ltd.
(continued on page 10)

[^1]OPTRON OPS 200 SERIES SWITCHES MEET SPECS AFTER 100,000 HOURS OF OPERATION
Even after 100,000 hours of operation at rated currents, OPTRON's new high reliability OPS 200 series optical limit switches will still meet specifications. New OPS 200 and OPS 200A limit switches combine the noncontact switching feature of popular optically coupled interrupter modules with the convenient mounting and actuating features of conventional mechanical switches to provide solidstate reliability in a mechanical switch package.

An optical shutter controlled by a snap-action mechanism interrupts the light path between a gallium arsenide infrared LED and a silicon photosensor. The condition of the photosensor, either illuminated or dark, determines the ON (closed) or OFF (open) state of the switch.

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Both new limit switches are available from stock in either normally open or normally closed conditions.

Detailed technical data on OPS 200 series limit switches and other OPTRON optoelectronic products chips, discrete components, reflective transducers, isolators and interrupter assemblies ... is available from your nearest OPTRON sales representative or the factory direct.

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HE new Dale resistors are more efficient to buy. A network of computer terminals throughout our three resistor plants gives you more useful production
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resistor efficiency you can use. It's part of an expansion program that has seen our floor space devoted to resistors grow from 300,000 square feet in 1970 to more than 400,000 square feet today. And much of this expansion has been devoted to automated facilities. Multistation winders let you specify the stability and power of wirewounds at a lower cost than ever... and batteries of laser spiralling machines turn out RNstyle metal film parts at machine-gun speed. We're making the most efficient resistors you can buy - and we're ready to prove it.

The new Dale resistors are made from more efficient materials than ever. Sophisticated equipment, like this scanning electron microscope, gives us state of the art capability for analyzing, identifying and specifying component materials. It's part of an integrated materials improvement, performance testing and quality control program we initiated 15 years ago in the early days of the Minuteman High Reliability Development Program. Today, one out of every 10 Dale employees is directly involved with Quality Control. Tangible results include: More than 100 separate QPL listings for wirewound and metal film resistors; the world's most reliable wirewound resistor (proven failure rate $.000021 \% / 1000$ hours). The new Dale resistors will give you less trouble-before and after purchase - than any others you can buy-and that's efficiency! Call 402-564-3131 for wirewound and 402-371-0080 for metal film.


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Our complete product line can be found in Electronic Design's GOLD BOOK. CIRCLE NUMBER 7


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## State



JOHNSON

## ACROSS THE DESK

(continued from page 7)
of Japan, a 77 -year-old company with recent sales volume exceeding $\$ 1.5$ billion.
The NEC Microcomputers advertisements featured MOS memories with MTBFs exceeding 1100 years and bipolar memories with life expectancies a few centuries longer.

Interested?
CIRCLE NO. 319

## A matter of a 0.1-V range not quite as expected

We built the op-amp scale expander described in "Ideas for Design" (ED No. 21, Oct. 11, 1975, p. 94). Unfortunately, trusting souls that we are, we did not check the arithmetic, the example given appeared so convincing.

Well, it works in full agreement with the formula for the $\mathrm{A}_{1}$ output voltage. However, the input attenuator divides by M , both the input voltage and its changes. Consequently only 0.1 , the input range of 1 V , appears at the input of $\mathrm{A}_{1}$. Since the over-all gain of $\mathrm{A}_{1}$ and $A_{2}$ is unity, there is no way to get a 1-V output range.

The example works if one has a multirange voltmeter with a $0.1-\mathrm{V}$ dc full-scale sensitivity. I do not know of any.

Any suggestions what to do with our expander?

Alex Azelickis
Vice President, Technical Relations Oak Industries Inc.
CATV Div.
Crystal Lake, IL 60014.

## The author replies:

Mr. Azelickis is correct in his statement that a $0.1-\mathrm{V}$ full-scalesensitivity meter is required, and I apologize for my failure to catch the decimal-point omission when proofreading.

The circuit was originally used in conjunction with a Heath IM104 VOM which has a $0.1-\mathrm{V}$ range, as do many VOMs and VTVMs including RCA's and several imports. However, if such equipment is unavailable it is merely necessary to
(continued on page 16)

# Sharpen your competitive edge. Use an HP computing calculator for design, analysis, control, and test. 

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 tors and plenty of peripherals: it's your choice. Three different models-the low-cost HP 9815, the powerful HP 9825 and the all-purpose HP 9830-offer a range of computing power.HP peripherals include paper tape punch and readers, printers, storage devices, a digitizer, a CRT, and an X-Y plotter. You choose the model and I/O options to configure a system just right for you.

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HP desktop computing systems put the power where the problems are.


## Four 50 MHz channels. Unique triggering facilities. Ideal for parallel data analysis.



As illustrated on the front panel detail, triggering is fully independent and multisourced. This gives a number of unique triggering possibilities and is just as useful for more conventional dual-trace measurements.


Here the main and delayed timebases are triggered from different channels, one example of the PM 3244 's unique triggering facilities. Signals can therefore be shown even when they are not related to the main timebase.
$\qquad$
Differential measurements can be made on the PM 3244 in the conventional manner or by using the composite triggering mode.

Zero in on
Four traces give you the logic story: show the relationships at a glance. But only the PM 3244 gives you four traces and fully independent triggering of main and delayed timebases. Thus the main timebase can be triggered on any of the four channels plus composite, external and line. The delayed timebase can be triggered on any of the four channels plus composite : independently! This gives a number of unique triggering possibilities, for example showing relationships that are not directly related to the main timebase, like the information in a data line when the main timebase is triggered on an address line.

## Doubles for dual-trace

 with extra performance.PM 3244 is the world's first fourchannel compact and all channels have full display facilities i.e. sensitivity, attenuation, invert, etc. It can therefore be used to make isolated or differential measurements and when you need conventional dual-trace displays, this scope also gives them, with two traces in reserve plus unmatched triggering facilities.

All displays are on a large $8 \times 10 \mathrm{~cm}$ screen and the compact construction weighs in at a mere 9.6 kg $(21 \mathrm{lb})$. The price is rather compact too! Write for more details. Read opposite about another data breakthrough.


Test \& Measuring Instruments


## 120 MHz with digitally delayed triggering to over 200 MHz . Ideal for serial data.

## ones and zeros



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The event is located using the illustrated delay counter, which can be set to count up or down at an adjustable speed as low as one step every two seconds. Specific sections can thus be examined and the counting stopped and stepped back to pick up and locate any irregularities.

The digitally delayed counter also overcomes problems of jitter, such as occur in mechanical systems like disk memories, tape drives, etc. In such cases, if the jitter is longer than one period, a conventional delayed timebase cannot be effectively employed. Once that display has been trapped, accurate time interval measurements can be macie using the normal delayed timebase controls.

In addition, the main timebase has a TTL triggering facility to eliminate triggering problems on preshoot, overshoot, ringing, etc.


As illustrated on the front panel detail, one push button and one control knob are all that's needed to operate the digital delay. The required event in the data stream 'lights up,' as shown, and can then be displayed using the normal delayed timebase in order to make accurate time interval measurements.

and zeros with the aid of this useful 64-page booklet. For further information simply circle the reader service number.

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## 17 days from now this trace will look the same as it does today. Now that's storage!

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Find out how the unique Gould OS-4000 Digital Storage Oscilloscope and the companion 4001 Output Unit can make your work more efficient and easier. Call your nearest Gould Sales Engineer for details. Or write Gould Inc., Instrument Systems Division, 3631 Perkins Avenue, Cleveland, Ohio 44114.

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Ask for catalog B-5508 and see if the Series 1800 is the answer to your complex switching needs.

## LEDEX INC.

123 Webster Street
Dayton, Ohio 45401
(513) 224-9891

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## ACROSS THE DESK

(continued from page 10)
change the feedback resistor of op$\operatorname{amp} \mathrm{A}_{2}$ to yield the desired gain.
For example, if the feedbackresistor, $R_{f}$, is changed from 2 to $20 \mathrm{k} \Omega$, the resulting gain of 10 allows using a $1-\mathrm{V}$ range. Similarly, a gain of 25 allows use of a $2.5-\mathrm{V}$ range.

## Misplaced Caption Dept.



Don't ever take my scope again without telling me.

Sorry. That's Antonio Pollaiuolo's "Hercules and Antaeus," which hangs in the Uffizi in Florence.

## Me an editor?

Maybe. If you would enjoy interviewing industry authorities and writing about the latest technological developments, you might enjoy being an editor. Electronic Design needs one in Los Angeles. Interested? Call Dave Kaye at (213) 641-6544.

If you can't take western smog, try the smog in New Jersey. We have an opening at our home office in Rochelle Park, NJ. Call Mike Elphick at (201) 843-0550.

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## News Scope

AUGUST 16, 1976

## Old and new techniques guided Viking to Mars

The guidance system that put the Viking Lander down so neatly on the surface of Mars has been in the works for seven and a half years, says Michael K. Mann, Subsystem Lead at Martin-Marietta's Lander Support Office in Denver, which had responsibility for the Lander's guidance package.

Proven techniques were used whenever possible, but innovative approaches were also incorporated.
"The strapdown inertial system was not a common approach when we began to put the system together," Mann recalls. "And it worked well." The inertial system was supplied by the Hamilton Standard Div. of United Aircraft.

The Terminal Descent Landing Radar, built by Teledyne-Ryan, also used a novel approach, and it too has been a success. The unit is a four-beam Doppler system that measures the rate of descent. The transmitter is an Impatt diode oscillator that supplies de to 13.3 GHz output. The output of each radar transmitter is 150 mW . "This device is considerably lighter than other transmitters that we considered at the time," Mann says.

The altimeter radar, also built by Teledyne-Ryan, "is certainly one of a kind," he adds. Its operational range is from 435,000 feet down to 135 feet. "And although the reading beyond 435,000 feet is ambiguous the altimeter accurately detected and measured the Martian surface at 800,000 feet. Due to the system's known ambiguity at such distances, however, the navigator did not accept the information."

The altimeter transmitter, which operates at 1 GHz , works in four modes. "We actually switched antennas between the first and second mode," Mann says. The first mode-when the lander is decelerating in the atmosphere-uses a


The Viking Lander's $10-\mathrm{lb}$ uhf radio is used for high data rate traffic from the surface to the Orbiter, where it is converted to $S$ band.
phased-array antenna that is built into an aeroshell, which is a 70-degree cone covered with an ablative material to protect the Lander from the heat during entry. The aeroshell was ejected at about 19,000 feet and the antenna went with it. Then the system switched to another antenna, on the bottom of the Lander, which was an inverted disc cone, and used it down to the 135 -foot point.

The radar altimeter had two "electronic countermeasures-like" problems to cope with, Mann explains. It had to distinguish between the ground and two pieces of discarded hardware-the aeroshell and the back of the lander, which was ejected later with a parachute. The problem was solved with special logic in the software, Mann says.
The guidance computer, built by Honeywell Aerospace, is similar to one still functioning in NASA's ATS satellite. It uses a 2 -mil plat-ed-wire 18-k memory-"a technique
that evolved during the development of the Viking," says James Doubek, Honeywell's technical director of the Viking Lander's computer and storage memory.

The only plated-wire memories used prior to this were 5 -mil wire memories that went into the ATS and the Minuteman ICBM.
"One thing we're proud of," Mann says, "is that the guidance worked without ever having an integrated flight test. We had wanted to flight test all the components together but due to time and money restraints, we weren't able to. The design was verified by computer simulation."

## High-speed FFT chip <br> set under development

Fast Fourier Transform (FFT) analyzers that sell for several thousand dollars may soon have some stiff competition from an FFT chip set under development at TRW.

The ICs, being produced under contract for the Air Force, can perform a multiplication and three addition operations on a 12 -bit parallel word-in only 240 ns . When used to perform a Fourier Transform, the set can complete the task on 1024 points in only 2.5 ms . More sample points or higher speed can be achieved by cascading devices and adding a larger external memory.

The three chips in the set are fabricated with a triple-diffusion process that permits very-largescale integration, according to James Buie, an engineer working on the project at TRW's Redondo Beach, CA facility. The main IC of the set is the signal-processing arithmetic unit (SPAU), which contains 15,000 devices on a 350 by $310-\mathrm{mil}$ chip-the equivalent of roughly 3000 gates.

The SPAU replaces about 55 conventional TTL/MSI ICs, Buie says. Among other things, the architecture of the SPAU provides for register transfers, parallel multiplication and simultaneous addition.

The second chip in the set is the signal-processing address-control chip (SPAC). This device controls accessing of the memory that stores the FFT data points. It also
controls the accessing of SIN and COS look-up-table ROMs.

SPAC chips may be cascaded to extend the total number of FFT sample points that can be handled. One SPAC can provide 32 points, two will accommodate 1024 points, and with three SPACs up to 32,768 points may be sampled.

The final chip in the set is called a signal-processing delay line or SPDL for short. It is basically a shift register composed of 60 D type flip-flops plus input/output control circuitry. The SPDL can accept digital words that are 12 bits wide and 5 bits long and is used to make data available to the processor when they are needed.

In addition to being used as a Fast Fourier Transform analyzer, the SPAU can also be used to filter and digitize all types of complex signals, Buie reports. Specific frequency components of a given signal can be filtered with greater speed and accuracy by FFT techniques than with other filtering methods.

The chip is not yet available, but should be on the market by the first quarter of next year, Buie notes. And although there is no firm pricing available yet, Buie estimates that the 3 -chip set will probably sell for about $\$ 300$. Considering that the 55 military-grade MSI devices the SPAU alone replaces cost about $\$ 750$, that's a bargain.

Another benefit of the new FFT chip set is a significant savings in the cost of assembly. Much smaller PC boards-with a five-to-one reduction in the total number of interconnections - can be used.

## Ocean seismic device smaller, uses less power

A new underwater seismic detection system designed with MOS and CMOS circuitry uses less than one-fifth the power required for earlier tape-loop systems.

Also, the combined battery volume and weight have been cut to $1 / 25$ th by the use of lithium batteries placed inside a small pressure hull along with the electronics, instead of being placed outside the hull in a large and costly pressure housing.

The new submersible earthquake-
sensing device, developed by MIT researchers, uses CMOS analog and digital circuitry for amplification and control along with the MOS memory, which replaces the continuously running tape loop normally used.

The system can remain on the bottom for up to 30 days, providing eight hours of seismic-event recording on a 9 -track tape recorder. Digital timing circuits release the system after 30 days, and onboard flotation spheres lift to the surface the pressure-hull cylinder with its circuitry, tape recorder and batteries.

The tape recorder is turned on only to record a peak of seismic activity, but to avoid missing critical data occurring before the peak the input signals are digitized and passed on to thirty 1-k MOS registers that are operated at a low voltage to reduce current drain. These registers replace the tape loop of earlier systems.

The 30 registers provide 18 seconds of signal delay from the seis-mic-sensor inputs.

## Solar cells power carts at nation's summer fete

Sun-powered vehicles are on display this summer in Washington, DC, and may be enjoyed along with the traditional customs and activities of the Festival of American Life, where they will be in operation.

The two vehicles resemble golf carts and belong to the National Park Service. One is fitted with a vacuum cleaner powered by a 1.6 hp motor, and is used to scoop up paper and trash at the festival site. In between morning and afternoon use it is plugged into a solar-cell array for recharging.


Sun-powered trash collector and gocart are recharged from 24-panel solar array.

The second vehicle is a cart used by Park Service personnel for transportation around the grounds. During idle periods it, too, is connected to the charging panels.

Both are equipped with six standard $36-\mathrm{V}$ batteries that are recharged by a 24 -panel array of silicon photovoltaic cells. The panels, mounted on aluminum stands, can be tilted to obtain maximum array output.

The 24 panels contain a total of 3456 3-in. diameter solar cells, with an active area of $176 \mathrm{ft}^{2}$.

The modules in the Washington experiment were produced by Solarex Corp., Rockville, MD, for an average cost of about $\$ 21$ per peak-watt output.

## Microwave oven contains a calculator chip

The latest application for calculator chips is in the kitchen-in Litton's new microwave oven. Dubbed the Memorymatic Model 420 , the new oven uses a Mostek calculator chip-the MK50285N-to make possible two programmable cooking cycles.

The chip acts as a countdown timer and allows the user to program the oven to operate for two different periods of time at two different power levels, according to Verle Blaha, vice president of engineering at Litton's Microwave Cooking Products Div. in Minneapolis, MN.

For example, a user can program the oven to defrost frozen food for a certain period of time and then automatically switch to a cooking operation. The amount of time a particular operation is programmed for is displayed on a fourdigit LED display.

The oven has ten different buttons to control the amount of power used for cooking. The maximum power available is 650 W , and the buttons are preset at $10 \%$ intervals. However, it is possible to obtain intermediate values of power by pushing two buttons in sequence.

For example, pressing 5 would give $50 \%$ power while pressing 5 and then 6 would give $56 \%$ of the maximum power. Power is controlled by a triac triggered by a digital signal.

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They're all standard features in MOSTEK's new DTMF tone generating CMOS circuits. High-performance characteristics of the MK 5085 and MK 5086 allow greater flexibility for integration into a broad range of communication applications, whether you're designing or re-designing. More functions are provided on-chip, resulting in fewer interface components, and obvious reduced system cost.

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Two outputs rather
than one. The MK5085/6 has two outputs theit switch to opposite polarities when

## Whether the input is analag ar digital. indisplaus the CRT is still Na.l

Despite the promise of such young technologies as LEDs, liquid crystals and gas-discharge cell arrays, the venerable cathode ray tube continues to survive and find

Ralph Dobriner<br>Managing Editor

new areas of application.
"The reason the CRT continues to survive a.nd find new uses is that very few applications have fully exploited the capabilities of this device," according to Jim Wurtz, senior applications engineer for Littion Industries, ElectroOptics Dept., San Carlos, CA.


High-resolution, 5-in.-round, film-recording CRT (upper left) and line-scan fiber-optic CRT-both available from Litton's Electron Tube Div.-are shown amidst some of the imagery recorded with these tubies.

The CRT, he notes, provides a virtually inertialess point of light whose brightness can be controlled over a great range and which can be randomly placed anywhere in a two-dimensional plane.
"Neither the data source nor the order of information is important. The CRT has sufficient memory for the smooth presentation of data, but is capable of instantaneous change."

## Evolution not revolution

Although there have been no dramatic breakthroughs in recent years, CRT technology continues to be marked by slow and steady improvements.

Two of the most significant trends in recent years are the advent of new and brighter phosphors that provide the displaysystem designer with both a wider choice of wavelengths and narrowband emission. Second, progress in semiconductors, making it possible to do things with the CRT that could not have been done a few years ago.

Finally, new electron emission systems now under development may hold the key to longer cathode life, lower heater power and higher emission density.

Today, there are more than 50 different CRT phosphor screens registered with the JEDEC Tube

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Council of the Electronic Industries Association. And, of course, there are unregistered specialized phosphors in use or under development by various CRT manufacturers.

By definition, phosphors for CRTs are inorganic crystalline materials that can convert electron energy into light. They are highly purified crystalline substances to which small amounts of impurities have been added.

The impurities serve as activa-
tors and, in combination with the host crystals, promote the phenomenon of luminescence. Fluorescence and the phosphorescence (persistence) characterize the useful properties of these, phosphors.
The nature of the luminescence is affected by the physical and chemical properties of a phosphor, the screen application and tube processing techniques used, and the mode of tube operation. The end use requirements of the CRT determine the phosphor used.


A contrast in display tubes: Above, Raytheon's 23 -in. CRT used in air-traffic-control displays. Left, $5-\mathrm{in}$. projection CRT available from Wat-kins-Johnson, Palo Alto, CA.


Peak spectral energy of phosphor type P-43 is located in a very narrow band centered close to the peak sensitivity of the human eye.

Practically all of the money spent for phosphor research comes from the entertainment TV tube business, But, Wurtz points out, there's also a lot of fallout that has helped the rest of the CRT business.

## Progress in phosphors

He cites in particular P-43 (see table), a phosphor that is getting wide use, especially in cockpit displays. P-43 is a line emitter, in that its peak spectral energy is located in a very narrow bandabout 550 nanometers-that is centered fairly close to the peak sensitivity of the human eye.

If a P-43 screen is combined with a notch light filter on the face of the CRT-the filter cuts out all the wavelengths except that of the phosphor-a net gain in the display's contrast is achieved.
Two relatively new phosphors are P-50 and P-51. Both are used in multicolor displays.

P-50 is a two-color screen consisting of red and green phosphor components. With low-voltage operation ( 8 kV ) the screen is red. At high-voltage operation ( 15 kV ) the screen's color changes to green. Intermediate colors are obtained at voltages between these levels.

P-51 is similar, except that red is obtained with only $6-\mathrm{kV}$ operation and green with 12 kV .

Engineers thinking of purchasing a CRT for a particular application are sometimes not aware of the essential specs or tradeoffs among the various phosphors.

First, the customer must decide whether he wants a white screen or green. If he decides on white, then there are several choiceseither a couple of the traditional P-4 phosphors or the newer P-45 material.

P-4 really consists of two materials, one blue and one yellow. In fact, if you hold a microscope to the face of your black and white TV screen you'll see that it is composed of little blue and yellow flecks which add up to white.
On the other hand, P-45 is a single-constituent white material that is brighter than the P-4 phosphor.

If the customer decides on a green display, he could use P-1, P-43 or P-31. Jim Wurtz, of Litton, explains:
"Each of these has different


## Phosphor characteristics (Standard JEDEC registered phosphors)

| E.I.A. <br> Phosphor | Emission color <br> fluorescence | Emission color <br> phosphorescence | Persistence | Application |
| :--- | :--- | :--- | :--- | :--- |

# Announcing the 1740A... a new 100MHz scope with fresh measurement ideas. 

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| E.IA. Phosphor | Emission color fluorescence | Emission color phosphorescence | Persistence | Application |
| :---: | :---: | :---: | :---: | :---: |
| P. 47 | Purplish blue | Purplish blue | Very short | High efficiency for scanning and recording |
| P. 48 | Yellowish green | Yellowish green | Very short | Flying spot scanners |
| P. 49 | Green, red | Green, red | $\begin{aligned} & \text { Red-medium } \\ & \text { Green-medium } \end{aligned}$ | Graphics and alphanumeric displays |
| P. 50 | Two color screen Red at 8 kV ; Yellow green at 15 kV | Red and Yellow green | $\begin{aligned} & \text { Red-medium } \\ & \text { Green-medium short } \end{aligned}$ | Multicolor displays |
| P. 51 | Two-color screen Red at 6 kV ; Green at 12 kV | Red and Yellow green | Red-medium Green-medium short | Multicolor displays |



For displays subject to severe environmental conditions, the trend is toward integrated CRT assemblies in which the deflection yoke is prelocated on an electrostatic-focus CRT. Shown are the components and CRT for a ruggedized airborne display made by Raytheon's Industrial Components Operation, Quincy, MA.
characteristics. The decay time and saturation may vary one way or the other. For a simple display application the buyer could just ask the supplier for a bright green display. On the other hand, if he's designing a cockpit display with limited lighting conditions the decision is a lot more critical,"

## Improving the contrast ratio

A continuing effort is underway by tube manufacturers to increase the contrast ratio of the CRT image.

The outside suface of almost every CRT is glass, and glass typ-
ically reflects $4 \%$ of the energy that strikes each surface. Since energy has to pass through both the front and back surfaces of the CRT faceplate, the information transmitted can be reduced by as much as $8 \%$.

In many applications that presents no major problem. For instance, in home TV, the room is normally dark and the contrast ratio between the room and CRT image is sufficient to provide a very good image.

But, try to watch TV in bright sunlight, the image washes out because there is not enough contrast to differentiate the projected im-
age from the background. In other words, the glare is so strong it makes the picture unreadable.

Although most people don't watch TV outdoors there are many applications where CRTs are viewed in direct sunlight or in conditions of very high ambient light.

What methods are tube manufacturers using to increase the contrast ratio of the image-besides increasing phosphor brightness that is?

The major method is to increase the contrast ratio of the CRT image by reducing the glare on the glass faceplate. Such efforts normally concentrate on the implosion panel required for most CRT applications.

The following are some of the ways to increase the contrast ratio of the implosion panel.

- Acid etch. The surface of the glass implosion panel is etched to diffuse the light. This is a cheap process and is typically used in low-cost, high-volume CRT applications.
- Plastic. Plastic panels are mechanically brushed or molded to diffuse light. They come in many colors, are also cheap, and are used in low-cost, high-volume applications.
- Gray glass. Again, it is low in cost, but offers limited CRT image improvement because of high front surface reflections.
- Coating. In this process the implosion panel is vacuum coated with an antireflection coating. It has a higher cost and much higher performance. Coated faceplates are widely used in military displays, air traffic control monitors, oscilloscopes and other critical display applications.
- Laminating. This process involves laminating (between sheets


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Ruggedized electron gun for CRT $22 \cdot \mathrm{~mm}$ neck is made by Raytheon.


1. The differences between the Watkins-Johnson laminar-flow electron gun and the traditional crossover gun is apparent in the diagram.
of glass or plastic) a colored plastic film or polarizing film. Frequently, the plastic sheets are covered with an antireflection coating to reduce the front surface reflections. This method is more expensive than the etching or gray glass approach, but is said to provide the best CRT images.

Mike Larro of Optical Coating Laboratory, Santa Rosa, CA believes that the best CRT image can be obtained by designing a bright phosphor and then enhancing the contrast with vacuum-deposited thin-film coatings.

The company has developed, what it calls, a High Efficiency Antireflection (HEA) coating that "has gained wide acceptance for customers with critical light-sensitive CRT applications." HEA, says Larro, can be applied to either
glass or plastic substrates to reduce surface reflection by a factor of 10 to one.

## A solid boost for CRTs

The impact of solid-state technology on CRTs is twofold. Because solid-state memories, microprocessors and information handling circuitry have become smaller, better and cheaper, the data to be displayed are available at higher rates. More important, the circuits and components for driving the tube itself have become smaller, faster, cheaper, more stable and more efficient.

Deflection amplifiers, high-voltage supplies, video drivers and shaping circuits have shrunk from vacuum tubes to discretes and then to ICs.

Solid-state memories and logic arrays, which enable the information to be arranged in optimum form, have had the greatest effect on CRT use.

For example, programmable read-only memories are now able to perform linearity correction in high precision displays.

If one tried to generate a perfect square on a magnetically deflected CRT it would tend to have a pincushion shape (with bulging, distorted sides) because of the geometric difference between the sine and tangent of the angle of deflection. In the past, pin-cushion or barrel correction was approached by placing permanent magnets around the neck of the tube. In fact, this is still done in low-resolution displays.

One of the problems with this approach is that magnets can inadvertently deflect the beam. As a result, this beam correction is now being done electronically rather than magnetically, by analog function modules available from such companies as Intronics, Newton, MA. It also can be done digitally, by means of PROMs, ROMs or microprocessors, where the desired mathematical functions are programmed into circuitry that automatically corrects for any distortions.

An example of one area of progress in drive circuitry is the cathode ray tube plotters now in operation that have sufficient accuracy and stability to generate circuitboard artwork, interconnect patterns for large-scale integrated circuits, and even diffusion masks.

Electron-gun design has not changed much over the years, although there have been many improvements in the precision of assembly and cathode materials.

Two important innovations in gun technology have been the laminar-flow gun pioneered by Watkins-Johnson a few years ago and the development of multiplebeam guns announced by Sylvania. Sylvania has since withdrawn from the industrial and military CRT business, but other companies have picked up its technology.

Currently the single-gun multibeam CRT is being used for a multi-element infrared display that sweeps together up to 20 beams -each less than 25 microns in

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size and each individually modulated. Another use for this device is in high-speed alphanumeric character generation.

Fig. 1 shows the essential differences in electron-beam trajectory and electric-field lines between the Watkins-Johnson laminar-flow electron gun and the traditional crossover gun.

The laminar-flow-gun CRT is said to have reduced grid drive requirements, increased resolution, increased brightness and reduced deflection focusing. The company says it has achieved spot sizes as small as 0.8 mil and brightness in excess of 20,000 foot-lamberts.

## Focus on scope CRTs

So far the discussion has centered on CRTs for a broad range of display applications, from the simple low-end computer terminal to the more sophisticated airborne cockpit displays.

One area of application that has probably spurred more advances in CRT design than any other is the oscilloscope.

Although some CRTs made today are not much different from those contained in the first scopes nearly 40 years ago, the capabilities of some of the latest CRTs have actually outpaced the electronics used in scope design.

The leading scope manufacturers -firms like Tektronix and Hew-lett-Packard-have placed such stringent requirements on their CRTs that they prefer to make their own. In selecting a CRT, the scope designer has to consider deflection factor, bandwidth, storage capability, reliability, display quality (which includes screen dimensions), trace brightness, spatial resolution and distortion.

Among thase, the biggest spur to design innovation is the quest for higher bandwidths. In 1955, a 30 MHz laboratory scope was considered exceptional. Today, $350-\mathrm{MHz}$ portables and $500-\mathrm{MHz}$ lab units are in common use. Special models have been built that operate into the gigahertz range.

Although storage CRTs have been around a long time, tube performance is continuing to improve.

Tektronix oscilloscopes use three types of storage CRTs-the proprietary bi-stable phosphor storage
tube, a variable persistence tube (sometimes called halftone storage) and a new fast-transfer tube. The last device can also provide operating modes that are similar to the simpler bi-stable and vari-able-persistence types.

Storage writing speeds are not quite as fast as conventional CRT speeds . . . yet. But they are catching up. Recent developments in transmission storage tubes at Tektronix have resulted in a fast stored-writing speed of 3000 divisions per $\mu \mathrm{s}(1350 \mathrm{~cm} / \mu \mathrm{s})$. Even greater speeds are expected in the near future.


Rectangular face fiber-optic CRT is used for direct photo-recording in electro-medical instrumentation. It's available from M-O Valve Co. Ltd., London.

The most common mistake made by engineers, according to a survey of CRT manufacturers, is to specify overly tight tolerances on the tube's electrical and mechanical properties.

## Design around the problem

With a tight tolerance, on the cutoff range, for example, a certain number of tubes in a particular production run will fall outside the range. And since it is not possible to get inside the vacuum envelope to make changes, the tube must be thrown away.

It is far more economical to design the circuitry to allow for a wide cutoff range to begin with.

In spite of advances in tooling, according to one manufacturer, it is still difficult to hold element spacings within the gun to tolerances that allow a narrow specifi-
cation of some of the operating voltages. Fortunately, this situation is easily allowed for in the drive circuitry without affecting the tube performance. The CRT envelope is obviously inviolable after it is made.

The traditional approach to specifying CRTs is to establish fixed values for all operating voltages except the control grid (G1), and then setting G1 to cut-off. In truth, improved system performance can be obtained by adjusting the first anode (G2) voltage to obtain a constant cut-off voltage. This procedure means that it's possible to use a much less expensive video amplifier, and it makes possible a uniform quality of display.

A widespread misconception about CRTs is that if you don't buy a standard item, you will pay more for the device. That may be true in some cases, but in most instances a tube design can be fit to an application with no engineering charge by putting together existing gun, screen and envelope designs.

## An abundance of CRTs

An indication of the enormous variety of CRT types available today can be gotten by flipping through product brochures of Clinton Electronics Corp., Rockford, IL.

One of the largest manufacturers of magnetic deflection black and white CRTs, the company turns out some two-million picture tubes-varying in size from 5 in. to 23 in.-each year.

Clinton says it has developed 500 distinct tube types and over 200,000 variations to meet a variety of applications. These include alphanumeric data displays, video monitors, electronic games, projection photography and others.

Almost as diverse a selection of CRTs is available from RCA's Industrial Tube Div., Lancaster, PA and RCA's Electronic Components Div., Harrison, NJ.

The range of products here includes instrument CRTs for portable oscilloscopes, ultra-high resolution tubes for medical instrument applications, flying-spot scanner CRTs, and special-purpose display storage tubes for military uses such as airborne weather radar.

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# Pedal exerciser with built-in $\mu \mathbf{P}$ quickly reveals your fitness level 

Exercising can be fun and interesting when a $\mu \mathrm{P}$ tells you how well you're doing. A pedal excerciser, called Dynavit, has been developed by Keiper of Rockenhausen, West Germany. It is controlled by a PPS-4/2 4 -bit $\mu \mathrm{P}$ from Rockwell International's Microelectronic Device Div., Anaheim, CA.

The $\mu \mathrm{P}$ accepts inputs of heart rate, age, weight and sex and calculates the appropriate leg loading for the pedals. After pedaling for 10 minutes, the $\mu \mathrm{P}$ calculates your Dynavit number-a measure of your physical fitness-and displays it on a 4 -digit LED readout. The Dynavit number is on a scale of 0 to 150 . Poor physical condition is represented by a reading between 0 and 50. An average individual with periodic exercise shows 50 to 75. An athletic person in good shape will come in at 75 to 100 . And a professional athlete may achieve a Dynavit number in excess of 100 .

## It displays your specs

In addition to displaying the Dynavit number, the LED readout can display your age, weight, sex, the elapsed time, the loading on the pedals (in pounds), your pulse rate and the number of calories consumed while exercising.

Weight, age and sex are punched in on the Dynavit keyboard. Heart rate is fed into the $\mu \mathrm{P}$ with either of two sensors. The optional sensors are an electro-optical earlobe clip that senses the blood flowing through the capillaries in the earlobe, and a three probe set of sensors that attaches either on the arms or the chest.

Resident in ROM is an algorithm that calculates the heart rate


A pedal excerciser with a brain, the Dynavit has a $\mu \mathrm{P}$ that monitors heart rate and combines it with age, weight and sex to provide the proper leg resistance on the pedals. After a 10 -min. exercise, the $\mu \mathrm{P}$ calculates a number and displays it. The number is a measure of physical fitness.
that is dangerous for the person on the Dynavit. The algorithm is based on age, weight, sex and initial heart rate. During normal operation, the Dynavit flashes a green LED at the rider.

If the rider's pulse rate slips into the danger zone, a red LED is flashed and the rider is thus warned to either stop or, by pushing a button on the console, to reduce the leg loading. If the option is taken to reduce the leg loading and complete the $10-\mathrm{min}$ exercise,


Heart of the Dynavit is the Rockwell International PPS-4/2 $\mu$ P. It accepts information on the Dynavit keyboard and acquired with the Dynavit heart probes. On a 4 -digit LED display the $\mu \mathrm{P}$ outputs a measure of physical fitness, the number of calories consumed during the exercise, the elapsed time, the pulse rate and additional data.
the $\mu \mathrm{P}$ automatically compensates for the different loading during a portion of the exercise, and still calculates an accurate Dynavit number.
The PPS-4/2 $\mu \mathrm{P}$ set in the Dynavit contains a CPU, an I/O chip, a $4-\mathrm{k} \times 8 \mathrm{ROM}$ and a $256 \times$ 4 RAM. A rechargeable battery allows the machine to start. The pedals operate an alternator that drives the electronics, recharges the battery and drives the resistive pedal load.

Although the current Dynavit sells for $\$ 600$ and is meant for home use, a hospital version currently under development will have a remote console and a minicomputer that can control from 20 to 50 exercise units, notes Robert Browing of Rockwell International.

At the moment, Dynavit can only be bought in Germany. However, within the next 6 months it is expected to be introduced in the United States.


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## Navy tests new contractor-freedom policy

Firms bidding on government contracts will have more elbow room to suggest their own ideas-rather than remain tied to rigid specifications -if a policy being sponsored by the Office of Federal Procurement policy catches on.

The test case is the Navy's Shipboard Intermediate Range Combat System (SIRCS) in which industry could propose any technological approach -"even bows and arrows," quipped one program official-as long as it did the job. SIRCS is an integrated system of weapons and fire control planned for ships of 1000 tons and larger. It would be a self-defense sys-tem-probably including missiles, various guns, radars and computers, although the exact mix would be up to the winning contractor.

If the SIRCS experience demonstrates that contractors can produce better hardware-without driving up costs-by relying on their own innovation, the Pentagon would like to apply the approach to other military missions.

## Sunspot effect on CB said not so bad as feared

The Commerce Dept.'s Office of Telecommunications has backed off from its earlier dire warnings that upcoming sunspot activity would jeopardize citizens-band radios transmitting at the assigned 27 MHz .

CBers became alarmed by the possibility that sunspots would render their radios useless. The FCC conceded that CB users depending on longdistance communications may be inconvenienced, but pointed out that FCC regulations prohibit using CB radio service for communications beyond 150 miles.

The issue centers on the relationship of sunspots to what is called skywave, or skip, interference, which occurs when radio waves reflected from the ionosphere "skip" several hundreds miles and interfere with local signals operating at the same frequency. Sunspot activity will be at its peak for the next three years.

## LCDs: solution seeking a problem?

Both the Air Force and Navy are continuing their search for liquid crystal displays (LCDs) that can replace conventional cathode ray tube (CRT) displays in aircraft cockpits, but LCD technology has not progressed sufficiently to dislodge CRTs.

Although in-house Navy studies have indicated LCDs would cut weight, volume and power requirements to half those of CRTs, neither service has enough confidence to commit LCDs to operational aircraft. The two new fighters, the Air Force's F-16 and Navy's F-18, are using CRTs and so is the experimental Air Force flight-control system, the Digital Avionics Information System (DAIS).

Principal beneficiary of the search has been Hughes Aircraft Co., which developed a 5 by 7 -in. LCD screen to display alphanumeric data for the Naval Air Development Center and a 2 -in.-square LCD capable of generating a black and white TV picture for the Air Force Avionics Lab. Both of them have remained in the laboratory.

Now Hughes is trying again with a combination LCD-holographics lens that could be used in a head-up display (HUD). Under contract to the Avionics Lab, Hughes will deliver a brassboard unit in which holographic flight data will be displayed right in front of a pilot's eyes. The holographic lens uses a laser operating at a wavelength of 530 nm to generate green symbols. However, the lab is not sure whether it will proceed to flight tests.

## Navy computer embroiled in controversy

The Navy's proposed new standard computer for future aircraft and missiles, the AN/AYK-14 (ED No. 14, Washington Report, July 5, 1976), is caught in a crossfire.

Two of the six finalists in the competition have dropped out and McDonnell Douglas, which would be the major user, is quietly trying to scuttle the program. Meanwhile, Congress is asking the Navy embarrassing questions about why it can't use an off-the-shelf computer instead.
H. Tyler Marcy, assistant secretary of the Navy for research and de-velopment-and a former IBM executive-defended the program on the grounds that the AYK-14 was needed to check the proliferation of airborne computers. Marcy also denied that the development competition was rigged for Univac, which benefited from the Navy's decision to require compatibility with the Univac AN/UYK-20 shipboard computer.

McDonnell Douglas has publicly complained that using a new computer would increase total costs of that program, and the House-Senate conference committee on the FY ' 77 defense authorization bill criticized the Navy plan to develop new systems where hardware already exists, and singled out the AYK-14 for special mention. The Navy was directed to study the possibility of procuring an off-the-shelf computer and report back its findings.

Marcy made it clear, however, that the Navy intends to proceed with the new computer.

Capital Capsules: The Environmental Protection Agency plans to develop a tunable diode laser spectroscope to measure gaseous sulfuric acid. The lasers will measure line strength of the vapor in the $870-895$ and 1210 to $1240-\mathrm{cm}$ spectral regions. ... The Energy Research and Development Administration has begun operation of its 10 mW Argus laser at the Lawrence Livermore Laboratory of the University of California. The neodymium glass laser, used in gaseous plasma research, is the forerunner of the 25 GW Shiva laser due to be operational in 1978. Shiva will be built by joining Argus lasers into an array that may eventually consist of 32 individual 10 MW lasers. . . . . The Coast Guard is ordering five more PPS-109 radars from AIL Div. of Cutler-Hammer for monitoring vessel traffic after two years of successful tests in San Francisco harbor. The new high-resolution, dualchannel radars will go into the harbors of Valdez, AK, Houston and Galveston, TX and New York.

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|  | whichever is less $\left(25^{\circ} \mathrm{C}, 500 \mathrm{VDC}\right)$ | whichever is less $\left(25^{\circ} \mathrm{C}, 500 \mathrm{VDC}\right)$ |
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Here's a counter so versatile, it can really be called universal. You get high accuracy, operating ease and a low price tag of just $\$ 1300$. It's modular so you can buy the capability you need. Not more. Not less. Start with the basic 8 -digit instrument with 100 MHz frequency range and 100 ns single shot T.I. resolution. You also get period, 10 ps time interval averaging, ratio, scaling and totalizing. Then you can add more: 512 MH twith 9 digits and 15 mv sensitivity; time base aging $<5 \times 10^{10} /$ day; and 10 ns single shot time interval with improved averaging. But look what else you get:

## *Or now to 1300 MNE with 20 mV sensitivity.' UNIQUE TRIGGER LIGHTS

tell you what's happening.
They're on when the input is greater than trigger level and vice versa. And they blink when the input channel is triggering from 0 to 100 MHz . Standard.


UNIQUE BUILT-IN DVM gives an instant accurate digital


display of trigger levels. Or use this option to measure external voltages $10 \mu \mathrm{v}$ to 1100 V auto-ranged, integrating, full floating, high common-mode rejection with switchable input filter. Optional.

HIGH SPEED MARKERS show just what your counter is
 doing with your input waveform. Use the markers on the second channel of your scope to see where the counter is triggering. Really useful thanks to the 5328A's 100 MHz ECL outputs. Standard.

EASY SYSTEMS INTERFACE with the HP Interface Bus simplifies integrat system. You get this programmability plus
 standard format
 data output with a single connector. Optional.

ARMED MEASUREMENTS solve difficult dynamic ARM $\begin{aligned} & \text { measurement problems. The counter } \\ & \text { goes to work when your command } \\ & \text { tells it to. Ideal for burst frequency or } \\ & \text { sweep generator linerarity measurements. }\end{aligned}$

These are just a few things, of course. There are many more thoughtful engineering innovations that combine to give you everything youre likely to need in a general purpose, medium-priced counter for a long time to come. We talk about them in our 12 page booklet. Write for one or ask your nearby HP field engineer for a copy. We want you to find why we call this universal counter universal.

- Domestic US Price only.


## Microprocessor Desigu

## Microprocessor analyzer enhances operator interaction with development systems

Microprocessor test equipment is finally catching up in sophistication with the product it's supposed to test. That's the message signaled by the AO 6800 , a $6800-\mu \mathrm{P}$ analyzer and development tool from AO Systems.

The AO6800 doesn't just grab and display information-it allows the operator to intervene in the $\mu \mathrm{P}$-system's operation.

Into the AO 6800 and its various displays come address, data and status information. Out of the AO6800 go signals to run, halt, single-step or reset the $\mu \mathrm{P}$.

With these capabilities, you can execute one program instruction and display both the op code of that instruction and the present value of the program counter. Or you can set in an address, and the analyzer will halt the $\mu \mathrm{P}$ when an address match occurs.

In the AO's monitor mode, the address display becomes a resettable counter, which provides a totalization of monitored address matches, while the data display shows memory or I/O read-write information.

Another feature, a memory-speed simulator, lets you simulate and adjust for slow memories in your prototype system. A zone control

partitions the memory into two sections: slow and normal.

Read-write capability of the AO Systems unit extends beyond the memory and I/O ports ( 65 k addresses) to the program counter and the internal registers of the 6800 (A, B, X, S).

With the AO's three-state buffering, negligible bus loading and choice of positive-true or
(continued on page 52)

## National Semiconductor offers the 8080A $\mu \mathrm{P}$ for $\$ 19.95$

The 8080 A 8 -bit microprocessor family is being offered by National Semiconductor, with its price set at $\$ 19.95$ ( $100-\mathrm{up}$ ).

The microprocessor, called the INS8080A, is a direct pin-for-pin and function-for-function replacement for the Intel device.
"We decided to build and market the 8080A because it has clearly become the industry's most popu ar general purpose microprocessor," Bill Baker of National says.

Also available is the INS8224 clock generator, which provides timing signals for the CPU and the system, and the INS8228, which provides system control and data bus buffering.
National Semiconductor Corp., 2900 Semiconductor Dr., Santa Clara, CA 95051.
(408) 737-5000.

## MICROPROCESSOR DESIGN

(continued from page 51)
negative-true signals, you can connect to practically any system configuration.

A number of options give the A06800 even more test power. For example, an optional sequence recorder stores up to 128 instrument
addresses in either of two modes, so you can trace through and debug programs. Other options include a hexadecimal display.

The cost of the basic AO6800? A surprising $\$ 695$; you can get one from stock to 6 weeks.

AO Systems, 1736 Front St., Yorktown Heights, NY 10598. (914) 962-4264.

CIRCLE NO. 502

## Microcomputer card comes in European standard card size



A new 8080-based microcomputer card conforms to Eurocard physical dimensions and pinouts. The card, called the DCE-1, packs an $8080 \mu \mathrm{P}, 512$ bytes of RAM, 24 ports of parallel I/O, 2 ports of serial I/O, five interval timers, sockets for $4-\mathrm{k}$ bytes of PROM, and additional communications control circuitry. The Eurocard standard, used in common market countries, specifies $100 \times 160-\mathrm{mm}$ card dimensions and standard edge connectors.
The PROM that you use may be an ultraviol et-erasable type 2708 or 2704 . The serial I/O communication is asynchronous, opto-isolated, and uses a full or half-duplex mode. Baud rates are programmable to $110,150,300,1200,2400,4800$, or 9600 with one or two stop bits.

The parallel I/O ports may be programmed to input or output data. The interval timers permit countdown delay or control of five independent events. Time periods may be programmed from $64 \mu \mathrm{~s}$ to 16.3 ms ; longer periods are attainable by cascading timers. The interrupts generated by these timers are fully vectored-the CPU program counter is loaded from different locations in memory depending upon which timer interrupt occurs. The 100 -piece-unit price of the card is $\$ 374$.
N. V. Data Applications International S.A., Dreve Des Renards 5, 180 Brussels, Belgium.

CIRCLE NO. 503

## 8080-based development system sells for $\$ 1976$



A microcomputer development system contains a card with an $8080 \mu \mathrm{P}$, a control panel, development software and memory. The whole system, dubbed the $8080^{+}$ Development Station, sells for $\$ 1976$.

The microcomputer card contains address and data buffers, and a section for additional circuitry.

The control panel, which may command the system, contains a hexadecimal entry keyboard, function/control keys, status indicators and a 4-digit hexadecimal address/data display. Control of the computer system may also be accomplished through a teletypewriter terminal for which an interface is provided.

Development software has been programmed into CMOS RAMs that contain a nickelcadmium battery for data retention during shipping. Additionally, the programs come on paper tape that may be loaded via a teletypewriter into the RAMs. The software consists of an editor and assembler, debug instructions, and file creation and edit capabilities.

System memory consists of 1-k words of RAM on the microcomputer board, plus 14-k words of available memory on a separate board (an additional 2-k words on the same

## No. <br> <br> Galileo is not the little old <br> <br> Galileo is not the little old NapaValley winemaker.

 NapaValley winemaker.}Galileo Electro-Optics Corp. isn't little nor relatively old. And no, we don't make wine either. Galileo is simply one of the world's largest producers of optical communication cable.

Our new Galite ${ }^{\text {TM }}$ family consists of five completely different types of optical communication cable. Because the Galite family is the widest range of cables available anywhere, we can thus service your particular needs best.

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Galite 3000 is a truly new and unique optical communication cable. And because of our exclusive
 rights to a patented glassmaking process, Galileo is its only source in North America. These fibers are constructed of high purity glass to reduce attenuation. The cladding glass, deposited by this patented process, is impervious to organic solvents and retains its unique properties at temperatures where plastic fibers deteriorate.
Galite 3000 is a step index optical fiber for transmitting data primarily from 70 to 330 meters/ 230 to 1100 ft . This range accommodates most current market applications such as linking computer terminals, shipboard and aircraft communication and control systems, and automated process controls.

For more information, write: Galileo Electro-Optics Corporation, Galileo Park, Sturbridge, Mass. 01518. Or call us at (617) 347-9191.

## Imagine what we could be doing for you right now.



## MICROPROCESSOR DESIGN

## (continued from page 52)

board is required by the system software). The $4-\mathrm{k}$ words of CMOS RAM containing the operating system may also be used if additional memory space is required.

The development system requires about 6.7 A at 5 V and 0.65 A at 12 V .
Monolithic Systems Corp., 14 Inverness Dr. East, Englewood, CO 80110. (303) 770-7400.
CIRCLE NO. 504

## Machine simulates an EPROM and is keyboard programmable



By simulating a 1702 -type EPROM, yet remaining programmable, the Precision Electronics PROM Simulator speeds up microprocessor program development. Each 8 -bit address is programmed individually via a hexadecimal keyboard. The unit connects to a microprocessor system via a 24 -pin connector that plugs into a 1702 socket.

Two models of the PROM Simulator are available. The S702A (\$290) contains NMOS RAMs and a socket for an external battery. The S702B ( $\$ 370$ ) has CMOS RAMs and an internal rechargeable battery that allows a specific program-hold period of 1000 hours.
Precision Electronics, 24 Copenhagen St., London N1, England.
CIRCLE NO. 505

## 4 and 8-channel d/a converter cards mate with the SBC-80/10

When microprocessor systems control industrial processes, one $d / a$ channel is required for each a/d channel for computer-assisted feedback control. Cards with up to $32 \mathrm{a} / \mathrm{d}$ channels have been available but few manufacturers offered more than two $d / a$ channels on the card.

Now, four and eight $\mathrm{d} / \mathrm{a}$ output channels sit on a peripheral card that is pin-compatible to the Intel SBC-80/10 and MDS CPU bus. One, the Model ST-800-DA8, provides $8 \mathrm{~d} / \mathrm{a}$ channels, and requires -15 and +15 V for power from an external source.

A second model, called the ST-800-DA4, has four d/a channels and an integral de/dc converter. The dc/dc converter generates its own $\pm 15 \mathrm{~V}$ from the +5 V bus so only the +5 V system power supply is needed. The settling time of each $\mathrm{d} / \mathrm{a}$ converter, including register loading, takes $4 \mu \mathrm{~s}$. Depending on the programming mode, two or more 2 -byte instruction sequences are required to update each output channel. Both the 4 and 8-channel cards are complete, stand-alone, addressable I/O systems. They include a hard-wired 8 -bit base-address decoder and a jumper-programmable interrupt level.

The processor can load all channels sequentially without specifying the address for each channel. A word counter is automatically incremented with each data-output cycle and compared to the preloaded last channel. When the last channel is reached, the processor is notified by way of a hard-wired interrupt level. Both cards contain an interval clock to initiate update scans by generating an interrupt. This scan timer uses a logic clock that may be adjusted up to one second.

The 8 and 4-channel systems are both built on $12 \times 6.75 \times 0.375-\mathrm{in}$. cards compatible to Intel's CPU bus pinout. Model ST-800-DA8 costs $\$ 695$ (single quantities) and the Model ST-800-DA4 costs $\$ 595$ (single quantities).
Datel Systems, Inc., 1020 Turnpike St., Canton, MA 02021. (617) 828-8000.
Select from this family of aluminum electrolytic capacitors designed for output filtering in switching power supplies
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- Suatiable for
parallel stacking
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capability

| Construction | Rolled-Section |  | Rolled-Section |  | Rolled-Section |  | Stacked-foil |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Terminal Configuration | 2 terminals, wire pins |  | 4 terminals, wire leads |  | 2 terminals, low or high female threaded |  | 2 terminals, strip-line, female threaded |  |
| Case Size Range (D. x L.) | $\begin{gathered} .326^{\prime \prime} \times .505^{\prime \prime} \\ \text { too } \\ 1.000^{\prime \prime} \times 1.625^{\prime \prime} \end{gathered}$ |  | $\begin{gathered} .750^{\prime \prime} \times 1.625^{\prime \prime} \\ 1.000^{\prime \prime} \times 3.625^{\prime \prime} \end{gathered}$ |  | $\begin{aligned} & 1.375^{\prime \prime} \times 2.125^{\prime \prime} \\ & 1.375^{\prime \prime} \times 5.625^{\prime \prime} \end{aligned}$ |  | $\begin{aligned} & 1.375^{\prime \prime} \times 2.125^{\prime \prime} \\ & 3.000^{\prime \prime} \times 5.625^{\prime \prime} \end{aligned}$ |  |
| Operating Temperature Range | $-55^{\circ} \mathrm{C}$ to $+105^{\circ} \mathrm{C}$ |  | $-55^{\circ} \mathrm{C}$ to $+105^{\circ} \mathrm{C}$ |  | $-55^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |  | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |  |
| WVDC Range |  | 6.3 to 100 |  | 5 to 200 |  | 5 to 55 |  | 6 to 50 |
| Capacitance <br> (Range ( $\mu \mathrm{F}$ ) | 4.7 to 6800 |  | 50 to 16,000 |  | 2,800 to 67,000 |  | 470 to 100,000 |  |
| Capacitance Tolerance | $-10,+100 \%$ |  | thru 50 V : $-10,+75 \%$ over 50 V : $-10,+50 \%$ |  | $\pm 20 \%$ |  | -0. $+100 \%$ |  |
| Max. Inductance (@1 MHz \& within $.125^{\prime \prime}$ of capacitor) | 20 nH |  | 2 nH |  | 20 nH |  | 2 nH |  |
| Max. ESR <br> (@ $25^{\circ} \mathrm{C}$ and 120 Hz ) | (8) | . 11 ohm |  | . 022 ohm |  | .004 ohm <br> $19.5 \mathrm{~A} @ 20 \mathrm{kHz}$ <br> $.010 \Omega @ 10-40 \mathrm{kHz}$ | $\begin{aligned} & 40 \\ & =0 \\ & 83 \\ & 08 \\ & 0.6 \\ & \hline-88 \end{aligned}$ | .0015 ohm <br> 54.6 A @ 1 kHz |
| RMS Ripple Current (@ $85^{\circ} \mathrm{C}$ ) |  | 2.61 A @ 100 kHz |  | 7.00 A @ 10 kHz |  |  |  |  |
| Max. Impedance (@ $25^{\circ} \mathrm{C}$ ) |  | . $06 \Omega$ @ 100 kHz |  | . $017 \Omega$ @ 10 kHz |  |  |  | . $001 \Omega$ @ 10 kHz |
| Engineering Bulletin |  | 3452 |  | 3458A | 3459 |  | 3443A |  |
|  |  | Check 281 on Reader Service Card |  | Check 282 on Reader Service Card |  | Check 283 on Reader Service Card |  | Check 284 on Reader Service Card |

4SE-6102R2
For complete technical data, write for Engineering Bulletin(s) (see table for bulletin numbers) on the capacitor(s) in which you are interested to: Technical Literature Service, Sprague Electric Company, 347 Marshall St., North Adams, Mass. 01247.

# Datatron's New LSI tests wafers 3 to 

Delays at the wafer-probe stage of LSI testing once proved costly. But no more. Not with Datatron's new 10 MHz clockrate test system. Credit our patented parallel parametric capability for speeding up waferprobe testing by 300 to 400 percent. This feature alone makes the LSI-800 an attractive investment for today's heavy LSI manufacturer or user.

## Comparing the Testers

To find out how the LSI-800 fares against competition, take a look at these tabulated wafer-probe test results:

In final test too, the LSI-800 clobbers the competition. Items-per-hour throughput topped all others by almost 450 percent - as the following numbers testify:


FINAL TESTS

| $\begin{aligned} & \text { The } \\ & \text { Testers } \end{aligned}$ | 7495 Register |  |  | $8080 \mu \mathrm{pu}$ |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Handler <br> Time (msec) | Tester <br> Time ( msec ) | $\begin{gathered} \text { 2-Station } \\ \text { System } \\ \text { Throughput } \\ \text { (items/hour) } \end{gathered}$ | Handler <br> Time (msec) | Tester <br> Time (msec) | $\begin{gathered} \text { 2-Station } \\ \text { System } \\ \text { Throughput } \\ \text { (items/hour) } \end{gathered}$ |
| Datatron LSI-800 | 500 | 150 | 14,400 | 500 | 900 | 4,000 |
| All Others | 500 | 550 | 6,500 | 500 | 4,000 | 900 |

## 800 Test System 4 times faster!



## Other Reasons for Choosing an LSI-800

Need other reasons for choosing an LSI-800? Well, an LSI-800 can generate exotic test patterns, enabling it to perform an infinite variety of tests on all classes of LSI devices. Multidimensional software lets you do device characterization. Production testing too. And you get instant access (via telephone) to the LSI programs in Datatron's Central Program Library (CPL). So you'll spend less money for programming support.

We can give you still other compelling reasons for choosing an LSI-800. To further pin us down, call or write today. Datatron, Inc., Test Systems Division, 1562 Reynolds Avenue, Irvine, CA • (714) 540-9330 • TWX 910-595-1589 • Mailing Address: P.O. Box 11427, Santa Ana, CA 92711.

## $\mathbf{1 0 0 \%}$ of the Capability, 28\% of the Price

To keep pace with an LSI-800, you need four of "their" testers (at $\$ 300,000$ each) plus four wafer-probe testers (at $\$ 20,000$ each). Total investment: $\$ 1,280,000$ for their system versus $\$ 370,000$ for an LSI-800-based system.


# Stop noise problems. HiNIL gives you the best protection available. Bar none. 

Noisy environments simply cease to be a problem when you design with High Noise Immunity Logic from Teledyne. HiNIL provides high immunity to any and all types of electrical noise, without the cost and inconvenience of special filtering or shielding.
Use HiNIL in place of your conventional I/O logic. It interfaces easily with TTL, DTL, MOS and CMOS. It will protect your CMOS inputs against static charge damage and SCR latchup. And it gives you a guaranteed dc noise margin of 3.5 V (as compared to 1.0 V for CMOS
and 0.4 V for TTL) without added filter circuits or tight supply regulation. Or design entirely with HiNIL. You will get even better noise protection and a simpler design, and you can use a low cost, loosely regulated power supply. The complete HiNIL family includes more than 40 devices, with more being added all the time. They're available in ceramic or molded plastic DIPs.
Get complete details on the full family of HiNIL logic circuits from your local Teledyne Rep or Distributor. Or contact us at the address below.

## 

## We want to be better

As far as I know, we run more corrections than any other technical magazine. That's not because we make more mistakes-I think we make far fewer-but because we shout about them more.
As a matter of policy, some publications never make mistakes or, at least, they close their eyes to them. It's well known that eyeclosing makes bad things go away.
An amusing example took place some years ago, when one publication wrote a searing article about the layoffs at Motorola's Semiconductor Div. The situation was so bad, the
 article pointed out, that Motorola had to drop out of the tantalum-capacitor business. That was a good trick since Motorola had never been in that business. When this gaffe was pointed out, the magazine-which was never wrong-simply published a follow-up article telling the world how great things were at Motorola.
We'll never do that to you. When we blow one, we'll tell you-right away. We know you can't work with faulty information, so we want to give you


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ELECTRONIC DESIGN is deeply honored to have received official recognition as a participant in the American Revolution Bicentennial Celebration, with authority to display the Bicentennial Symbol. the best available-not only in our editorial pages, but in our advertising pages as well.
That's why we make such a fuss about our mistakes and why we've published an accuracy-policy statement in every issue for the past 17 years. (It's on page 138 in this issue.) And though we feel we have the most accurate magazine in our field, we'd like to make it even more so. You can help.

If we make a mistake, or if an advertisement is not quite as accurate as one might prefer-perhaps because of excess enthusiasm on the part of the manufacturer-please let us know, so that we can investigate and take appropriate measures. We make it easy by providing space for your comments on the Reader Service Card. But we welcome your letters and phone calls, too.

We always do.

George Rostiky
Editor-in-Chief

## THESE LOU-COST CRS5ETTE PROCRAMI LOMDERS delluer top performance for your milROPROLE550R BRSED 5Y5TEm5.

All four of these program loaders use EPI's patented Speed Tolerant Recording (STR ${ }^{\circledR}$ ) technique to give you bit error rates of less than 1 soft error in $10^{7}$ bits and less than 1 hard error in $10^{8}$ bits. With STR, you get high data reliability in read and write modes while using a relatively low-cost recorder
and inexpensive tape cassettes.

Interfacing is similar to most paper-tape reader/ punch units now in use. And with better data reliability, faster loading, more storage, lower price, and briefcase or rack mounting, EPI cassette program loaders make sense for both the
end user and OEM.
For more information on these low-cost alternatives to paper tape loading, contact Electronic Processors Inc., 1265 West Dartmouth Ave., Englewood, Colorado 80110. Phone (303) 761-8540.


See Megatronix Ltd. Booth 212

Custom designs, like this STR-110T
for the Texas Instruments 5 TI Programmable Control System, can handle your special loader needs.

You can get an intelligent loader. automatic verification in both read and write modes. remote control. Tell us what you need. Chances are an EPISTR loader can handle it,


8-bit parallel loading at standard TTL levels and at transfer rates to 125 characters per second is provided by the STR-210. Its high speed and storage capacity of 100,000 characters on a 300 -foot cassette makes it ideal for memory dumps as well as program loading. Priced at just $\$ 700$.

OEM systems recorder, the STR-100, is a complete tape-drive unit that provides full remote signal or character control of all transport functions. It includes read-write electronics, control and timing logic, and motor-control logic. All you need to supply is a mounting location, power supply, and an interface with the controlling I/O device and you have a reliable unit for program storage and retrieval.

8-bit serial loading with switchselectable rates to 1,200 baud are provided by the STR-LINK. The transmission mode (half or full duplex) is also switch selectable for maxi-
mum flexibility. Data capacity is
100,000 characters on a 300 -foot cassette. Priced at $\$ 1,190$.


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# Enhance LED visibility under high <br> ambient light levels by using bandpass and other optical filters to boost contrast. 

Artificial lighting or ordinary bright daylight can turn a LED display into a lackluster eyestrain. But the contrast in the same display can be restored with an optical filter-bandpass, polarizer, louver or other type.

Such filters enhance the contrast between the ON and OFF conditions in two ways: (1) By minimizing the reflected ambient light from the face of the display, and (2) By maximizing the emitted light that reaches the eye of a viewer. An additional goal is to maximize the contrast between the OFF segments and the display package and background.

The contrast ratio, CR, is defined as : $\mathrm{CR}=\frac{\text { Source luminance-Background luminance }}{\text { Background luminance }}$.

The contrast-improvement ratio. CIR, is therefore given by

$$
\mathrm{CIR}=\frac{\mathrm{CR}(\text { with filter })}{\mathrm{CR}(\text { without filter })}
$$

It is desirable to have as high a CR as possible. Improvements in contrast are revealed by measurement of the CIR.

## Segment contrast vs display background

The term "contrast ratio" is usually applied to the whole face of a display. With stretchedsegment displays, it is difficult to obtain a high value of segment ON/OFF contrast while effectively "concealing" the display package from view. For example, a display with a black package is easily concealed; however, the OFF segments will be visible. This can be traced to the difference in reflectivity between the OFF segments and the black package.

You can reduce the difference in reflectivity between the OFF segments and the package of a stretched-segment display by adding a small amount of dye to tint the segments. You can also color the display package to match the OFFsegment color. With an appropriate optical filter in front of the display, the OFF segments tend

[^3]to be indistinguishable from the background.
One point to remember is that a colored package is more visible than a black package. Because of this, you must decide which is more important-concealment of the OFF segments or of the display package. The usual choice is to conceal the segments.

Contrast can be enhanced under artificial lighting by use of wavelength optical filters. Under bright sunlight, enhancement becomes more difficult and requires additional techniques -such as louvered filters combined with shading of the display.

The effect of a wavelength optical filter is illustrated in Fig. 1. The filtered portion of the


1. "Before and after" comparison demonstrates what an optical filter can do to enhance LED display visibility.
display can be read easily, while the OFF segments are not readily apparent. By comparison, it's difficult to read the unfiltered portion of the display.

The 1931 CIE (Commission Internationale De L'Eclairage) observer curve, also called the photopic curve, represents the eye response of a standard observer to various wavelengths of light (Fig. 2). The vivid color ranges are also identified in the figure. The curve peaks at 555 nanometers ( nm ) in the yellowish-green region. This peak corresponds to 680 lumens ( 1 m ) of luminous flux per watt of radiated power.

Two wavelengths of LED emission are important to the user: the peak $\left(\lambda_{1}\right)$ and the

2. How the average eye responds to light of various wavelengths is shown by the photopic, or CIE curve (International Commission for Light Standards).

3. Typical filter transmittance curves are for use with standard (HP) GaAsP red LED displays.
dominent $\left(\lambda_{4}\right)$. The former is the wavelength at the radiated peak of the emitted spectrum and can be used to estimate the approximate amount of emitted light passed by an optical filter. For example, if a filter has a relative transmission of $40 \%$ at a given $\lambda_{p}$, approximately $40 \%$ of the emitted light at the peak wavelength will pass through the filter, while $60 \%$ will be absorbed. This gives a designer an initial estimate of the amount of light lost.

## Two important wavelengths

The dominant wavength, $\lambda_{d}$, defines the color of a LED display. Since a LED approximates a monochromatic light source, $\lambda_{d}$ can be defined as that single wavelength that is perceived by the eye to match the total radiated spectrum of the device.

For example, the $\lambda_{\mathrm{d}}$ of Hewlett-Packard's "yellow" display-which has a peak wavelength of 583 nm -is 585 nm . As shown in Fig. 2, the color corresponding to $\lambda_{\mathrm{d}}=585 \mathrm{~nm}$ is yellowishorange. Therefore an optimum filter for yellow LEDs will be yellowish-orange, or amber. Both peak and dominant wavelengths are usually listed on the data sheets of LED displays and lamp products.

## Wavelength filters

Most manufacturers of wavelength filters also provide relative transmittance curves for their products. The relative transmittance of an optical filter with respect to wavelength is given by :
$T(\lambda)=$ Luminous flux with filter at wavelength $\lambda$ Luminous flux without filter at wavelength $\lambda$. Sample transmittance curves are presented in

Figs. 3 through 6. They represent approximate filter characteristics for various ambient light levels. The over-all curve shape and wavelength cut-off points are chosen in direct relationship to the LED-radiated spectrum. Each filter curve shown is empirically determined and is similar to commercially available products.

The higher the ambient light, the more optically dense the filter must be to absorb the light reflected from the face of the display. Because the emitted light is also strongly absorbed, the display must be driven at a relatively high average current to be readily visible. In dim ambients -where the emitted light is much higher than the ambient-the filter can have a high value of transmittance and the display can be driven at a lower average current.

Note that dim ambients fall in the range of 3 to 20 foot-candles ( 32 to 215 lux), moderate ambients in the range of 20 to 100 foot-candles

4. High-efficiency red LEDs need filters with slightly lower cut-off points than standard LEDs.
(215 to 1076 lux), and bright ambients in the range of 100 to 500 foot-candles ( 1076 to 5382 lux). Remember that a foot-candle $=\operatorname{lm} / \mathrm{ft}^{2}$ and a lux $=1 \mathrm{~m} \mathrm{~m}^{\prime}$.

Listed on each filter transmittance curve (Figs. 3, 4, 5 and 6) are empirically selected ranges of relative transmittance values (at the peak wavelength) that may give satisfactory filtering. For instance, a filter to be used with a yellow display in moderate ambient lighting might have a transmittance value at the peak wavelength $T\left(\lambda_{p}\right)$ of between 0.15 and 0.30 . For best results, the filter wavelength cut-off should therefore occur between 530 and 550 nm .

## Wavelength filters predominate

When you select a filter for optimum enhancement, carefully consider the transmittance curve shape, the attenuation at the peak wavelength and the wavelength cut-off-all in relation to the LED radiated spectrum and the ambient light level.

Wavelength filters are the most effective, and hence the most widely used, method of enhancement under artificial lighting. However, because of the high ambient level, these filters are not very effective in daylight. Here, louvered filters are best.

Figs. 7 through 10 show the relationships between artificial light. daylight, fluorescent and the spectra of LED displays, both unfiltered and filtered. The photometric spectrum (shaded curve) is obtained by multiplication of the LEDradiated spectrum $f(\lambda)$ by the photopic curve, $y(\lambda)$. The filtered photometric spectrum is what the eye perceives when it views a display through a filter. Thus the filtered spectrum $=f(\lambda) y(\lambda)$ $T(\lambda)$. The ratio of the area under the filtered spectrum to that under the unfiltered spectrum

5. Filters for yellow displays emit in the eye's most sensitive area, transmit less light than those for red.
is that fraction of the visible light emitted by the display that is transmitted by the filter:
Fraction of available
light from filtered display

$$
=\frac{\int f(\lambda) y(\lambda) T(\lambda) d \lambda}{\int f(\lambda) y(\lambda) d \lambda}
$$

While a filter attenuates a portion of the light emitted by the display, it also shifts the dominant wavelength and thus the perceived color. For a given display spectrum, the color shift depends on the cut-off wavelength and the shape of the filter transmittance characteristic.

Which filter to select among those available depends on which filter and LED combination is most pleasing to the eye. You must experiment with filters, since you can't pick the best from the transmittance curves alone. Thus the filter spectra of Figs. 3 through 6 are merely suggested starting points. They are similar to those of commercially available filters.

## Filters for red, yellow and green

To filter reflected ambient light from red displays ( $\lambda_{1}=655 \mathrm{~nm}$ ) requires a long-wavelength pass filter with a sharp cut-off in the 600 -to-$625-\mathrm{nm}$ range (Figs. 3 and 7b). Under bright fluorescent light the red filter is very effective, because of the low concentration of red in the fluorescent spectrum. By contrast, it is difficult to filter red displays in bright incandescent light because of the large amount of red in the ambient spectrum.

For high-efficiency red displays ( $\lambda_{v}=635$ nm ), a long-wavelength pass filter with a cutoff in the 570 -to- $590-\mathrm{nm}$ range gives essentially the same results as standard red displays (Figs. 4 and $8 b$ ). The resulting color is rich, reddish orange.

The peak wavelength of a yellow LED $\left(\lambda_{1}=\right.$

583 nm ) falls in that region of the photopic curve in which the eye is most sensitive (Fig. 9 a). Since there is a high concentration of yellow in the spectrum of fluorescent light, and a lesser amount in incandescents, filters of greater density than red ones at the peak wavelength are required for yellow displays. Most effective filters are the dark yellowish-orange, or dark amber (Fig. 5).
A low-transmittance, yellowish-orange filter, as shown in Fig. 9b, gives a color similar to that of a gas-discharge display. Note that pure yellow filters provide very little contrast enhancement.

With green displays-in which the peak wavelength of 565 nm is only 10 nm from the peak of the eye's response-effective filtering is difficult (Fig. 10a).
The long-wavelength pass filter used for red and yellow displays is not effective, and you must combine the dyes of short and long-wavelength filters to form a good bandpass yellowgreen filter with a peak at 565 nm (Fig. 6). The best filters for green LEDs are yellow-green bandpass types. Those peak at 565 nm and drop off rapidly between 575 and 590 nm . The filter passes 550 to 570 nm while sharply reducing the longer wavelengths in the yellow region (Fig. 10b). Pure green filters aren't recommended because they peak at 520 nm and drop off rapidly in the $550-\mathrm{to}-570-\mathrm{nm}$ range.

To filter green LED displays in fluorescent light, a filter with a low transmittance value at the peak wavelength is needed. This is because of the high concentration of green in the fluorescent spectrum. It is easier to filter green displays in bright incandescent lighting because of

6. Green displays are most difficult to filter. Best results are obtained from yellow bandpass types.
the low concentration of green in the ambient spectrum.

Manufacturers of wavelength filters include Panelgraphic Corp. (Chromafilter), SGL Homalite, and Rohm \& Haas (Plexiglas). Others are listed in Table 1. Table 2 lists filter products, with recommended applications.

## Daylight requires different tack

To reduce artificial light or daylight reflected from the face of a display-without a substantial reduction in display-emitted light-check into louvered filters (Fig. 11). Inside a plastic sheet are thin, parallel louvers that can be angled (during manufacture) with ${ }^{-}$respect to the surface normal. In the zero-degree filter the louvers are

7. Where the emitted wavelengths of a red LED fall with respect to various light sources and the eye's re-
sponse (a). After filtering with a long-wavelength filter, the eye perceives the spectrum shown in "b."

8. The relationship of various light sources to the photometric spectrum of the eye and the radiated spec-
trum of high-efficiency red LEDs (a). A long-wavelength pass filter has the effect shown in "b."

9. Yellow LEDs emit a spectrum that is fairly close to the eye's relative response (a). The peak of the LED is

583 nm , while the eye's peak is at 555 nm . A filter cuts the emitted light by $75 \%$ (b).
perpendicular to the filter surface.
Operation of a louvered filter is similar to that of a Venetian blind (Fig. 12) : Light from the LED display passes between the louvers to the viewer. Off-axis ambient light is prevented from reaching the face of the display; therefore can't be reflected to the viewer. The result is a very high contrast ratio with minimal loss of emitted light at the on-axis viewing angle. The tradeoff, however, is restricted viewing angle.

The zero-degree louver in Fig. 11 has a horizontal viewing angle of 180 degrees. However, the vertical inoluded viewing angle is only 60 degrees. The louver aspect ratio-that is, the ratio of lower depth to distance between louvers -determines the viewing angle. A list of louver
options is given in Fig. 12.
Some applications require louver orientation of other than zero degrees. For instance, an 18degree louvered filter may be used on the sloping top surface of a point-of-sale terminal. In another application a 45 -degree louver mounts on overhead instrumentation to block ambient light from ceiling-mounted lighting fixtures.
In bright sunlight the crosshatch louver is most effective. Essentially this involves two zero-degree, neutral-density filters oriented at 90 degrees to each other. With the cross-hatch, red, yellow and green digits can be mounted side by side in the same display, and all digits will be clear in bright sunlight-so long as the sunlight is not parallel to the viewing axis. Again, this

10. Closest to the photopic curve is the radiated spectrum of green LEDs, almost an exact fit (a). For this
technique restricts vertical and horizontal viewing. The effective viewing cone is an included angle of 40 degrees for a filter aspect ratio of 2.75:1.

Neutral-density louvered filters work in most bright ambients without the aid of a secondary wavelength filter. However, colored louvered filters can give additional filtering-but at the expense of emitted light.

The Light Control Div. of the 3 M Co. offers louvered filters for LED displays under the trade name "Light Control Film."

## What about specular light?

Another filter, the circular polarizer, cuts light reflected specularly from front surfacesthat is, light that reflects without scattering. Polished glass or plastic faceplates belong to this category.

A circular polarizer consists of a laminate of a linear polarizer and a quarterwave plate (Fig. 13). The latter's optical axis is parallel to the flat surface of the polarizer and is oriented at 45 degrees to the linear polarization axis.

In operation, nonpolarized light first is linearly polarized into X and Y components. As the light passes through the quarter-wave plate, these polarized components emerge 90 degrees out of phase with each other and form a helical pattern with respect to the optical path. The emerging light-termed "circularly polarized"is reflected from the specular surface, which reverses the direction of the circular polarization.

When the light passes back through the quar-ter-wave plate, it becomes linearly polarized at 90 degrees to the linear polarizer, and the reflected ambient light is blocked.

In this way the circular polarizer slashes am-
reason, green LEDs are the most difficult to enhance and require a passband filter (b).

11. A neutral-density louvered filter can reduce reflected light without cutting into that emitted.
bient light by more than $95 \%$. However, emitted light passing through the polarizer is also re-duced-by approximately $65 \%$ at the peak wavelength. This necessitates increased drive current for the display-more than that required for a wavelength filter.

## Be cautious with sunshine

To obtain additional selective filtering, circular polarizers are normally colored. But caution: Since prolonged exposure to ultraviolet light will destroy the filter's polarizing properties, outdoor applications require an ultraviolet filter in front of the polarizer.

## Table 1. Filter and bezel products

| Manufacturer | Product |
| :---: | :---: |
| PANELGRAPHIC CORPORATION <br> 10 Henderson Drive <br> West Caldwell, New Jersey <br> 07006 <br> Phone: (201) 227-1500 | Chromafilter R Wavelength Filters with antireflective coating-red, yellow, green |
| SGL HOMALITE <br> 11 Brookside Drive Wilmington, Delaware 19804 Phone: (302) 652-3686 | Wavelength filters; Two optional antireflective surfaces; three plastic grades; red, yellow, green |
| 3M COMPANY <br> Visual Products Division <br> 3M Center, Bldg. 235-2E <br> Saint Paul, Minnesota 55101 <br> Phone: (612) 733-5747 | 3M-brand Light control film; louvered filters |
| ROHM AND HAAS <br> Independence Mall West <br> Philadelphia, Pennsylvania 19105 <br> Phone: (215) 592-3000 | Plexiglas; Sheet and molding powder; wavelength filters; sold as Oroglas in Europe |
| ROEHM, GmbH Chemische Fabrik 6100 Darmstadt Kirschenallee <br> West Germany <br> Phone: (06161) 8061 | Plexiglas; Wavelength filters |
| POLAROID CORPORATION Polarizer Division 549 Technology Square Cambridge, Massachusetts 02139 <br> Phone: (617) 864-6000 | Circular polarizing filters |
| E. KASEMANN GmbH D 8203 Oberaudorf West Germany <br> Phone: (08033) 342 | Circular polarizing filters |
| NORBEX DIVISION Griffith Plastics Corporation 1027 California Drive Burlingame, California 94010 Phone: (415) 344-7691 | Digibezel ${ }^{1 \times 1}$ Plastic bezels for LED displays |
| INDUSTRIAL ELECTRONIC ENGINEERS, INC. <br> 7720-40 Lemona Avenue <br> Van Nuys, California 91405 <br> Phone: (213) 787-0311 | Plastic bezels for 0.30 -inch ( 7.62 mm ) tall LED displays |
| ROCHESTER DIGITAL DISPLAYS, INC. <br> 120 North Main Street Fairport, New York 14450 Phone: (716) 223-6855 | Complete mounting kits for HP 5082-7300, .7700 and -7600 displays. |

In the United States circular polarizers can be purchased from the Polaroid Corp. In Europe, E. Kaseman of West Germany produces highquality units.

## Reducing glare is important

A filtered display still may not be readable if glare is present on the filter surface. Glare can be reduced by the addition of an antireflection surface to the filter. Both sections of the display in Fig. 14 are filtered, but the lefthand filter has an antireflection surface while the righthand filter does not.
Basically an antireflection surface is a mat, or textured, finish or coating that diffuses incident light. But emitted light is also diffused. The filter should be mounted as close to the display as possible to prevent a fuzzy image.

Table 2. Wavelength filter products
Filter Product Type of LED display Ambient lighting

## Panelgraphic Chromafilter ${ }^{\circledR}$ with Antireflection

| Ruby Red 60 |  |  |
| :---: | :---: | :---: |
| Dark Red 63 | Standard Red | Moderate <br> Bright |
| Yellow 25 | Yellow | Dim |
| Amber 23 | Green | Moderate |
| Green 48 | Moderate |  |

SGL Homalite, Grade 100

| H100-1605 | Standard Red | Moderate |
| :--- | :---: | :---: |
| H100-1670 | High-Efficiency Red | Moderate |
| H100-1726 | Yellow | Dim |
| H100-1720 |  | Moderate |
| H100-1425 | Green | Dim |
| H100-1440 | Moderate |  |
| LR-72: 0.5 inch (12.70 mm) mounting distance from |  |  |
| display |  |  |
| LR-92: Up to 3.0 inch ( 76.20 mm ) mounting distance |  |  |
| from display |  |  |

Rohm \& Haas

| Plexiglass 2423 Oroglas 2444 | Standard Red | Moderate |
| :---: | :---: | :---: |
| 38168 Molding | Green | Moderate |


12. To change the performance with louvered filters, combine any of the characteristics listed in the table.

Panelgraphic Corp.'s Chromafilters come with an antireflection coating, while SGL Homalite offers two grades of a molded antireflection surface. And both 3 M Company and Polaroid offer antireflection options for specialized applications.

Optical coating companies will apply antireflection coatings, though this is usually expensive. Among such companies are Optical Coating Labs, Inc., Santa Rosa, CA; Optics Technology, Inc., Redwood City, CA ; and Valpey

13. Circular polarizers cut light specularly reflected from glass or plastic front faces, and thus reduce glare.


WITH ANTI-REFLECTION SURFACE WITHOUT ANTI-REFLECTION
14. Still another way to cut glare is to give a filter an anti-reflection coating that diffuses incident light.

Corp., Holliston, MA.
Note the improved appearance of a front panel that has a display set off by a bezel. A blackplastic, satin-chrome or brushed-aluminum bezel can accent the display and attract the viewer's eye. For best effect, look into custom bezels. Commercial black-plastic bezels for digits up to $0.3-$ in. high ( 7.62 mm ) are available (Table 1).

Other steps may also improve a display's visibility. Consider recessing the display and filter -from $0.25 \mathrm{in} .(6.35 \mathrm{~mm})$ to $0.5 \mathrm{in} .(12.7 \mathrm{~mm})$ to add shading. If a double-sided printed-circuit board is used, keep traces away from the viewing area or cover the top-surface traces with a dark coating so they can't be seen. -

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# Head off dc/dc-converter problems. <br> You can attack cross-current conduction-an enemy of converter performance-in three ways. 

Converter designers, rest easy. You can overcome the number-one killer of dc/dc converters-cross-current conduction. In fact, three solutions are possible:

- Minimize the storage time of the switching transistors.
- Delay turn-on of one transistor until its complement turns off completely.
- Limit the cross-current peak by increasing the source impedance to current transients.

An effective cure usually requires some combination of the three.

Without adequate precautions, any driven dc/ dc converter will have a transitory period in which the two opposite-ended transistors conduct simultaneously. The phenomenon, known as crosscurrent conductance (CCC) or "through current," can slash reliability, diminish efficiency and boost EMI.

Even worse, CCC causes high current transients through the converter's power transistors. Up to 100 times the steady-state current can flow, causing severe thermal stresses. In time, CCC can destroy the transistors.

The probability of catastrophe increases at high ambient temperatures when transistor gain goes up, and raises CCC transients even more. The results: thermal runaway and eventual destruction.

Designers sometimes try to counter the effects of CCC by resorting to higher power transistors and more massive heat sinks, both of which increase the circuit's ability to survive-at the ex pense of cost and size.

## Understanding storage time

For a transistor to turn off, excess charge must be removed. The faster the discharge rate, the shorter the storage time.

An exact computation of storage time, $t_{s}$, is rather complicated and relies on charge-control concepts. However, two widely used equations for

[^4]$t_{s}$ reveal the influencing elements. The first equation is given by :
\[

$$
\begin{equation*}
\mathrm{t}_{\mathrm{s}}=\mathrm{T}_{\mathrm{x}}-\frac{\left(\mathrm{I}_{\mathrm{B} 1}-\mathrm{I}_{\mathrm{c}} / \boldsymbol{\beta}_{\mathrm{o}}\right)}{\mathrm{I}_{\mathrm{B} 2}+\frac{\mathrm{I}_{\mathrm{B} 1}+\mathrm{I}_{\mathrm{c}} / \boldsymbol{\beta}_{\mathrm{o}}}{2}}, \tag{1}
\end{equation*}
$$

\]

where $T_{x}$ is the lifetime of the excess charges in the base. For the case in which $\mathrm{I}_{\mathrm{c}} / \beta_{\mathrm{o}} \ll \mathrm{I}_{\mathrm{B} 1}$ (the worst case relative to storage time), Eq. 1 can be reduced to:

$$
\begin{equation*}
\mathrm{t}_{\mathrm{s}}=\mathrm{T}_{\mathrm{x}} \frac{1}{\frac{\mathrm{I}_{\mathrm{B} 2}}{\mathrm{I}_{\mathrm{B} 1}}+\frac{1}{2}} \tag{2}
\end{equation*}
$$

Another formula often found is:

$$
\begin{equation*}
\mathrm{t}_{\mathrm{s}}=\mathrm{T}_{\mathrm{x}} \ln \frac{\mathrm{I}_{\mathrm{B} 1}+\mathrm{I}_{\mathrm{B} 2}}{\mathrm{I}_{\mathrm{c}} / \beta_{\mathrm{o}}+\mathrm{I}_{\mathrm{B} 2}} \tag{3}
\end{equation*}
$$

Both equations give approximately the same results, with the best agreement obtained at $\mathrm{I}_{\mathrm{B} 1} / \mathrm{I}_{\mathrm{B} 2}$ $<4$.

To reduce $\mathrm{t}_{\mathrm{s}}$, the focus shifts to $\mathrm{I}_{\mathrm{B} 1}, \mathrm{I}_{\mathrm{B} 2}, \mathrm{I}_{\mathrm{C}} / \beta_{\mathrm{o}}$ and the associated variations in these parameters with temperature. Except for $\beta_{\mathrm{o}}$, the parameters are largely under the designer's control.
In most converter designs, you avoid marginal gain conditions by ensuring a base drive significantly higher than that required to bring the transistor to the verge of saturation-that is, higher than $\mathrm{I}_{\mathrm{c}} / \boldsymbol{\beta}_{\mathrm{o}}$. Thus, Eq. 2 offers a workable approximation to the actual storage time, and the equation shows that storage time is directly proportional to $\mathrm{T}_{\mathrm{x}}$ and inversely to $\mathrm{I}_{\mathrm{B} 2} / \mathrm{I}_{\mathrm{B} 1}$.

## How to minimize storage time

To keep $t_{s}$ down, select a transistor with as low a $\mathrm{T}_{\mathrm{x}}$ as possible. Unfortunately, this parameter is not supplied by semiconductor manufacturers.

However, since $T_{x}$ is inversely related to the forward alpha-cutoff frequency, you can select a device with a high cutoff frequency or a low storage time. The latter is specified by most manufacturers, and some vendors supply curves of storage time vs. base drive.

For a given transistor the most effective way to eliminate $t_{s}$ is to keep the transistor out of saturation. This can be done fairly easily. But in most switching, high-current applications, the resulting extra dissipation cannot be tolerated.


1. As temperature goes up, so does transistor gain. The resulting higher beta produces excess base-drive current, and so significantly boosts storage time. At low temperatures, a reverse effect occurs.

You should then attack the term $\mathrm{I}_{\mathrm{B} 2} / \mathrm{I}_{\mathrm{B} 1}$ in Eq. 2 , and reduce $t_{s}$ by increasing this ratio as much as possible. To do so, you can reduce $\mathrm{I}_{\mathrm{B} 1}$ or increase $I_{B 2}$. The former implies drastically less drive current-which can mean marginal gain conditions at low temperatures. It is therefore more practical to increase $\mathrm{I}_{\mathrm{B} 2}$ to 5 to 10 times the forward drive current.

In any given circuit, significant changes in storage-time duration can occur with gain or temperature variations. It is quite important to anticipate these changes before selecting a solution, and particularly for worst-case designs.

The quantities of $\mathrm{I}_{\mathrm{B} 1}$ and $\mathrm{I}_{\mathrm{B} 2}$ are usually controlled by the base-drive source and the series resistance in the base circuit. Within a given circuit, the currents are substantially fixed and not likely to change.

Gain variations between transistors or temperature fluctuations will alter $I_{c} / \beta_{0}$ significantly. As the gain changes so does the storage time, as can be seen clearly from Eq. 3.

For fixed base currents, storage time becomes inversely proportional to $I_{c} / \beta_{o}$ or directly proportional to the dc gain, $\beta_{0}$. The smaller the $\mathrm{I}_{\mathrm{B} 2}$, the higher the dependency of $t_{s}$ on $\beta_{0}$.

The dependency of $t_{s}$ on the dc gain is easily explained in terms of excess charge in the base junction of the saturated transistor. The charge, which is directly responsible for storage time, is a function of $I_{B X}$, the surplus base current. Since $I_{B X}=I_{B 1}-\left(I_{c} / \beta_{o}\right)$, its magnitude depends on $\beta_{0}$.

In the extreme, where $\beta_{\mathrm{o}}=\infty$ or $\beta_{0}=0, \mathrm{I}_{\mathrm{BX}}$ approximately equals $\mathrm{I}_{\mathrm{B} 1}$ or zero, respectively. In the latter case, storage time is nil.

Actually, with a given test condition, gain variations of $\pm 50 \%$ can exist in transistors carrying the same JEDEC number. Even worse, a tempera-

2. Note the drop in storage time as collector current increases, and how large the variations in $\mathrm{t}_{\mathrm{s}}$ are compared with other switching parameters. The variations must be accounted for in design.
ture variation from 25 to 125 C can double the gain; if the temperature swings from 25 to -55 C, the gain can be chopped in half (Fig. 1).

## One design, one solution

When a circuit must survive such large variations, a solution that eliminates CCC for one transistor at one temperature will not necessarily work for other transistors (even of the same type) or at other temperatures.

You must try to boost $\mathrm{I}_{\mathrm{B} 2}$ as high as possible to diminish the effects of worst-case $\beta_{0}$ variations. Also you must validate your solution empirically, since variations in $\beta_{o}$ are given for a typical case, and, by and large, are not guaranteed by manufacturers.

Gain changes with temperature variations are compounded by temperature-induced changes in the forward base-emitter voltage drop, causing the storage time to increase even more. Typical tempco for most silicon transistors is $-2 \mathrm{mV} /{ }^{\circ} \mathrm{C}$.

Thus the base-drive source, $\mathrm{E}_{\mathrm{b}}$, should be much larger than $V_{B E}$, so that $I_{B 1}$ varies negligibly with changes in $\mathrm{V}_{\mathrm{BE}}$. A drive source of 4 to 5 V is adequate. As a further precaution, use a basedrive series resistor that changes as a function of temperature, and so keeps $I_{B 1}$ or $I_{B X}$ fixed.

Fortunately, storage time drops sharply as collector current, $\mathrm{I}_{\mathrm{C}}$, increases (Eq. 3). The exact relationship between these two parameters depends on the initial $\mathrm{I}_{\mathrm{Bx}}$, on $\mathrm{I}_{\mathrm{B} 2}$ and on temperature.

Figure 2 shows $\mathrm{t}_{\mathrm{s}}$ as a function of $\mathrm{I}_{\mathrm{c}}$, with $\beta_{0}=$ 12.5 and $\mathrm{I}_{\mathrm{B} 1}=\mathrm{I}_{\mathrm{B} 2}$. Here, a change of collector current from 1 to 15 A drops $\mathrm{t}_{\mathrm{s}}$ from 1.4 to $0.4 \mu \mathrm{~s}$. Such a change must be accounted for in selecting a solution.

What actually occurs in the circuit? In the case of the push-pull converter, $Q_{1}$ and $Q_{2}$ conduct
simultaneously during the storage intervals. The collector currents of $Q_{1}$ and $Q_{2}$ flow through the primary of the transformer, cancelling flux changes.

## To catch a transient

The transformer behaves as a short, and the CCC transients are limited only by the dc resistances of the circuit (transformer, leads, collector resistivity) and by the capacity of the transistors to conduct heavy current.

By contrast, in the H-bridge, cross-current transients shoot through the two series transistors, $\mathrm{Q}_{1}$ and $\mathrm{Q}_{2}$, which conduct simultaneously during the storage intervals. Half a cycle later, the transient shoots through $\mathrm{Q}_{3}$ and $\mathrm{Q}_{4}$. In both cases, a virtual short is placed momentarily on the B+line.

There are several possible ways to minimize storage time (Fig. 3). The main idea is to prevent transistor saturation by starving the base
from excess drive, $\mathrm{I}_{\mathrm{x}}$. The surplus current diverts into the collector junction when the latter drops below a level, predetermined by the designer.

Now with the transistor out of-but on the verge of-saturation, storage time is almost eliminated. Whenever the collector voltage of $Q_{1}$ attempts to drop below $1.5 \mathrm{~V}, \mathrm{CR}_{1}$ starts to conduct and diverts the excess base current into the collector.

The threshold level of 1.5 V is determined by the $V_{B E}$ of $Q_{1}$ and by the voltage drops of $\mathrm{CR}_{1}$, $\mathrm{CR}_{2}$ and $\mathrm{CR}_{3}$. The level is also influenced by the temperature effect on the voltage drops, so that it is likely that at a high temperature-say 100 C -the threshold voltage will be 1.2 V . At -50 C , the threshold will be 1.8 V .

The circuit in Fig. 3b is similar except that a resistive divider, $R_{1}$ and $R_{2}$, determines the threshold value below which the collector voltage will not drop. This value is approximately $\mathrm{E}_{\mathrm{CC}} \mathrm{R}_{2} / \mathrm{R}_{\mathrm{L}} \mathrm{h}_{\mathrm{fe}}$ for the case where $\mathrm{E}_{\mathrm{CC}}$ is much greater than $\mathrm{V}_{\mathrm{CE}}$ (threshold). The dependency on temperature is

## What is cross-current conduction?

Cross-current conduction stems mainly from storage in a converter's switching transistors. The phenomenon appears as a stretching out of the saturated conduction period after base drive is removed. In a sense, CCC can be viewed as a transitor turn-off delay.

In comparison with turn-on delay, $\mathrm{t}_{\mathrm{d}}$, however, storage time, $\mathrm{t}_{\mathrm{s}}$, is typically 10 times or more in duration, and is also more sensitive to collector current, ambient temperature and other biasing conditions.

The figure shows the collector switching characteristic of a typical silicon transistor. Four transitions are of interest: delay time, $\mathrm{t}_{\mathrm{d}}$, rise time, $\mathrm{t}_{\mathrm{r}}$, storage time, $\mathrm{t}_{\mathrm{s}}$, and full time, $\mathrm{t}_{\mathrm{f}}$. Reverse base current $I_{B 2}$ (equal to $I_{B 1}$ ) flows after

forward base drive $I_{B 1}$ is removed.
Despite the negative base-current pulse, the transistor remains in hard saturation for a period, $\mathrm{t}_{\mathrm{s}}$, before the fall-time occurs. This storage time can stretch if $\mathrm{I}_{\mathrm{B} 2}$ is prevented from flowing.
Consider now the correlation between the switching times of the two push-pull transistors. The collector voltages of $Q_{1}$ and $Q_{2}$ are shown synchronized to the base drive. Overlap periods $t_{\mathrm{s} 1}$ and $\mathrm{t}_{\mathrm{s} 2}$ result from the storage times of $\mathrm{Q}_{1}$ and $Q_{2}$.

During these periods, the $\mathrm{B}+$ line is almost shorted, causing the high collector current spikes shown at the bottom of the figure. Note that the turn-on delay time and the rise and fall


3. How to avoid transistor saturation: various diode clamping circuits shunt excess base current into the
less than that of the circuit of Fig. 3a, and the use of a voltage divider permits more freedom in design.

In still another circuit, only one biasing diode, $\mathrm{CR}_{1}$, is needed (Fig. 3c). Hence, the thres-

collector (a, b, c). In (c) the collector is clamped to 0.7 V. Another circuit, the Darlington, does the same job (d).
hold, $\mathrm{V}_{\mathrm{CE}}$, is approximately equal to the $\mathrm{V}_{\mathrm{BE}}$ of the transistor, that is, 0.7 V . Temperature variations are half as much as those in the circuit of Fig. 3a.

Finally, the circuit in Fig. 3d keeps $Q_{1}$ out of

saturation with a Darlington arrangement. In such a scheme, $Q_{1}$ cannot saturate, but $Q_{2}$ can. Operation is not, therefore, fool-proof and other means are needed to reduce the storage time of $Q_{2}$. One way: hook-up $\mathrm{CR}_{2}$ and $\mathrm{R}_{3}$, which act quickly to discharge the base junction.

All of the methods shown in Fig. 3 keep the transistor out of saturation, but somewhat hamper efficiency. Thus these solutions are obviously suitable only where the collector current is small and where the extra dissipation is acceptable.

Luckily, there are other ways to attack the CCC problem-quick discharge of the base junction, for one.

## A speedy solution

Quick discharge of the base junction can drastically shorten storage time without necessarily keeping the transistor out of saturation. The

4. Another way to minimize storage time is to get rid of base charges fast. In one variation, a buffer $A_{1}$ sinks current to the $-1-V$ source (a). In (b) $A_{1}$ handles just $\mathrm{I}_{\mathrm{B} 2}$. Another quick-discharge method uses a transformer to apply a negative pulse during the off period (c).
method is not as effective as maintaining the transistor in the linear mode, yet it is more advantageous from the point of view of efficiency and power loss.

The idea behind quick discharge is to boost $\mathrm{I}_{\mathrm{B} 2}$, thereby significantly increasing the $\mathrm{I}_{\mathrm{B} 2} / \mathrm{I}_{\mathrm{B} 1}$ ratio. Figures 4a through 4c demonstrate different methods. In (a) resistor $R_{1}$ limits $I_{B 1}$, and $I_{B 2}$ faces a very low-impedance path through both diode $\mathrm{C}_{\mathrm{R} 1}$ (a fast recovery diode) and the current sinking buffer, $\mathrm{A}_{1}$.

It is desirable, but not absolutely necessary, to return the negative side of the buffer to -1 V so that $Q_{1}$ sees a negative pulse at turn off.

In Fig. 4a, $\mathrm{R}_{2}$ doesn't have to be small since, to increase the base drive efficiency, the discharge path is actually through $\mathrm{C}_{\mathrm{R} 1}$. In fact, $\mathrm{R}_{2}$ can be eliminated altogther.

In Fig. 4b, fast discharge occurs when $A_{1}$ sinks the base charges through $\mathrm{CR}_{1}$. Sinking buffer $\mathrm{A}_{1}$ handles only the reverse current $I_{B 2}$, not $I_{B 1}$. Consequently, the thermal stress of $\mathrm{A}_{1}$ is reduced.

In yet another improvisation, $R_{2}$ limits $\mathrm{I}_{\mathrm{B} 2}$ to the desired value, and a drive transformer $\mathrm{T}_{1}$, provides a positive voltage, $\mathrm{E}_{\mathrm{b}}$, for turn on and an equal negative voltage for turn off. Resistor $R_{1}$ fixes $I_{B 1}$, and $R_{2}$ determines the reverse current (Fig. 4c).

The circuits of Fig. 4 don't solve the CCC problem completely. To render the coupe de grace requires a fool-proof method to delay one transistor's turn on until the other transistor turns off completely.

The artificial delay time, $\mathrm{T}_{\mathrm{D}}$, so introduced should be somewhat longer than the anticipated worst case $\mathrm{t}_{\mathrm{s}}$. Remember, worst case occurs at the high temperature extreme, where $\beta_{0}$ is a maximum.

Admittedly, when $T_{D}$ equals $t_{s}$ at high temperature the storage time increases over its value at room temperature (or that of a low-gain transistor). Also $T_{D}$ greatly exceeds $t_{s}$ when the latter reaches a minimum at a low temperature. When $\mathrm{T}_{\mathrm{D}}=\mathrm{t}_{\mathrm{s}}$, the square wave shape of the converter is maintained.

On the other hand, when $T_{D}>t_{s}$, a blank period occurs between turn-on and turn-off, and a notch in switching appears. Such a notch does not necessarily lead to a loss of important circuit properties, but the designer must account for this phenomenon.

## Notches, ripple and regulation

For $T_{D}>t_{s}$, both transistors are off for some interval, ( $T_{D}-t_{s}$ ), every half cycle. During this period, the circuit looks like a high impedance to the transformer secondaries. Voltage spikes can result if the secondaries store energy, with no outlet for discharge.

5. Best method to avoid CCC is to keep one transistor off until the other turns off entirely. In the push-pull converter, $\mathrm{R}_{1}, \mathrm{R}_{2}, \mathrm{C}_{1}, \mathrm{C}_{2}$ do the trick, and the diodes provide fast turn off (a). Other ways to get delay: use

Furthermore, the notch in the conduction period increases the ripple component of the rectified-and-filtered secondary voltage. However, with a free-wheeling diode in the inductive filter of the secondary, you can neglect the impact of $T_{D}>t_{s}$.

When $\left(T_{D}-t_{s}\right)>0$, consider an important additional effect on regulation. Assume a converter is energized from a regulated B+ line and it provides a fixed load current from the secondary. Suppose also, that an L-C circuit, with a relatively small inductor, filters the output.

Since the line is regulated and the load is fixed, once the voltage losses in the transformer, rectifiers and filters are accounted for, the output voltage will be as well regulated as the $\mathrm{B}+$ line.
The regulation of the outputs will, however, be disturbed when $T_{D}$ starts to exceed $\mathrm{t}_{\mathrm{s}}$, and a blank period in the wave shape occurs. As the difference between $T_{D}$ and $t_{s}$ grows, the outputs can drop by as much as $2\left(T_{D}-t_{s}\right) / T$, where $T$ is the cycle duration.
The shorter the cycle (higher switching frequency), the higher will be the impact of each microsecond of notch on the wave shape. For a $25-\mathrm{kHz}$ system, each $1 / 2 \mu \mathrm{~s}$ of excess delay can cause a $2.5 \%$ change in the output voltage. It is crucial, therefore, to first minimize $t_{\mathrm{s}}$, and then do the same to $\left(\mathrm{T}_{\mathrm{D}}-\mathrm{t}_{\mathrm{s}}\right)$. A third option: keep the operating frequency as low as possible.

a mag amp (b) or an inductive time constant (L/R) as in (c). A more sophisticated circuit uses feedback to guarantee that delay time equals storage time under all operating conditions (d).

One simple way to introduce a delay is with an R-C network in the drive path (Fig. 5a). Components $R_{1}$ and $C_{1}$ delay the turn-on of $Q_{1}$-and $R_{2}$ and $\mathrm{C}_{2}$ do the same for $\mathrm{Q}_{2}$. The actual delay depends on the RC values, the amplitude of the square-wave drive signal and the threshold of the transistor.

In most cases, $\mathrm{T}_{\mathrm{D}}$ will equal some fraction of the R-C time constant. For instance, when $\mathrm{E}_{\mathrm{b}}$ equals $4 \mathrm{~V}, \mathrm{~T}_{\mathrm{D}}$ is approximately $1 / 5$ to $1 / 6$ of RC . Note the two fast-recovery diodes, CR , and $\mathrm{CR}_{2}$, in Fig. 5a. These provide the quick discharge of the base junction and minimization of $t_{s}$.

The advantage of the mechanism of Fig. 5a is its simplicity; the major disadvantage, however, is the circuit's loose accuracy. But the circuit will probably be adequate for $95 \%$ of applications.

Actually, selection of tighter tolerances and the use of temperature-compensation can provide better accuracy.

## Remember the mag amp?

Another delay method is the magnetic amplifier (Fig. 5b). Since the mag amp controls a delay in the 1 to $3-\mu \mathrm{s}$ range, and since the applied voltage, ( $\mathrm{E}_{\mathrm{b}} \simeq 4 \mathrm{~V}$ ) is small, it can be only slightly larger than can fit into a TO5 case.
(continued on page 78)

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(continued from page 77)
The delay action arises because of the ability of the mag amp's gates (the two windings in the $I_{B 1}$ path) to act as a high impedance and thus keeps $I_{B 1} \simeq 0$ for a portion of the cycle. The amount of delay is a function of the current in the control winding and is therefore determined by $R_{3}$ and voltage source $\mathrm{E}_{\mathrm{C}}$.

With $R_{3}$ a temperature-dependent resistor, it's possible to make $T_{D} \simeq t_{s}$, not only at room temperature but also at the hot and cold extremes. Rectifiers $\mathrm{CR}_{1}$ and $\mathrm{CR}_{2}$-needed for the operation of the mag amp-also block $I_{B 2}$. You should keep $R_{3}$ and $R_{4}$ small to discharge $Q_{1}$ and $Q_{2}$ quickly.

In still another delay circuit, an inductor with two windings helps to create the required delay (Fig. 5c). In this configuration, $T_{D}$ is a function of the time constant $L / R_{1}$ or $L / R_{2}$. Diodes $\mathrm{CR}_{1}$ and $\mathrm{CR}_{2}$ provide a fast-discharge path.

Perhaps the most sophisticated and accurate delay circuit is based on a gating scheme (see Electronic Design, Vol. 20 No. 13, June 22, 1972, p. 100). In Fig. 5d, R ${ }_{1}$ and zener $\mathrm{CR}_{1}$ form a $5-\mathrm{V}$ source. Network $\left(\mathrm{R}_{2}\right.$ and $\left.\mathrm{CR}_{2}\right)$ drains a small amount of current (about 3 mA ) from the source through $Q_{1}$ when the latter saturates.

## Foolproof circuit guarantees results

At saturation, the voltage on the anode of $\mathrm{CR}_{2}$ drops below 1 V and is recognized by inverter $A_{1}$ as a low. As a result, $A_{1}$ 's output goes high, and $Q_{4}$ is driven into hard saturation, preventing $\mathrm{Q}_{2}$ from turning on.

When $Q_{1}$ ends its saturation period (composed of conduction plus storage time), its collector voltage jumps to twice the $B+$ voltage. Then $\mathrm{CR}_{2}$ becomes back biased, and the voltage on its anode rises to 5 V . Inverter $\mathrm{A}_{1}$ sees a high input, puts out a low, and drives $Q_{4}$ off.

The arrangement guarantees that $\mathrm{Q}_{2}$ is starved of base-drive current-as long as $Q_{1}$ conductsand that no cross-conduction occurs. The circuit for $Q_{2}$ is identical.

The circuit is fool-proof because feedback "watches over" the transistor state of conduction. The only disadvantage is the relative complexity : Packaging can be a problem with the circuit in discrete form. But IC vendors, take note -monolithic design is certainly feasible.

The third method-limiting transient current during CCC by insertion of an impedance in the B + line-is not highly recommended. In this method, an inductor is connected in series with the center tap of the transformer or in the emitter leads of both push-pull transistors.

A diode across the inductor allows reset of the flux. However, an inductor in the high-current path implies higher power losses and a slow down of turn on and turn off. -

## HP announces pulse-parameter control in a word generator



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# Pick the right p-i-n diode for transceiver antenna switches. Some simple equations and approximations can make selection easy and prevent overload damage. 

When p-i-n diodes are used as antenna switches, they offer higher reliability, better ruggedness and faster switching speeds than electromechanical relays.

For most low-power transceivers (those with less than 2-W rf output) diode switches can be designed without paying much attention to power limitations. Such restrictions do become important at higher power levels, where damaging overloads can occur-from mismatched antennas or extreme operating temperatures, for example.

The basic circuit for an electronic switch consists of a p-i-n diode connected in series with the transmitter section, and a shunting diode connected a quarter wavelength ( $\lambda / 4$ ) away from the antenna in the direction of the input amplifier of the receiver section (Fig. 1a).

Lumped elements can be used to simulate the $\lambda / 4$ section (Figs. 1b and 1c), and of course, are preferable for transceivers that operate at long wavelengths.

When switched into the transmit state each diode becomes forward biased. The series diode appears as a low impedance to the signal heading towards the antenna, and the shunt diode effectively shorts the receiver's antenna terminals to prevent overloading. Transmitter insertion loss and receiver isolation depend on the diode resistance, $\mathrm{R}_{\mathrm{D}}$.

In the receive condition, the diodes are at zero or reverse bias and present essentially a low capacitance, $\mathrm{C}_{\mathrm{T}}$, which creates a direct low-inser-tion-loss path between the antenna and receiver. The off transmitter is isolated from this path by the high-impedance series diode.

## Transform impedances with $\lambda / 4$ sections

The quarter-wave line in the antenna serves a dual purpose:

1. During the transmit states it transforms the shunt diode from a low to a high impedance, at the antenna port.

Gerald Hiller, Director of Microwave Engineering, Unitrode Corp., 580 Pleasant St., Watertown, MA 02172.


P-i-n diodes can be used as antenna switches if you follow a few simple design rules.


1. A quarter-wave section of transmission line is used to separate the transmit and receive sections of a transceiver (a). For transceivers using long-wavelength frequencies, lumped elements eliminate the long lengths of transmission line ( $b$ and c ).
2. During the receive state it presents a lowloss, low-reflection path to the receiver port.

The amount of power dissipated by the diodes in the transmit state must be determined. Power dissipation in the p-i-n diode is simply the square of the rf current multiplied by $R_{D}$.

You can easily analyze the switch performance for conditions such as transmitter power, worstcase antenna standing-wave ratio (SWR) and ambient temperature. First, you should redraw

2. The equivalent circuit of the antenna switch (in the transmit state) represents the diode as simple resistances (a). To make calculations easier you should normalize all circuit elements (b).
the circuit of Fig. 1a to the form shown in Fig. 2 a ; this lets you define and calculate the rf-signal relationships that occur at the design frequency of the quarter-wave transformer. (Most transceivers are relatively narrow band, within $\pm 10 \%$, and the accuracy lost by using the relationships across the entire bandwidth is insignificant.)

The transmitter can be represented by a voltage source, $\mathrm{V}_{\mathrm{g}}$, that has an internal impedance, $\mathrm{Z}_{0}$. The maximum available power from the generator can be found by:

$$
\begin{equation*}
\mathrm{P}_{\mathrm{AV}}=\mathrm{V}_{\mathrm{g}}{ }^{2} / 4 \mathrm{Z}_{0} \text { watts. } \tag{1}
\end{equation*}
$$

The diode resistances are considered equal and each has a value of $R_{D}$. You can represent the antenna impedance at $\mathrm{R}_{\mathrm{A}}$ and the receiver impedance, for perfect matching, as $\mathrm{Z}_{0}$. The quarterwave transformer is also assumed to have a characteristic impedance of $\mathrm{Z}_{0}$.

To simplify and generalize the analysis, let's normalize the component values of the circuit to the nominal transmission-line impedance, $\mathrm{Z}_{0}$
(Fig. 2b). The circuit elements are now identified as follows:

$$
\begin{align*}
& \mathbf{r}=\mathrm{R}_{0} / \mathbf{Z}_{0} \text { ( } \text { normalized diode resistance) }  \tag{2}\\
& \boldsymbol{\sigma}=\mathrm{R}_{\mathrm{A}} / \mathbf{Z}_{0} \text { or } \mathrm{Z}_{0} / \mathrm{R}_{\mathrm{A}} \text { (antenna SWR) } \tag{3}
\end{align*}
$$

The antenna SWR, $\sigma$, represents a resistive mismatch. The series diode is highly stressed
when a mismatch stresses the low impedance appears at the antenna junction; similarly, the shunt diode is highly stressed when a high impedance appears at the antenna junction. In most transceivers, and particularly in those for mobile units, the worst-case SWR is used because the distance between the true reflection plane and the antenna junction varies whenever the vehicle moves.

Since the switch has a finite resistance, it will cause a loss in rf output power. The loss, in decibels, can be calculated by:
insertion loss $=$

$$
\begin{equation*}
10 \log \left\{4 \sigma /\left[(\sigma+1)^{2}+(r+1)^{2}\right]\right\} d B \tag{4}
\end{equation*}
$$

Graphically, you can plot this equation for antenna SWR values of 1, 1.5, and 2 (Fig. 3a). At all SWR values for $r=0$, the insertion loss is the same as the mismatch loss. Eq. 4 shows that the insertion loss is theoretically small-less than 0.2 dB for values of r less than $0.02\left(\mathrm{R}_{\mathrm{D}}=1 \Omega\right.$ in a $50 \Omega$ system). The improvement is small by reducing the diode resistance to less than $1 \Omega$.

## Diode isolation affects performance

How well the diodes isolate the receiver section of the transceiver can also be calculated:
receiver isolation $=$

$$
\begin{equation*}
10 \log \left\{4 \mathbf{r}^{2} \boldsymbol{\sigma}^{2} /\left[(\sigma+1)^{2}(\mathbf{r}+1)^{4}\right]\right\} \mathrm{dB} \tag{5}
\end{equation*}
$$

Plots at Eq. 5 show the effect of SWR on receiver isolation and clearly depict the $6-\mathrm{dB}$ degradation in receiver isolation that occurs when the transmitter is totally mismatched. The degradation must be included when you design the receiver protection. The isolation for values of $r$ less than 0.02 is about 34 dB when the antenna is matched.

Higher isolation values are theoretically possible but are not generally achievable due to other circuit losses and radiation effects. Inductance in the shunt diode path will also be detrimental to isolation. If you need higher isolation, just add quarter-wave sections to the circuit.

The power dissipated by the series and shunt diode can be found from two formulas:
$\mathrm{P}_{\mathrm{D}}($ series $)=$
$4 \mathrm{r} \mathrm{P}_{\mathrm{Av}}\{(\sigma \mathrm{r}+\mathrm{r}+\sigma) /[(\mathrm{r}+1)(\sigma \mathrm{r}+\sigma+\mathrm{r}+1)]\}^{2} \mathrm{~W}(6)$ $\mathrm{P}_{\mathrm{D}}$ (shunt) $=4 \mathrm{r}^{2} \mathrm{P}_{\mathrm{AV}} /\left[(\sigma+1)^{2}(\mathrm{r}+1)^{4}\right] \mathrm{W}$ (7)
For $r \ll 1$, Eqs. 6 and 7 reduce to:

$$
\begin{equation*}
\mathrm{P}_{\mathrm{D}}=4 \mathrm{r} \sigma^{2} \mathrm{P}_{\mathrm{AV}} /(\sigma+1)^{2} \mathrm{~W} \tag{8}
\end{equation*}
$$

Eq. 8 is a handy approximation of the power dissipation in both the series and shunt diodes. It implies an $R_{D}$ that is much less than the typical $50-\Omega \mathrm{Z}_{0}$. And, any inaccuracy that Eq. 8 introduces results in a more conservative estimate of diode dissipation; and you won't end up with an underrated device (Fig. 4).

Even though diode resistance has little effect on insertion loss, it can greatly affect the amount of power the switch will dissipate. From matched
antennas to totally mismatched units, power dissipation increases by four times.

You can find the maximum-allowable transmitter power $\mathrm{P}_{\mathrm{T}(\text { max })}$, by solving Eq. 8 for $\mathrm{P}_{\mathrm{Av}}$ and then substituting $\mathrm{P}_{\mathrm{D}(\max )}$, the maximum-allowable diode dissipation for $P_{D}$. If you're working in $50-$ $\Omega$ systems you can simplify the equation even fur-
ther (assuming infinite SWR):

$$
\begin{equation*}
\mathrm{P}_{\mathrm{T}(\max )}=12.5 \mathrm{P}_{\mathrm{D}} / \mathrm{R}_{\mathrm{D}} \mathrm{~W} . \tag{9}
\end{equation*}
$$

As a p-i-n diode handles rf power, its junction temperature rises. The temperature reached depends on $P_{D}$, the ambient temperature ( $\mathrm{T}_{\mathrm{AMB}}$ ) and the thermal resistance between the diode junction and the external ( $\Theta_{J A}$ ). The transmitter

## Basics of p-i-n diode operation

P-i-n diodes operate as rf and microwave resistors whose resistance values are controlled by forward current levels. A p-i-n diode is built from high resistivity (low conductivity) silicon and has an intrinsic layer sandwiched between a p and an n layer (Fig. A). When the diode has a forward current, holes and electrons are injected in the i-region. They do not completely recombine, but instead form some quantity of stored charge. The stored charge causes the effective resistivity of the i-region to be much lower than intrinsic resistivity.
The amount of stored charge, Q , is governed by the recombination time in the i-region (commonly called the carrier lifetime), $\tau$, and the level of the forward current, $\mathrm{I}_{\mathrm{t}}$. The relationship between these three terms can be expressed simply as:

$$
\begin{equation*}
\mathrm{Q}=\mathrm{I}_{\mathrm{t}} \times \tau \text { coulombs. } \tag{A}
\end{equation*}
$$

The resistance of the i-region, $R_{D}$, is inversely proportional to Q and can be expressed ${ }^{3}$ as:

$$
\begin{equation*}
\mathrm{R}_{\mathrm{D}}=\mathrm{w}^{2} / 2 \mu \mathrm{Q} \text { ohms, } \tag{B}
\end{equation*}
$$

where w is the i-region width and $\mu$ the ambipolar carrier mobility in silicon.

Note that the resistance of the diode is ideally only affected by carrier lifetime and i-region width, not by the junction area of the diode. In practical devices, a reduction of carrier lifetime occurs when the chip area is reduced.
Eqs. A and B indicate that diode resistance is inversely proportional to the applied de forward current. The de voltage-current characteristics of a p-i-n diode are similar to those of a conventional p-n junction diode.

For example, a UM4001B p-i-n diode, at 25 C , has a forward voltage of 0.75 V at a current of 0.1 A and 0.85 V at 1 A . The total power dissipated is the sum of the dc power and the rf power (Fig. B). However, dc power levels are usually much lower than rf levels and can be ignored in all but marginal applications.
When zero or reverse biased, the p-i-n diode appears as a capacitor, C, in parallel with a resistance, $\mathrm{R}_{\mathrm{p}}$. At rf frequencies, the capacitance is independent of reverse voltage and can be represented as:

$$
\begin{equation*}
\mathrm{C}=\epsilon \mathrm{A} / \mathrm{w} \text { farads }, \tag{C}
\end{equation*}
$$

where $\boldsymbol{\epsilon}$ is the dielectric constant of silicon, A the area of the i-region and w the width of the i-region.
$R_{p}$ depends on both reverse voltage and fre-


quency. Its value, to a large extent, is determined by the resistivity of the i-region material.
The ideal p -i-n diode for power switching should have a long carrier lifetime for low resistance and a thick i-region to keep the capacitance per unit area low. Thus a large thermal contact area is available without increasing the capacitance to a level that makes the diode useless at high frequencies.

Electrical specs for p-i-n diodes often give the diode resistance at a specific forward current, and the diode capacitance at a specific reverse voltage and frequency. Also, curves are usually available on the data sheet to present parameters as a function of frequency and forward current.
power the diode handles should not raise the junction temperature above its maximum allowable value, $\mathrm{T}_{\mathrm{J}(\max )}$.

When the thermal resistance and maximum junction temperature are specified for a diode, the maximum power dissipation can be found:

$$
\begin{equation*}
P_{D(\max )}=\left(T_{J(\max )}-T_{A M B}\right) / \theta_{J A} W . \tag{10}
\end{equation*}
$$

Often, though, p-i-n diodes have their $\mathrm{P}_{\mathrm{D}(\max )}$ specified for an ambient temperature of 25 C and a maximum junction temperature. In such cases, a derating factor (the inverse of the thermal resistance) should be included in your calculations.

For diodes mounted in threaded-stud packages, the thermal-resistance rating indicates how well the heat can be transferred from the junction to an infinite heat sink to which the study is attached. The $\Theta_{\mathrm{JA}}$ rating assumes that no heat is removed from the terminal not connected to the stud.
Stud mounted p-i-n diodes are available with either the p or n sides connected to the stud. These types can be used for the shunt section of the switch. Diodes are available with a beryllia insulator placed between the diode and the case to permit total electrical isolation and still provide a good thermal path. Totally isolated diodes, of course, are used as the series switch elements.

## Include lead length in power calculations

The power rating of diodes in axial-lead packages is a little more difficult to compute because the leads are part of the heat-flow path. The rating depends on the lead material and geometry, which in turns determine the $\Theta_{J A}$ of the leads. For example, the UM4001B from Unitrode has $0.04-\mathrm{in}$. diameter silver leads. The graph in Fig. 5 a shows how the thermal resistance changes as lead length increases.
The slope of the graph line is $37 \mathrm{C} / \mathrm{W}$ per inch of lead length, which is one half the $\theta_{J A}$ of the lead material since the device is thermally symmetrical. To compute the $\mathrm{P}_{\mathrm{D}(\max )}$, any thermal resistance in the interface between the lead and heat sink must be included.
Fig. 5 b shows the power ratings of a leaded UM4001B compared with a stud-mounted UM4001C $\left(\Theta_{J A}=6 \mathrm{C} / \mathrm{W}\right)$ and a stud-mounted, beryllia-isolated UM4001D $\left(\Theta_{J A}=8 \mathrm{C} / \mathrm{W}\right)$. The maximum junction temperature of these diodes is 175 C and all ratings assume no heat is removed by convection.

Another potential thermal limitation of p-i-n diodes is the maximum allowable current. This rating is usually given if there is a possibility of fuse action occurring within the diode package at a current level lower than the diode power rating.

If a maximum current rating is given, make sure that the maximum rf current, at worst-case SWR, doesn't exceed that rating. For instance, a

15-W transmitter would probably supply approximately 1.1 A of rf current at total SWR mismatch; a 1-A diode obviously can't handle the potential overload.

## Design procedures are simple

Let's look at some full design examples. A $50-\Omega$ diode switch must handle 500 W at an antenna SWR of 2 and a heat-sink temperature of 90 C . The series diode in the switch must dissipate most of transceiver power loss, so let's evaluate its requirements first. At a $90-\mathrm{C}$ ambient the UM4001D has a maximum $\mathrm{P}_{\mathrm{D}}$ of 10.5 W (Fig. $5 b)$.
(continued on page 86)

3. You can calculate insertion loss (a) or receiver isola. tion (b) by finding $r$ and $\sigma$. Insertion loss increases with $r$, and receiver isolation improves as $r$ decreases.

4. As $r$ increases above 0.13 , the approximate dissipation curves diverge from curves based on exact equations. No problems are caused, however, since the approximate values are higher than the real ones, actually adding a safety margin.
(continued from page 85)
If you solve Eq. 8 for $r$, you get a value of $r=$ 0.0118 , or $\mathrm{P}_{\mathrm{D}}=0.59 \Omega$. Using $\mathrm{R}_{\mathrm{D}}=0.50 \Omega$ you can look up the forward current on the curve of $R_{D}$ vs. $I_{f}$ (see box p. 84 Fig. B). The curve of the UM4001D shows that the forward current is 70 mA . Thus a diode such as the UM4001D, with a forward current rating of 100 mA , is more than adequate.

As another example, let's assume a p-i-n diode has the following ratings: $R_{D}=0.6 \Omega, \Theta_{J A}=250$ $\mathrm{C} / \mathrm{W}$ and $\mathrm{T}_{(\max )}=125 \mathrm{C}$. Find the diode's trans-mit-power capability. To start with, several other factors must be assumed-the transceiver's antenna system has a characteristic impedance of $50 \Omega$, the switch must be able to withstand an infinite antenna SWR and the heat sink temperature cannot go above 55 C .

You can start by calculating the power dissipation, using Eq. 14 :

$$
\mathrm{P}_{\mathrm{D}(\max )}=(125-55) / 250=0.28 \mathrm{~W} .
$$

Since $R_{D}$ is much less than $50 \Omega$, Eq. 9 can be used to solve for the maximum transmitting power:

$$
\mathrm{P}_{\mathrm{T}(\max )}=(12.5 \times 0.28) / 0.6=5.83 \mathrm{~W} .
$$

If a diode with an $\mathrm{R}_{\mathrm{D}}$ of $1 \Omega$, a $\Theta_{\mathrm{JA}}$ of $20 \mathrm{C} / \mathrm{W}$ and a maximum temperature of 175 C is used, $\mathrm{P}_{\mathrm{D}}$ increases to 6 W and $\mathrm{P}_{\mathrm{T}(\max )}$ jumps up to 75 W . $-{ }^{-1}$

## References

1. Chorney, Paul, "Design Curves for a Low-Drain PIN Diode TR Switch," Microwave Journal, July 1972, pp. 3134.
2. Chorney, Paul, "Low Drain PIN-Diode TR Switch," Microwave Journal, February, 1973, pp. 16-20.
3. Leenov, D., "The Silicon PIN Diode as a Microwave Radar Protector," IEEE Transactions on Electronic Devices, Vol. 11, 1964, pp. 53-61.

4. The thermal resistance of leads on axial-lead diodes must be included in many power dissipation calculations (a). Some typical power-rating curves of the UM4001 series of leaded and stud-mounted p-i-n diodes show the improvement in power dissipation possible just by using short leads or a stud-mount package (b).

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# Measure signal peaks accurately despite low rep rates and narrow pulse widths. This 'go-fast' technique can do things the usual peak circuits can't. 

Accurately measuring peak voltages is a difficult design challenge, full of pitfalls for the unwary. Component limitations pose severe problems for simple designs.

The solution: an advanced design called the "Go-Fast" peak detector. It accurately measures the peaks of repetitive signals-or even single pulses-of almost any shape, rise time or duration. The circuit's range extends from slowly varying de to narrow pulses that have durations as short as 500 ns . It can also handle a wide sig-nal-voltage range-from 20 mV to 10 V peak.

## Sharp rises are troublesome

Factors of particular importance in the design of peak detectors include the input signal's char-acteristics-rise time, dwell, repetition rate and dynamic range-and the detector's ability to capture and retain a measurement long enough to transfer it to a more permanent storage.

Sharp signal rise, for example, is a major reason peak detectors overshoot. Narrow, singleshot signals and those with low repetition rates require high charging currents to fully charge a temporary storage capacitor.
Such currents are not easily achieved for $500-$ ns signals. A wide signal range ( 40 dB or more) requires a low detection threshold with good linearity, which is hard to get.

Further, once the signal is detected, the detector's output must be able to hold its measurement until the data can be transferred to a more permanent recording or metering system. Thus, output "droop" is a significant design constraint.

The basic circuit (Fig. 1) is reasonably accurate for relatively slow rise times, long-duration pulses and repetitive waveforms. But it suffers serious errors with both single, narrow pulses and wide, repetitive pulses-if the pulse train has fast leading-edge rise times.

To overcome dynamic-range limitations im-

[^5]

This peak detector, designed around the "Go-Fast" technique, monitors acoustic emissions from fatigue cracks in an aircraft-materials-testing system.
posed by the diode's forward-voltage drop, the designer might resort to a preamplifier to increase signal levels so that the drop is negligible. But, a $40-\mathrm{dB}$ range requires signal levels of over 100 V -not practical with most standard solidstate components.

A better approach is to insert the diode into a feedback loop (Fig. 2). That way you cure the problems caused by the forward-voltage drop, and improve the diode's linearity at the same time. However, a multitude of problems remain.

## Slew rate is limited

When the input preamplifier can't follow the input signal, the peak detector will register a peak reading far below the true value.

For example, in a preamplifier that has a slew
rate of $10 \mathrm{~V} / \mu \mathrm{s}$, a signal input pulse of 6 V and $500-\mathrm{ns}$ wide will drive its output to only 5 V . In addition, the drop in $\mathrm{D}_{1}$ allows only about 4.3 V to be stored in capacitor $\mathrm{C}_{\mathrm{s}}$. As a result, a false reading as low as $71 \%$ of the true value is obtained. Further, a 500 -ns input signal with a $10-\mathrm{V}$ peak would also store only 4.3 V , giving an error greater than $50 \%$.

Many op amps have a current-limit safeguard designed-in to protect against short-circuit conditions. Others are current limited by their components.

Current limits pose a problem. The reasonably large capacitor needed to store voltage requires


1. The basic peak-detector circuit has very limited capability. It can only be used to measure signals that have high levels and high repetition rates.

2. To overcome some of the limitations inherent in a simple diode peak detector, the diode can be included in the feedback loop of an op-amp circuit. Many problems still remain though, so a more sophisticated circuit is needed to attain accurate results.
an appreciable current to avoid excessive delay in the feedback circuit. A large capacitor keeps down the effects of the drift from offset-bias current in the post amplifier, $\mathrm{A}_{1}$, and leakage in diode $\mathrm{D}_{1}$. A fast charging rate is mandatory; delays contribute to overshoot.

For example, suppose the amplifier has a slew rate sufficient to reach the peak value of a $10-\mathrm{V}$ pulse in 500 ns . With its output current limited to perhaps 10 mA , a $0.01-\mu \mathrm{F}$ capacitor can only charge to about $1 \mathrm{~V}-1 / 10$ of the true readingeven in a period as long as $1 \mu \mathrm{~s}$.

With a $500-\mathrm{pF}$ capacitor, the circuit could charge to 10 V in 500 ns . But all is still not well: The $500-\mathrm{ns}$ delay in the feedback loop can cause a huge overshoot, because the preamplifier is driven hard into saturation for the full charge period. It then takes at least another 500 ns to
recover its maximum slew rate. Up to $100 \%$ overshoot occurs in some cases.

## Diodes are far from ideal

The effects of $D_{1}$ 's forward capacitance are largely compensated for by feedback action; thus, forward capacitance is of little consequence.

Far more important are the effects of the diode's reverse capacitance. The main feedback loop is disabled when an input pulse drops to a negative value, and a new feedback loop is established through $\mathrm{D}_{2}$. This action causes a sudden drop in voltage on $\mathrm{D}_{1}$ 's anode that capacitively couples a negative spike into the storage-capacitor circuit. At best, the spike reduces the stored voltage. At worst, it propagates through the feedback loop, driving the preamplifier on again and producing ringing and measurement errors.

And, of course, other nonideal characteristics of diodes also take their toll of a peak-detector circuit's performance. Such problems include re-verse-recovery time, $\mathrm{T}_{\mathrm{RR}}$, forward-recovery time, $\mathrm{T}_{\mathrm{FR}}$, forward-voltage drop and leakage current.

Charge is stored in a diode when forward current flows. When the diode bias is suddenly reversed, this stored charge maintains a reverse current flow. Reverse current may flow only 200 ns in fast-recovery diodes and many microseconds in ordinary diodes (Fig. 3a). Unfortunately, reverse current discharges the storage capacitor to a lower voltage.

Perhaps less troublesome is forward-recovery time (Fig. 3b). When forward voltage is first applied to a diode, current carriers need a finite time to cross the junction; thus, most of the input voltage drops across the diode for the first few hundred nanoseconds. High-voltage diodes typically have long forward-recovery times, $\mathrm{T}_{\mathrm{FR}}$.

Once recovered, a forward-voltage drop of from 0.2 to 1 V still remains. This drop is the only adverse diode characteristic almost fully corrected by feedback.

A problem not helped by feedback is diode leakage. Leakage current causes droop in the stored voltage. Highly dependent on temperature, droop rate, because of this leakage, doubles approximately every $10-\mathrm{C}$ temperature rise.

The designer could use a transistor connected in a diode configuration to significantly improve this leakage problem. A better solution, however, is to use junction FETs, $\mathrm{D}_{1}$ and $\mathrm{D}_{2}$, connected as shown in Fig. 4; both leakage and junction-capacitance effects are thereby improved.

## Capacitors aren't ideal either

The single major problem source most often overlooked in storage capacitors is the tendency for some types to have "memory." Ceramic capac-

3. Reverse-recovery time, $\mathrm{T}_{\mathrm{RR}}$, and forward-recovery time, $\mathrm{T}_{\mathrm{FR}}$, take a toll of a peak-detector's performance.

4. A "Go-Fast" peak-detector circuit that uses a comparator preamplifier, FET diodes, a FET-input feedback op amp, a constant-current charging source and a highquality polystyrene storage capacitor, can accurately measure the peak voltage of even a single $500-\mathrm{ns}$ pulse.
itors, in particular, tend to store charge within the dielectric. Over a period of time, a charge on a capacitor tends to "soak" into the dielectric and a decrease in the capacitor's terminal voltage results.

If the capacitor is then partially discharged and isolated, some of the soaked-in charge migrates out and raises the voltage slightly. The recovered voltage is proportional to the voltage originally impressed.

In peak detectors, this memory effect appears during peak-voltage storage time as voltage droop, and after reset as zero-voltage creep, which is proportional to the last stored voltage. In peak detectors, therefore, stay away from ceramic capacitors. Instead, consider polycarbonate or polystyrene units.

Unfortunately, the storage capacitor's voltage also can be changed by the post amplifier's bias currents-and discharged by its input impedance. A FET-input op amp can help with both of these problems. Because a FET has low offset-bias characteristics and an extremely high input impedance, charge on $\mathrm{C}_{\mathrm{s}}$ would remain undisturbed.

Even though the post amplifier doesn't supply current to the storage capacitor, its slew-rate and current-limit characteristics are still very important. This amplifier provides feedback to the preamplifier, and any delays because of slew-rate limiting may cause overshoot. If the output amplifier must drive appreciable load capacitance, the amplifier's current-limit performance must be able to handle the load.

## Current switching makes the detector go fast

Even with suitable pre and post op amps, overshoot remains a major stumbling block. You can reduce the problem with standard feedback com-pensation-a shunt capacitor, $\mathrm{C}_{\mathrm{F}}$, across feedback resistor, $\mathrm{R}_{\mathrm{F}}$-and the selection of an optimum storage capacitor, $\mathrm{C}_{\mathrm{s}}$. But that only works for a given pulse shape and repetition rate, and doesn't totally do the job even then. Since an unrealistically high slew rate is needed to maintain the small closedloop error required during charging of $\mathrm{C}_{\mathrm{s}}$, some other capacitor-charging technique must be used to avoid overshoot.

The solution to charging the storage capacitor without overshoot lies in the use of a constantcurrent charging technique (Fig. 4). During the time that an input signal is present, $\mathrm{C}_{\mathrm{s}}$ charges at a fixed rate via a gated current source, $\mathrm{Q}_{1}$. The rate of charge is adjusted to fully charge $\mathrm{C}_{\mathrm{s}}$ within the time of the minimum expected pulse width.

Circuit operation is as follows: The signal to be detected, $\mathrm{E}_{\mathrm{i}}$, at the positive input of comparator $\mathrm{CO}_{1}$ toggles its output HIGH. This action gates the constant-current source, $\mathrm{Q}_{1}, \mathrm{ON}$ via FET junction-diode, $\mathrm{D}_{1}$, to charge the storage capacitor, $\mathrm{C}_{\mathrm{s}}$.

When the voltage across $\mathrm{C}_{\mathrm{s}}$ reaches a level equal to $\mathrm{E}_{\mathrm{i}}$ the comparator toggles LOW because of feedback from the FET-buffered amplifier, $\mathrm{A}_{1}$. This diverts $\mathrm{Q}_{1}$ 's output through FET-diode $\mathrm{D}_{2}$ to be "sunk" into the comparator's output.

With $\mathrm{C}_{\mathrm{s}}$ now isolated, its charge is held by the reverse-biased $D_{1}$ junction and the very-high input impedance of the FET-buffered amplifier, $\mathrm{A}_{1}$. Once $A_{1}$ 's output measurement is taken, FETswitch $\mathrm{S}_{1}$ can momentarily discharge $\mathrm{C}_{\mathrm{s}}$, thus resetting the circuit for another peak detection.

The fast comparator provides very-high switching rates and low delays. FET diodes and their symmetrical connection eliminate the previously explained diode problems, and a high-speed FETinput amplifier allows the use of a small polystyrene capacitor for $\mathrm{C}_{\mathrm{s}}$. Therefore, in Fig. 4 stored peak-signal droop is practically nonexistent. And even the power supply for the circuit is surge free, because the charge current from $\mathrm{Q}_{1}$ is diverted, not switched off. -

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## Ensure uniform LED-display brightness with these simple design tips

Ever hook up a LED display and have it look like a patchwork quilt with almost every segment at a different brightness?

Consider the plight of the engineer who designs an instrument with a readout of several segmented numerical displays. Each display uses two LED chips per segment with a forward-voltage drop per segment, $V_{f}$, specified at 20 mA to have a typical value of 3.4 V and a maximum of 4 V .

The segments, when driven from a regulated 5-V supply, lose 0.8 V across the decoder-driver. The engineer therefore chooses a $40-\Omega$ limiting resistor to obtain the $20-\mathrm{mA}$ drive,

$$
0.02=\frac{5-0.8-3.4}{40}
$$

That, of course, is the classic load-line solution. But a $40-\Omega$ load and $5-\mathrm{V}$ source is the wrong solution.

At 20 mA , each segment of the display does give nearly identical brightness. But a $40-\Omega$ load resistor, starting from a $5-\mathrm{V}$ supply can't provide each segment with 20 mA .

The $\mathrm{V}_{\mathrm{f}}$ across individual segments in the display may cluster closely around the typical 3.4 V-at 3.2 to 3.6 V , for example. However, the drive current will vary from 25 to 15 mA over this $\mathrm{V}_{\mathrm{f}}$ range, and down to 5 mA at the rated maximum of 4 V .

Obviously, a higher-voltage supply source and a larger load resistance can solve the problem. An unregulated $9-\mathrm{V}$ source and $240-\Omega$ load resistors will minimize the effect of spread in $\mathrm{V}_{\mathrm{f}}$. For the typical $3.4-\mathrm{V}$ segment,

$$
\frac{9-0.8-3.4}{240}=20 \mathrm{~mA}
$$

Now the variations range from 20.8 mA for a $3.2-\mathrm{V}$ segment to 19.2 mA for a $3.6-\mathrm{V}$ segment, a current ratio of less than 1.1:1. Even a $4-\mathrm{V}$ segment would be driven at 17.5 mA .

The cost of greater brightness uniformity, of


A high source voltage relative to $\mathrm{V}_{\mathrm{f}}$ allows use of larger load resistances. Current and illumination variations are thus reduced over the normal spread of $\mathrm{V}_{\mathrm{f}}$, but power dissipation increases.
course, is a somewhat higher power consumption.
Another approach is to use LEDs with a lower $\mathrm{V}_{\mathrm{f}}$. For example, certain GaP displays, driven from 5 V through a decoder-driver and $90-\Omega$ resistors, pass 10 mA at a typical $\mathrm{V}_{\mathrm{f}}$ of 2.5 V . But since $\mathrm{V}_{\mathrm{f}}$ ranges from 2.3 to 2.7 V , the current varies widely from 7.7 to 12.2 mA -much too widely for a uniformly bright display. Replacement of the displays with lower $\mathrm{V}_{\mathrm{t}}$, GaAsP units having a typical value of 1.7 V , and a range of 1.6 to 1.8 V , would narrow the current range to between 17.7 and 18.8 mA with the same driver and $90-\Omega$ resistors. No other circuit modifications are required.

Bob Woods, Product Reliability Manager, Litronix, 19000 Homestead Rd., Vallco Park, Cupertino, CA 95014.

Circle No. 311


Triplett's newest panel instruments, the Series GL and GL/B, feature glass windows, mattefinish phenolic cases and a dial design that can readily accommodate multiple scales. They are available in $31 / 2^{\prime \prime}, 4 \frac{1}{2} 2^{\prime \prime}$ and 5 $1 / 2^{\prime \prime}$ sizes.
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# 555 one-shot circuit features negative output with positive triggering 

The standard 555 monostable configuration is well known for its negative-level triggering and positive output pulses. But the same device may also be used with positive-going triggering to provide negative output pulses.

The new configuration may be said to be a mirror image of the standard one shot-but their timing equations are identical because of the symmetry of the 555 's internal-comparator levels.

$$
\mathrm{T}=1.1 \cdot \mathrm{R}_{\mathrm{t}} \mathrm{C}_{\mathrm{t}}
$$

The pulse width is independent of the supply voltage.

The modification circuit (Fig. 1) not only uses a minimum of components, but also offers the significant bonus of two independent outputs, one of which is TTL compatible.

At start-up, the trigger input to pin 6 is LOW, $\mathrm{C}_{\mathrm{t}}$ is discharged and pin 2 is held LOW, momentarily. This condition trips the internal latch and forces output-1 (pin 3) HIGH, which charges $\mathrm{C}_{\mathrm{t}}$. Capacitor $\mathrm{C}_{\mathrm{t}}$ charges to a voltage near $\mathrm{V}_{\mathrm{cc}}$ and the circuit remains in this state.

With a positive-going input trigger greater than $+2 / 3 \mathrm{~V}_{\text {cc }}$, which is the threshold level for pin 6, output-1 is forced LOW. This action effectively grounds the point between $R_{t}$ and $R_{1}$. Capacitor $\mathrm{C}_{\mathrm{t}}$ then begins to discharge through $R_{t}$ towards ground, until the voltage reaches $1 / 3$ $\mathrm{V}_{\mathrm{cc}}$, which is the pin-2 threshold level. When this level is reached, output-1 switches HIGH again, quickly recharging $C_{t}$ to $V_{c e}$ via $D_{1}$ and $R_{1}$.

An advantage of this new configuration is the availability of the pin-7, open-collector output-2; it's uncommitted in this design. Pin 7 may be used as a 5-V TTL drive, regardless of the timer's supply voltage; it has a drive-current capability similar to that of pin 3 .

Both outputs may be used simultaneously, but if the best possible timing accuracy is desired only output-2 should be used. Loading of pin 3 can affect timing, particularly at low supply voltages.

If desired, $\mathrm{R}_{1}$ may be deleted with a slight sacrifice in timing accuracy. In the absence of $R_{1}$, pin 3 can't pull-up to $V_{c c}$, and therefore $C_{t}$
won't charge fully between output pulses.
Like the standard one shot, this mirror-image configuration requires an input trigger pulse whose width is less than that of the output pulse. The range of permissible $R_{t}$ values, however, is somewhat less than in the standard one shot because the bias current at pin 2 is five times larger than that of pin 6.

Walter G. Jung, Pleasantville Labs, 1946 Pleasantville Rd., Forest Hill, MD 21050.

Circle No. 312


A mirror-image one-shot configuration for the 555 timer provides a negative output pulse for positive input triggering-the reverse polarities of the standard arrangement.

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Circle 291

See more with the most sensitive RCA photomultiplier tube. Astronomers and spectroscopists, RCA has 2" dia., 11-stage PMTs with the characteristics you need. For critical photon counting applications, the GaAs C31034A has the highest known photocathode responsivity over its entire $200-930 \mathrm{~nm}$ spectral range, plus dark noise of only 100 cps max. at $-20^{\circ} \mathrm{C}$. You can also get lower dark count rates, to 12 cps max. With InGaAs types you get extended spectral ranges, to 1030 nm . These are just a few of RCA's wide line of PMTs for all types of low-light-level detection systems.


## Projection kinescopes for clear, bright large-screen TV.

From our 3 -in. dia. tubes for small meeting rooms to the big 7-in. dia. size for theaters and other larger viewing areas: RCA gives you one of the broadest lines to choose from. We offer electrostatic and magnetic focus types for Schmidt and dioptric systems, for color and monochrome. All with the power needed for bright, clear images. If you have unusual system requirements, we can help.

Circle 293

## How to make your CCTV camera so sensitive it can see by starlight.

 Apollo astronauts used cameras with RCA Silicon Intensifier Tubes to take TV pictures even within the moon's dark shadows. These tubes may be ideal for your new CCTV camera, too... whether it's for surveillance, astronomy or other uses where scene light levels can range down to $1 \times 10^{-4} \mathrm{fc}$. Our line, the industry's broadest, includes 16,25 and 40 mm types. 16 mm tubes are available with a new target that minimizes blooming effects. In fact, no matter how unusual your application, chances are we have the camera tube for it.

Circle 294

If electro optics can solve your problem, remember: EO and RCA are practically synonymous. No one offers a broader product spectrum. Or more success in meeting special needs. Call on us for design help or product information. RCA Electro Optics, Lancaster, PA 17604. Phone 717-397-7661.

## State diagrams for a 555 timer aid development of new applications

The NE-555 timer finds its way into many diverse applications. Unfortunately, the specifications usually supplied by its manufacturers don't tell the whole story of how the 555 operates. An analysis of the state diagrams of the 555 can greatly help the design of new applications.

Fig. 1 is a simplified block diagram of the 555 . The device has three inputs and two outputs. A negative-going voltage to the so-called trigger input, TR, can affect both outputs when the input drops to less than one-third of the supply voltage, $\mathrm{V}_{\text {cc }}$. A positive-going voltage labeled the threshold input, TH, can affect both outputs when the threshold rises above two-thirds of $\mathrm{V}_{\text {re }}$. A negative-going reset voltage, $R$, resets the outputs when the reset signal drops below 0.4 V .

The 555 contains internal memory, a set-reset flip-flop. Thus, its behavior must be described by sequential logic, which is best depicted with a state diagram that shows the possible transitions. Fig. 2 presents the state diagram of the timer's most general configuration, the one with its three inputs all available. It applies to use of the 555 in a circuit such as a one-shot multivibrator, and to any as yet undiscovered applications.

The simpler state diagram in Fig. 3 shows the result of wiring together the threshold (pin 6) and trigger (pin 2) inputs. The two inputs that


1. This simplified block diagram of the 555 shows the two signal inputs, two outputs and reset input that the timer provides.


# Ise introduces five new ways to make the competition turn green. 

Your competition probably already thinks they're using the perfect display in whatever it is they make. Let them keep thinking it. While you prove them wrong with a new Itron display.
They're designed to make the competition turn green. Which also happens to be the color of the segments.

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a little heart into it.
ifron


## FG209M2

ef $=10 \mathrm{~V}$
$\mathrm{ec}=\mathrm{eb}=40 \mathrm{Vp}-\mathrm{p}$
$\mathrm{ic}=10 \mathrm{mAp}$
ic $=10 \mathrm{mAp}-\mathrm{p}$
$i b=8 \mathrm{mAp}-\mathrm{p}$
Wd. 205 mm
Lg. 40 mm
Segment 9 mm

FG179F2

ef $=7 \mathrm{~V}$
$\mathrm{ec}=\mathrm{eb}=35 \mathrm{Vp}-\mathrm{p}$
$\mathrm{ic}=7 \mathrm{mAp}-\mathrm{p}$
$\mathrm{ib}=5.5 \mathrm{mAp}-\mathrm{p}$
Wd. 170 mm
Lg. 40 mm
Segment 9.5 mm
Digital Clock Display
Instruments \& Terminal Units Display
FG512A1
ef $=3.5 \mathrm{~V}$ $e c=e b=24 V p-p$ ic $=4 \mathrm{mAp}-\mathrm{p}$ $i b=3 \mathrm{mAp}-\mathrm{p}$ Wd. 100 mm Lg .40 mm Segment 12 mm


FG425A1
ef $=5.5 \mathrm{~V}$
$\mathrm{ec}=\mathrm{eb}=35 \mathrm{Vp}-\mathrm{p}$
ic $=8 \mathrm{mAp}-\mathrm{p}$
ic $=8 \mathrm{mAp}-\mathrm{p}$
$\mathrm{ib}=6.5 \mathrm{mAp}-\mathrm{p}$
$\mathrm{ib}=6.5 \mathrm{mAp}-\mathrm{p}$
Wd .140 mm
Wd. 140 mm
Lg. 59 mm
Lg. 59 mm
Segment 25 mm

Linear Analog Display


## FG120S1

ef $=55 \mathrm{~V}$
$\mathrm{ec}=\mathrm{eb}=35 \mathrm{Vp}-\mathrm{p}$
$\mathrm{ic}=4 \mathrm{mAp}-\mathrm{p}$
$\mathrm{ib}=0.2 \mathrm{mAp}-\mathrm{p}$
$W \mathrm{Wd}$.
Lg. 40 mm
Segment 8 mm

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now remain are designated $T \cdot T$ and $R$. This simplified state diagram applies to astable-multivibrator use.

In each state diagram, only one input is allowed to change at a given time to avoid ambiguities and race conditions.

All possible combinations of the input states with L for low and H for high are enclosed in circles-a double circle for HIGH outputs and single for LOW. The ordering of the input variables in the circles is TH, TR, and R. Transitions between states are shown by directed lines.

A HIGH input means that a particular input variable is above its previously described threshold level. LOW means that it is below. A HIGH output means pin 3 is near $\mathrm{V}_{\mathrm{cc}}$ and the discharge output at pin 7 is open circuited; LOW means near-zero volts at pin 3 and a low-resistance discharge path at pin 7.

Burt Sandberg, Engineer, Fermi National Accelerator Laboratory, P.O. Box 500, Batavia, IL 60510.

Circle No. 313

# Staircase generator divides frequencies by large factors 

An up/down staircase generator can divide square-wave frequencies by large factors. The circuit in the figure divides by 12 over the frequency range of 300 Hz to 14 kHz . It needs no reset pulse and is cascadable. By stepping both up and down, it doubles the dividing range; a staircase generator that steps only one way would need twice the dynamic-voltage range. This range doubling is especially valuable for circuits with low power-supply voltages.

Amplifier $A_{1}$ is pumped up via resistor $R_{2}$, when gating transistor $Q_{1}$ is on ; down via $R_{1}$, when $Q_{1}$ is off.

The down pulses are twice as large as the up pulses ( $\mathrm{R}_{1}=\mathrm{R}_{2} / 2$ ) ; thus with $\mathrm{Q}_{1}$ off, a down pulse via $R_{1}$ not only cancels an up pulse, but also provides a net down step. Amplifier $\mathrm{A}_{2}$ is a Schmitt-trigger circuit that samples the output voltage of $A_{1}$ to control $Q_{1}$ and also provides a square-wave output.

Because of the $1: 2$ relationship of $\mathrm{R}_{1}$ and $\mathrm{R}_{2}$, frequencies are divided by even values. Changing $\mathrm{C}_{1}$ or $\mathrm{C}_{2}$ changes the dividing factor. An increase in differentiating-capacitor $\mathrm{C}_{1}$ decreases the divisor; an increase in $\mathrm{C}_{2}$ increases it. Odd-

## IFD Winner of April 12, 1976

David Ludington, Engineer, General Electric Co., Aircraft Equipment Div., Utica, NY 13503. His idea "Digital Circuit Detects FrequencyModulated Signals" has been voted the Most Valuable of Issue Award.

Vote for the Best Idea in this issue by circling the number for your selection on the Reader Service Card at the back of this issue.


The staircase-generator frequency divider is pumped up via $R_{2}$ with $Q_{1}$ on. When the $A_{2}$ Schmitt trigger trips, it turns $Q_{1}$ off and the generator is stepped down via $R_{1}$, which has half the value of $R_{2}$.
integer division, if desired, is possible by upsetting the $1: 2$ relationship of $\mathrm{R}_{1}$ and $\mathrm{R}_{2}$.

Tom Frederiksen, Design Engineer, National Semiconductor, 2900 Semiconductor Dr., Santa Clara, CA 95051.

Circle No. 314


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For your copy of our reliability report or complete resistor network data, write: CTS OF BERNE, INC., 406 Parr Road, Berne, Indiana 46711. Phone (219) 589-3111.

## The Raytheon $\mathbf{8 9 0 0}$ family gains five new members

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Whereas: Raytheon brought you RAYASM, the powerful micro-assembler available on the NCSS computer network,
Now Therefore: Raytheon announces five additional 2900 family components, giving us a total of seven, To Wit:

2901A 4-bit Microprocessor Slice-the fastest, most powerful LSTTL microprocessor in the world. With its cycle-saving two-address architecture, the 2901's speed can't be touched.
2905 A 4-bit Bus Tranceiver- general purpose open collector bus interface device. Data to the 100 milliampere bus drivers is provided by a 4-bit register with a two-way multiplexer at its inputs. Data from the bus receiver may be held in the 4-bit receiver latch on its way to the three state receiver outputs.
2906 A 4-bit Bus Transceiver with parity -is equivalent to the 2905 but with the addition of an on-chip parity generator/checker.
2907 A 4-bit Bus Transceiver-similar to the 2906 with the two-way multiplexer at the input to the bus driver register elimi-
nated to allow the device to be packaged in the space saving 20-pin DIP.

2909 A Microprogram Sequencer-that can branch anywhere in memory, perform sub-routines, then return with up to four levels of sub-routine nesting. The device is a cascadable 4-bit slice which allows addressing of up to 4 K words of microprogram with three devices.
2918 A General Purpose 4-bit Registerwith two sets of outputs: TTL and three state. This useful combination can reduce your package count for those status, command, and instruction registers which must drive both your control logic and a data bus.
$93415{ }^{A} 1024 \times$ 1-bt fully Deocosed Random Access Memory-for your high-speed data and control stores.

Raytheon LSI is on the move. More 2900 family components soon to come include PROM's, sequencers, look-ahead carry generators, additional RAM's, and other goodies now in process.

For complete details, contact your local distributor or Raytheon Company, Semiconductor Division, Dept. 2900, 350 Ellis Street, Mountain View, CA 94042, (415) 968-9211.

# Am 2900 \& SUPPORTED <br> <br> \section*{SOLD \& SUPP HERE

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# Smart DVM stays on the job while its plug-in reference is calibrated 



Hewlett-Packard, 1501 Page Mill Rd., Palo Alto, CA 94304. (415) 493-1501. See text.

Glance at Hewlett-Packard's new system DVM and you'll surely ask: that's a voltmeter? Not only does the 3455 A look different, it acts like no other commercial DVM.

- No other DVM checks its dc and ohms calibration against a removable reference and then corrects itself for errors.
- No other DVM offers 5 or 6digit resolution at the push of a button. In the high-resolution mode the unit can measure at a fast rate of six readings each second.

To top it all off, the HP unitthough not exactly a mathematical wizard-can scale voltage or resistance measurements into readings in other units (like rpm) or in percentage error.

Key to the HP performance is the use of two $\mu \mathrm{Ps}$-one for computation and programming, the other for error correction. To calibrate the unit-for dc and ohms onlyyou unplug the reference and make just four adjustments. To keep the meter working during calibration, plug in a previously calibrated spare reference ( $\$ 150$ each). The meter will then correct itself against the reference.

Also included in the 3455A's $\$ 3200$ price is remote program-
ming via IEEE 488, the industry standard for instrument interfacing.

Basic capabilities of the fully guarded instrument include five ranges of dc and ac volts ( 0.1 to 1000 V ) and six ranges of resistance. Range selection allows automatic, manual or remote operation, with $50 \%$ overrange readings (excluding the $1000-\mathrm{V}$ range).

The 90-day dc accuracy of the HP unit is specified at $\pm(0.005 \%$ of reading +1 digit) on the $10-\mathrm{V}$ range, and this accuracy holds up even at the unit's top speed of 24 readings $/ \mathrm{s}$. At that speed, the normal-mode rejection (NMR) is better than 60 dB . Also specified are the $24-h$, six-month and oneyear accuracies.

To put the NMR into perspective, remember that in DMMs, the faster you go, the less noise immunity you usually get. Some DVMs do let you flip in a filter to cut noise down-but at the cost of speed.

On its ac function, the 3455A's true-rms converter reads out to 1 MHz with crest factors of $7: 1$ at full scale. If you don't want true rms, order option 01, take $\$ 200$ from the price, and you'll get average readings instead.

In remote operation, the 3455A's front panel tells you what range
and function you're on and gives the status of the IEEE bus. To speed up programming, just set the controls, and a bus instruction will send out the settings in the form of four 8-bit words. You can then use the same words as an input to reprogram the DVM to the previous settings.

Actually, the HP unit isn't the first smart DVM. Systron-Donner's Model 7115 took that honor last September at Wescon.

The 5-1/2-digit 7115 has a lot in common with the HP 3455A: it automatically calibrates itself and diagnoses itself for internal failures, and it also crunches numbers.

But whereas the HP box performs relatively simple arithmetic, the Systron unit-with an optional keyboard-solves third-order equations (for normalization or linearization), averages 10 or 100 readings, compares for high/low limits, and does even more.

The basic 7115 -which measures dc volts and dc/dc ratio only-sells for $\$ 2500$, compared with the HP's $\$ 3200$ price. But resistance, truerms and ac volts are all optional in the 7115, as is the IEEE interface.

Delivery of the HP 3455 A is stock to 30 days.
For Hewlett-Packard
CIRCLE NO. 301
For Systron-Donner
CIRCLE NO. 302

## INSTRUMENTATION

## 3-1/2-digit DMM sports low price tag


$B \& K$ Precision, 6460 W. Cortland Ave., Chicago, IL 60635. (312) 889-9087. \$170.

Model 283 3-1/2-digit multimeter uses 0.41 -in LED displays for high visibility. It measures de volts, ac volts, dc current, ac current and resistance. A special low-voltage circuit permits measuring resistance of transistor-shunted resistors. The unit has $100 \%$ overrange capability on four ranges. All readings have an automatically positioned decimal point. Basic de accuracy is $\pm 0.5 \%$ on the 1.000 , 10.00 and 100.0 ranges.

CIRCLE NO. 303

Logic analyzer grabs 8 channels of data


Digital Broadcast Systems, Brentwood Lane, P.O. Box 381, Madison, AL 35758. (205) 837-2183. \$1595 w probe; stock to 60 days.

Model $80-\mathrm{M}$ digital analyzer records and displays up to eight channels of digital signals on any conventional oscilloscope. The unit operates at speeds to 12 MHz . Variable threshold encompasses a wide range of logic families. Spike detection and word recognition are standard features. The memory function stores a 1024-bit record for each of the eight channels. When the record function is enabled, the $80-\mathrm{M}$ triggers on a preselected word, a trace, or an external trigger source.

Instrument recorder stores 7 channels


Lockheed Electronics, Plainfield, NJ 07061. (201) 757-1600. \$12,950.

This 7-track instrumentation recorder, STORE 7, is aimed at precision direct, FM or FM/direct recording. The unit provides seven channels for data in addition to one edge track for voice on a $1 / 2$ in. tape and any thickness of tape down to triple-play. Intended for fixed or mobile use, the STORE 7 operates from $115 / 230 \mathrm{~V}$ ac or from an 11-to- 32 V dc source. The unit accepts NAB reels of 8 -in. diameter and can continuously record for 12 h on all tracks simultaneously.

CIRCLE NO. 305

## Problem:

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## Solution:

Bud's 18 marketing groups have 97 sales engineers. Call us, and get the name of the man with the answers now.


## Need waveforms? Get three kinds for \$245



Krohn-Hite, Avon Industrial Park/ Bodwell St., Avon, MA 02322. (617) 580-1660. \$245; stock.

Model 5800 A function generator offers sine, square, and triangle waveforms over a range from 0.2 Hz to 2 MHz . Distortion is less than $0.3 \%$ throughout the audio range. The low-cost unit provides a frequency-control tuning range of 1000:1 from a single front-panel dial, without switching multiplier ranges. Convenient pushbuttons permit selection of three tuning multiplier ranges and the three waveforms. The $50-\Omega$ output is adjustable from 5 mV pk-pk to 15 V pk-pk (open circuit) by an infiniteresolution vernier and a $40-\mathrm{dB}$ pushbutton attenuator.

CIRCLE NO. 306

## Low-cost scope offers triggered sweep, more



Simpson Electric Co., 853 Dundee Ave., Elgin, IL 60120. (312) 6972260. $\$ 395$.

Among the many features of the 455 T general-purpose scope are external triggering, internal $(+$ and - ) and line triggering, and a TV sync separator. The sweep control has TVH, TVV, VITS and linesync positions for rapid display of key television signals and a range of 19 sweep time bases from 0.5 $\mu \mathrm{s} / \mathrm{cm}$ to $0.5 \mathrm{~s} / \mathrm{cm}$. Sensitivity is $10 \mathrm{mV} / \mathrm{cm}$, and bandwidth is 12 MHz with smooth rolloff for usability into the $27-\mathrm{MHz} \mathrm{CB}$ band.

## CHECKDIGIAL ICS FASTERTHAN ASCOPESAAER THAN AVOLTMETER

You're looking at the most convenient and efficient way developed to check digital IC's: CSC's Logic Monitor. It speeds digital design and testing by accurately and automatically displaying static and dynamic logic states of DTL, TTL, HTL and CMOS DIP IC's. All in a compact, self-contained $16-$ pin circuitpowered unit.

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## Wideband op amp settles in 85 ns to $0.1 \%$

M. S. Kennedy, Pickard Dr., Syracuse, NY 13211. (315) 455-7077. $\$ 125$ (1 to 9); stock.

A high-power wideband amplifier, the Model 850, has a slewing rate of $1000 \mathrm{~V} / \mu \mathrm{s}$. The op amp has a FET differential input and a bandwidth of 100 MHz . Its settling time to $1 \%$ is 50 ns and is 85 ns to $0.1 \%$. The amplifier can deliver 125 mA and operates over -55 to +125 C .

CIRCLE NO. 308

## System expander adds 48 single-ended channels

Analogic, Audubon Rd., Wakefield, MA 01880. (617) 246-0300. \$200 (100-up) ; stock.

The MP6848 multiplexer expander increases the range of the company's MP6812 and MP6912 dataacquisition systems from 16 to 64 channels. Two versions of the expander are available-a low power, single supply CMOS version for high-level logic applications that draws 0.33 W , has a throughput rate of 27.5 kHz and can optionally run from $\pm 15-\mathrm{V}$ power supplies. The TTL version has outputs that can drive 10 TTL loads each at a throughput rate of 35 kHz and consumes 1 W . These specs are valid for use with the MP6812. To add an additional 48 channels to the MP6912, the MP6848 can provide throughput rates up to 100 kHz and requires 1.1 W for the CMOS version. All three versions of the MP6848 maintain an overall system accuracy of $\pm 0.025 \%$ FSR at maximum throughput rates. Each expander contains a 48-channel multiplexer, an extension for the multiplexer addresscounter register, a multiplexer address decoder and a break-beforemake circuit that prevents shorting of a newly selected channel with the previously selected channel The MP6848 is housed in a $2 \times$ $4.6 \times 0.375-\mathrm{in}$. insulated steel case (the same as the MP6812) that fits $0.5-\mathrm{in}$. card spacing.

CIRCLE NO. 309

## Hybrid d/a converter cuts costs not corners

Hybrid Systems, 87 Second Ave., Burlington, MA 01803. (617) 2721522. See text; stock to 4 wks.

In addition to its cost of only $\$ 29$ in unit quantities, the DAC349 12 -bit d/a converter has fully calibrated output ranges of $\pm 5,+10$ and 0 to -10 V . The hybrid converter has a built-in reference, ladder and output op-amp. Its accuracy tempco is $30 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ and linearity drift is $15 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$. The converter has a settling time to $0.05 \%$ of $10 \mu \mathrm{~s}$ and the output can drive a 10 mA load. The unit requires only 300 mW from $\pm 15 \mathrm{~V}$ supplies and is housed in a 24-pin DIP. Linearity error of the converter from -25 to +85 V is $\pm 1 / 2$ LSB and from -55 to +125 C is $\pm 1$ LSB.

CIRCLE NO. 310

## Focus correction circuit operates to 800 kHz



Intronics, 57 Chapel St., Newton, MA 02158. (617) 332-7350. \$70 (1 to 9); stock.

The FC101 focus correction module for CRTs can accurately correct errors in tubes which use either magnetic or electrostatic focusing. It is a companion to the company's C200 series of geometric "pincushion" correction modules. The module has a 800 kHz fullpower output frequency with $1 \%$ full scale typical accuracy. A slew rate of $55 \mathrm{~V} / \mu \mathrm{s}$ and a settling time of $1 \mu \mathrm{~s}$ to $1 \%$ (for a $10-\mathrm{V}$ step input) go along with an operating range of -25 to +85 C and a total error vs temperature of $0.06 \%$ $\mathrm{FS} /{ }^{\circ} \mathrm{C}$. The module comes in a $1.5 \times 1.5 \times 0.4 \mathrm{in}$. epoxy package.

CIRCLE NO. 320

## Instrumentation amp drifts only $1 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$

Burr-Brown, Box 11400, Tucson, AZ 85734. From $\$ 13$ to $\$ 19.50$ (100-up); stock to 4 wks.

The 3626 series of hybrid instrumentation amplifiers starts at only $\$ 13$ each in 100 -unit quantities. The laser-trimmed amplifiers are housed in 14-pin DIPs. A unique feature of the 3626 family of amplifiers is its flat curve of input offset-voltage drift vs gain. Three versions are available: The permium 3626 CP has a drift of $<1 \mu \mathrm{~V} /$ ${ }^{\circ} \mathrm{C}$ at $\mathrm{G}=1000$ and $<2 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ at $\mathrm{G}=5$. The 3626 BP a drift of $<3 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ at $\mathrm{G}=1000$ and $<4 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ at $\mathrm{G}=5$. The lowestcost 3626 AP has a drift of $<6$ $\mu \mathrm{V} /{ }^{\circ} \mathrm{C}$ at $\mathrm{G}=1000$ and $<8 \mu \mathrm{~V} /$ ${ }^{\circ} \mathrm{C}$ at $\mathrm{G}=5$. Both the BP and CP versions have maximum gain nonlinearity of $0.01 \%$ at $G=5$. Com-mon-mode rejection, from dc to 60 Hz , with a $1-\mathrm{k} \Omega$ source unbalance is $>80 \mathrm{~dB}$ with gain $=10$ to 1000 . The AP version nonlinearity is $0.02 \%$ at $\mathrm{G}=5$ and the CMR is $>74 \mathrm{~dB}$ from $\mathrm{G}=10$ to 1000 . Other specs, common to all versions, include a gain range of 5 to 1000 ; differential and commonmode input impedances of $5 \times 10^{9}$ $\Omega$ in parallel with 3 pF ; an input bias current of $\pm 50 \mathrm{nA}$; an input noise of $2 \mu \mathrm{~V}$ pk-pk; and a settling time of 0.02 ms at $\mathrm{G}=5$ to 12 ms at $\mathrm{G}=1000$.

CIRCLE NO. 321

## 4-digit clock module just needs transformer

National Semiconductor, 2900 Semiconductor Dr., Santa Clara, CA 95051. (408) 732-5000. From \$10 (100-up); stock.

A miniaturized digital clock circuit, the MA1002 has $0.5-\mathrm{in}$. high LED digits. The clock modules combine an LSI clock IC, fourdigit LED display, a power supply (less transformer) and associated discrete components on a single PC board. Just a transformer and switches must be added. Timekeeping may be done from inputs of either 50 or 60 Hz , depending on the model selected and display formats of 12 or 24 hours are available.

CIRCLE NO. 322

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CIRCLE NUMBER 61



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Rockwell International


## MODULES \& SUBASSEMBLIES

## Modular clock oscillator handles 10 TTL loads

Vectron Laboratories, 121 Water St., Norwalk, CT 06854. (203) 853-4433. From \$35 (50-up); stock to 60 days.

The CO-238T DIP-compatible clock oscillator can drive 10 TTL loads. Any frequency in the 3 -to-$20-\mathrm{MHz}$ range is available. Each set frequency has a tuning adjustment for setting accuracy of $\pm 0.0001 \%$. The oscillators operate from 5 V dc and have a stability of better than $\pm 0.0025 \%$ over 0 to 70 C . A stability of $\pm 0.0003 \%$ is optional. The low-profile module plugs directly into a 14-pin socket and measures only $0.5 \times 0.8 \times$ 0.35 in.

CIRCLE NO. 323

## Full MIL range converter handles 12 -bit inputs

Teledyne Philbrick, Allied Dr. at Rte. 128, Dedham, MA 02026. (617) 329-1600. From $\$ 200$ (unit qty.); stock.

The 4058 series of 12 -bit d/a converters has a nonlinearity of $\pm 0.5 \mathrm{LSB}$ and an operating range of -55 to +125 C. Two versions of the converter are available: the 4058-83 comes with $100 \%$ screening to MIL-STD-883, Method 5004, Class B including internal and external visual, stabilization bake, constant acceleration, fine and gross leak, burn-in and temperature cycling. The 4058 is identical except that burn-in and temperature cycling are omitted. Model 4058's current settling time to within $\pm 0.01 \%$ of final value for a full scale step is guaranteed to be less than 200 ns , or less than $2.5 \mu \mathrm{~s}$ for voltage settling. Operation over -55 to +125 C is possible since the units have a $\pm 10$ $\mathrm{ppm} /{ }^{\circ} \mathrm{C}$ maximum full scale temperature coefficient. Zero stability is $\pm 1 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ for current outputs and only $\pm 5 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ for voltage ranges. Programmable full-scale outputs include $\pm 2 \mathrm{~mA},+4 \mathrm{~mA}$, $\pm 10 \mathrm{~V},-10 \mathrm{~V}, \pm 5 \mathrm{~V},-5 \mathrm{~V}$ and $\pm 2.5 \mathrm{~V}$. All units have internal references and are housed in 24-pin hermetically sealed DIPs.

## 32 character display contains all drive ckts



Micon Industries, 252 Oak St., Oakland, CA 94607. (415) 7636033. $\$ 5 /$ digit.

The Model 932 alphanumeric display module contains 32 bright, 9 -segment character positions, drive circuitry and storage register. The entire circuit is housed on an $8.2 \times 2 \times 0.5-\mathrm{in}$. PC board. All ASCII upper-case characters and symbols can be displayed. The display has a viewing angle of 90 degrees vertical or horizontal, and may be covered with a red glareproof window. Code options include ASCII, Baudot and EBCDIC. The board terminates in a 16 -pin connector. Backspace and character blanking are optional functions available for operator editing or correction.

CIRCLE NO. 325

## S/d converter boasts $\pm 30 \mathrm{~min}$ accuracy



ILC Data Devices Corp., Airport International Plaza, Bohemia, NY 11716. (516) 567-5600. \$195; stock.

The SDC-620, a 10 -bit tracking synchro-to-digital converter, is accurate to $\pm 30 \mathrm{~min}$. The converter is transformer isolated and comes in 11.8 or $90 \mathrm{~V}, 400-\mathrm{Hz}$ and $90-\mathrm{V}$, $50-$ to $-400-\mathrm{Hz}$ versions. It uses a ratiometric type II servo technique and is thus insensitive to voltage and frequency variations. Tracking within rated accuracy is possible at speeds of up to 100 rps for $400-$ Hz units and 25 rps for $50-\mathrm{to}-400-$ Hz units. The $\mathrm{K}_{\mathrm{a}}=40,000$ for $400-\mathrm{Hz}$ models and 2000 for 50 -to-$400-\mathrm{Hz}$ units.

CIRCLE NO. 326

## IN WIRE-WRAPPING



OK MACHINE \& TOOL CORPORATION
3455 Conner St., Bronx, N.Y. 10475 (212) 994-6600/Telex 125091

Checkmated by high pushbutton switch costs? $\qquad$
These three new Centralab Pushbutton Switch products are real money savers, yet they offer the highquality features of all Centralab switches. Contact your Centralab Distributor for details. Ask for a copy of Centralab's New Pushbutton Switch Catalog, Series No. 301.


New Status Indicator Button. Adds visual display to non-lighted switches. The button, with a unique fluorescent display, uses reflected ambient light to indicate switch status. 6 display colors available.

Low Cost Lighted Switch uses T-13/4 wedge base lamp. Many lens and color options.


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How versatile? The overwhelming response to our Undiscovered Genius contest proves the true versatility of Repco's modular RF links. Prizes were awarded for these winning ideas:

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What can you do with RF links? While building a remote-control shark may be a little too zany for you, consider some of the innovative and commercially proven applications now utilizing Repco modular RF links:

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Repco's modular RF links are designed for voice, lowspeed digital, or tone operation. Frequencies available are 25-50 Mhz, 72-76 Mhz, 132-174 Mhz, and 450470 Mhz ranges ( $66-88 \mathrm{Mhz}$ for overseas usage). All units are built to stringent FCC \& EIA specifications.
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(305) 843-8484 TWX 810-850-0120

## DATA PROCESSING

## 3-megabit storage unit weighs only 5.5 lb



Digital Laboratories, 377 Putnam Ave., Cambridge, MA 02139. (617) 876-6220. \$75 (single qty.) ; 2 wks.

So small that it can be routinely carried in a briefcase or field service kit, the Model ACT-1200 stores 3 Mbits, yet weighs only 5.5 lb and has dimensions of $12.1 \times 7.7 \times 3.3$ in. Designed primarily for field testing and program loading with small computers, the ACT- 1200 plugs into any computer or terminal's standard serial asynchronous port. The unit can record and playback at standard rates up to 1200 baud using RS-232C interfacing. Most minicomputers can use existing paper tape oriented software. For example, the ACT-1200 can be used to record source programs following a PUNCH command and reload the data following a READ command. Similarly, the output of an assembler or compiler may be recorded for future loading via standard object loader programs.

CIRCLE NO. 327

## Static IC memory fits into DEC PDP-8 chassis

WE Computer Extension Systems, 17311 El Camino Real, Suite 176 , Houston, TX 77058. (713) 4888830. WE-VM8E4: \$400;E8: $\$ 650$ (unit qty).

The WE-VM8E4 or E8 semiconductor memory boards plug directly into Digital Equipment Corp.'s PDP-8 Omnibus chassis. The boards contain either $4-\mathrm{k}$ or $8-\mathrm{k}$ words of memory. They are completely software compatible with the standard PDP-8 operating systems. The $1024 \times 1$ NMOS static RAM design uses minimal power. The $8-\mathrm{k}$ memory requires 2.2 A at +5 V dc. A jumper matrix allows mem-ory-address assignment in $4-\mathrm{k}$ increments. An unconditional warranty is offered for one full year.

CIRCLE NO. 328

## Lower cost computer emulates another

Datum, 1363 S. State College Blvd., Anaheim, CA 92806. (714) 5336333. See text; 60 days.

The EI/16 Nucleus Package line of computer systems emulates General Automation's SPC-16 at a lower cost. The Nucleus Packages are made up of four system configurations and a variety of peripheral controllers in a single enclosure. They are designed to use RTOS, DBOS and data management software. The EI/16-RT Nucleus Package is designed for real-time, discbased applications. The hardware includes the processor with $32-\mathrm{k}$ of 16-bit memory, a controller for up to four cartridge discs, and serial I/O for a teletypewriter or CRT. This package is priced at $\$ 13,950$. The EI/16-DB Nucleus Package is an expanded version of the RT system with a controller for a line printer, card reader and card punch. The printer controller permits speeds of 300,600 and 1200 line $/ \mathrm{min}$. Its price is $\$ 16,350$. The EI/16-D3 Nucleus Package consists of a computer with $32-\mathrm{k}$ words of main memory and controllers for fixed or removable discs, line printer, card reader, card punch and up to four CRT terminals. Line speeds are program-selectable from 100 baud to 9600 baud. The price is $\$ 18,350$. The EI/16-D4 Nucleus Package is an augmented version of the D3 with $48-\mathrm{k}$ of core, controllers for 25.6-Mbyte disc, line printer, card reader, card punch and four CRTs. The price is $\$ 24$,750.

CIRCLE NO. 329

## Microcomputer connects to $\mu$ P-based systems

Interdata, Oceanport, NJ 07757. (201) 229-4040. \$868 (100-up).

The model $5 / 16$, a single board computer, is compatible with the I/O busses of the 8080 and 6800 $\mu$ Ps. Another I/O bus connects to Interdata peripherals and is compatible with the manufacturer's processors and software. The board includes a full 16 -bit processor with 16 general-purpose registers, 114 instructions, and $8-k$ bytes of NMOS dynamic random access memory.

CIRCLE NO. 330

## SLIM-MOX

 NOW VICTOREEN QUALITY COSTS LESS THAN A DOLLAR.Victoreen announces SLIM-MOX, our new, thick-film, flat substrate resistor. Compact in design, it carries with it all the quality and dependable performance you have come to expect from Victoreen.

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SHELLER-GLOBE CORPORATION


## DATA PROCESSING

## 21-slot card cage plugs into Data General CPUs



Data Engineering Associates, 6330 Alder St., Houston, TX 77081. (713) 665-8860. \$1350; stock to 90 days.

A card-cage assembly, supplied in kit form, connects to Data General's Nova or Eclipse computers. The basic kit consists of a card cage, a cable and cable-terminator assembly, wrapped-wire boards, a power supply, and wire-list aids. The card cage has 21 slots with motherboard and set of connectors. The chassis requires $18.75 \times 19 \mathrm{in}$. of rack space. The two boards are for customer-designed wrappedwire circuits. Each board will accommodate 56 ICs and has space for mounting special-purpose modules. The power supply provides 5 V at 12 A . The computerized wirelist aids include wire-list forms and a program on paper tape.

CIRCLE NO. 331

## Desktop calculators are easy to use

Victor Comptometer Corp., 3900 N. Rockwell St., Chicago, IL 60618. (312) 539-8200. $\$ 99.50$ (type 204), $\$ 159$ (type 210).
The Medalist 204 and 210 desk calculators are said to be "human engineered for efficient operation." The units have large plus and minus keys that perform repeat addition and subtraction. A single key does percent, add-on and discount calculations. The 210 has two memories, change sign, square root and a five-position specialfunction switch; the 204 has one accumulating memory. The 12 -column display on the 204 and the 14 column display on the 210 provide punctuation, a memory light and negative sign, error and overflow indication.

CIRCLE NO. 332

## 4800-baud modem has adaptive equalizer

AMBAC Industries, Inc., TeleDynamics Div., 525 Virginia Dr., Ft. Washington, PA 19034. (215) 643-3900. \$3250.

A Bell-208A compatible modem features an all-digital adaptive equalizer that minimizes errors produced on unconditioned telephone lines. The Model 7208A provides full-duplex or simplex transmission for point-to-point or multipoint polling applications at a 4800 bit/s rate. The 7208A has built-in on-line and off-line test capabilities with front-panel controls and diagnostic lights. The internal equalizer employs a mean-square algorithm which continually optimizes parameters without restarting the remote transmitter. No operator attention is required. The modem also has common-carrier and terminal loopback provisions.

CIRCLE NO. 333

## Unit controls data from tape transports

Kennedy Co., 540 W. Woodbury Rd., Altadena, CA 91001. (213) 798-0953. \$1650. 45-60 days.

The Model 1629 half-duplex interface unit allows control between RS-232-C compatible terminals and buffered tape transports without requiring processing by a mainframe computer. The unit allows off-line key-to-tape or tape-to-tape data transfer via hardwires or telephone lines at rates from 110 to 19,200 baud.

The interface unit converts parallel data bytes from the tape to the correct RS-232-C serial standards. Thumbwheel switches accommodate either $5,6,7$ or 8 bits per character. To match lines another thumbwheel switch selects rates from 110 to 19.2 K bits $/ \mathrm{s}$ in nine steps. ANSI compatible formatting, read-after-write and automatic error-tape correction are standard features. LED display clusters provide complete indication concerning communication status, errors, and operation conditions. The unit measures $17.13 \times$ $3.41 \times 11.94 \mathrm{in}$. Power requirements are either 115 V or 220 V at 48 to 62 Hz .

CIRCLE NO. 334

Mil-spec tape transport accesses data in 20 sec


Qantex, 200 Terminal Dr., Plainview, NY 11803. (516) 681-8600. $\$ 2175$ (unit qty with electronics)

The Model 700, a militarized digital cartridge tape transport, accesses data on the tape in an average of 20 seconds. It uses the 3 M DC 300 A data cartridge as the storage medium. Up to 23 M bits of data may be stored on one cartridge. The Model 700 records and reads at 6000 byte/s and has a 30 in. bidirectional tape speed. The transport measures $7 \times 8.25 \times$ 3.125 in., and is optionally available with the electronics on separate PC cards.

CIRCLE NO. 335

## Floppy-disc unit fits on HP 9830A calculator

Infotek Systems, 733 E. Edna Pl., Covina, CA 91723. (213) 966-7431. $\$ 3895$; stock.

A floppy disc, Model FD-30, fits on top of the Hewlett-Packard 9830A scientific calculator. The disc unit emulates the 9830 cassette system so that no changes in existing software are required. The cassette-control commands and syntax of the 9830 are recognized by the FD-30, and all such programs operate without modification. The unit stores data on one floppy disc that would require five to seven cassettes. The discs provide $305-$ kbytes of user area. The FD-30 has a height of 4 -in., and fits in place between the calculator and printer.

CIRCLE NO. 336


Visit Booth 228 - IEEE Los Angeles Show - Wescon/76 CIRCLE NUMBER 69

New ROTARY SWITCHES feature adjustable stops \& are only $.606^{\prime \prime}$ in diameter. Molded-in terminals prevent contamination. For PC or hand wired use. Available in 1 through 4 poles with $36^{\circ}$ detent; waterproof types too! 10,000 operations @ rated . 5 amps, 125 VAC load.
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A reliable, accurate easy to install, "one shot' thermal limiter.

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$58^{\circ}$ to $242^{\circ} \mathrm{C}$. $\left(136^{\circ}\right.$ to $468^{\circ} \mathrm{F}$.)
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Used where installation or space restrictions rule out MICROTEMP ${ }^{\circledR}$ thermal cutoffs.

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## (ID



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## Analog switch arrays come in four versions

Motorola, 3501 Ed Bluestein Blvd., Austin, TX 78721. (512) 928-2600. From $\$ 0.70$ (100-up); stock.

An eight-channel analog multiplexer, a dual four-channel unit, a triple, two-channel unit and a quad bilateral switch have been added to Motorola's CMOS switch line. They are the MC14051, an 8PST switch; the MC14052, a 4PDT switch; the MC14053, a DP3T switch; and the MC14066, that has four SPST switches. All are controlled with a binary input. The switches have an ON/OFF output voltage ratio of typically 65 dB , a cross-talk between switches of 80 dB at 1 MHz for the MC 14051 , 2 and 3 and 50 dB at 8 MHz for the MC14066 and can operate at frequencies of up to 65 MHz when powered by a supply of 10 V . The total operating supply range is 3 to 18 V . The MC14051, 2 and 3 are available in 16 -pin DIPs, and the MC14066 in a 14-pin DIP. Two temperature ranges are available: -55 to +125 C ("AL" suffix, ceramic package) and -40 to +85 C ("CL" and "CP" suffix in ceramic and plastic).

CIRCLE NO. 339

## Analog-signal delay line has 32 different taps



Reticon, 910 Benicia Ave., Sunnyvale, CA 94086. (408) 738-4266. Under $\$ 10$ (OEM qty.); stock.

The TAD-32 tapped analog delay line has 32 equally spaced taps on an n-channel bucket-brigade circuit. Each tap is individually buffered, permitting variable loading of the taps. The output is a full-wave "box car" waveform, thus permitting direct summation of the desired tap weights without additional filtering. Tap-delay is linearly variable with sampling rates from 1 kHz to 5 MHz , with a dynamic range in excess of 80 dB. Several devices may be cascaded directly without interface electronics for applications requiring a large number of taps. The TAD-32 comes in a 40 -pin DIP.



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## New! LONG DELAYS

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OUR COMPLETE PRODUCT LINE CAN BE FOUND IN ELECTRONIC DESIGN'S GOLD BOOK. CIRCLE NUMBER 75

## INTEGRATED CIRCUITS

## 8-bit d/a converter has MIL-883 processing

Hybrid Systems, Crosby Dr., Bedford, MA 01730. (617) 275-1570. See text; stock to 4 wks.

An economically priced 8-bit d/a converter, the DAC334-8-M/B (or C), is available fully processed to MIL-STD-883 level B or C. It is designed for operation over the temperature range of -55 to +125 C. The unit is pin-for-pin equivalent to the MC1408/1508 and costs $\$ 19.95$ for the level B version and $\$ 20.95$ for level C. It comes packaged in a $16-\mathrm{pin}$ hermetically sealed DIP. The current output settles in 300 ns and has a range of 0 to 2 or $\pm 1 \mathrm{~mA}$ depending upon pin interconnections and input coding.

CIRCLE NO. 341

## 16-k ROM claims access times of under 450 ns

American Microsystems, 3800 Homestead Rd., Santa Clara, CA 95051. (408) 246-0330. For plastic packages: $\$ 14$ (1000-up), plus mask charge of $\$ 1000 ; 8 \mathrm{wks}$.

The S6831 (Series/A/B/C) 16k MOS ROMs are organized as $2048 \times 8$. They are made using a silicon-gate, depletion-load process, have access times of only 450 ns and consume only 150 mW . The S6831A is pin compatible with the Intel 2316A and 8316A. The B version is compatible with the Intel 2316B, 8316B and the Motorola 68317 , and the $C$ version with the Electronic Arrays 4600. The 450ns access time is maintained, even with capacitive loads of up to 130 pF on the output. The S 6831 series units are available in plastic or ceramic packages.

CIRCLE NO. 342

## Filler

We learn from Tom Nawalinski of Hewlett-Packard that when Noah commanded his animals to multiply, two snakes refused on the grounds that they were adders. Bringing to bear his engineering ingenuity, Noah placed the snakes on a table he constructed from a small tree. "Even adders can multiply," he said, "on a log table."

## Digital display driver provides 4-1/2 digits

EFICS, 85 X 38041 Grenoble Cedex, France. 68 francs (500-up); stock.

Capable of driving $3-1 / 2$ or $4-1 / 2$ liquid crystal displays, the CDD contains its own up/down counter, latches, decoder and multiplexer. The p-channel MOS IC is housed in a 28 -pin DIP and provides a maximum count of 20,000 . A 10 to 30 V supply that delivers a typical current of 3 mA is required to power the circuit. The CDD can count at rates from 0 to 80 kHz and has an operating temperature range of -20 to +85 C .

CIRCLE NO. 343

## Digital watch circuits have up to six functions



Intersil, 10900 N. Tantau Ave., Cupertino, CA 95014. (408) 9965000. From \$8 (100-up); stock.

The ICM7214, a five-function time-keeping circuit, has alphanumeric capability. It provides readout of hours, minutes, day, date and seconds. It also features a perpetual calendar, which must be reset only once every four years. The ICM7214A is a sixfunction version which also provides a readout of the month. Both interface directly with existing nine-segment LED displays. The circuits operate with two spst switches. Antibounce circuitry on the switch inputs can handle up to 31 ms of switch bounce. The only external components required in addition to the display and the power source are a $32,768 \mathrm{~Hz}$ crystal, a trimming capacitor and two switches. The circuits require a current of $4 \mu \mathrm{~A}$, with the display off and have an output current drive of 6 mA per segment at a $25 \%$ duty cycle.

CIRCLE NO. 344

## Gas-discharge driver ckt adjusts for each load



Signetics, 811 E. Arques Ave., Sunnyvale, CA 94086. (408) 7397700. 100-up pricing: \$2.75 (5849); $\$ 2.50$ (585-9); stock.

The NE584/585 gas-discharge display drivers have completely integrated, closed-loop current feedback systems for self-adjusting to accommodate changing load conditions. The NE584 cathode driver is current programmable and can drive up to nine segments. The NE585 anode driver can drive up to nine digits. Current capability is 5 mA per segment for the signal driver and 35 mA per digit for the digit driver. No capacitors or resistors are needed.

CIRCLE NO. 345

## Peripheral support ckts are TTL compatible

Fairchild Camera and Instrument Corp., 464 Ellis Street, Mountain View, CA 94042. (415) 962-3816. See text.

Peripheral drivers, dual line drivers and quad line receivers have been added to the company's line of TTL compatible support circuits. The 75470 peripheral drivers have an output current capability of 300 mA at 55 V and are pin-for-pin replacements for the 75450 and 75460 series units. Ac switching speeds are 45 ns . The 75112 dual line drivers can deliver 18 to 30 mA from each half and have no line transient effects during powerup or power down when inhibited. The quad line receiver, 75154, can meet the requirements of EIA standard RS-232-C. Normal operation is from a $5-\mathrm{V}$ supply but a built-in option permits operation from a $12-\mathrm{V}$ supply. Prices for the circuits start at $\$ 0.81$ for the 75470 family, $\$ 1.88$ for the 75112 family and $\$ 2.48$ for the 75154 series.

CIRCLE NO. 346

## S/h amplifier acquires signals in only $4 \mu \mathrm{~s}$

Teledyne Philbrick, Allied Dr. at Rte. 128, Dedham, MA 02026. (617) 329-1600. \$14.75 (lge. qty.); stock.

The Model 4856 IC sample-andhold amplifier operates in the inverting or noninverting mode with or without gain. Acquisition time,
to $0.1 \%$ of full scale, is typically $4 \mu \mathrm{~s}$ with an external $0.001-\mu \mathrm{F}$ capacitor. Other specifications include a $50-\mathrm{ns}$ aperture plus aperture delay time, a $5-\mathrm{V} / \mu$ s slew rate and $0.005 \%$ nonlinearity. The 4856 has short circuit protection on the output and its offset voltages can be externally trimmed. It is housed in a 14-pin DIP.

CIRCLE NO. 347


Thomas Electronics, Inc., is currently producing a wide range of high resolution tubes for: Optical Character Recognition, Photo Recording, Hard Copy Printout and Photo Typesetting applications. Included in this range are optical quality nonbrowning glass and fibre optics strips faceplate CRT's in all sizes. All of these tubes can be supplied with special screen types for improved performance, in addition to the standard phosphor screens.
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## POWER SOURCES

Economy modules deliver to $\pm 300 \mathrm{~mA}$


Semiconductor Circuits, 306 River St., Haverhill, MA 01830. (617) 373-9104. About $\$ 50$; stock-2 whs.

Four new models join the econ-omy-priced APS Series of encapsulated dual-output power supplies. The new entries deliver short-circuit-protected output currents of $\pm 300 \mathrm{~mA}$ at either $\pm 12 \mathrm{~V}$ or $\pm 15$ V dc, depending on model. Housed in a compact $2.5 \times 3.5 \times 1.56-\mathrm{in}$. plug-in module and producing up to 9 W of output, the new power sources boast MTBFs in excess of $150,000 \mathrm{~h}$ and are said to operate up to 18 C cooler than competitive supplies.

CIRCLE NO. 348
Bench supply satisfies wide range of needs


Kepco, 131-38 Sanford Ave., Flushing, NY 11352. (212) 461-7000. $\$ 475$; stock.

Model MPS 620M provides the bench experimentalist with the three commonly used voltage/current combinations for IC circuits. The $5-V$ output is adjustable from 0 to 6 V , is rated 0 to 5 A at any voltage setting, and is operable without derating to 50 C . The $\pm 20-\mathrm{V}$ outputs are varied with a 10 -turn control in a tracking mode and provide a 1-A output through the entire range (either dual 20 V or a single 0 to 40 V output).

CIRCLE NO. 349

50-W supply works at 75\% efficiency


Tele-Dynamics, 525 Virginia $D r$., Fort Washington, PA 19034. (215) 643-3900. \$240.

Model TD101 5-V 10-A regulated power supply produces a dc output of 50 W , yet measures only $6-1 / 2$ $\times 4-1 / 2 \times 1-1 / 2 \mathrm{in}$. and weighs 1.7 lb . Efficiency is $75 \%$ at full load. The TD101 operates with an ac input of 90 to $130 \mathrm{~V}, 47$ to 450 Hz , and features remote sensing, current limiting, inherent shortcircuit protection, and a built-in crowbar that trips at 6 V . Regulation is $\pm 0.1 \%$, line and load.

CIRCLE NO. 350

## Multiple-output supplies come in 24 models



Modular Power Inc., 4818 Ronson St., San Diego, CA 92111. (714) 279-1641. From \$135; stock-4 wks.

A new line of power supplies is aimed at logic and linear applications. The TR Series consists of 24 models, available in single, dual and triple output, in three different case sizes with outputs ranging from 5 to 28 V dc and 2 to 50 A . Features include: dual input terminals for $115 / 230-\mathrm{V}$-ac operation, cooling fan on all models, full protection against short-circuit and overload conditions. Typical regulation is $0.01 \%$ line and $0.02 \%$ load. Ripple is $0.01 \% \mathrm{rms}$.

CIRCLE NO. 351

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- Switch-on reset
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Box 110-F Painesville, OH 44077 (216) 354-2101 TWX: 810-425-2250

Flexible cable assembly handles high rf power


Omni Spectra, Inc., 21 Continental Blvd., Merrimack, NH 03054. (603) 424-4111. $\$ 150$ to $\$ 300$ (small qty); 2 to 3 wks.

The OSSC 300 series of flexible cable/connector assemblies carry up to 1000 W of CW rf power. Assemblies in this series have low losses, a low VSWR and low rf leakage and feature rugged construction. Cable assemblies are constructed of $1 / 2-\mathrm{in}$. OD high-power, flexible coaxial cable and the manufacturer's type SC coaxial connectors. Available connectors include right angle and straight plugs, and bulkhead and straight jacks. Power ratings, derated for maximum safe operation, are $1000 \mathrm{~W}, 3.5 \mathrm{GHz}, 25$ C at sea level, to $485 \mathrm{~W}, 9 \mathrm{GHz}$, 110 C at $60,000 \mathrm{ft}$. Cable retention force is 300 lb . Connectors are manufactured of stainless steel.

CIRCLE NO. 352

## Machine cuts glass envelopes without wax

DKA, Inc., 14014 Northwest Passage, Marina Del Rey, CA 90291. (213) 823-5172.

The Waxless Cutter cuts glass cases for diodes, capacitors and reed switches without the wax potting and cleaning required by existing technology. The result is a cleaner, simpler operation that allows significant savings in labor, production and maintenance costs over current methods. The machine cuts both glass tube and cane, and eliminates hand operations associated with the wax potting process. The operator loads the hopper of the Waxless Cutter with loose glass tubes and removes the cut cases from the collector box.

CIRCLE NO. 353

Coax connector needs no special tools or solder
Bunker Ramo Corp., RF Div., 33 E. Franklin St., Danbury, CT 06810. (203) 743-9272.

The $83-58 \mathrm{FCP}$ coax connector installs onto RG-58 A/U cable without solder, special tools or adapters. To attach the connector, the user strips the coaxial cable and pushes the connector parts onto the center conductor and braid. The contact is squeezed at the tip to secure the center conductor. If you wish to reuse the connector, the contact can be soldered. No braid soldering, combing of cable braid, special crimping tools or adapters are needed.

CIRCLE NO. 354

## Wire terminals fit wide range of mounting bolts

Thomas \& Betts, 36 Butler St., Elizabeth, NJ 07207. (201) 3544321. 16BA22: $\$ 5.70$ per 100 (500up), 10BC14: $\$ 7.80$ per 100 (500$u p)$.

Two wire terminals accommodate several different sizes of mounting studs. One, the 16BA22, handles \#22 through \#16 AWG wire and stud sizes \#6, 8 and 10 . The other, 10BC14, handles \#14 though \#10 AWG wire, and stud sizes 8,10 and $1 / 4$. The two types replace up to nine terminals formerly needed to cover the same job requirements. They are uninsulated, and have a locking fork design that permits them to be slipped on the stud or bolt with the ease of a forked terminal, with virtually the same security as a ring terminal.

CIRCLE NO. 355

## Paper tape winder features gentle handling

Continuous Expression Processor, Inc., 12 Main St., Natick, MA 01760. (617) 235-2980. \$29.95.

A lightweight battery-powered paper-tape winder will not mar or otherwise tear tapes that may have snagged. The design features left or right-hand operation and lifetime lubrication. Tape threading and roll removal are simple. The winder uses two " C " type batteries and can wind tape rolls of up to $1-1 / 4 \mathrm{in}$. wide and 5 in . dia.

## L-shaped socket enables vertical IC mounting



Electronic Molding Corp., 96 Mill St., Woonsocket, RI 02895. (401) 769-3800. 88¢ ( 100 up ).
A socket is designed to save space in mounting LEDs and ICs on PC boards. The "L" socket requires one-third the mounting space of conventional LED sockets, and half the space of IC sockets. The socket comes with 12,14 and 16 pins. DIP and LED forming tools are also available for the complete range of $0.300-\mathrm{in}$. centerline devices. Gold-plated, fourfinger, contacts offer a firmer grip for more dependable lead connections, and closed-end terminals prevent solder wicking. The phenolic material will not soften during soldering operations.

CIRCLE NO. 431

## Wrapped-wire board holds 22-pin DIPs

EECO, 1441 E. Chestnut Ave., Santa Ana, CA 92701. (714) 8356000. $\$ 100$ (unit qty).

The model H-2964-01 wrappedwire board holds up to 24 dual-inline 22 -pin ICs. The unit is designed for 4 -k RAMs. Four different voltages can be accommodated; $\mathrm{V}_{\mathrm{DD}}, \mathrm{V}_{\mathrm{CC}}, \mathrm{V}_{\mathrm{Ss}}$, and $\mathrm{V}_{\mathrm{BB}}$. Posts are available for power connects and tabs are available for ground connections. The board comes with one tantalum and 12 ceramic capacitors. Extra capacitors may be added if needed. The board itself is made of flame-resistant glass epoxy with dimensions of $5.06 \times 4.14 \mathrm{in}$.

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## APPLICATIONS

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| O CYCLE <br> u TIME | $3.0 \mu \mathrm{~s}$ | $0.9 \mu \mathrm{~s}$ |
| a a TIME | $1.0 \mu \mathrm{~s}$ | $0.32 \mu \mathrm{~s}$ |
| POWER SUPPLY | +5Vonly | $+5 \mathrm{~V},-12 \mathrm{~V}$ |
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## PACKAGING \& MATERIALS

## PC board-edge connector handles ten amperes

Berg Electronics, Route 83S, New Cumberland, PA 17070. (717) 9386711.

A PC board connector called the Power-Edge connector has a cur-rent-carrying rating of 10 A per contact. Designed for use with single-sided 0.062 in.-thick boards, the connector is available in 5,9 and 15 positions with 0.156 in . spacing between contacts. The contacts take 14 to 16 AWG wire with up to $0.15-\mathrm{in}$. OD insulation. The current-carrying ability centers on the dual-beam design of the contact. The primary beam provides low insertion force with simultaneous positive pad-wiping action. When mated with the PC board, the secondary beam provides additional force and mechanical retention of the connector. Contacts are crimped to prestripped wire using a hand tool or bench press. They then snap into position in the connector housing.

CIRCLE NO. 357

## Inexpensive enclosures made from extrusions



E-Tronics, 16774 Schoenborn St., Sepulveda, CA 91343. (213) 8927279. See text.

Electronic housings assemble from aluminum extrusions that mate by tongue and groove. The extrusions come in 165 stock sizes. Assembled package sizes range from $2 \times 2 \times 4 \mathrm{in}$. to $4 \times 8 \times$ 12 in . The extrusions are slotted on the inside with 0.062 in . guides to accommodate circuit boards 3.775 in . wide. Plain or transistor mounting surfaces are finned for convection cooling and have a temperature coefficient of $0.08 \mathrm{C} / \mathrm{W} /$ ft . Prices start at $\$ 2.75$ for a complete enclosure.

## Protective cases have polyfoam cushioning



Melmat, Inc., 2909 Oregon Ct., Torrance, CA 90503. (213) 3203350. $6 \times 4 \times 2$ in., $\$ 2(1-49)$; stock.

Kudl-Pak cases are made from either injection-molded polypropylene or thermoformed ABS, and come filled with convoluted polyurethane foam. The convolutions interlock when the case is closed, so various shaped parts are held securely in place, thus eliminating the custom interiors previously needed to hold products. A range of case sizes is offered, each capable of holding dozens of shapes. All units are reusable.

CIRCLE NO. 359

## Fiber-optic cable used for data transmission



Du Pont Co., Plastic Products and Resins Dept., Wilmington, DE 19898. (302) 774 -2291. $\$ 5 / m$ (50 m-up).

A fiber optics cable called PFX-S is intended for data transmission use. It is made with a pure-silica core and plastic jacket. The cable features attenuation of $80 \mathrm{~dB} / \mathrm{km}$ at a transmitted-light wavelength of 800 nm . The large numerical aperture of light acceptance of the cable allows noncritical cable-tocable splices and connection to LEDs with little transmission loss. The cable material is said to be extremely tough and will resist mechanical stresses better than other glass or silica fiber optics. PFX-S can be bent around a $3-\mathrm{mm}$ radius without breaking. The PFX-S fiber optic material also resists radiation better than glass bundles.

CIRCLE NO. 360


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CIRCLE NUMBER 84

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- VSK 320, 330 \& 340-3A series. Epoxy package, axial leads. 475 mV ('F). 150A surge. 30 mA ( i ) at $\mathrm{T}_{\mathrm{L}}=100^{\circ} \mathrm{C}$.
- VSK 520, 530 \& 540-5A series. Epoxy package, axial leads. 450 mV ( ${ }^{\text {VF }}$ ). 250A surge. $75 \mathrm{~mA}\left({ }^{\prime} \mathrm{R}\right)$ at $\mathrm{T}_{\mathrm{L}}=100^{\circ} \mathrm{C}$.
- VSK 1520, 1530 \& 1540-15A series in DO-4 metal stud cases. 600 mV ( ${ }^{\mathrm{V} F}$ ). 300A surge. $75 \mathrm{~mA}\left({ }^{\mathrm{i}} \mathrm{R}\right)$ at $\mathrm{T}_{\mathrm{c}}=100^{\circ} \mathrm{C}$.
- VSK 3020T, 3030T \& 3040T-30A series. Center-tapped, common cathode, 15A per leg in TO-3 package. 630 mV ( ${ }^{V}$ F). 300A surge. $75 \mathrm{~mA}\left({ }^{\prime} R\right)$ at $\mathrm{T}_{\mathrm{c}}=100^{\circ}$.
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## COMPONENTS

Joy stick drives pots in slew-control system


Beckman, 2500 Harbor Blvd., Fullerton, CA 92634. (714) 871-4848.

A joy-stick control assembly combines two single-turn potentiometers in a 1-1/2-in. square, cast-aluminum case. A self-centering control lever, moving through a square-pyramid envelope, meshes via gears with a two-gang and a single-gang potentiometer. One potentiometer may be at maximum travel at 29 degrees from center; the other can move from maximum at one end through zero to maximum at the other end. Originally engineered to slew a moving-map display of ground terrain in aircraft, the joy-stick control allowed horizontal, vertical or combined angular positioning. It is especially suited for application in electronicgame equipment.

CIRCLE NO. 361
Cooling fans deliver 16 cfm of air


Micronel U.S., Box 271, Hudson, MA 01749. (617) 568-8542. \$22.80 ( 100 up ) ; stock to 4 wks .

Cooling fans feature multivane impellers driven by high-efficiency dc or ac motors. Compact mechanical packages with carefully designed aerodynamic characteristics give good pressure/flow performance. Spotlite Model V571 has an output of 16 cfm in free air. Case size is $2.45 \mathrm{in}^{2}{ }^{2}$ Weight is 7 oz .

## Transient protectors provide ns limiting

MCG Electronics, Inc., 279 Skidmore Rd., Deer Park, NY 11729. (516) 586-5125. $\$ 8.53$ to $\$ 13.53$ ( 100 up ) ; 2 wks.

The SLP protectors were expressly designed to protect signal/ data/telephone lines from transient overvoltages caused by lightning, heavy machinery, elevator motors, generators, etc. The unit interfaces between the signal line and the sensitive circuit (typically containing semiconductors) to provide a blend of nanosecond voltage limiting and brute-force protection. The SLP's recover automatically to standby when the need for protection has passed. Output clamping levels range from $\pm 5$ to $\pm 200 \mathrm{~V}$ (selectable) for input voltage levels exceeding $\pm 20 \mathrm{kV}$ (pulse, $10 \mu \mathrm{~s}$ ). CIRCLE NO. 363

## Small potentiometers feature metric sizes



Allen-Bradley, 1201 S. Second St., Milwaukee, WI 52304. (414) 6712000.

A completely metric panel potentiometer, the Mini-Metric Type M, has a 7 -mm-long bushing and 3 mm -diameter shaft. This $10-\mathrm{mm}$ cube component is available with a switch and two resistor sections. A plastic case, shaft and bushing provide electrical isolation. The Type M is particularly suitable for handheld and other portable equipment. Resistances are standard IEC val-ues- $100,220,470 \Omega$ to $1.0 \mathrm{M} \Omega$. The resistance element is conductive plastic. Independent linearity has a maximum deviation of $5 \%$ in the range of $100 \Omega$ to $100 \mathrm{k} \Omega$. Power ratings are 0.1 W on the panel section and 0.05 W on the rear section at $40 . \mathrm{C}$. Temperature range is -25 to 100 C .

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## COMPONENTS

Switches hold wires with push-in technique


Stackpole Components Co., P.O. Box 14466, Raleigh, NC 27610. (919) 828-6201.

Push-in-lead slide and rocker switches (DPDT or SPDT) meet UL and CSA ratings of up to $6 \mathrm{~A}, 125 \mathrm{~V}$ ac. Lock clips inside a nylon base hold wires firmly in place. No soldering, special connectors, machines or fixtures are required. Wires are simply inserted into openings in the bases of the switches. Once inserted, they cannot be removed by hand.

CIRCLE NO. 365
Ironless-rotor motors come with 72-tooth tachs


North American Philips Controls Corp., Cheshire Industrial Park, Cheshire, CT 06410. (203) 2720301. $\$ 17$ : $24 \mathrm{~V}, \$ 15.55$ : 12 V ( 500 up ) ; prototypes from stock.

Ironless-rotor motors are provided with 72 -tooth tachogenerators in 24 and $12-\mathrm{V}$-dc models. Power input is 4.35 and 3.6 W , respectively. The design eliminates the need for pre-established poles resulting in minimal cogging, smooth operation and low noise levels. A modified double-shaft version of the motor with a $26-\mathrm{mm}$ rear-shaft extension is also available for use on encoders or similar dual-drive applications.

CIRCLE NO. 366

## Film-Teflon capacitors come in two styles



Custom Electronics, Inc., Browne St., Oneonta, NY 13820. (607) 4323880.

New precision, film-Teflon capacitors are ultra-stable, low-loss, low-temperature-coefficient capacitors, suited for the high-insula-tion-resistance requirements at temperatures to 200 C (specials to 250 C). An extendedfoil style offers low-dissipation factors and high circulating-current capabilities, which favor its selection for use in high-power resonant and pulse circuits. The metalized film style is self-healing in the event of voltage breakdown. Its more-uniform temperature coefficient and low volume per microfarad makes it ideal for resistor coefficient matching and for use in making small capacitors. Typical specifications include a capacitance range from 0.0005 to $4.0 \mu \mathrm{~F}$, a working voltage from 50 to 600 WV dc and an operating temperature range from -55 to 200 C (specials to 250 C ) and a standard tolerance of $10 \%$ and precision tolerances to $1 / 4 \%$.

CIRCLE NO. 367

## Clutch/brake units mount easily

Inertia Dynamics, Inc., 12 Bridge St., Collinsville, CT 06022. (203) 693-0203. From \$12 (OEM qty).

These compact clutch brake packages provide easy mounting. The units simply slide on driven or driver shaft and are secured by set screws. The field magnet is restrained from rotating with a pin or torque-arm through an antirotation tab. Sizes range from 0.903 -to- $2.600-\mathrm{in}$. OD and provide torques from 2.5 to 80 lb -in. Units are available with bores from $3 / 16$ to $5 / 8$ in., four standard dc voltages $-90,28,24,12 \mathrm{~V}$-and two coupling styles for in-line or parallelshaft applications.


C\&K's new Model 1101 subminiature SPDT slide switch has a proven internal mechanism be-
 cause it's the same one we've been using for years to build our famous toggle switches. We've retained all the toggle terminal and sealing options and added a spring-loaded teflon actuator. It's a powerful 6 amp (at 120 VAC) slide switch offering 40,000 actuations at full load. Because the actuator is only .200 " high, the 1101 slide switch maintains a low profile but deep downit's a proud little son-of-a-toggle. C\&K Components, Inc. 103 Morse St., Watertown, Mass. 02172, U.S.A. Tel: (617) 926-0800 Telex: 92-2546 TWX: 710-327-0460.

| Model | Output VDC | Output Current Rating-Amps $45^{\circ} \mathrm{C} \quad 50^{\circ} \mathrm{C} \quad 60^{\circ} \mathrm{C} \quad 70^{\circ} \mathrm{C}$ |  |  |  | $\begin{gathered} \text { Price } \\ \text { Qty-100 } \\ \hline \end{gathered}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| OEM | $\pm 12$ | 0.5 | 0.44 | 0.33 | 0.20 | \$33 |
| AD-1-152 | $\pm 15$ | 0.6 | 0.53 | 0.40 | 0.24 | \$33 |
| OEM | $\pm 12$ | 1.0 | 0.86 | 0.66 | 0.40 | \$39 |
| AD-2-152 | $\pm 15$ | 1.2 | 1.06 | 0.77 | 0.48 | \$39 |
| OEM | $\pm 12$ | 3.0 | 2.65 | 1.96 | 1.20 | \$68 |
| AD-3-152 | $\pm 15$ | 3.6 | 3.18 | 2.36 | 1.44 | \$68 |

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European Div., Elmwood Sensors Ltd., North Shields. Tyne and Wear, England.

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If you design, specify and use beryllium copper parts, the Braun catalog can put a lot of information at your fingertips. Braun's 25 years of experience in the art of producing beryllium copper products helped to fill this book with useful engineering data about precision stampings, contacts and springs in addition to intricate formed parts such as finger stock, rings and clips. Technical data includes: charts and tables on physical properties; information on tolerances; specification
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## CIRCLE NUMBER 94

## IMC FAN FACTE

## IMC introduces fan for cooling sandwiched areas

IMC's new, high performance FULMAR fan features maximum efficiency for cooling high power density enclosures and rows of printed circuit board arrays. Unique design of this fan allows for convenience of "Side by Side" mountings for maximum airflow distribution and stable motor performance under low voltage (brown out conditions). Fulmar's low noise level is a natural for computer room use. Circle the "Bingo" for details! For immediate service please call Fred Taylor, Sales Manager at (603) 332-5300 or write.


MICROWAVES \& LASERS
Resistive splitters span dc to 18 GHz


Weinschel Engineering, Gaithersburg, MD 20760. (301) 948-3434. $\$ 425$ to $\$ 500$; 30 days.

Resistive power splitters-Models 1870 A through 1872 A -can be used as part of a leveling loop or as a reference in a ratio system. The $50-\Omega$ splitters span the range of dc to 18 GHz , and all have Type N input connectors. Outputs consist of Type N (1870A), APC-7 precision $7-\mathrm{mm}$ (1871A) and WPC7 precision $7-\mathrm{mm}$ (1872A). VSWR is 1.07 maximum to 2 GHz and 1.15 maximum to 18 GHz . Maximum input power is 1 W average, 1 kW peak. Insertion loss (between input and either output) is 6 dB nominal, and outputs track to within 0.15 dB to $2 \mathrm{GHz}, 0.20 \mathrm{~dB}$ to 8 GHz and 0.25 dB to 18 GHz .

CIRCLE NO. 369

## Digital signals shift phase

Merrimac Industries, 41 Fairfield Pl., West Caldwell, NJ 07006. (201) 228-3890. $\$ 595 ; 45$ to 60 days.
Digitally controlled phase shifters can be used for applications through the hf-to-uhf frequency ranges. The PSD series of shifters varies the phases of rf signals in binary increments up to $360^{\circ}$. They are driven directly from TTL logic controls. Over-all phaseshifting accuracy is less than the least significant bit. Incremental accuracies of better than $\pm 0.2^{\circ}$ per bit are typical. A key member of this new family, the Model PSD-$84-53-1$, is an 8 -bit unit that uses p-i-n diodes to switch precise lengths of cable in or out. The model has a $30-$ to $-76-\mathrm{MHz}$ frequency range, $+10-\mathrm{dBm}$ maximum input and $4-\mu$ s switching time.

CIRCLE NO. 370

## 12.4-GHz mixer aims at high i-f uses



Vari-L Company, 3883 Monaco Pkwy., Denver, CO 80207. (303) 321-1511. \$350; stock-2 wks.

DBM- 1200 is a $12.4-\mathrm{GHz}$ doubly balanced mixer suitable for high-i-f-frequency applications. The mixer comes in a miniature, SMAconnector package and offers mutually overlapping $R$, L and i-f port frequency coverage. The $R$ and $L$ frequency coverage is from 0.5 to 12.4 GHz with the i-f port covering de to 3.5 GHz . Typical conversion loss is 6.0 dB .

CIRCLE NO. 371

## Lightweight filters seek space-comm use



Frequency Engineering Laboratories, Farmingdale, NJ 07727. (201) 938-9221. $\$ 875$ to $\$ 945$ (5-9); 10 wks .

Bandpass filters directed toward space-communication uses operate in the 4 and $6-\mathrm{GHz}$ bands. The units offer insertion losses of 0.2 $d B$ over an equal-ripple bandwidth of 500 MHz , and pk-pk gain slope within the bandwidth of less than 1.0 dB . Designed to operate in $10^{-6}-\mathrm{mm}$ vacuum, the $5.75-\mathrm{oz} 4$ GHz unit (P/N 30EM1-826027) handles 50 W of incident power inband, with SMA input and CPR 229 output. The $6-\mathrm{GHz}$ version (P/N 50EM1826027) weighs 6.4 oz, and uses UG441/U input and SMA output.

CIRCLE NO. 372

# Iike peas in a pod... 


all ball bearings may look alike, until you see what Chassis-Trak has done with them. Chassis-Trak ball bearing slides offer you . . . spacers that eliminate mounting problems by allowing no interference in the center of the slide . . . spacers that provide plenty of room to clear the mounting hardware . . . and standard mounting holes calibrated at $1^{\prime \prime}$ intervals that make special hole patterns unnecessary. Mounting couldn't be easier and the holes make the slides lighter in physical weight without sacrificing load carrying capacity. Chassis-Trak ball bearing slides move with ease because of smooth ball bearing action. This new construction allows faster delivery. Order Chassis-Trak for the best in Single Extention, Full Extention or Full Extention (over and under) ball bearing slides.

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DISCRETE SEMICONDUCTORS

## GaAs FETs operate at microwave frequency



Plessey Semiconductor, 1674 McGaw Ave., Irvine, CA 92714. (714) 540-9945. \$116; GAT-3, \$167; GAT4 (1-24); Stock.

Two new high performance, 1$\mu$ m-gate gallium-arsenide FETs provide high gain and low noise at microwave frequencies. The Model GAT 4 operates up to 12 GHz with a noise figure of 1.8 dB and associated gain of 12 dB at 4 GHz . At 10 GHz , it has a maximum available gain (MAG) of 8.5 dB and a noise figure of 3.5 dB . Model GAT 5, designed for higher performance requirements, operates to 18 GHz with a noise figure of 2.5 dB and associated gain of 8 dB at 6 GHz . At 10 GHz it has a MAG of 10.3 dB and a noise figure of 3.5 dB . The units are available as chips or in microstrip packages.

CIRCLE NO. 373

## Power series includes 200-V, 20-A rectifier

TRW Power Semiconductors, 14520 Aviation Blvd., Lawndale, CA 90260. (213) 679-4561. $\$ 6.40$ to $\$ 13.20$ (100-999); stock.

A series of high-efficiency, 20A power rectifiers includes the only $200-\mathrm{V}, 35-\mathrm{ns}$ units currently available on the market, according to TRW. In addition to fast re-verse-recovery time and high peakinverse voltage, the series offers very low forward-voltage drop0.9 V at 10 A . Five models in the series, 1 N 5812 to 1 N 5816 , range from 50 to 150 V in $25-\mathrm{V}$ increments. The $200-\mathrm{V}$ model is designated SVD200-20.

CIRCLE NO. 374

## High-voltage transistors dissipate 2 W

Motorola Semiconductor Products, Inc., P.O. Box 20294, Phoenix, AZ 85036. (602) 244-4556. \$0.58 to $\$ 0.63$ (100-999); stock.

Capable of dissipating 2 W at an ambient temperature of 25 C , these npn high-voltage Duowatt transistors in TO-202AC packages are designed for medium-power driver applications. Newly introduced, the transistors designated as 2N6591, 2N6592 and 2N6593 have minimum $B V_{\text {ceo }}$ of 150,200 and 250 V , respectively.

CIRCLE NO. 375

## Transient suppressors handle 15,000-W peaks



General Semiconductor Industries, Inc., 2001 W. Tenth Pl., P.O. Box 3078, Tempe, AZ 85281. \$2.36 (100 up) ; stock.

TransZorb $\mu$ P-series (Models MPTE 5 through 45) transientvoltage suppressors, when placed across $\mathrm{V}_{\mathrm{ss}}, \mathrm{V}_{\mathrm{dd}}$ or $\mathrm{V}_{\mathrm{cc}}$ lines and transient-prone data lines, protect bipolar and MOS microprocessors from damaging voltage transients. The devices have subnanosecond response times. Nine different models are available in the voltage range of 5 to 45 V . The clamping factor-the ratio of clamping voltage to breakdown voltage-is only 1.33 at full-rated power and 1.20 at $50 \%$ rated power. This assures a relatively small change in clamping voltage with increases in peak pulse current. Maximum ratings for the $\mu$ P-series suppressors are $1500-\mathrm{W}$ peak-pulse power dissipation for 1 ms and $15,000 \mathrm{~W}$ at 10 $\mu \mathrm{s}$; steady power dissipation is 5 W at a duty cycle of $005 \%$

## LED linear arrays pack 100 chips into 2 in.



Digital Components Corp., 1111 E. Elizabeth Ave., Linden, NJ 07036. (201) 925-0200. \$46: red, \$51: green, $\$ 56$ : yellow and $I R$ (1-10); stock to 4 wks.

DCC's 100-LED linear arrays feature very dense packaging of individual LED chips and internal wiring that minimizes the number of external connections needed for multiplex operations. Outstanding features of the arrays include 100 LEDs on 0.020 -in. centers; need for only 20 external connections; a low profile, only $0.125-\mathrm{in}$. thick; a low current requirement; compatibility with most standard ICs; end-to-end stackability to form arrays of $4,6,8,10$ or more inches long; availability in red, green, yellow and infrared. These arrays are ideally suited for bar and dialgraphs.

CIRCLE NO. 377

## Bipolar transistor features low noise

Hewlett Packard, 1501 Page Mill Rd., Palo Alto, CA 94304. (415) 493-1501. \$150 (1-9); stock.

Designers of ultra-low-noise amplifiers will find this new microwave bipolar transistor suitable for 1 to 4 GHz . Designated the Model HXTR-6101, the new transistor has a specified noise figure of 2.7 dB , typical, at 4 GHz and 1.5 dB , typical, at 1.5 GHz . Typical gain is 9.0 dB at 4 GHz and 15 dB at 1.5 GHz . The transistor is a fully ion-implanted device with submicron emitter widths. A local oxidation process reduces pad parasitics to improve gain, and a selfaligning mask technique increases yield. The chip is packaged in the HPAC-70GT-a metal-ceramic hermetic package that meets the environmental requirements of MIL-S-19500 and the test requirements of MIL-STD-756/883.

CIRCLE NO. 378

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# Application Notes 

## Optoelectronics

A 192-page "Optoelectronics Manual" includes seven sections on practical user-oriented information relating to emitters, detectors and couplers . . . theory, system design, reliability measurements, circuits, symbols and terms and specifications. Send $\$ 3$ plus applicable tax to GE Semiconductor, Electronics Park, Bldg. 7-49, Syracuse, NY 13201.

INQUIRE DIRECT

## Gate turn-off SCR

An eight-page application note (No. U-71) explains the operation of the GTO-SCR and describes its advantages over previously available devices. Detailed applications are presented for dc circuit breakers, multivibrators, inverters and and ignition systems. Unitrode Corp., Watertown, MA

CIRCLE NO. 379

## Magnetrons

Principles of operation, design considerations and applications of voltage tunable magnetrons are given in a 20-page guide. VTM Microwaves, Boulder, CO

CIRCLE NO. 380

## Time-code data

"Time-Code Data Indexing Handbook" presents the theory on time-code data indexing on different recording mediums such as magnetic tape, video tape, camera film and oscillographs. Datametrics, Wilmington, MA

CIRCLE NO. 381

## Flashtube guide

"Everything You Always Wanted to Know About Flashtubes" is a 14-page guide to the characteristics, specifications and selection of Xenon gas-discharge tubes. Siemens Corp., Special Components Div., Iselin, NJ

CIRCLE NO. 382

# Bulletin Board 

Signetics has dropped the 1-24 quantity price from $\$ 72$ to $\$ 26.50$ for its 2650 general-purpose, 8 -bit n-channel $\mu \mathbf{P}$. The 100-999 price is $\$ 21.50$.

CIRCLE NO. 383

RLC Electronics has announced major price reductions in its coaxial switch lines.

CIRCLE NO. 384

Spectral Dynamics has reduced the price of its Model SD360 FFT analyzer by $\$ 9000$ per unit.

CIRCLE NO. 385

Texas Instruments has introduced four prerecorded software libraries for the SR-52 programmable calculator.

CIRCLE NO. 386

Intel has introduced a program that allows low-cost MCS-40 microcomputer components to be used in hostile temperature environments as well as in the normal commercial temp range.

CIRCLE NO. 387

Rapidata has announced the release of PROBE graphics, a new service for preparing graphs on standard terminals, user plotters or plotting terminals and Radidata's plotters.

CIRCLE NO. 388

Hewlett-Packard has introduced the HP-27-a 6-oz personal calculator that combines all of the most frequently used scientific, statistical and financial functions. It is priced at $\$ 200$.

CIRCLE NO. 389

Prices on Analog Devices' 16channel AD7506 and eight-channel differential AD7507 monolithic CMOS analog multiplexers have been reduced by up to $46 \%$.

CIRCLE NO. 390

## Vendors Report

Annual and interim reports can provide much more than financial position information. They often include the first public disclosure of new products, new techniques and new directions of our vendors and customers. Further, they often contain superb analyses of segments of industry that a company serves.

Selected companies with recent reports are listed here with their main electronic products or services. For a copy, circle the indicated number.

CalComp. Printers and plotter/ printers, disc memory systems, direct-access memory systems and automated tape library.

CIRCLE NO. 391
Astro Systems. Switches, synchro converters, synchro readouts, shaft encoders, laboratory standards, indicators, automatic drafting systems and plug-compatible memories.

CIRCLE NO. 392
National Semiconductor. Semiconductors, integrated circuits, consumer products, point-of-sale equipment, stand-alone terminals, polling subsystems and scanners.

CIRCLE NO. 393
VSI Corp. Specialized metal products.

CIRCLE NO. 394
Pentron Industries. Plastics, coils and electronic measuring instruments.

CIRCLE NO. 395
Beckman. Analytical and electronic instruments, precision elec-tro-products and chemical products.

CIRCLE NO. 396
Computer Automation. Computers.
CIRCLE NO. 397
Tandy Corp. Consumer electronics, passive components, vacuum tubes and transistors.

CIRCLE NO. 398
Tektronix. Test and measuring equipment and information displays.

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## New Literature



## Digital instruments

Features of digital instruments for any type of physical measurement are highlighted in a fourpage brochure. Doric Scientific, San Diego, CA

CIRCLE NO. 411

## Power supplies

Specifications and prices on more than 800 power converters are included in a 26 -page catalog. Tecnetics, Boulder, CO

CIRCLE NO. 412

## Epoxy systems

A four-page catalog presents one-part epoxy systems in a format designed for the buyer's convenience in selecting "mix" with the desired properties from up to 16 different systems. Emerson \& Cuming, Canton, MA

CIRCLE NO. 413

## Transient voltage protectors

A general description of miniature voltage surge arrestors, surge life data, environmental ratings, dimensions and specifications are included in a four-page catalog. C.P. Clare \& Co., Chicago, IL

CIRCLE NO. 414

## Rf, microwave amplifiers

Solid-state rf amplifiers from class-A medium power to class-A/B-or-C high power ( $1000-\mathrm{W}$ ) are described in a short-form catalog. The catalog covers more than 500 rf and microwave amps from 2 MHz to 4.2 GHz . Microwave Power Devices, Plainview, NY

CIRCLE NO. 415

## Electronic packaging

A 128-page handbook describes electronic packaging and interconnect components. Stanford Applied Engineering, Santa Clara, CA

CIRCLE NO. 416

## Aluminum knobs

Many different styles and sizes of aluminum knobs are shown in a six-page brochure. Kurz-Kasch, Wilmington, OH

CIRCLE NO. 417

## Linear, conversion products

Drawings, specifications and ordering information on linear and conversion products are contained in a 12 -page brochure. Precision Monolithics, Santa Clara, CA

CIRCLE NO. 418

## Crystal oscillators

Crystal and clock oscillators ranging from 1 Hz through 400 MHz with stabilities from $\pm 0.01 \%$ to $1 \times 10^{-10}$ are covered in a brochure. Vectron Laboratories, Norwalk, CT

CIRCLE NO. 419

## Miniature attenuators

Low-cost $11 / 16$-in. rectangular attenuators are described in a brochure. TRW/IRC Potentiometers, St. Petersburg, FL

CIRCLE NO. 420

## Disconnect hardware

Disconnect terminal boards, terminated lead assemblies, disconnect hardware and fuse clips and holders are featured in an eight-page catalog. Keystone Electronics, New York, NY

CIRCLE NO. 421

## Switches

Miniature, subminiature and microminiature switches are highlighted in a 44-page catalog. C\&K Components, Watertown, MA

CIRCLE NO. 422

## Resistive components

Summary specifications and descriptions on resistive components are presented in a 20 -page catalog. Bourns Trimpot Products Div., Riverside, CA

CIRCLE NO. 423

## Sweeper system

Specifications, features and options for the Model $4310 \mathrm{~A} / \mathrm{K}$ multiband sweeper system are presented in a 12-page catalog. Application data include block diagrams. Weinschel Engineering, Gaithersburg, MD

CIRCLE NO. 424

## Integrated circuits

Specifications for $a / d$ and $d / a$ converters, instrumentation amps , $\mathrm{s} / \mathrm{h}$ amps, multiplexers, references, FET-input and bipolar op amps and multiplier/dividersall in IC form-are given in a 12-page brochure. Analog Devices, Norwood, MA

CIRCLE NO. 425

## Fasteners

A revised 24-page catalog that gives complete engineering, application and purchasing information for hundreds of fasteners, is available. Nylock Div., Shelton, CT

CIRCLE NO. 426

## $\mu$ C data book

A 260-page reference covers more than 100 microcomputers, $100 \mu \mathrm{Ps}$ and 1000 related memory and I/O devices manufactured throughout the world. Detailed specifications and operational characteristics of chips, cards and systems appear in 15 easy-to-compare technical sections. A one-year prepaid subscription (two complete editions) costs $\$ 54.50$ U.S. and $\$ 56.80$ international. D.A.T.A., Inc., 32 Lincoln Ave., Orange, NJ 07050.

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CIRCLE NUMBER 103

# menofiom 

". . . well-organized, extremely well written . . . highly recommended for practicing engineers..." IEEE Transactions

## DIGITAL SIGNAL ANALYSIS

## Samuel D. Stearns_

This is an ideal master handbook on today's signal processing procedures and systems, containing recent advances, new design material, and a comparison between continual and digital systems that's extremely helpful to newcomers to the field. Featuring a foreword by Richard Hamming, the book contains a review of linear analysis; sample-data systems; analog-to-digital and digital-toanalog conversion; the discrete Fourier transform and the fast Fourier transform algorithm; spectral computations; non-recursive and recursive digital systems; computer simulation of continual systems; analog and digital filter designs, and more. 288 pages

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Jack Steingraber
You'll find this a fast and efficient guide to the fundamentals of FORTRAN. The main objective here, in fact, is to provide an abbreviated means of learning the language and of becoming familiar with language manuals - so it's just right for the engineer whose main interest may lie elsewhere. You'll find lots of sample problems along with complete solutions, although you're constantly encouraged to try various routines and analyze your own success or failure. The step-by-step format is perfect for self-study. 96 pages

HAYDEN BDOK COMPANY, INC. 50 Essex Street, Rochelle Park, New Jersey 07662


## Electronic Design

## RENO, NEVADA

"DIGITAL FILTERS AND SIGNAL PROCESSING: THEORY, DESIGN, AND APPLICATIONS"

AN INTENSIVE SHORT COURSE September 23, 24, 25, 1976
Holiday Inn, 6th Street, Reno, Nevada

## Summary

This intensive short course is intended to provide an introduction or update on the subjects of digital filters and digital signal processing. Included are the following topics: sampled data theory, filter structure, stability concepts, discrete fast Fourier transforms, correlation methods, binary number systems, A/D and D/A conversion, analysis and synthesis techniques and practical design examples. Although the course presumes a Bachelors' degree, non degree engineers should find the course understandable and useful.
Lecturers: Dr. Sanjit Mitra (UC Davis); Dr. Stan White (Rockwell International); Dr. John Kleppe (Scientific Engineering Systems)
Cost: \$295 which includes course fee and a summary booklet of class notes. How to enroll: Advanced enrollment required. Send payment/purchase order and request for hotel reservations or request for additional information to: Scientific Engineering Systems, Inc., 55 North Edison Way, P.O. Box 1171, Reno, Nevada 89504
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CIRCLE NUMBER 106

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| TIP 29B | RCA 29B | TIP 31B | RCA 31B | TIP 41B | RCA 41B | DTS 413 RCA 413 |
| TIP 29 C | RCA 29 C | TIP 31C | RCA 31 C | TIP 41C | RCA 41C | DTS 423 RCA 423 DTS 431 RCA 431 |
| TIP 30 | RCA 30 | TIP 32 | RCA 32 | TIP 42 | RCA 42 |  |
| TIP 30A | RCA 30A | TIP 32A | RCA 32A | TIP 42A | RCA 42A |  |
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[^2]:    National Semiconductor
    2900 Semiconductor Drive, Santa Clara, CA 95051

[^3]:    Dave Evans, Applications Engineer, HPA Div., 650 Page Mill Rd., Palo Alto, CA 94304.

[^4]:    Aron Levy, Power-Supply Group Leader, The Bendix Corporation, Guidance Systems Div., Teterboro, NJ 07608.

[^5]:    J. A. Tabb and M. Roginsky, Advanced Development Staff, Lockheed-Georgia Co., Marietta, GA 30063.

