Unusual electrostatic cooling uses kilovolts and microamperes to reduce surface temperatures hundreds of degrees in seconds. A high-voltage ionic discharge
from stationary probes removes the heat without fans or blowers. Applications? Possibly to cool semiconductors and high-power laser optical elements. Page 22.


Hewlett-Packard believes that design engineers doing digital IC work should have a scope that's intended for digital IC work. You shouldn't have to face a makeshift adaptation of a scope that doesn't really "have what it takes," or an "overkill" unit with lots of capabilities you don't need and can't afford.

That's why we've developed two new additions to the HP 180 Scope System.

The first is a new mainframe, the 180 C/D. Like our 180 A/AR, it's compatible with all 180-System plug-ins to 100 MHz real time, plus the easy-to-use 1 GHz sampling plug-in and the 12.4 GHz sampling/TDR plug-in.

In addition, the $180 \mathrm{C} / \mathrm{D}$ incorporates new circuitry advances that allow optimized CRT performance. A 15 kV accelerating potential now gives you greater brightness and higher writing speed. Thus, photographic writing speed is several times faster-1500 div/ $\mu \mathrm{s}$ (1 $\mathrm{cm} / \mathrm{div}$ ) with the standard P31 phosphor. These advanced capabilities make the 180 C/D fully compatible in single-shot response with the 180 System's fastest plug-ins.

The second bright idea is a new $75-\mathrm{MHz}$ vertical plug-in-the 1808A. This bandwidth capability makes the 1808A ideal for testing digital circuits using $T^{2} \mathrm{~L}$ or ECL, yet saves you a healthy amount in comparison with a 100MHz system.

## Two bright new ideas

As you'd expect, the 1808A gives you the usual selection capabilities found in scopes of this class-deflection sensitivity ( $5 \mathrm{mV} / \mathrm{div}$ to $5 \mathrm{~V} / \mathrm{div}$ ) and polarity. In addition, however, the 1808A also gives you a new first selectable input impedance ( $50 \Omega$ and $1 \mathrm{M} \Omega$ )!

And despite all these advantages, both 180 C/D and the 1808A are priced very competitively. The 180C is under \$1,000; the rack-version 180D is slightly higher. The 1808A is $\$ 880-\$ 800$ without probes. Compatible time bases range from $\$ 450$ to $\$ 700$.

For further information on either of these new additions to the "more-to-come" HP 180 System, contact your local HP field engineer. Write Hewlett-Packard, Palo Alto, California 94304, for data sheets. In Europe: 1217 Meyrin-Geneva, Switzerland.

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[^0]
## Reliability is a single-sided frame, a ball and a cricket room.



Our Type 45 rotary stepping switch is made to be forgotten. We build them to work hard, fast and long without constant fiddling or adjusting. They've got to be able to work in heat or cold, take bumps and grinds and still click-click along with close-spaced consecutive operations.

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Ball bearing anchor for good measure The armature assembly has to be securely fastened to keep it from wiggling up and down, or everything goes out of whack. So we choose a big stainless steel pin and secure it with wide bearings to the armature yoke. To make sure this pin never slips out of the yoke, we drill a hole in both ends. Then we force a steel ball bearing into these holes. This expands the walls of the pin into and against the walls of the armature and the whole assembly is anchored for life. We're the only ones that do it this way. So we're the only ones that offer a lifetime fit.
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Typical Characteristics TRIGAC I
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Model Number Input Signal

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Resolution
Accuracy Logic Levels

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## A cheer for keeping you readers awake

Congratulations to your editorial staff for attempting to awaken the notoriously absent-minded engineer to public consciousness. In your June 10 issue editorial ("All You Train Drivers Better Start Talking") Mr. Egan briefly mentions the contribution of the engineering society in improving the engineer's "status, respect and security." That issue also makes brief mention of the September IEEE elections in an article regarding the impending candidacy of Irwin Feerst ("A Hot Race for a Change for IEEE Presidency?" News Scope).

The electrical engineering community needs an honest appraisal of the IEEE's efforts to promote engineering as a true profession, and of the alternatives offered in the upcoming IEEE elections. Michael A. Allocca
Design Engineer
Data Trends, Ins.
Parsippany, N.J.

## Subway power plan is a nice plan, but-

News Scope of June 10, 1971 ("Energy Storage Planned to Save Subway Power") quotes a plan to save power for the New York City Metropolitan Transit Authority by using a flywheel to convert kinetic energy of the train into electrical energy as it decelerates and vice versa.

This scheme was tried some years ago on the City of London bus system, and while it showed promise, the mechanics of keeping it working were well-nigh impossible. To reduce the friction of the high-speed flywheel in its cage with the surrounding air, a vacuum was tried, which showed excellent
promise except for the fact the seals on the shaft would not permit them to hold a vacuum.

To my knowledge, this scheme has now been discontinued by the City of London because of this impractical type of mechanical device.

Fred A. Kahl
P.O. Box 817

Goleta, Calif. 93017

## A clarification on market data

In the article "Electronics at Mid-1971," ED 14, July 8, 1971, p 23 , I am misquoted three times.

I am erroneously quoted as stating that 198.8 million ICs were shipped in 1970 . I stated that 298.8 million were shipped. Secondly, I stated that the average selling price of ICs drops approximately $30 \%$ for each doubling of the accumulative volume. In the article the key word "accumulative" was somehow deleted. I did not state that industry revenue in 1971 would jump $40 \%$ over 1970. At best, we can expect $8 \%$.

Rolland F. Smith
Manager, Market Research and Planning
Signetics
811 E. Arques Ave.
Sunnyvale, Calif. 94086

## Accuracy is our policy

In the Aug. 5, 1971 issue (ED 19, p. 70), a mechanical counter for the Veeder-Root Co. of Hartford, Conn., was incorrectly described as an electrical counter. This mechanical counter, known as the Little Miracle, is used for electricalequipment applications.

Electronic Design welcomes the opinions of its readers on the issues raised in the magazine's editorial columns. Address letters to Managing Editor, Electronic Design, 50 Essex St., Rochelle Park, N. J. 07662. Try to keep letters under 200 words. Letters must be signed. Names will be withheld on request.


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# designer's calendar 

Oct. 31-Nov. 4

Engineering in Medicine \& Biology Conference (Las Vegas) IEEE, John Hanley, Brain Research Inst., Univ. of Calif., Los Angeles, Calif. 90024

CIRCLE NO. 413

## NOVEMBER 1971

| $\mathbf{S}$ | $\mathbf{M}$ | $\mathbf{T}$ | $\mathbf{W}$ | $\mathbf{T}$ | $\mathbf{F}$ | $\mathbf{S}$ |
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## Nov. 2-3

Eastern Electronics Packaging Conference (Boston) Sponsors: IEEE, ASTM, S.M. Stuhlbarg, Raytheon Co., Box 605, Hartwell Rd., Bedford, Mass. 01730

CIRCLE NO. 414

## Nov. 3-5

Northeast Electronics Research \& Engineering Meeting (NEREM), Boston, Sponsor: IEEE, IEEE Boston Office, 31 Channing St., Newton, Mass. 02158

CIRCLE NO. 415
Nov. 15-18
Fall Joint Computer Conference, (Las Vegas) Sponsors: IEEE, AFIPS, AFIPS Hdqs., 210 Summit Ave., Montvale, N.J. 07645

CIRCLE NO. 416

Nov. 16-19
Conference on Magnetism \& Magnetic Materials (Chicago) Sponsors: IEEE, The American Institute of Physics, F.M. Mueller, Argonne National Laboratory, Argonne, Ill. 60439


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## Burroughs



## Digital Equipment goes from minis to whoppers

Long the king of the minicomputer world, Digital Equipment Corp., has decided to take on the big boys in the big computer field as well.

The Maynard, Mass., company has built and is now accepting orders for five models of what it calls its DECsystem-10 family of machines. They range in performance from that offered by the IBM $370 / 135$ to the IBM $370 / 165$. Prices range from a little below $\$ 400,000$ to $\$ 2$-million.

Two of the five systems are dual processor systems-the first of this type that DEC has produced. The company also announced several new peripherals and communication devices for its large systems.

According to Winston R. Hindle Jr., DEC group vice president, the company believes that "the capabilities offered by this new product family will substantially increase our share of the largecomputer systems market and expand Digital's growth opportunities in the years ahead."

The large systems business presently accounts for $20 \%$ of DEC's sales and is expected to increase by at least $50 \%$ next year, Hindle predicts.

The DECsystem-1077, largest of the new systems, is a dual-processor system, with at least 131,072 , 36 -bit words of core memory shared by the two processors. The 1077 sells for about $\$ 2$-million and has throughput power exceeding that of the IBM $370 / 155$, the Univac 1108 and the CDC 6400, Digital reports.

DEC has announced a new equity leasing policy for the large systems. This allows the purchaser to pay for the large systems over a five or six-year period. Although common to the large
computer market, this is a new policy for DEC.
One of the most significant features of the new system, says John Leng, DECsystem-10 group manager, is that they are the only systems in the industry that provide real-time, multi-programbatch, remote-batch and timesharing capabilities with a single operating system and a single command language.
"Competitive systems typically cost $50 \%$ more and do not provide the equivalent capability."

DEC has also introduced new peripheral equipment for the DECsystem-10 line, including a communication multiplexer built around the company's PDP-11 minicomputer. Other new peripherals are two magnetic-tape units, a remote-batch terminal built around a PDP-8, a dualtrack disc drive and three card readers.

## Paris components show wide open to U.S. in '72

The 40 -year-old Composants Electroniques, one of Europe's outstanding electronics shows, is breaking with tradition this year by allowing United States manufacturers without French representatives to participate for the first time. In the past only companies with local offices or licenses, or those with French distributors or agents, have been permitted to exhibit.

The 1972 show of electronic components and instrumentation will be held in Paris April 7-13. American companies wishing to participate can write William A. Warnes, BIC-948, United States Department of Commerce, Washington, D. C. 20230.

## Economist sees threat to electronics industry

A Stanford Research Institute economist, Kenneth W. Taylor, has warned that the U. S. electronics industry faces overpowering competition from foreign countries unless it can get its labor force to be more productive.

Speaking before the Hayden Marketing Seminar at Wescon in San Francisco, Taylor questioned "how effective Nixon's polices will be in the long run for fighting inflation, exportation of jobs and the like, as long as people of other countries believe in working harder than workers in the U. S.-with the same or better capital equipment than we furnish them."

He reminded the audience that "international trade is a two-way relationship" and asked: "If the United States puts restrictions on imports to reduce the outflow of jobs, where will foreign nations get the money to buy our exports?"

## MW system increases transmissions to CATV

A new technique using microwaves has been developed for transmitting multiple television channels within cable television networks. Instead of amplitude-modulating each channel, as is now done, the new approach combines the multiple channels by multiplexing them at vhf frequencies. The multiplexed signal is then frequency-modulated on X-band, then amplified and transmitted to the receiving antenna.

The developer of the technique is the Laser Link Corp. in Woodbury, N. Y.-a name that could give the erroneous impression that the system uses a laser. It doesn't; it operates at microwave frequencies.

The new technique has been used to build a system called Airlink that is expected to be much cheaper than other systems, says Ira Kamen, president of the company. "Airlink will cost a little more than $\$ 80,000$ for a 20 -mile FM link, as opposed to approximately $\$ 145$,000 for a 10 -mile conventional am-plitude-modulated microwave link (AML). A 5-mile cable costs almost $\$ 120,000$, and if it is underground,
the cost doubles," says Kamen.
Using frequency-division multiplex/frequency modulation (FDM/ FM), the system can transmit up to 18 TV channels on one rf carrier in the 12.7 -to- $12-95-\mathrm{GHz}$ band, according to J. H. Vogelman, senior vice president of the Laser Link Corp. Given a wider bandwidth, it can transmit 32 channels on one rf carrier. The conventional ampli-tude-moduation link is limited to a single TV channel for each rf carrier.

Among other advantages, Vogelman says, the new system can transmit in as many as 21 directions simultaneously with ranges in excess of 10 miles. AML is limited to four directions of less than 10 miles, he says.

The new system is also reported capable of unrestricted line-ofsight transmission to a comfortable range of 20 to 25 miles. AML is limited to about nine miles, Vogelman says.

FDM/FM penetrates weather regardless of the conditions, because of its 20 W power reserve. If more power is needed, the new system can operate up to the FCC limit of 60 W by paralleling three traveling-wave tubes.

## A new image enhanceer developed for TV

The first automatic image enhancer for use by the broadcast industry has been developed by CBS Laboratories in Stamford, Conn. The unit, called Mark 3, sharpens color television signals while they are being transmitted. It is being mass-produced by CBS and should be available in November.

Intended as a replacement for the more than 1500 manual image enhancement systems now used here and abroad, the new device was developed jointly by Renville H. McMann Jr., executive vice president of CBS Laboratories, and Clyde Smith, a senior broadcast engineer. The company has filed for patent protection.

According to CBS, solid-state technology has made it possible to reduce the size of the automatic image enhancer to less than half that of the manual system now in use.

The process of image enhancement was first introduced by CBS Laboratories in 1968 and has since become standard in the broadcast industry. Lack of sharpness is a common problem in color television broadcasting; it accounts for the difficulty in identifying the football or numerals on a player's jersey during fast-moving plays, for example.

The enhancer increases sharpness and detail by performing a process of vertical and horizontal equalization. Differences between the middle and outside lines of three successive lines on the television picture are added to the middle line. This provides a higher contrast image and permits the viewer to identify clearly both the ball and the player.

## Balky antenna control knocks out ATS-3

A locked antenna control system on Applications Technology Satellite 3 has caused the fouryear old experimental satellite to stop transmitting weather pictures and other data.

ATS-3 is in stationary orbit at $70^{\circ} \mathrm{W}$ Longitude, 22,300 miles above Colombia.

NASA officials believe the 805pound spacecraft gets heated up when the sun is north of the equator in the summer. And, they add, because the antenna is on the top, or north side of the spacecraft, the drive or control system probably overheats, causing the antenna to stop spinning.

The spacecraft spins at 100 revolutions per minute and the antenna spins in the opposite direction at almost the same speed which keeps the antenna pointed toward earth.

If the ATS-3 situation is consistent with past problems of a similar nature, NASA scientists expect it will recover as the sun slowly moves south.

## FCC adopts new rules for remote TV stations

Despite objections of the National Association of Broadcasters, the National Broadcasting Company and Columbia Broadcasting Sys-
tem, the Federal Communications Commission has adopted new rules developed by the Electronic Industries Association for monitoring. the performance of remotely operated television stations. The rules, to be effective Oct. 5, give the users until next April 1 to equip their stations with the proper test equipment.

As Noel Luddy, chairman of the EIA's Broadcast Equipment Section, explains it: "The remote station is monitored by transmitting special test signals that are inserted on lines 18 and 19 in the vertical blanking area of the picture. These lines are not seen on home receivers."

With the problems of monitoring off-the-air signals in mind, the EIA proposed three different test signals to permit the station operator to evaluate the transmitter's performance.

The National Association of Broadcasters, NBC and CBS say that extensive field tests should be conducted prior to the adoption of the rules. In particular, CBS points out that in demodulating the TV signal off the air, distortion could be introduced that is not in the original signal.

The EIA says that the absolute and relative levels of its proposed signals have been designed to minimize the effect of interaction with one another. It contends its method permits accurate measurements through noise.

## Ultrasonics explored as a cancer detector

Ultrasonic equipment may have found a new and highly important role as an early detector of lung cancer, according to findings by Prof. C. Hellmuth Hertz of the Institute of Technology in Lund, Sweden.

A few years ago Hertz, along with other Lund researchers, introduced a radar technique for threedimensional heart diagnosis. Now he has developed an ultrasonic device that has a repetition frequency high enough to provide extremely well-defined oscillographical curves of the vocal cord movements. If the cycles are unsymmetrical, the prognosis may well be cancer.


You can't blame engineers or purchasing agents for trying to save every last penny on resistors these days. But lowest price doesn't necessarily mean lowest cost. For example, most manufacturer's color bands won't stand up to the cleaning methods used to remove excess flux. Or they darken and become illegible from the heat
produced in normal usage. This can mean costly identification errors on your production line. The unnecessary expense of rework. Our solution? A-B quality. Bright, crisp identification of Allen-Bradley's specially formulated paints. Baked on to stay on. Designed to resist aging. Discover the other ways to save money. Ask your nearest

A-B distributor for our free booklet " 7 ways to tell the difference in fixed resistors." Or write Allen-Bradley Electronics Division, 1201 South Second Street, Milwaukee, Wisconsin 53204. Export: Bloomfield, New Jersey 07003. Canada: Galt, Ontario. United Kingdom: Bletchley, Bucks.

# High-voltage ionic discharges provide silent, efficient cooling 

A $30-\mathrm{kV} \quad 200-\mu \mathrm{A}$ electrostatic discharge that reduces the red-hot $1675-\mathrm{F}$ temperature of a $1000-\mathrm{W}$ heating coil to a dark 975 F in a second or two (see cover photo) may compete with fans and blowers in electronic applications, according to the inventor of the technique.
It removes heat by the effect of a non-arcing ionic corona dis-

Jim McDermott<br>East Coast Editor

charge from the negative ends of high-voltage probes.

The cooling method, patented by Oscar Blomgren Jr., vice president of Inter-Probe, Inc., North Chicago, Ill., has been applied to welding and metallurgical processes, but its use for electrostatic cooling of optical elements in a high power $\mathrm{CO}_{2}$ laser system was recently investigated by General Dynamics in Fort Worth, Tex., a licensee of Inter-Probe. According to Dr. K. G. Kibler, senior research scientist at General Dy-


The effectiveness of the electrostatic method in cooling heat sinks is demonstrated in the setup shown above. Equal amounts of heat, from the two burners, are supplied to the identical heat-sink halves. The pyrometer readings show the $185 \cdot \mathrm{~F}$ temperature drop when the field is applied.
namics, it shows considerable promise for the laser application as well as for other electronic uses.

The method may be used where vibration and the noise of motors and blowers cannot be tolerated and also where the object to be cooled has an unusual geometry that might restrict the use of blowers or duct work. Kibler points out that electrostatic cooling can also require less power than blowers and fans.

## Cooling discovery accidental

The cooling effect of a highvoltage discharge was accidentally discovered by Blomgren when he tried to use an electric field to keep an acetylene flame from touching the inside of a pipe. He was attempting to solve a burnernozzle deterioration problem.

Blomgren says there is still much to be learned about the precise mechanism that causes the increased rate of heat dissipation. Kibler describes the action as a corona discharge, or electric wind, that creates vortex columns in the air adjacent to the heated surface.

Normally a thin boundary layer of air clings to heated surfaces. The layer acts as an insulating barrier, inhibiting the rate at which adjacent, cooler air can carry away the heat. But with the action of the electric field, vortex columns are created, Kibler explains. These pull in cooler air from regions outside the normal boundary layer. It is this swirling action that apparently provides the transport mechanism by which the heat transfer rate is increased.

Just how this laminar boundary layer is distributed and heat transfer is improved by the application of the high-voltage discharge is shown in Schlieren


A high-voltage discharge cools a hot surface by producing a turbulence that disturbs the thin boundary layer. At the left, from a Schlieren movie by General Dy-

namics, a 1100-F copper plate has minimum heat transfer with no field. At the right, the turbulence of a $20-\mathrm{kV}$ field lowers temperature 300 F .
movies-pictures that reveal normally invisible phenomena, like heat waves-taken by General Dynamics in its investigations (see accompanying photos).

In the Schlieren setup, highvoltage fields were applied to a vertical copper plate three inches in diameter and one-half inch thick. The plate covered a hole in a fire-brick wall containing a $900-\mathrm{W}$ electrical heating element. The temperature of the plate was stabilized at 1100 F , with no field applied. When 20 kV was applied through a probe, the temperature of the plate fell 300 degrees.

A problem with high power lasers, such as $\mathrm{CO}_{2}$ devices, is the generation of heat in the optical elements through which the beam passes. In the worst case, this heat can destroy the elements; in lesser cases, it can distort them.

Because of the configuration of optical systems, fan or blower cooling is frequently too bulky and not feasible. To seek a better method, General Dynamics conducted extensive experiments with electrostatic cooling of germanium and Irtran 2, a polycrystalline zinc-sulfide window produced by Eastman Kodak. Both elements, because of their long wavelength transmission characteristics, are widely used with infrared lasers.

General Dynamics' Dr. Kibler reports that the electrostatic cooling was "effective" for both materials. Irtran, which is used for high-power $\mathrm{CO}_{2}$ laser systems, distorts at temperatures of only


1. Data on electrostatic cooling of laser optical elements was provided by this basic test setup.

2. The best electrostatic cooling of the Irtran window was obtained with the setup shown above.
a few hundred degrees fahrenheit, Kibler notes.

In a typical experimental arrangement by Kibler (Fig. 1), flat cylindrical Irtran specimens were placed horizontally over an electric hot plate, surrounded by insulating bricks. By controlling the power input to the hot plate with a Variac, and by monitoring the temperature with thermocouples, the experimenter obtained a constant temperature.

Electrostatic probes used in the experiment included needles, discs of fine-mesh screen and rings of knife blades. The specimens were grounded with copper rings.

With a constant heat input that brought the Irtran to 270 F , application of 25 kV to the probe lowered the temperature by 140 $F$ in a few minutes.

Kibler points out that an interesting feature of the electrostatic method of heat transfer is the possibility of shaping the electric field by shaping the probes or by using arrays of probes. This can permit the use of high-voltage cooling in inaccessible areas without interference with the functions of the object to be cooled. A good example is the use of this method to cool Irtran and germanium IR windows without obstruction of the window aperture.

All of the probes tried in the General Dynamics experiments lowered the temperature, but by different degrees. The maximum cooling was observed for a probe made up of eight knife blades in
a circle (see Fig. 2).
To simplify thermodynamic analysis, Kibler set up an experi-ment-a simple needle probe positioned vertically above the center of one of the windows, as shown in Fig. 1.

Reliable cooling results, Kibler reports, were obtained with needle probe spacings of two to six inches from the window. The results showed that the cooling depended largely on the ionic current flow, which ranged from 10 to $60 \mu \mathrm{~A}$. Applied voltages ranged from 5 to 30 kV . The window temperature of 240 F was reduced 20 F at 5 kV and 100 F at 30 kV .

No cooling was noticeable until about $10 \mu \mathrm{~A}$ began to flow. At current values of 50 to $60 \mu \mathrm{~A}$, a saturation effect began to appear, and the drop in temperature with an increase in current grew smaller (see Fig. 3).

To determine how electrostatic cooling compared with forced air cooling, Kibler ran trials using an aluminum block of $2 \times 6 \times 1$ inches with thermocouples installed in holes drilled halfway through the block. From stabilized temperatures of 250 F , and with a constant heat input, the temperature decreased at the rate of 8 F per minute with the application of high voltage. It dropped 11 F per


This 30-kV electrostatic cooling power supply was designed by InterProbe to provide up to $800 \mu \mathrm{~A}$.
minute when a fan was used, but the power required for the electrostatic method was only 3 W compared with 14 W for the fan.

While the precise phenomena that produces electrostatic cooling has not yet been pinpointed, Kibler says it appears to be heat transfer by boundary-layer disruption. As he explains it, the temperature gradient between the object to be cooled and the surrounding air may be considered as confined to a very thin film of air that clings to the object's surface. Outside this film, most of the temperature difference vanishes, due to mixing of the air. Within the film, or boundary

3. Electrostatic cooling depends strongly on the current flow. Here, cooling of an Irtran-2 element with a needle probe begins at $10 \mu \mathrm{~A}$.
layer, the heat transfer takes place by conduction. However, the electrostatic cooling effect apparently disrupts this boundary film to allow convection, as well as conduction, to occur closer to the surface of the object, and thus more effectively to cool it.

Kibler says that his best cooling result was obtained with an Irtran specimen at 280 F . The specimen was cooled to 130 F by applying 27.5 kV at $100 \mu \mathrm{~A}$ through the probe.

The heat dissipated without the electrostatic field in this particular experiment was calculated, in terms of the film heat-transfer coefficient, to be $9 \mathrm{BTU} / \mathrm{hr} / \mathrm{ft}^{2} /{ }^{\circ} \mathrm{F}$. With application of the field, the coefficient increased to 35 . Improvement in this case was therefore roughly $4: 1$. In more typical cases the improvement factor was found to be about 2:1 (Fig. 3).

The cooling effect is one that is highly dependent on the geometry of the situation. Few generalities can be drawn, Kibler says, based on their own experience.

## Semiconductor fins are cooled

To demonstrate the application of electrostatic cooling to highpower semiconductor radiators, Inter-Probe constructed a special setup. In it, individual blow torches heated two sections of radiator fins, each instrumented with a thermocouple and its own pyrometer.

The burners were adjusted to stabilize the temperatures of both heat sinks at 500 F . Then a 28 kV electrostatic field at $1000 \mu \mathrm{~A}$ was applied to one radiator to reduce its temperature by 185 F . The other heat sink remained at 500 F , with no field applied to it.

General Dynamics is exploring the possibilities of using the new cooling method for IC boards and other electronic components.

Dramatic effects, in addition to cooling, have been observed in the application of electrostatic fields to welding, says Inter-Probe's Blomgren. Metallurgical changes have been noted that cannot be ascribed to simply breaking down the boundary layer. These include control of grain size and hydrogen content, and elimination of inclusions and voids.


Speed's the name of the game in the MOS clock driver business these days. Whether you're driving a long shift register or one of the new MOS memories like the MM1103.

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The speedy new MH0026 has also been designed to be driven from standard DTL/TTL circuits.

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driver (MH0007), one capable of dc operation (MH0009), a 10MHz clock driver (MH0012), a dual ac coupled driver (MH0013), the world's first monolithic (the low cost MH0025) and a TTL-to-MOS memory interface driver (MH0027). Incidentally, our drivers are available in TO-5, TO-8 and one watt mini dip packages.

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# All-solid-state TV set wins a rave for easy servicing 

An all-solid-state television receiver that can be serviced like a tube set was the goal that General Electric's Television Receiver Products Dept. in Portsmouth, Va., set in designing the company's first all-transistor TV chassis.

The final design of the 19 -inch portable-TV chassis, designated U-1 by GE, not only achieves this goal ; it incorporates a host of additional features that won for it the highest serviceability rating ever awarded by the National Electronic Association. The association, an organization of licensed TV service organizations and technicians, works with the TV manufacturers to simplify the servicing of television receivers.

## A departure in design

"The high rating was the result of several departures from previous transistor-TV design," says W. J. Meyer, GE's manager of product service. Innovations include the following:

- The U-1 chassis has a single half-wave, high-impedance, unregulated $140-\mathrm{V}$ power supply, with taps at 130 and 22 V-a type commonly found in tube sets. Some solidstate TV chassis have one or two low-voltage, highly regulated power supplies that are difficult to troubleshoot.
- The relatively high transistor voltages of $+22,+130$ and +140 permit the use of a $20 \mathrm{k} \Omega / \mathrm{V}$ voltohmmeter, as with tube sets. With a $22-\mathrm{V}$ source for transistors element voltages can more easily be read and interpreted than with 12 V transistor sets.
- Current measurements that usually require unsoldering a circuit are not necessary.
- Transistors are all npn, eliminating the need for a nega-


Seven transistors-power and signal—and an audio module plug into GE's new PC chassis. Power transistors can't be improperly plugged into their sockets. Signal transistors are protected against faulty insertion.
tive, regulated power supply.

- Plug-in transistors are provided for circuits in which the semiconductors are most likely to fail. The rest of the transistors in the main chassis are soldered in place, because their expected failure rate is very low. This also provides a clue for the technician to look elsewhere first for trouble.
- For ready accessibility, the receiver has three separate chassis. The main one is printed-circuit board on which components are grouped by function: i-fs, detectors, sync, video and sweep-generating circuits. The PC conductor pattern in the main chassis is designed so that any single component can be removed without unsoldering any other. The deflection
system is on a separate metal chassis, as are the power supply and output stage. GE's Meyer notes that sets have been designed that require removal of the chassis to replace a fuse-a task difficult to explain to the set owner.

Rapid removal of components is a part of GE's new design.

- The main PC circuit board is held in place by two spring clips that can be pried loose with a screwdriver.
- The high-voltage transformer assembly, the vertical output transformer and the picture-tube filament transformer are each secured with one screw.
- The loudspeaker is held with spring fasteners that can be quickly removed with pliers.


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## A robot worker being developed to assist space-station crews

When the space shuttle arrives at the orbiting space base, who is going to move all those heavy supplies from the shuttle to the base? There'll be big telescopes, hundreds of pounds of electronic experiments to be moved in and out, spare parts, food and even mail.

And who in the space base is going to get out of a warm bed after a hard day's work to step out into space so a loose solar cell can be checked?

Hopefully, a robot or teleoper-ator-now officially called "a remote manipulator system"-will do all this and a great deal more.

Working on the project is Bell Aerospace, a division of Textron in Buffalo, N. Y., under a contract with the National Aeronautics and Space Administration's Marshall Space Flight Center in Huntsville, Ala.

## Feasibility under study

The immediate project is for Bell to determine the feasibility of using controllable arms on a spacecraft. Eventually arms might be fixed to the space shuttle itself or built on a special subsatellite that could be carried aloft by a mother craft and released in space or shot into space by its own booster.

Looking at future possibilities, Heinz Fornoff, chief engineer of Bell Aerospace's Space Systems Dept., says: "Fifty to 100 -foot manipulators on the space shuttle might be used to hold onto a space station for docking; they could bring an ailing satellite back to earth, or they could help add a new compartment to an orbiting station."

John F. Mason<br>Military-Aerospace Editor



Arms on this flyable subsatellite can be controlled remotely by an operator in a nearby spacecraft or from the ground. Television cameras strategically placed on the robot show the operator the results of his maneuvers.

Built as a subsatellite, the unit would be equipped with several television cameras that would relay, pictures to a human operator, either in a nearby spacecraft or on the ground, and the operator could command the manipulator by radio to do any number of chores. It could inspect alien satellites, remove and replace experimental packages, and assemble or erect space structures or large antenna arrays. And it could transfer cargo and even rescue an astronaut in distress.
"Another application," Fornoff says, "would be as a rover for the moon or for Mars."

Here is how the operation of the subsatellite manipulator is envisioned:

The operator sits at a console, about the size of a small desk, with
antennas. He maneuvers the robot remotely by manipulating two control handles, as he watches one or more television screens showing pictures from the robot. Digital and analog radio commands are sent over 40 channels. The subsatellite is propelled by thrusters. Solenoids provide precise control of the thrusters.

Integrated circuits are used throughout the unit. Two means of stabilizing the system under consideration are control-moment gyros and rate gyros.

NASA is considering proposals from industry for designing a manipulator system to be used on the first 10 flights of the space shuttle.

An experimental model, built in the form of a subsatellite with two mamipulative aims, is being tested
at Bell's space-simulation facility. The facility consists of a room with a 20-by-24-foot floor, where two platforms float on virtually frictionless jets of pressurized gas $1 / 1000$ th of an inch thick.

The floor is flat to within $2 / 1000$ th of an inch accuracy. The platforms are remotely controlled from individual consoles, each equipped with a display from television cameras aboard the units.

With this facility, each attribute of the space environment needed to evaluate a robot concept, except


Operator watches the robot on several television screens and commands it by 40 radio channel links.
vertical motions, can be achieved on earth with a five-degree of freedom unit. It is installed in a lowfriction gimbal ring (for pitch and roll motions) that operates on an extremely low-friction, air-bearing platform (for yaw, forward, reverse and lateral motions) within the one-g earth field.

When friction is reduced to extremely low levels, instability, control system requirements and docking difficulties associated with a flight robot can be simulated, Fornoff says.
"The results of the experiments," Fornoff says, "are expected to establish the performance capabilities and design criteria that we hope are first steps toward development of the operational remote manipulator systems that will play an increasing role during man's second decade in space."

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Ultra-fast recognition of red or IR wavelengths in laser detection, light demodulation, shaft/position encoders, switching and logic circuits in the nanosecond range demands ultra-fast devices. MRD500/510 PIN diodes typically respond in 1 ns ; conventional, bulk-effect detectors need longer response times. Both units feature high sensitivity and are available with convex or flat glass lenses in standard, TO-18 cases. Exclusive Annular* passivation ensures long-term reliability and stability.

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Critical to any application requiring stable characteristics over very-long-term operating life expectancy is detector sensitivity which must remain constant so system biasing is not thrown off spec. Similar to beta

measurement in a conventional transistor, phototransistor sensitivity $=$ output current $\div$ light input. Curve shows the sensitivity for a standard hermetic, MRD300 detector family having little or no change in documented or projected sensitivity beyond 4,000 hours of testing. Indications from this and other ongoing tests show Motorola's family of Annular passivated light detectors to have identical reliability as standard metal-can transistors which have shrugged off millions of hours of rugged, mil-type life testing without significant failures.

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## technology abroad

Homopolar generators and motors - very-high-current, low voltage dc devices - with a built-in system for removing contaminants from their gallium-indium liquidmetal brush rings are being developed by the GEC Hirst Research Center in Wembley, England. An electrolytic cell, supplied by voltage from the machine itself, removes the contamination products. The company reports that an experimental $5-\mathrm{kW}$ motor-generator/torque-converter and a generator that yields 6 kA at 1 V have been tested successfully with the new system. Homopolar generators, which work on the Faraday disc principle, are gaining increasing favor for the production of large direct currents.

CIRCLE NO. 451
Electrolytic capacitors with aluminum foil electrodes that are spaced with glass fiber instead of paper reduce failure in high capacitance, low-volume units, according to Philips of the Netherlands. Now in production by Philips, the new 121 -series of capacitors is cheaper than solid tantalum counterparts. Capacities are in the 2.2 -to $-330-\mu \mathrm{F}$ range, with $20 \%$ tolerances and working voltages from 6.3 to 40 V dc. Philips reports a failure rate of $0.02 \%$ per 1000 hours at rated voltage and $2-\mathrm{C}$ ambient temperature, increasing to $0.2 \%$ at 12 C .

CIRCLE NO. 452
Five to eight times better performance from a new zinc-air primary dry battery than is provided by an equivalent Leclanche cell is claimed by Crompton-Parkinson Ltd. of England. The battery is about $2 \times 1 \times 1 / 2$ inches and weighs slightly more than an ounce. It has an open-circuit rating of 2.8 V . The voltage remains almost constant as 250 mA is drawn continuously over 10 hours. A design innovation is placement of the air-breathing cathode faces of the two series-connected cells
toward each other on each side of an air channel. This protects the faces from damage and gives the battery a smooth exterior.

CIRCLE NO. 453
Italy's first major computer project, the Selenia CDG 30/32-a real-time machine developed initially for military and air traffic control-is now under final evaluation. The machine makes extensive use of LSI technology and has a powerful set of 112 instructions. Since word lengths of 32 , 30,16 and 15 bits can be chosen by microprogramming, the computer can be used efficiently in real-time digital conversion. Production is expected to start next year.

CIRCLE NO. 454
An experimental "programmed driving" system in which urban drivers are told how to get to their destinations by computer is being tested by Siemens of West Germany. If the idea becomes a reality, drivers will install a transceiver in their cars to program their position and destination, using street names that are encoded into six-digit figures. A central computer tells the driver whether to turn left or right or to drive straight ahead, as well as at what speed he needs to travel to get through the green lights without stopping.

CIRCLE NO. 455
A $\$ 588$ digital counter using MSIMOS circuits is being produced by Venner Electronics in England. The MOS circuits are found in both the counting and dividing chains. These circuits replace over 24 integrated circuits in the more conventional approach. The counting chain has a high-speed TTL circuit, followed by three MOS chips that provide the seven-digit count capability. In the divider chain, a $10-\mathrm{MHz}$ crystal output is divided down to 1 MHz by one TTL decade and two MOS chips.

CIRCLE NO. 456

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Meanwhile send for detailed information. And if you want an evaluation sample of the DAC-12QZ or the DAC-ioZ just call, or write us on your letterhead. Both are in full production and we have lots in stock. Analog Devices, Inc., Norwood, Mass. 02062, (617) 329-4700.


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# washingtonreport 

## Air Force to select associate contractors for B-1

The Air Force expects to name associate contractors to develop the electronic countermeasures and infrared surveillance systems for the B-1 bomber. The countermeasures associates should be picked by the middle of next month, and the surveillance associates by November. They will work with the over-all avionics contractor, who will put the whole package together.
Meanwhile, the Air Force says the following companies will provide the Government-furnished avionics for the aircraft, navigation and weapons delivery : Northrop, the stellar inertial system; Delco Electronics, the Carousel inertial system; General Precision Laboratories, the Doppler radar; Stewart Warner, the radar altimeter; General Electric, the forward-looking radar; Texas Instruments, the terrain-following/avoidance radar; Hughes and Texas Instruments, a forward-looking infrared sensor; Westinghouse and RCA, a low-light-level TV, and Raytheon, the threat-associated electronic countermeasures system.

For mission and traffic control, Collins Radio will provide the radio, Avco, the AN/ARC-123 hf radio, Collins Radio, the ILS-70 instrument landing system, Hoffman Electronics, the AN/ARN-91 Tacan navigation aid, Motorola, the AN/APX tracking beacon, and Magnavox, the AN/ URC-64 uhf air rescue beacon. Earlier estimates said that the off-the-shelf avionics would probably cost about $\$ 4.8$-million per aircraft and weigh about 3900 pounds. The Air Force says now that these estimates are high, but declined to come up with new figures.

## Collins Radio gets emergency communications contract

A $\$ 7.7$-million contract between the Navy and Collins Radio, Inc., of Cedar Rapids, Iowa, has signaled the start of production of the Navy's Sect-Submarine Emergency Communications Transmitter-system. The contract is expected to be the first in a series over five years to outfit nuclear submarines with communications buoys that would go into action in any undersea emergency that the craft encountered.

Sect touched off an international alert a few years ago when a Navy laboratory working on the buoys allowed the prototype to start transmitting the "submarine down" message. Brass in the Pentagon and the Norfolk Atlantic Fleet Headquarters were rapidly heading for their panic buttons when triangulation established that the "underwater" broadcast was coming from a Washington, D. C., suburb.

## Airlines push talks on Microwave System

The board of directors of Aeronautical Radio Inc. (ARINC), the communications arm of the nation's airlines, met in the first of a series of meetings this week to establish policy on the possible construction of a $\$ 257$-million microwave coaxial cable communications system to serve the airline industry. ARINC's chairman, John S. Anderson, says that
should the airlines go to their own system and forsake the present leasing arrangements with AT\&T, they could save as much as $\$ 50$-million a year. They would also, he says, "eliminate the uncertainties of future tariff increases."

ARINC has told the Federal Communications Commission that two AT\&T tariff changes less than a year and a half apart almost doubled airline communication costs to $\$ 60$-million a year, and projections of usage indicate that by 1980 the cost of leased facilities would reach almost $\$ 140$-million annually. The possibility of an airline-owned and operated communications system has been in the incubation stage for several years and, until recently, has been scoffed at by some segments of the communications industry as too costly for the airlines and as merely a ploy by ARINC in a rate case pending before the FCC.

But ARINC points out that the system would not, of course, be built in one swoop; that it would be financed as it was being constructed through user charges.

## Navy seeks $\mathbf{\$ 5 0}$-billion building program

The Navy has given Congress a guideline for a minimum rebuilding program that would cost approximately $\$ 50$-billion for shipbuilding over the next decade. Envisioned in the unofficial plan, sponsored by the Chief of Naval Operations, Admiral Elmo R. Zumwalt, is the construction of a nuclear carrier every three years, seven nuclear submarines every year, one nuclear-powered guided-missile frigate every year and introduction of the undersea long-range missile system ULMS subs by the mid-1980s.
Meanwhile, the General Accounting Office in a report to Sen. William Proxmire (D-Wis.) has charged that the Navy shipbuilding program, as it now exists, has had little competitive bidding for contracts and that the advantages of what little competition there has been has been largely negated by extensive design changes. The report says that changes in 1970 accounted for $22 \%$ of the over-all costs. This year's budget calls for $\$ 3.3$-billion in shipbuilding funds.

Capital Capsules: The Air Force has pushed up the selection date for its advanced Loran-Loran C/D program, designed to outfit 3000 tactical aircraft with precise navigation equipment over the next five years. It now expects to pick a contractor before the first of the year. The Navy has stepped up efforts to equip its tactical aircraft with better electronic countermeasures gear to combat improved Soviet missiles and early-warning radar. It shifted $\$ 12.4$-million from other programs to accelerate the Itek AN/ALR45 threat-warning receiver and the Magnavox AN/APR-27 passive missilewarning receiver programs. Only $\$ 900,000$ had been allocated for the programs in the budget. . . . Lockheed has won a $\$ 1.3$-million contract from the Air Force for development work on the Malfunction, Detection, Analysis and Recording-Madar-subsystem for the C-5A aircraft. Madar is called an "aircraft cardiograph" because it continuously checks 1200 test points during flight. . . NASA now expects to launch the first of its three orbiting solar satellites about this time next year. They will cost about $\$ 80$-million and be built by Hughes for examination of the sun-particularly its corona, which is 400 times the temperature of the sun itself. . . . The Commerce Dept. reports that U. S. exports of business machines totaled $\$ 839$-million in the first half of this year, up from $\$ 770$-million over the same period last year. Computers, peripherals and parts accounted for $68 \%$ of the exports or $\$ 569$-million. The biggest importers were West Germany, Japan, Britain, France and Canada.


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| Pout (Watt) | $\begin{aligned} & \text { SSB } \\ & 30 \mathrm{MHz} \end{aligned}$ | $\begin{gathered} \mathrm{VHF} \\ 175 \mathrm{MHz} \mathrm{FM} \end{gathered}$ |  | $\begin{gathered} \mathrm{UHF} \\ 470 \mathrm{MHz} \text { FM } \end{gathered}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | 28 V | 13.5 V | 28 V | 13.5 V | 28 V |
| 0.5 |  |  |  | 2 N 4427 |  |
| 1.0 |  | 2N4427 |  |  |  |
| 1.5 |  |  | 2N3866 |  |  |
| 2.0 |  |  |  | BLX65 |  |
| 2.5 |  |  | 2N3553 | BLX66 |  |
| 3.0 |  | 2N3924 |  | BLX67 | BLX92 |
| 4.0 |  | BFS22A | BFS23A |  |  |
| 6.0 |  |  | 2N3375 |  |  |
| 7.0 |  | 2N3926 |  | BLX68 | BLX93 |
| 8.0 | BLXI3 | BLY87A | BLY91A |  |  |
| 12.0 |  | 2N3927 |  |  |  |
| 13.0 |  |  | 2N3632 |  |  |
| 15.0 |  | BLY88A | BLY92A |  |  |
| 20.0 |  |  |  | BLX69 | BLX94 |
| 25.0 |  | BLY89A | BLY93A |  |  |
| 40.0 |  |  |  |  | BLX95 |
| 50.0 | BLX14 | BLY90 | BLY94 |  |  |
| 100.0 | BLX15 |  |  |  |  |

# Centralab ofiers immediate delivery on functional modules 



Centralab, the industry leader in thick film microcircuitry, now has combined its recent advances in packaging and chip hybrid technology to bring you five new functional modules available for immediate delivery from stock. These modules are sealed in ceramic packages with 14 swaged terminal pins universally spaced $.600^{\prime \prime}$ row-to-row and $.100^{\prime \prime}$ apart to facilitate printed circuit board mounting.

| Module | Function | Rating | Suggested Applications |
| :--- | :--- | :--- | :--- |

## DESCRIPTION

FM-1110, 1203, 1403: Single, dual and quad drivers
Designed to accept standard DTL and TTL logic levels and to drive loads which require high power. Consist of single or multiple NAND/NOR gates and high gain amplifiers.

## FM-2100: MOS clock driver

Designed to accept standard DTL and TTL logic levels and universally drive MOS circuitry. Consists of a three input AND function followed by a power inverter.

FM-3110: Programmable monostable multivibrator
A flip-flop which, when triggered by an input pulse, generates an output pulse of prescribed width, with control through interconnection of appropriate package pins.
*FM-4110: RC clock oscillator
An RC astable multivibrator and an output buffer stage capable of providing a square wave output at a predetermined fixed frequency. It can operate down to 5 Hz with the addition of external capacitors.
*FM-5110, 5111: Overvoltage crowbar
A high speed electronic voltage sensing element and switch designed to protect voltage sensitive electronic devices by shunting out the supply voltage when high transients or other overvoltage conditions are experienced on the supply line.
*FM-5120: Electronic fuse
The electronic equivalent of a fuse which features accurate threshold levels, high speed and reset capabilities. Available in a variety of current threshold levels.
*FM-6110: Power operational amplifier
An operational amplifier designed to provide output capabilities far beyond those obtainable with equivalent monolithic IC's.
*These modules are scheduled for introduction in 1971.

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Programming services are available at Intel, at our major distributors or at our representatives in the U.S., Europe and Japan.
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For immediate delivery of 1701 or 1702 E-ROM's programmed for EBCDIC-8 to ASCII-8 call your local Intel distributor, Cramer Electronics, Hamilton Electro Sales, Industrial Components, or Electronic Marketing. In Europe contact Intel at Avenue Louise 215, B 1050 Bruxelles, Belgium. Phone 492003. In Japan contact Nippon IC, Inc., Parkside Flat Bldg. No. 4-2-2, Sendagaya, Shibuya-Ku, Tokyo 151. Phone 03-403-4747.
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# Overseas trade showsa powerful sales tool 

The overseas trade picture for U.S. electronics firms is really beginning to brighten. There are two reasons for this. The first is the slow but steady upward revaluation of foreign currencies-particularly the yen-relative to the American dollar. This will enable U.S. exports to compete more favorably with products from abroad.

A second more subtle, but equally important, reason is the increasing number of trade fairs and exhibitions that will be held overseas aided by the efforts of independent U.S. overseas marketing organizations as well as the
 U. S. Department of Commerce. For example, in April of next year, 116 U. S. manufacturers of computers and computer-related data equipment will exhibit their products at a special exhibition in Moscow (see News Scope, "U. S. concerns step up sales pitch to Soviet," ED 18, September 2, 1971, p. 18).

Another Russian electronics exhibition in which U.S. firms will participate is scheduled to be held in Leningrad the following October. A show principally for the small computer manufacturer will be held in Munich November 30 to December 3 of next year. Other major trade shows are scheduled for London, Paris, Frankfurt, Milan and Tokyo.

According to the U.S. Department of Commerce there will be over 300 industrial-type trade fairs held outside of the United States in the next two years. U.S. participation in these shows is being pushed by the Dept. of Commerce's Export Licensing Division. As one spokesman told Electronic Design: "We give top priority to processing the licenses of exhibitors at the overseas trade fairs. We know these exhibitions mean U.S. sales."

Another spokesman for International Media and Exhibits, Inc. of Newark, N. J., organizer of the Moscow exhibition, recently observed: "The countries of Eastern Europe place a great deal of importance on trade shows. These shows are considered so important a means of disseminating technical information that participation in them is almost tantamount to success in terms of potential sales."

The message is clear. The market outlook for U.S. electronic products in both communist and non-communist countries has never looked brighter than at this time. And what better way for U.S. firms to get in on the action than to participate in the numerous overseas trade shows that are being planned. For more information on these trade exhibitions write to Export Business Relations Div., U.S. Department of Commerce, Bureau of International Commerce, Washington, D.C. 20230.


# Use an N-bit detector for phase-locking. You get 360X2N electrical degrees of phase modulation and a hold range that is $2^{N}$ times that of a single-bit loop. 

Single-bit digital phase-lock loops have two disadvantages that limit their use: narrow phase modulation and narrow hold-in ranges. But with a multi-bit phase detector in the loop, these disadvantages are greatly minimized.

With an N-bit phase detector, the phase modulation and hold-in range is $2^{N}$ times greater than that obtained from a single-bit loop. The improved performance results from the use of a subtractor in the phase detector that prevents the loss of phase tracking when the counters overflow.

The most common type of digital phase-lock loop is the single-bit loop (see Fig. 1). When the loop is locked, the following features are available:

- The loop's phase-modulation range is 360 electrical degrees.
- Its hold-in range is $\mathrm{K}_{\phi} \mathrm{K}_{\mathrm{vco}} \mathrm{Hz}$.
- Its resolution with respect to changes in phase modulation and input frequency $f_{1}$ is infinite.

If the loop is not locked, the low-pass filter output beats at a frequency equal to $\left|\mathrm{f}_{1}-\mathrm{f}_{2}\right|$. This frequency difference must approach approximately $\mathrm{K}_{\phi} \mathrm{K}_{\mathrm{vco}} / 2 \pi \mathrm{~Hz}$ before the loop will lock.

A multi-bit phase-lock loop having the same closed-loop bandwidth, response time and phase margin as the single-bit loop is shown in Fig. 3. The basic difference between this and the simple loop is the multi-bit digital phase detector. It consists of two N -bit counters, an N -bit subtractor and a digital-to-analog converter.

The counters are straight N-bit binary ripple or clocked types. Any modulus can be used, provided it is the same for both counters. The subtractor is an N-bit, two's-complement, full subtractor that contains the difference between the numbers in the counters at any time. When the loop is locked, but a phase difference that is not a multiple of $360^{\circ}$ exists, the subtractor puts out a pulse width proportional to this phase shift. The amplitude of the pulse, before filtering, equals the amplitude of the least significant bit.

[^1]

1. A single-bit digital phase-lock loop is the simplest phase-locking circuit (a), and the most often used. Input frequency $f_{1}$ is locked in phase to $f_{2}$; the magnitudes are equal. The loop transfer function is in terms of the Laplace variable and a time constant $\tau$ (b).

2. The N -bit digital phase-lock loop provides a $\mathbf{2}^{\mathrm{N}}$ improvement factor over the single-bit loop with respect to phase modulation, hold-in and capture ranges. The subtractor tracks phase differences continuously; phase tracking is unaffected by counter overflows.

Filtering yields the average value of this signal.
A timing diagram illustrates the operation of the phase detector (see Fig. 3). The phase difference between $f_{1}$ and $f_{2}$ is 2.5 bits. This corresponds to 900 electrical degrees if the distance between each pulse is considered to be 360 electrical degrees. The pulses in this circuit can slide past each other without losing phase tracking, unlike a single-bit phase-detector.

The locked multi-bit loop has the following:

- Phase-modulation range of $360 \mathrm{x} 2^{\mathrm{N}}$ electrical degrees, because there are $2^{\mathrm{N}}$ unique states the subtractor can assume. The phase-modulation range for a three-bit device, for example, is zero degrees to 2880 .
- Hold-in range of $\mathrm{K}_{\phi} \mathrm{K}_{\mathrm{vco}} 2^{\mathrm{N}} \mathrm{Hz}, 2^{\mathrm{N}}$ times that of the simple loop.
- Infinite resolution with respect to phasemodulation and input frequency changes, as in the single-bit loop.

When the loop is not locked, the two frequencies cause a beat frequency. Now, however, the beat frequency is $\left|f_{1}-f_{2}\right| / 2^{\mathrm{N}} \mathrm{Hz}$. That's $2^{-N}$ times the beat frequency of the single loop. The loop
can now capture and lock when the two frequencies differ by as much as $2^{\mathrm{N}} \mathrm{K}_{\phi} \mathrm{K}_{\mathrm{vco}} / 2 \pi \mathrm{~Hz}$.

Another way of taking advantage of the multibit loop is to allow the phase-modulation range to remain $360^{\circ}$ but to raise $f_{1}$ and, therefore, $f_{e}$ by $2^{\mathrm{N}}$. To keep the dynamic properties the same, K has to be reduced by $2^{\mathrm{N}}$. The ripple frequency to be filtered is $2^{\mathrm{N}}$ times the frequency it was before. The higher ripple frequency is easier to filter, and the capture range is still $2^{\mathrm{N}} \mathrm{K}_{\phi} \mathrm{K}_{\text {veo }} 2 \pi$ Hz.

The multi-bit phase detector is also insensitive to coincidence between input and feedback pulses. This is a problem that plagues up-down counter types of phase detectors.

The counters and subtractor can be built with IC logic. With the 930 DTL series, for example, each bit requires two RST or J-K flip-flops for the counters and two quad-two input gates for the subtractor. Bits can be added, therefore, at a cost of four packages per bit. Any standard digital-to-analog conversion scheme can be used for the $\mathrm{d} / \mathrm{a}$ converter. The implementation of a five-bit phase detector is shown in Fig. 4.

3. The outputs of a three-bit phase detector are shown as if all of the outputs were added with their proper binary weighting. The subtractor output maintains the
phase difference of $f_{1}$ with respect to $f_{2}$ through all counter-overflow transitions. The two pulse trains can slide past each other without loss of phase lock.

4. In this complete five-bit phase detector, the counters are connected to the subtractor at points labeled in
common. The D output provides a two's-complement subtraction of the $B$ input from the $A$ input.

The N-bit phase detector is by far the most important component in the multi-bit phase-lock loop. Let's take a closer look at it.

## Track phase continuously

A typical interior bit circuit of the subtractor is shown in Fig. 5a. This circuit is used as the basic unit to obtain continuous phase tracking as the counters overflow.

When Boolean algebra is applied to the interior circuit,
$X=\overline{\overline{A_{j}} \overline{B_{j}}} \overline{\overline{A_{j}} B j}=A_{j} B_{j}+\overline{A_{j}} \overline{B_{j}}$ and $Y=\overline{\left(A_{j} B_{j}+\overline{A_{j}} \overline{B_{j}}\right) C_{j-1}}$.

The output difference is calculated as $D_{j}=\bar{X} \bar{Y}+\overline{\mathrm{X}} \overline{\mathrm{C}_{j-1}}+\overline{\mathrm{Y}}+\overline{\mathrm{Y}} \overline{\mathrm{C}_{\mathrm{j}-1}}=$
$A_{j} B_{j} C_{j-1}+\overline{A_{j}} \overline{B_{j}} C_{j-1}+A_{j} \overline{B_{j}} \overline{C_{j-1}}+\overline{A_{j}} B_{j} \overline{C_{j-1}}$, and the borrow-out is
$\mathrm{C}_{\mathrm{i}} \equiv \overline{\mathrm{Y}} \overline{\overline{A_{j}} \mathrm{~B}_{j}}=\overline{\mathrm{Y}}+\overline{\mathrm{A}_{j}} \mathrm{~B}_{j}=\mathrm{A}_{j} \mathrm{~B}_{j} \mathrm{C}_{\mathrm{j}-1}+$
$\overline{A_{j}} \overline{B_{j}} C_{j-1}+\overline{A_{j}} B_{j}$.
The equations verify that a two's-complement subtraction is performed.

Consider now the phase detection by a four-bit device as a counter overflows. Initially let $\mathrm{A}=15$ and $\mathrm{B}=8$. The relative phase is represented by D , and $\mathrm{D}=(15-8)=7$. The binary subtraction is performed in a table (Fig. 5b), and the result agrees with that obtained from the analog subtraction. On the next pulse, $\mathrm{A}=15, \mathrm{~B}=9$ and $D=6$. A binary subtraction of $A$ and $B$ gives the same result.

The following pulse causes counter "A" to overflow and results in $\mathrm{A}=0$ and $\mathrm{B}=9$ (Fig. 5c). The binary subtraction gives $D=7$, the correct value, and not -9 , the analog result. The disagreement occurs for negative differences because the last borrow-out is thrown away and end-around-borrow is not used. However, the example shows that the detector has tracked the phase difference continuously through a counter overflow.

## Use for a velocity servo

The phase-tracking capability of the multi-bit loop makes possible its use for applications not normally considered for the single-bit loop. Take this design, for example:

A velocity servo, with steady-state variations of $0.025 \%$ maximum, is capable of following a constant input frequency (Fig. 6). The servo responds to periodic shaft-modulation commands by running faster or slower, in accordance with the input modulation command.

The required position modulation is $\pm 150^{\circ}$ of load shaft angle, and the maximum input frequency, $f_{1}$, is 4 kHz . The required encoder frequency is about 64 kHz , which is obtained with an encoder that has 1000 line-pairs per revolution. To facilitate the phase detector mechanization, the encoder repetition rate is divided by 16. The selection of $\mathrm{N}=6$ results from the observation that $1000 / 16$ cycles per $360^{\circ}$ of shaft rota-

5. The basic unit of the subtractor is the jth-bit circuit (a). An example using a four-bit detector gives identical analog and binary results for $A=15$ and $B=8$. But for negative differences, as when $A=0$ and $B=9$, corresponding to a counter overflow, only the two's-complement subtraction gives the correct phase difference (b).
tion gives $5.7^{\circ}$ per cycle. And since the required modulation is $\pm 150^{\circ}$, or $300^{\circ}$ total, $2^{N}$ must be at least $300 / 5.7$.

It is impossible to mechanize this loop with a single-bit phase detector, because of the modulation range required. The phase-lock frequency has to be about $1 / 32 f_{1}$, thereby lowering the ripple frequency to be filtered. In addition the ripple amplitude to be filtered is 64 times the amplitude resulting from the multi-bit detector. With these limitations, a reasonable bandwidth position loop (300 rad/s, in this case) cannot be realized.

## Test your retention

Here are questions based on the main points of this article. Their purpose is to help you make sure you have not overlooked any important ideas. You'll find the answers in the article.

1. Why does the N-bit phase-lock loop give better performance over the single-bit loop?
2. How does the N-bit subtractor maintain phase lock when either counter overflows?

3. An application of a six-bit digital phase-lock loop is this precision velocity servo. This design minimizes the
inherent position or phase lag of a standard, singleintegration servo. A single-bit loop cannot be used.


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## Simplify sample-and-hold design by making an orderly approach, using graphical aids and doing careful worst-case analysis

Sample-and-hold circuit design involves a series of difficult compromises, and keeping all of the variables, and their effects sorted out can be troublesome. But a carefully tailored approach, and the use of graphical aids, can make the job a lot easier.

A sample-and-hold circuit (Fig. 1) senses and stores the average value, over a simple interval, of a variable input signal. Three modes of opera-tion-sample, hold and reset-must be carefully analyzed in the design, and the characteristics of the switching circuitry are important too. Of these four separate design problems, the hold mode (Fig. 2) is the most critical. This is because circuit operation and performance is committed for the longest time interval.

Assume, for our design example, that the range of the input voltage $V_{i}$ must be 0.5 to 8 V , sample time $\mathrm{t}_{\mathrm{s}}$ must be 13.5 ms , and hold time $t_{\mathrm{h}}$ must be 140 ms . The circuit must operate from 0 to $100^{\circ} \mathrm{C}$ and have a maximum sample error $\epsilon_{\mathrm{s}}$ of $0.5 \%$ and maximum hold error $\epsilon_{\mathrm{h}}$ of $0.75 \%$. The sample error is to be defined as

$$
\epsilon_{\mathrm{s}}=\left[-\left(\mathrm{V}_{\mathrm{i}}-\mathrm{V}_{\mathrm{o}}\right) / \mathrm{V}_{\mathrm{i}}\right] \times 100 \%
$$

and the hold error as

$$
\epsilon_{\mathrm{h}}=\left\{\left[\left(\mathrm{V}_{\mathrm{o}}\right)_{\mathrm{t}=0}-\left(\mathrm{V}_{\mathrm{o}}\right)_{\mathrm{t}=140 \mathrm{~ms}}\right] /\left(\mathrm{V}_{\mathrm{o}}\right)_{\mathrm{t}=0}\right\} \times 100 \%
$$

## Begin with the hold circuit

The primary concern in the hold mode is to ensure that the output drift rate remains within specified limits. Drift rate is defined as

$$
\begin{equation*}
\frac{\mathrm{d} \mathrm{~V}_{\mathrm{o}}}{\mathrm{dt}}=\frac{\mathrm{I}_{\mathrm{LT}}}{\mathrm{C}_{1}} \tag{1}
\end{equation*}
$$

where $d V_{\mathrm{o}} / d t$ is the rate of change in output voltage time, $I_{L T}=V_{o} / R_{e q}$ is the op-amp bias current plus all leakage current, $\mathrm{R}_{\text {eq }}$ is the total equivalent leakable resistance and $\mathrm{C}_{1}$ is the integrating capacitor. Solving Eq. 1 for $\mathrm{R}_{\mathrm{eq}}$ results in

$$
\begin{equation*}
\mathrm{R}_{\mathrm{eq}}=\frac{\mathrm{t}_{\mathrm{n}}}{\mathrm{C}_{1}}\left\{\ln \left[\left(\mathrm{~V}_{\mathrm{o}}\right)_{\mathrm{t}=\mathrm{t}_{\mathrm{s}}} /\left(\mathrm{V}_{\mathrm{o}}\right)_{\mathrm{t}>\mathrm{t}_{\mathrm{s}}}\right]\right\}^{-1} \tag{2}
\end{equation*}
$$

[^2]

1. A basic sample-and-hold circuit consists of an integrator circuit and FET switches. The FETs, Q1 and Q2, provide maximum isolation between the input and the stored signal. Transistor Q3 removes charge stored on capacitor $\mathrm{C}_{1}$ during reset. A truth table shows all possible input combinations.

2. The hold-mode equivalent circuit contains leakage paths through which $\mathrm{C}_{1}$ discharges during the hold mode. Resistor $\mathrm{R}_{\mathrm{L} 3}$ is the OFF resistance of Q2 and Q3, and $\mathrm{R}_{\mathrm{LC}}$ is the leakage resistance of $\mathrm{C}_{1}$. The resistances establish the rate at which the output voltage drifts.

3. The sample-mode equivalent circuit design requires $\mathbf{R}_{\mathrm{O}_{1}} \ll \mathbf{R}_{\mathrm{i}} \ll \mathbf{R}_{\mathrm{L} 1}$. This results in minimum attenuation of the input voltage and achieves a further reduction of the integration error.
a function of the input offset voltage drifts and the input bias current drifts (curve b) is calculated as

$$
\begin{equation*}
\epsilon_{\mathrm{Td}}=\frac{\left(\mathrm{t}_{\mathrm{s}} / \mathrm{RC}_{1}\right)\left(\mathrm{I}_{\mathrm{b}} \mathrm{R}+\mathrm{V}_{\mathrm{od}}\right)+\mathrm{V}_{\mathrm{od}}}{\mathrm{~V}_{\mathrm{i}}} \% /{ }^{\circ} \mathrm{C} \tag{5}
\end{equation*}
$$

where $\mathrm{V}_{\text {od }}$ is the drift rate in $\mu \mathrm{V} /{ }^{\circ} \mathrm{C}$. Curve d is Eq. 5 projected to $100^{\circ} \mathrm{C}$.

The change in output voltage due to changes in supply voltage (curve c) is calculated as

$$
\begin{equation*}
\epsilon_{\mathrm{PS}}=\frac{\mathrm{PSRR}}{\left(\mathrm{~V}_{\mathrm{i}}\right)_{\mathrm{min} .}}\left(1+\frac{\mathrm{t}_{\mathrm{s}}}{\mathrm{RC}_{1}}\right) \% \tag{6}
\end{equation*}
$$

where PSRR is the power supply rejection ratio in $\mu \mathrm{V} / \mathrm{V}$.

## Use a trial-and-error technique

A design value of $\mathrm{t}_{\mathrm{s}} / \mathrm{RC}_{1}=1$ is selected from Fig. 3 on the basis of the following considerations:

- for the specified input voltage range, increasing gain to improve low-level accuracy causes the output to become saturated at the high-level inputs;
- signal attenuation causes system-performance degradation for all levels of input and may contribute to circuit instability;
- the error for this ratio is negligible and
- checking circuit performance is practical and easy.

The allowable drift rate is easily calculated. Summing the errors for $\mathrm{t}_{\mathrm{s}} / \mathrm{RC}_{1}=1$ gives a total error of $0.37 \%$. With a hold error of $0.65 \%$, the allowable drift error $\epsilon_{\mathrm{d}}=(0.65-0.37)=0.28 \%$. The worst-case error occurs for $\mathrm{V}_{1}=0.5 \mathrm{~V}$, and during a time interval of 140 ms . Thus, the allowable drift rate is $\mathrm{dV} \mathrm{o}_{\mathrm{o}} / \mathrm{dt}=(0.28)(0.5) / 140=$ $10.0 \mathrm{mV} / \mathrm{s}$.

For the calculation of $\mathrm{C}_{1}$, current $\mathrm{I}_{\mathrm{L} T}$ is determined for a worst-case temperature of $100^{\circ} \mathrm{C}$. The FETs used are both 2N4092 types with a maximum leakage current of 20 nA (at $100^{\circ} \mathrm{C}$ ). The maximum bias for the op amp is 10 nA , and

5. The reset-mode equivalent circuit applies during discharge of $C_{1}$. The input voltage is attenuated by $R_{01}$, where $R_{01} \ll R_{i}$, and isolated by $R_{L, 2}$. Unity gain compensation eliminates instability.
the capacitor leakage is 5 nA . The value of capacitor $\mathrm{C}_{1}$ is calculated from Eq. 1: $\mathrm{C}_{1}=[2(20)+$ $10+5] / 10=5.5 \mu \mathrm{~F}$. The closest standard capacitor is $5.6 \mu \mathrm{~F}$ with $1 \%$ tolerance and $-0.2 \%$ change over the temperature range.

With the op amp and integrating capacitor selected from hold-mode considerations, let's turn now to the design decisions determined by the sample mode, the reset mode and the switching circuit.

## Check the sample mode

An equivalent circuit representing the sample mode is shown in Fig. 4. From this equivalent the input resistance $\mathrm{R}_{\mathrm{i}}$-a fixed and variable resis-tance-is calculated. Since the error caused by the integrator is negligible, the integrator output

6. The FET leakage parameter $I_{\text {DGo }}$ which has a marked dependence on voltage (a) and temperature (b) is used to determine $\left(\mathrm{R}_{\mathrm{t}, 1}\right)_{\text {min }}$ or $\left(\mathrm{R}_{\mathrm{t},: 2}\right)_{\text {min }}$.

3. Plotting the calculable errors from the op-amp specs and its feedback components leads to an isolation of each error, highlighting troublesome
areas. The cumulative error is compared with the maximum allowable design error. A reasonable ratio of $t_{s} / R C_{1}$ is then selected.
where $t$ represents an interval such that $t_{s}<t<$ $t_{h}$. Resistance $R_{\text {eq }}$ can also be expressed in terms of the components of Fig. 2:

$$
\begin{equation*}
\mathrm{R}_{\mathrm{eq}}=\frac{1}{\frac{2}{\mathrm{R}_{\mathrm{L} 2}}+\frac{1}{\mathrm{R}_{\mathrm{LC}}}+\frac{1}{\mathrm{~A}_{\mathrm{o}} \mathrm{r}_{\mathrm{i}}}} \tag{3}
\end{equation*}
$$

where $R_{L 2}$ is the OFF resistance of each of two identical FET switches, $R_{\text {LC }}$ is the leakage resistance of the integrating capacitor and $A_{0} r_{i}$ is the equivalent leakage resistance of the op amp.

## Select the op amp

The selection of the op amp is now made on the basis of Eqs. 1-3. These expressions imply that an op amp with a low leakage current and high input impedance, such as the TOA7809, is needed to minimize drift rate.

Having selected the amplifier, we calculate the value of the integrating capacitor $\mathrm{C}_{1}$ by determining the maximum allowable drift rate that satisfies the design criteria. The drift rate is affected by error sources inherent within the amplifier and dependent on the circuit parameters. The major sources are the accuracy of integration, the offset voltage and current drifts with respect to temperature, and the power-supply rejection ratio.

The effects of these errors can be calculated from the known characteristics of the op amp
and its feedback components. Other sources of error-such as the effects of drift vs time and system degradation as a function of component life-are not readily calculable. For these effects a worst-case condition of $15 \%$ over-design is assumed and $\epsilon_{\mathrm{h}}$ is decreased from $0.75 \%$ to $0.65 \%$.

## Plot the error curves

A graph relating the calculable errors to a common normalized variable is shown in Fig. 3. The variable is the integration gain factor $t_{s} / \mathrm{RC}_{1}$, where $\mathrm{RC}_{1}$ is the integrator time constant.

The curves are generated from component values and specifications obtained from the manufacturer on the components selected. All curves are plotted for an input integrating resistance in the range $1-10 \mathrm{k} \Omega$.

The inaccuracy of integration (curve a) is approximated by the following expression:

$$
\begin{equation*}
\epsilon_{\mathrm{in}} \approx \frac{50(\mathrm{P}+1)}{\mathrm{A}}\left(\frac{\mathrm{t}_{\mathrm{s}}}{\mathrm{RC}_{1}}\right) \% \tag{4}
\end{equation*}
$$

where $P=\left(R_{0} / R_{1}\right)+\left(R / R_{i d}\right)+\left(R_{0} R / R_{L} R_{i d}\right), R_{0}$ is the amplifier output resistance, $\mathrm{R}_{\mathrm{L}}$ is the load resistance, $R$ is the integrating resistor, $R_{i d}$ is the amplifier differential input resistance and A is the open-loop gain of the op amp. The values for P and A are determined from the specification sheet on the particular amplifier chosen.

The percent change in the output voltage as
is described by the ideal case: $\mathrm{V}_{\mathrm{o}}=\mathrm{V}_{\mathrm{i}}\left(\mathrm{t}_{\mathrm{s}} / \mathrm{RC}_{1}\right)$, where $R=R_{i}+R_{L 1}$, and $R_{L 1}=30 \Omega$, the leakage resistance of the FET. The integration gain constant $\mathrm{t}_{\mathrm{s}} / \mathrm{RC}_{1}=1$ and $\mathrm{R}_{1}=13.5 / 5.6=2.41 \mathrm{k} \Omega$. A convenient form for $R_{i}$ is a $2.32 \mathrm{k} \Omega$ fixed resistance and a $200-\Omega$ potentiometer in series. Assuming initial adjustments within $\pm 0.05 \%$, the deviations of output voltage from initial adjustments range from $0.3 \%$ to $0.6 \%$ over the temperature range.

The equivalent circuit for the reset mode is shown in Fig. 5. This mode of operation normally places the amplifier in a potentially unstable state, because the input resistance is very large and the resistance in the feedback path has been made very small. However, since the actual input signal is being attenuated, the circuit configuration becomes that of a voltage follower and, with unity gain compensation, it remains stable during reset.

## The switching circuit is important, too

Another important factor in this design is the switching circuit, especially at the input. A design consideration is that semiconductor switches do not exhibit the ideal zero ON resistance nor infinite OFF resistance. And the bias voltage must be selected to ensure turn-on and turn-off at the correct times.

The finite resistances of the FETs cause voltage errors. However, with the components selected a typical value for this error is about $0.0005 \%$ in the initial amplitude of the output. For the hold mode this tends to aid rather than degrade
performance.
A more important consideration is ensuring the correct turn-on and turn-off times. The ON condition for a FET exists when $\left(\mathrm{V}_{\mathrm{i}}\right)_{\text {max }} \geqslant \mathrm{V}_{\mathrm{EE}^{-}}$ $\mathrm{V}_{\mathrm{RD}}$, where $\mathrm{V}_{\mathrm{EE}}$ is a positive gate voltage and $\mathrm{V}_{\mathrm{RD}}$ is the voltage required to keep the gate diode reverse-biased-normally about 50 mV .

For the OFF condition, the relation to be satisfied is $\left(V_{i}\right)_{\min } \leq V_{C C}-V_{\mathrm{FD}}-\mathrm{V}_{\mathrm{p}}$, where $\mathrm{V}_{\mathrm{CC}}$ is a negative gate voltage, $\mathrm{V}_{\mathrm{FD}}$ is the forward-voltage drop of the gate diode and $V_{p}$ is the pinch-off voltage. For the FETs selected, $\mathrm{V}_{\mathrm{RD}}=0.05 \mathrm{~V}$, $\mathrm{V}_{\mathrm{FD}}=0.6 \mathrm{~V}$ and $\mathrm{V}_{\mathrm{p}}=7.0 \mathrm{~V}$. The required gate voltages, with $0 \mathrm{~V} \leq \mathrm{V}_{\mathrm{i}} \leq 8.0 \mathrm{~V}$, are $\mathrm{V}_{\mathrm{EE}} \geq 8.05 \mathrm{~V}$ and $\mathrm{V}_{\mathrm{cc}} \leq-7.6 \mathrm{~V}$. The respective voltages selected are +15 V and -15 V .

The equivalent leakage resistance $\mathrm{R}_{\mathrm{eq}}$ (see Eq. 3 ) is now calculated from the leakage parameters of the FETs. This calculation is important because it represents the leakage path for current while in the hold mode.

In the hold mode, the leakage path between the drain and gate is significant. To determine the minimum value of this resistance ( $\mathrm{R}_{\mathrm{L} 1}$ or $\mathrm{R}_{\mathrm{L} 2}$ ) consider the curve shown in Fig. 6a. The value of $\mathrm{I}_{\mathrm{Dg}}$, the drain-to-gate leakage with the source open, for $\mathrm{V}_{\mathrm{DG}}$ equal to -15 V is approximately 50 pA or 0.5 nA . This value of $\mathrm{I}_{\mathrm{DGO}}$ is projected to the $25^{\circ} \mathrm{C}$ point on the curve of Fig. 6b.

Since the gate leakage current vs temperature is linear, the value of $\mathrm{I}_{\mathrm{Dg}}$ may be extrapolated to the $100^{\circ} \mathrm{C}$ point for the worst-case. Thus, $\mathrm{I}_{\mathrm{DGO}}$ at $100^{\circ} \mathrm{C}$ is approximately 3.5 nA . However, the maximum value of $\mathrm{I}_{\mathrm{DGO}}$ for $\mathrm{V}_{\mathrm{DG}}=$

7. The final sample-and-hold circuit design includes all of the components selected, biasing for the FET switches, the compensation network and
the output of the op amp set adjustment. The adverse effects of worst cases are compensated for by an over-design technique.

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8. Measured data of the sample-and-hold design show the drift rate (a) plotted for one-second averages. Drift rates are significantly smaller for $\mathrm{t}_{\mathrm{h}} \ll 1 \mathrm{~s}$. The accuracy with which a given input is reproduced at the output is shown in b. Plotted in $c$, the percent change in the output voltage.

20 V is 0.2 nA from the specification sheet for the 2N4092 transistor.

Projecting this point on to the curve in Fig. 6 b , and extrapolating to the $100^{\circ} \mathrm{C}$ point, the maximum value of $\mathrm{I}_{\mathrm{Dg}}$ for $\mathrm{V}_{\mathrm{DG}}=15 \mathrm{~V}$ is found to be approximately 20 nA . The minimum value of $\mathrm{R}_{\mathrm{L} 1}$ or $\mathrm{R}_{\mathrm{L} 2}$ in the integrator circuit is calculated: $\mathrm{R}_{\mathrm{L} 1}=\mathrm{R}_{\mathrm{L} 2}=\mathrm{V}_{\mathrm{DG}} / \mathrm{IDGO}=750 \mathrm{M} \Omega$. The equivalent leakage resistance is, from Eq. $3, \mathrm{R}_{\mathrm{eq}}=1 /\left[2 / 750 \times 10^{6}\right.$ $\left.+1 / 16 \times 10^{10}+1 / 540 \times 10^{6}\right]=221 \mathrm{M} \Omega$.

The basic sample-and-hold circuit design is now complete, with the final schematic as shown in Fig. 7. Graphs of measured data for the circuit are shown in Fig. 8. The test data show circuit accuracy in duplicating the input signal amplitude and the accuracy in holding this amplitude for a specified period of time. Drift rates for different amplitudes of output over the temperature range for steady-state non-switching conditions are shown in Fig. 8a. Figures 8 b and 8 c show circuit performance over the temperature range $0-100^{\circ} \mathrm{C}$ for switched conditions. Except for the region below 1.0 V , the amplitude is reproduced to an accuracy of $0.25 \%$ while the droop is held to $0.1 \%$.


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## Improve vhf amps with an attenuator to separate the main amp from the feedback path. You get less distortion and increased stability.

When designers put automatic level control in a high-frequency amplifier, they normally feed back part of the output signal directly to an active amplifier transistor. This gives the proper control all right, but it also introduces such unwanted side effects as these: second-order and cross-modulation distortion, bandpass deviations, possible amplifier instability and mismatching. These conditions can be minimized by setting up a variable attenuator as the key feedback element.

The attenuator uses components that are nonlinear at low frequencies but that exhibit low distortion at vhf frequencies. Input and output impedances are maintained constant, independent of the attenuation value. With this level control, the active amplifier element is not part of the feedback path.

In addition to the inter-stage attenuator, of course, the control loop consists of an output rf coupler, an rf preamplifier, a detector and a dc amplifier that drives the control elements (see Fig. 1). In the ideal case, the variable-attenuator transfer function is the inverse of an errorproducing function.

To determine the over-all design requirements on the loop, let's define $\mathrm{E}_{\mathrm{i}}$ as the nominal input voltage and $\Delta$ as any undesirable change from the nominal voltage. The ratio change $\mathrm{E}_{\mathrm{R}}$ of the input voltage for a given $\Delta$ is

$$
\begin{equation*}
\mathrm{E}_{\mathrm{R}}=\left(\mathrm{E}_{\mathrm{i}}+\Delta\right) / \mathrm{E}_{1} . \tag{1}
\end{equation*}
$$

The transfer function $\mathrm{A}_{\mathrm{T}}$ of the attenuator to compensate for this change is

$$
\begin{equation*}
\mathrm{A}_{\mathrm{T}}=\mathrm{E}_{\mathrm{i}} / \mathrm{E}_{\mathrm{i}}+\mathrm{e}_{\mathrm{t}}, \tag{2}
\end{equation*}
$$

where $e_{f}$ represents an equivalent feedback voltage applied to the attenuator. This feedback equivalent is, in turn, related to the change in output voltage from its nominal value by a factor b , so that

$$
\begin{equation*}
\mathrm{e}_{\mathrm{t}}=\mathrm{b} \Delta \mathrm{eo}, \tag{3}
\end{equation*}
$$

where $\Delta \mathrm{eo}$ is the change in output voltage for a given change in input voltage, and $b$ is some conversion factor that proportionally relates the

[^3]

1. Automatic level control loop uses attenuator action apart from the active elements. An optimum design requires an appropriate gain distribution among the various blocks, as well as bias stability for each block.
feedback quantity $e_{t}$ to the change in output voltage. The quantity $b$ includes conversions of the rf signal to a voltage corresponding to an attenuation factor rather than to an equivalent $\mathbf{r f}$ feedback voltage, as in the usual feedback loop. Its value for these purposes can be greater than unity.

For a given open loop gain of $A_{i}$ in the forward loop of the amplifier,

$$
\begin{equation*}
\Delta \mathrm{e}_{\mathrm{o}}=\mathrm{A}_{\mathrm{i}} \mathrm{E}_{\mathrm{i}}\left(\mathrm{E}_{\mathrm{R}} \mathrm{~A}_{\mathrm{T}}-1\right) . \tag{4}
\end{equation*}
$$

Substituting from Eq. 3 for $\Delta \mathrm{e}_{0}$ gives
$\mathrm{e}_{\mathrm{t}}=\mathrm{bA} \mathrm{A}_{\mathrm{i}} \mathrm{E}_{\mathrm{i}}\left(\mathrm{E}_{\mathrm{R}} \mathrm{A}_{\mathrm{T}}-1\right)$,
and substituting $\mathrm{e}_{\mathrm{t}}$ from Eq. 3 in Eq. 2 results in

$$
\begin{align*}
\mathrm{A}_{\mathrm{T}} & =\frac{\mathrm{E}_{\mathrm{i}}}{\mathrm{E}_{\mathrm{i}}+\mathrm{bA}_{\mathrm{i}} \mathrm{E}_{\mathrm{i}}\left(\mathrm{E}_{\mathrm{R}} \mathrm{~A}_{\mathrm{T}}-1\right)}  \tag{5}\\
& =\frac{1}{1+\mathrm{bA}\left(\mathrm{E}_{\mathrm{R}} \mathrm{~A}_{\mathrm{T}}-1\right)} . \tag{6}
\end{align*}
$$

Ideal control requires that $A_{T}=1 / \mathrm{E}_{\mathrm{R}}$, or $\mathrm{E}_{\mathrm{R}} \mathrm{A}_{\mathrm{T}}=1$. But this leads to the trivial case $\mathrm{A}_{\mathrm{T}}=1$, a condition that exists only when there is no input error. This means that for $\mathrm{A}_{\mathrm{T}} \neq 1$, the condition of $\mathrm{E}_{\mathrm{R}} \mathrm{A}_{\mathrm{T}}=1$ cannot exist and some output error must always be present.

To minimize this error, it is necessary that $\left|\mathrm{E}_{\mathrm{R}} \mathrm{A}_{\mathrm{T}}-1\right|$ be minimized. Equivalently, from

|  | Advantages | Disadvantages |
| :--- | :--- | :--- |

Eq. 6, this magnitude is expressed as follows:

$$
\begin{equation*}
\left|\mathrm{E}_{\mathrm{R}} \mathrm{~A}_{\mathrm{T}}-1\right|=\left|\frac{1-\mathrm{A}_{T}}{\mathrm{~b} \mathrm{~A}_{\mathrm{i}} \mathrm{~A}_{\mathrm{T}}}\right| \tag{7}
\end{equation*}
$$

For any given $A_{T}$, the magnitude $\left|E_{R} A_{T}-1\right|$ is minimized as the quantity $\mathrm{bA}_{i}$ increases. Therefore the greater the open loop gain $\mathrm{A}_{1}$ and the greater the transfer ratio b, the greater the level stability. With the transfer ratio constant, an increase in input error results in increased attenuation and output error. Output error limit, as a function of expected input error, is therefore defined for maximum attenuation.

## Select the attenuator

The design of the level control begins with the selection of a configuration and control element for the variable attenuator. Any of several configurations can be used. These are listed in Table 1, along with their main advantages and disadvantages. Not shown are the coupling capacitors needed for dc isolation between elements, and the isolation chokes and bypass capacitors needed for coupling to the dc amplifier.

The selection of a configuration requires a
tradeoff between complexity, cost, range of attenuation and ability to maintain input and output impedances over the attenuation range. The latter factor affects loading of the stages and is a determining one in maintaining passband shaping and in ensuring gain stability of the stages. Because of this factor, both the L-pad and balanced bridge arrangements, which don't maintain all impedances constant, are more limited in use. The bridged T-pad requires only two variable elements in comparison with the three elements required by the T-pad. It therefore is slightly cheaper and less complex than the T-pad and is more widely used.

Some commonly used control elements are described in Table 2, together with the advantages and disadvantages of each type. The ideal control element:

- Does not dissipate de power.
- Does not introduce distortion to signals.
- Achieves a wide range of impedance values for low de control voltages.
- Has a high- Q variable reactance element or a purely resistive variable resistance element for all frequencies in the amplifier passband.
- Is environmentally stable.


## Table 2. Compare the control elements

| Elements | Advantages | Disadvantages |
| :--- | :--- | :--- |
| P-i-n diode | Wide control range for small signal current. | Expensive. |
| Thermistor | Low shunt capacitance. <br> Relatively inexpensive. <br> Low distortion at vhf. | Temperature sensitive. <br> Low impedances require large drive current. |
| Voltage-variable <br> capacitive diode | Dissipates no power. <br> High-Q element. <br> High reliability. | Reactive device. <br> Limited control range. |
| Photodiode | Isolates rf and dc control circuits. | Sensitive to environment. <br> Low impedances require large drive current. <br> Limited life. |


2. The control element determines the attenuator currents. Shown here is the characteristic curve of the control element selected (a). From the curve and the calculated attenuator-resistance values, the required range of currents is determined for the design specs. (b).

- Simplifies isolation between rf circuitry and de control circuitry.
- Is small, light, and inexpensive.
- Is highly reliable.

From the entries in Table 2, it is obvious that no control device is perfect, and tradeoffs among characteristics are necessary to select a good device.

Once an attenuator is selected, the design of a feedback loop can proceed. Consider, for example, an automatic level control with the following specifications: attenuation range of 12 dB , allowing voltage control of $+100 \%$ and $-50 \%$ of the nominal input level ; attenuator input and output impedances of $75 \Omega$; initial attenuator insertion loss of 2 dB (maximum) ; loop tightness of $\pm 0.5 \mathrm{~dB}$ change in output level for $\pm 6 \mathrm{~dB}$ change in input level; nominal output level at reference output of 100 mV rms (for a single carrier or the rms composite of several carriers) and a response time (for output stabilization with a delta change at the input) of 10 ms .

For this example, the bridged-T configuration is used because it is the simplest configuration having constant input and output impedances. An

3. The bridged-T attenuator maintains constant input and output impedances as two elements, which can be varied by a single control, are varied simultaneously.

HP 3001 p-i-n diode is used as the control element because of its wide control range for small signal current. The diode characteristic curve is shown in Fig. 2 a .

## Seven steps to design

The design procedure is separated into seven parts, with each devoted to a critical component or parameter:

1. The attenuator currents are determined by first calculating the bridged-T component values. These are found from commonly known formulas ${ }^{1}: \mathrm{R}=$ characteristic impedance $=75 \Omega$, $R_{1}=R(K-1)$, where $K=\sqrt{N}$ and $N$ is the ratio of input to output power levels and $R_{2}=R /$ ( $\mathrm{K}-1$ ). For an initial insertion loss of 2 dB , $\mathrm{N}=1.56$ and thus, $\mathrm{R}_{1}=19.5 \Omega$ and $\mathrm{R}_{2}=290 \Omega$ (see Fig. 3).

Including the attenuation range with the initial insertion loss yields a total of 14 dB , for which $\mathrm{N}=25, \mathrm{R}_{1}=300 \Omega$ and $\mathrm{R}_{2}=19 \Omega$. And, finally, for a midway attenuation value of $8 \mathrm{~dB}, \mathrm{R}_{1}=$ $113 \Omega$ and $\mathrm{R}_{2}=50 \Omega$.

To achieve these resistance values, the current
values in Fig. 2b are required. This shows that a current swing of $(5-0.08) \approx 5 \mathrm{~mA}$ is needed to achieve a $14-\mathrm{dB}$ attenuation range. And a maximum current of only 5 mA is required at the minimum attenuation value of 2 dB . Since this is readily achieved, the requirement of maximum initial insertion loss of 2 dB is also easily achieved.
2. The feedback ratio is found from the design specifications. The maximum output variation is $\pm 0.5 \mathrm{~dB}$ for a $\pm 6 \mathrm{~dB}$ change in input. This corresponds to an output level change of $\pm 5 \mathrm{mV}$ rms for a nominal output level of 100 mV rms. Therefore an output error range of 10 mV rms results in a current swing of 5 mA through the attenuator diodes for 12 dB of attenuation control. Calculating the feedback ratio: $\mathrm{b}^{\prime}=(5 \mathrm{x}$ $\left.10^{-3}\right) /\left(10 \times 10^{-3}\right)=0.5 \mathrm{~mA} / \mathrm{mV}$.
3. The output coupler is selected on the basis of three factors: minimal loading to the output signals, minimal insertion loss as voltage is transferred to the rf amplifier and stable transfer ratio with environmental changes.

The coupling device that best fulfills these objectives is a passive directional coupler. It isolates the feedback loop from the forward loop and transfers the voltage at a prescribed tap loss with minimal insertion loss. And being entirely passive, it can remain very stable with time and temperature.

A $10-\mathrm{dB}$ tap loss is reflected as $0.5-\mathrm{dB}$ loss in output level. A $100 \pm 5.0-\mathrm{mV} \mathrm{rms}$ output level is then sampled as a $33 \pm 1.6-\mathrm{mV}$ rms signal level at the output of the coupler.
4. It's preferable to place most of the feedback gain in the rf amplifier because its output-level stability can be greater than that of the dc amplifier. But the total gain in the rf amplifier is limited by cost and space (much higher gain per stage can be achieved at dc than at the vhf frequencies), detector level and rf amplifier stability. The most significant factor is amplifier stability, since any change of level in the feedback path, before the comparator, is directly reflected as an error in output level. This is due to the fact that the output level must shift in an equal and opposite direction to the feedback change to maintain a signal level at the comparator equal to that of the reference.

With an rms input voltage to the rf amplifier of 35 mV , yielding a peak level of 50 mV , the voltage gain of the rf amplifier is limited to a value of $8: 1$ ( 19 dB in the matched case). This results in a peak voltage level of about 300 mV to the diode detector. With an input level of 33 $\pm 1.67 \mathrm{mV}$ and a voltage gain of $8: 1$, the rf amplifier presents a signal of $264 \pm 13.3 \mathrm{mV} \mathrm{rms}$ to the detector.
5. An efficient rf detector has the qualities of low shunt capacitance and fast inverse recovery time. The detector should also be charged from a low-impedance source in the interest of speed. And it should work into a high impedance load so the peak signal is maintained.
These characteristics determine the conversion efficiency of the detector-the ratio of detected dc level to peak level of the rf signal. The higher the efficiency, the greater the gain of the feedback loop for given rf amplifier and dc amplifier gains. In effect, high efficiency of the rf detector means, for a given loop tightness, less stringent gain requirements for the feedback amplifiers.

This is important because conversion efficiencies can range from almost nothing to a maximum of unity. A conversion efficiency of 0.75 is considered quite good. A loop with a detector of this efficiency, for a given loop tightness, needs only one-half the gain in its amplifiers compared with a loop that has a detector efficiency of only 0.375 .
6. The problem of dc level instability exists because the diode detector usually has some forward bias, which also supplies bias current to the dc amplifier. Changes with temperature in forward voltage drop across the diode detector are usually reflected as changes in current through the dc amplifier-and therefore as changes in signal through the attenuator control element.

This type of change essentially results in a bias on the control element that is independent of the error signal and that thus serves to change the signal on the control element and the attenuation. Sensing of this change by the feedback loop results in a slight change in output level. The sensed error is $e_{o}=e_{t} / b$.

One arrangement to assure dc stability is shown in Fig. 4a. The de amplifier is assumed to be fully stabilized by operational feedback and by internal compensation of the base-to-emitter junction of its first stage. The dc amplifier is also assumed to have a very high input impedance. The detector diode junction drop is then compensated for by the use of an additional diode of the same type, which tracks the change in detector diode voltage drop with temperature, so that the input voltage to the dc amp remains stable.

A much more accurate means-but also a more costly and complex one-of assuring de stability is shown in Fig. 4b. A balanced diode pair (unbalanced by rf detection) feeds a balanced differential dc amplifier. One diode-amplifier pair balances the other, so that without a change in rf signal, a balanced output level is maintained independent of temperature changes.

The advantage of this scheme is that all parameters that are a function of temperature are compensated by the fully balanced arrangement.

4. Compensation networks protect against dc level instabilities from the detector-dc-amp combination. A simple circuit compensates for the detector voltage drop (a). A more complex circuit provides a balanced scheme, compensating temperature-sensitive parameters (b).

The limitation in using this arrangement is that of securing inexpensive balanced detector diodes and transistors that can closely track each other's parameters as a function of temperature.

If the detector conversion efficiency is assumed to be about 0.707 , then an rms level of $264 \pm 13.3 \mathrm{mV}$ from the output of the rf amplifier yields a peak detected dc level of $264 \pm 13.3$ mV at the input of the dc amplifier.
7. System considerations can determine part of the dc amplifier design. One factor is that response time is limited by the lowest frequency of modulation, when AM carriers are used as sam-
pled signals. A second factor is that tight loops that require high-gain dc amplifiers can be obtained with a tradeoff of fast response time for loop tightness. In this case, the option is not needed.

Achieving the required response time calls for the use of a dc amplifier with a $3-\mathrm{dB}$ bandwidth of 35 Hz . If the sampled rf signal contains amplitude modulation, then the lowest modulating frequency should be about ten times greater, or 350 Hz .

The currrent gain of the dc amplifier is calculated from the amplifier input impedance $\beta \mathrm{R}_{\mathrm{E}}$, where $\beta$ is the current gain in the first stage and $\mathrm{R}_{\mathrm{E}}$ is the emitter resistance. For the design example, the current gain $A_{1} \approx 0.2 \beta \mathrm{R}_{\mathrm{E}}$, where $\mathrm{R}_{\mathrm{E}}$ is in ohms. The required loop tightness and response time are achieved with $\beta=100$ and $\mathrm{R}_{\mathrm{E}}=50 \Omega$ : the required dc amplifier gain is about 1000 .

With the dc amplifier gain calculated, the basic design procedure is formulated. The final blockdiagram design is shown in Fig. 5. For the de amp, only the input and output stages are indicated. The p-i-n-diode attenuator is shown with its coupling networks to the rf signal source and to its de control source.

In the bridged-T configuration, the series diode and shunt diode resistances move in impedance directions opposite to each other as a function of attenuation. This lends the attenuator to a differential amplifier drive as the control. The use of this amplifier also increases the over-all level stability of the dc amplifier and reduces balanced interference, such as hum and noise, from the dc supply.

## Reference:

1. Reference Data for Radio Engineers, Fourth Edition, ITT Corp., pp. 250-253.

2. In a typical level-controlled amplifier, shown here in block form, proper decoupling of rf signals and coupling
of dc control signals are essential for the required frequency response over all attenuator values.

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## ideas for design

## Long-period analog timer uses small capacitors

Analog timers using integration techniques generally require large, unwieldy integrating capacitors to obtain long time intervals.

But if a high-input impedance buffer amplifier is inserted into the integrator circuit, the effect of input impedance, offset, and bias current in the integration amplifier are minimized, allowing a high-quality capacitor of reasonable size and cost to be used in the timing circuit (see diagram).
Op amp $\mathrm{A}_{1}$ serves as a high-input impedance buffer, isolating the timing network $R_{3} \mathrm{C}_{1}$ from the integrating amplifier $\mathrm{A}_{2}$. Op amp $\mathrm{A}_{3}$ is connected as a Schmitt trigger, with $D_{2}$ and $D_{3}$
furnishing a stable reference voltage for the trigger trip point.

Transistor $Q_{1}$ and $Q_{2}$ serve as saturated switches sourcing the voltage divider $\mathrm{R}_{18}-\mathrm{to}-\mathrm{R}_{28}$ for timing selections. The timer output is negative, but reversing $\mathrm{D}_{1}$ produces a positive output pulse.

Resistor $R_{6}$ is adjusted to minimize drift measured at the output of $A_{2}$ and with the wiper of $\mathrm{S}_{1}$ grounded. Resistor $\mathrm{R}_{18}$ is adjusted to set 0.5 minutes timing-interval accuracy.
D. L. Ruhberg \& F. R. Wuensche, Narco BioSystems, Inc., Houston, Tex. 77017.

Vote for 311



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## EKG detector improves heart-rate measurement

Standard EKG detectors employ Schmitt trigger circuits. But these detectors can fail to detect heart beats because of noise or a wildly varying EKG signal-as when unrestrained activity affects the EKG signal.

A circuit that reliably detects EKG signals is shown in Fig. 1. This circuit can also be used to detect an EKG signal that is being telemetered from a remote location.
The input amp (A1) differentiates the EKG signal coming from the preamp. The nonlinear characteristics of the following diode network improve the signal-to-noise ratio. For small signals the impedance of the diodes is large and these signals are greatly attenuated.
The signal is then passed through a low-pass filter, which has variable gain capability. The filter is followed by a standard level detector whose detection point is adjustable via the bias potential provided by emitter follower Q1. This allows the system to discriminate against any small residual signals which might get through the low-pass filter.

The output of the level detector is a pulse with an approximate peak-to-peak value of 1.5 V . The level detector is followed by a monostable multivibrator (Q2 and Q3), which produces an $80-\mathrm{ms}$
signal for each negative transition of the level detector.

Transistor Q4 is provided to inhibit the output of the monostable circuit during periods of heartrate reinforcement, which can take the form of shock treatment to stimulate particular heart rates. Shock periods often lead to spurious counts.

Transistor Q5 provides an interface for the remainder of the system.

Typical output data are shown in Fig. 2 for a standard detector and the EKG detector described.

Gordon Silverman, Research Associate, Rockefeller University, New York, N. Y. 10021.

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2. Heart beats are missed when using a standard detector for very large disturbances (a). Even in 'normal' conditions, the EKG signal can vary widely (b). The EKG detector handles both easily.


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## Digital multiplexing without a multiplexer

It's possible to multiplex four 4-bit words without using a multiplexer.

The standard approach (a) for reformating a single 16 -bit word into four 4 -bit words is very straight forward. The system control inputs are LOAD-to load the 16 -bit word into the 16 -bit holding register-and INCREMENT-to format each 4 -bit word. The 2 -stage counter and decoder provide the four steer inputs needed by the 4 -bit multiplexer, which performs the formating operation.

But with reconnection (b) the multiplexer is eliminated. The four MSI devices shown are serial/parallel shift registers, with the numbers in the boxes showing how the 16 -bit word is loaded into the 16 -bit register. The increment signal is used as the serial/parallel shift clock, and the load signal is used as the parallel enable signal. Each 4 -bit register shifts right one bit with each increment signal not accompanied by the load signal.

Edward G. Linde, Senior Engineer, Radiation Systems Div., Harris-Intertype Corp., Melbourne, Fla. 32901

Vote for 313


A simple reconnection of the standard multiplexer circuit (a) results in a multiplexing system without the multiplexer (b).


## Fix lag/lead relation in digital $90^{\circ}$ phase shifter

In an ordinary $90^{\circ}$ phase shifter, two $180^{\circ}$ out-of-phase signals are fed into two flip-flops and a quadrature output is obtained. The problem with this type of phase shifter is that the output signals can be either leading or lagging one another, depending on the state of the Q outputs prior to signal application. And you don't know which leads and which lags.

This problem is eliminated with a single transistor phase locking circuit (a). When the Q output of FF2 goes positive it produces a negative going pulse at the collector of Q1. This pulse is applied to the clear input of FF 1, setting its Q output to a logic ZERO before the positive edge of its input signal arrives. Thus the Q output of FF 1 always lags the Q output of FF 2 by $90^{\circ}$ (b).

The time constant in the base circuit of Q1 is set to ensure that the clear pulse at FF 1 is not present when the leading edge of the input signal arrives.

William Lennox, Electronic Engineer, Microdyne Corp., Box 1527, Rockville, Md. 20850.

Vote for 314


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## Voice-operated switch dissipates only $600 \mu \mathrm{~W}$

Voice-triggered switches generally find limited use because of their power consumption, external component requirements and physical size. But all of these disadvantages can be minimized, as shown in the diagram.

A $\mu \mathrm{A} 735$ op amp is used as the comparator circuit. Whenever the voice level is above the threshold level set by R1, there is a negative output from the $\mu \mathrm{A} 735$. This output is used to trig-
ger the CD4007, which is connected as a lowpower monostable multivibrator. The circuit has a total standby power drain of $600 \mu \mathrm{~W}$.
Resistor R2 and capacitor C2 provide a 2 -second time constant. The charge on C2 is reset each time the CD4007 is triggered, and the output level trips when the first voice signal is detected. It does not reset until 2 seconds after the last voice signal.

Larry Mellenbruch, University of Texas P.O. Box 8029, 10000 FM Road 1325, Austin, Tex. 78712.

Vote for 315


Low-power, minimum components and small size characterize this voice-operated switch (a). The

output level sets when the voice signal is above the threshold level (b).

By forward-biasing or reverse-biasing common transistors to the breakdown point, a number can be made to exhibit negative-resistance characteristics. This allows transistors to be used in applications that normally require more sophisticated components.

An example of the reverse-breakdown case is shown in the diagram-an expanded dc point-topoint plot of the breakover point of the 2 N 2218 .

To linearize a characteristic curve, the collector and base are connected together through a resistor, so that the base breaks down first, rounding off the sharp foldover in the curve. The collector starts conducting at the point corresponding to the intersection of the base-toemitter and collector-to-emitter curves. The remainder of the characteristic is then a composite of the two, which, if properly proportioned, results in a linear curve.

A series resistor in the collector-to-emitter circuit enables the vertical slope to be changed from negative to positive without changing the shape of the curve.

Richard L. Phares and Robert N. Luther, Huntsville, Ala.

Vote for 316

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## Eliminate alignment problems in FM detectors

Most FM detection circuits include an inductive coil that requires alignment. But with a circuit employing a $\mu$ A754 IC and a ceramic filter, no circuit adjustment is necessary (a).

The $\mu \mathrm{A} 754$ consists of a high gain 4 -stage limiting i-f amplifier, a doubly balanced quadrature detector and a driver amplifier. The normal quadrature coil has been replaced by a four-pole ceramic filter-which can be used because a separate i-f output terminal is available on the $\mu$ A754.

Resistors $\mathrm{R}_{1}$ and $\mathrm{R}_{2}$ are added to provide the required source matching, and $R_{2}$ also provides a dc bias path for the detector (normally provided through the quad coil). Typical ceramic filter impedances are $1 \mathrm{k} \Omega$ or less. At these low impedance levels the internal 4 -pF capacitor has little effect on performance and may be neglected.

Performance data (b) is given for a 4.5 MHz signal frequency.

Rodney Smith, Fairchild Semiconductor, Mountain View, Calif. 94040.

Vote for 317


Costs are saved by eliminating the tuning coil in this FM detector and i-f amplifier circuit (a). The
data show that ceramic band-pass filters are usable as frequency/phase converting elements.

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[^4]

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Hybrid Systems Corp., 95 Terrace Hall Ave., Burlington, Mass. Phone: (617) 272-1522. P\&A: see text; stock.

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The DAC372-8, $-10,-11$ and -12 are $8,10,11$ and 12 -bit medi-um-speed voltage-output units.

They feature a $10-\mu$ s settling time. The DAC372i-8, -10, -11 and -12 are $8,10,11$ and 12 -bit highspeed current-output units. They feature a fast $300-\mathrm{ns}$ settling time.

The third class is the DAC372WB. It consists of the DAC372-WB-8, $-10,-11$ and -12 which are $8,10,11$ and 12 -bit high-speed voltage-output units. They are extremely fast, settling in 950 ns .

All the DAC372 converters drift only $30 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ (except 8 -bit models which drift $50 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ ). All will operate from $\pm 5$ or +10 V as standard. An option is available for operation from $\pm 10 \mathrm{~V}$.

Each of the converters is pincompatible with each other having dual-in-line spacing.

Prices are as follows: $\$ 39, \$ 49$, $\$ 69$ and $\$ 75$ for $8,10,11$ and 12 -bit units, respectively, of the mediumspeed voltage-output and highspeed current-output converter classes. The high-speed WB class of voltage-output converters cost $\$ 84$, $\$ 94, \$ 114$ and $\$ 120$, for $8,10,11$ and 12 -bit models, respectively. All prices are for quantities of 1 to 9 .

## 1-MHz vector module computes $\sqrt{\mathrm{X}^{2}+\mathrm{Y}^{2}}$



Intronics, Inc., 57 Chapel St., Newton, Mass. Phone: (617) 332-7350. P\&A: \$95; stock.

Model VM101 vector operator module computes $\sqrt{\mathrm{X}^{2}+\mathrm{Y}^{2}}$ to $1 \%$ accuracy with a $1-\mathrm{MHz}$ bandwidth. This analog computation would normally require 3 analog multipliers connected together and the output bandwidth would vary with signal level. In the VM101 the bandwidth is independent of the signal level. The computation is made instantaneously, but can be averaged by the addition of an external capacitor.

CIRCLE NO. 252

## Four-digit DVM uses LED display



Data Technology Corp., 1050 E. Meadow Circle, Palo Alto, Calif. Phone: (415) 321-0551. $P \& A$ : $\$ 749$; 2 wks.

A new 4-digit DVM displays its readings with LEDs. Model 351 is fully compatible with earlier model 350 , accepting all plug-in card options interchangeably. The 351 incorporates dual-slope integration, is autoranging, has $80 \%$ overrange capability and four de ranges. Readings of 1 to 10 per second are determined by manual setting.

CIRCLE NO. 253

## $60-\mathrm{MHz} 5-\mathrm{mV} / \mathrm{cm}$ scope improves triggering



Dumont Oscilloscope Laboratories, Inc., 40 Fairfield Pl., West Caldwell, N J. Phone: (201) 228-3665. P\&A: $\$ 2045$ (rack-mount version); 45 days.

Model $106260-\mathrm{MHz}$ scope features a unique triggering system which offers flat full-bandwidth trigger sensitivity, settable trigger levels on small signals and independence between position and triggering controls. The scope has $5-\mathrm{mV} / \mathrm{cm}$ sensitivity, calibrated delayed sweep, variable hold-off and an 8 -by- $10-\mathrm{cm}$ screen.

## Tiny 3-1/2-digit DPM dissipates but 4.2 W



Electro-Numerics Corp., 2961 Corvin Dr., Santa Clara, Calif. Phone: (408) 738-1840. P\&A: \$189; stock.

The model 300 DPM is a bipolar 3-1/2-digit meter featuring small size, low cost and only 4.2 W of power consumption. It zero-drifts $2 \mu \mathrm{v} /{ }^{\circ} \mathrm{C}$, full-scale drifts 30 ppm $/{ }^{\circ} \mathrm{C}$ and needs zero warmup time. Standard BCD outputs and a high degree of noise immunity are available. The Model 300 has a resolution as low as $100 \mu \mathrm{~V}$.

CIRCLE NO. 255

## AM/FM sweep generator has a digital synthesizer



Telonic Industries, Inc., 21282 Laguna Canyon Rd., Laguna Beach, Calif. Phone: (714) 494-9401. P\&A: \$1250; Sept. 1971.

The model 1019 AM/FM 0.25-to-$115-\mathrm{MHz}$ sweep generator includes a digital frequency synthesizer. All functions are pre-programmed with front-panel pushbutton selectors, covering rf and i-f frequencies with less than $1 \%$ distortion. Automatic frequency tracking allows "hands off" alignment, with up to 30 frequency markers.

## Low-cost generators span 0.03 Hz to 3 MHz



Interstate Electronics Corp., Box 3117, Anaheim, Calif. Phone: (714) 772-2811. $P \& A: \$ 295$ to $\$ 495$.

Four new low-cost function generators span 0.03 Hz to 3 MHz . Series 30 generators, designated F31 through F34, feature output amplitudes of 10 V pk-pk into $50 \Omega$ with voltage offset designed into each instrument. Output waveforms include sine, square and triangle with variable width pulse and sweep available.

CIRCLE NO. 257

Battery powered scope
is 3 instruments in 1


Hewlett-Packard Co., 1501 Page Mill Rd., Palo Alto, Calif. Phone: (415) 493-1501. P\&A: $\$ 2700$ (plus $\$ 200$ for battery); March, 1972.

A new portable scope combines waveform storage, variable persistence and a $35-\mathrm{MHz}$ lab scope into one instrument that operates on battery power. In the storage mode, the 1703 A retains a waveform for more than an hour. In the variable-persistence mode, the trace can be made to fade out within $1 / 4$ to 1 minute. In the normal mode, the CRT has the persistence of P31 phosphor ( $40 \mu \mathrm{~s}$ ).

CIRCLE NO. 258

## Dual CRT scope multiplexes input

Vu-Data Corp., 7595 Convoy Court, San Diego, Calif. Phone: (714) 279-6572. $P \& A: \$ 1695$ : stock to 30 days.

A new systems scope provides dual-CRT multiplexed displays in only $1-3 / 4 \mathrm{in}$. of panel height, each offering a de-to- $10-\mathrm{MHz}$ bandwidth. Two rows of front-panel pushbuttons allow selective display of 14 rear-panel and 2 front-panel signals on the MS700A. Any of 8 channel A inputs can be displayed on one CRT while any one of 8 channel $B$ inputs is displayed on the other.

CIRCLE NO. 259

## Real-time analyzer spans 0.03 Hz to 50 kHz

Spectral Dynamics Corp., Box 671, San Diego, Calif. Phone: (714) 278-2501.

By adjusting a 3-digit positioner on the front panel of the new SD301C real-time analyzer, the operator can easily move a bright spot across the CRT screen until it rests exactly on that point of a spectrum whose frequency he wishes to determine. The component's frequency is within 1 part in 1000 of the total frequency range in which he is working. Range of the analyzer is from 0.03 to 50 kHz .

CIRCLE NO. 260

## DMM with 27 ranges costs only $\$ 385$

Hickok Electrical Instrument Co., 10514 Dupont Ave., Cleveland, Ohio. Phone: (216) 541-8060. P\&A: \$385; 60 days.

The low-cost 3301 digital multimeter features 27 measurement ranges. This includes 100 mV to 1 kV ac and dc, at $100-\mu \mathrm{V}$ resolutions and accuracies of $0.5 \%$ and $0.1 \%$, respectively. Ac and dc currents are measured from $100 \mu \mathrm{~A}$ to 1 A , at $100-\mathrm{nA}$ resolutions and accuracies of $0.5 \%$ and $0.2 \%$, respectively. Resistance ranges cover $100 \Omega$ to $100 \mathrm{M} \Omega$.

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## SOUTHEAST

Grice Electronics, Inc.
Pensacola, Fla.
Hammond Electronics, Inc. Orlando, Fla.

Sunset Electronics, Inc. Cocoa Beach, Fla.
P. I. Burke Company Louisville, Ky.

## MIDWEST

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 and only Heat Gun-madedust for
electronic assembly

## Ungar's new,feather light, solid state Heat Gun \#6955

Forget the old all-purpose hot-and-heavies. Ungar gives you the only flameless heat gun built to meet your special needs in electronics assembly and lab work. Now tackle shrink tubing, solder sleeves, shrink film pagkages, epoxy curing, parts drying/ heating/cooling and numerous other jobs ... all with this versatile new gun. And get greater accuracy, better handling, safer operation than ever before.

First, it's an ultra light 17 ounces (literally pounds lighter than other guns). That's because it's all solid state and encased in a rugged polyester glass case. Your girls get all the fatigue-free handling accuracy you've always needed. For even more operator comfort and safety, heat is isolated at the nozzle area ... the handle stays cool!

Next it's versatile. Works cold or hot (a high $850^{\circ} \mathrm{F}$ approx.) and includes a standard set of baffles. Also, it stands by itself for hands-free operation. But that's not all, in addition to a neoprene plug and cord, it's trigger actuated, U.L. listed and carries Ungar's guarantee of excellence. You can get the new Princess at a pretty lightweight price, too.

It's at your nearest electronic distributor or dealer now. Order the Ungar Princess Heat Gun today - prove to yourself that in the age of solid state Ungar really outguns the heavyweights.

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Tung-Sol single phase and three phase bridge rectifiers come in standard size packages. It's their current ratings and forward surge ratings that are larger. They give you added performance reliability - and at no additional cost!


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## Microstrip relays have long life



Fifth Dimension, Inc., Box 483, Princeton, N.J. Phone: (609) 9,245990. $P \& A$ : under $\$ 20$; stock to 5 whs.

Two new miniature relays provide lifetimes of 100 -million operations in microstrip applications. They measure $1 / 2$ by 1 by $7 / 16 \mathrm{in}$. The Logcell model SLR1103 rf relay has a $50-\Omega$ impedance. Over dc to 1 GHz it exhibits a VSWR of only $1.2: 1$, an insertion loss of 0.3 dB and isolation of 37 dB . The Logcell SLH1104 high-speed-pulse relay switches pulses of $150-\mathrm{ps}$ risetime with $10 \%$ distortion.

CIRCLE NO. 262

High-Q ceramic caps handle up to 50 W


Aerovox Corp., New Bedford, Mass. Phone: (617) 994-9661.

A new line of high-Q monolithic microwave ceramic capacitors feature power capability to 50 W . Insertion loss is less than 0.03 dB at 10 W . Type HF-1 pellet and HF-11 silver-foil-lead pellet units are available at working-voltage ratings of $100,200,300$ and 500 V dc from 0.1 through 1000 pF . Five tolerances range from $\pm 1 \%$ to $\pm 20 \%$. VSWR rating is less than 1.05:1.

CIRCLE NO. 263

## 70-W vhf transistor

 has infinite VSWR

TRW, Inc., Semiconductor Div., 14520 Aviation Blvd., Lawndale, Calif. Phone: (213) 679-4561. P\&A: \$67.50 (100 quantities); stock.

A new hybrid transistor provides 70 W of cw broadband vhf power with infinite VSWR capability. The J01001 works in a fixed-tuned circuit over any $70-\mathrm{MHz}$ increment from 30 to 200 MHz . It is guaranteed to deliver 70 W at 180 MHz , works from 28 V dc, has $6-\mathrm{dB}$ gain and $70 \%$ efficiency and dissipates 146 W . VSWR is infinite at 60 W of output.

CIRCLE NO. 264

## 100-W rf capacitor operates at 1.5 GHz



Johanson Manufacturing Corp., 400 Rockaway Valley Rd., Boonton, N. J. Phone: (201) 334-2676. P\&A: $\$ 9.50$ to $\$ 6.25 ; 6$ to 8 wks.

A new high-power variable capacitor is designed to handle 100 W of cw power in a high-currentmode application and to operate at 1.5 GHz . This new quartz-dielectric capacitor, model 7558 , has a unique slotted-piston design to improve thermal dissipation and power handling ability. It also features noisefree dynamic tuning and non-magnetic construction. Capacitance range is from 0.6 to 2.5 pF . CIRCLE NO. 265

## Thin-film resistors work up to 10 GHz

Sage Electronics Corp., 1212 Pitts-ford-Victor Rd., Pittsford, N.Y. Phone: (716) 586-8010.

Thin-film microwave resistors are available with essentially flat response from dc to beyond 10 GHz . Their nonspiralled manufacture provides minimum inductance. The resistance materials are pyrolytically deposited carbon film on ceramic substrates. Standard TCs are $0 \pm 70 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ for metal-film types and $-250 \pm 100 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ for carbon-film types.

CIRCLE NO. 266

## Internal-mirror $\mathrm{He}-\mathrm{Ne}$ lasers deliver 5 mW

Spectra Physics, 1250 W. Middlefield Rd., Mountain View, Calif. Phone: (415) 961-2550. Price: from $\$ 365$ to $\$ 625$.

A series of internal-mirror He Ne lasers are available with 3,4 and $5-\mathrm{mW}$ power outputs. They feature expected tube lif times over $10,000 \mathrm{~h}$. Two models of the new lasers are available: the 134 with an integral power supply and the 135 where the laser head and power supply are separate. The new lasers feature cold aluminum cathodes, humidity resistant window seals and hard-dielectric-coated optics.

CIRCLE NO. 267

## Photodetector/amplifier units come in TO-5:

The Meret Co., 1050 Kenter Ave., Los Angeles, Calif. Phone: (213) 826-5248. Price: from $\$ 75$ to $\$ 375$.

A new integral filter/detector preamplifier, MCA-9125, combines several new electro-optical features in one 4 or 8 -pin TO-5 unit. The device is suited as a receiver frontend in ranging and communication systems using GaAs lasers or LEDs where detection of high modulation rates ( 20 MHz ) or sharp pulses is important.

## DO YOU USE 3000 TRANSISTORS / DIODES



FOR INCOMING INSPECTION

$100 \%$ incoming inspection of discrete semiconductors is now economically feasible even if you use only 3000 devices a month. The new Lorlin Model T8BQ tester screens out defective transistors and diodes before assembly and substantially reduces the cost of trouble-shooting completed instruments both in the plant and in the field. In addition, inspection thruput is increased, and the quality of the finished product is enhanced. Write for Lorlin Application Note No. 38 to see how you can pay for a T8BQ with the first 33,700 devices you test.

## \$7400 COMPLETE

Lorlin has responded to the industry's increased need to automate incoming inspection by streamlining a tester for this specific application and by refining production techniques to provide an economical instrument.

## TESTS MOST DEVICES

Small signal through power devices are accommodated. Ranges include $0.1 \mathrm{nA}-10 \mathrm{~A}, 10 \mathrm{mV}-600 \mathrm{~V}$. Model T8BQ performs all common dc tests with $1 \%$ accuracy. An option is available for FET testing.

## EASY TO USE - FAST - RELIABLE

Model T8BQ is an 8 test instrument, easily programmed by referring directly to the device data sheets. The operator simply inserts the device into the test socket, pushes the test button and instantly observes the test results on the fail indicators, one corresponding to each test. The tester is built and guaranteed for 10 years continuous service.

[^5]
## Software simulation program simplifies circuit analysis

Softech, 391 Totten Pond Rd., Waltham, Mass. Phone: (617) 8996900. Price: see text.

A new software program known as AEDCAP (automated engineer ing design circuit analysis program) allows novice as well as expert engineers to easily perform a multitude of circuit analysis. The new program avails the designer of the following:

- Dc analysis for circuit operating point, input, and transfer characteristics.
- Ac analysis for driving-point and transfer responses.
- Transient analysis for determination of time responses to arbitary inputs.
- Temperature and environmental changes.
- Built-in models for pn-junction and zener diodes; bipolar, MOS and junction FETs and linear circuit elements.

AEDCAP features convenient editing facilities, built-in circuit models, library files, functions and graphical output.

A single circuit-description language is sufficient with AEDCAP to allow the designer to create
models, sub-circuits and circuits for filing or for immediate analysis. Any number of results can be saved from a single analysis and can then be printed or displayed, singly or together, with or without post-processing functions.

The program utilizes a uniform sparse matrix approach, taking full advantage of the fact that typical electronic networks do not contain complex interconnections. Because most entries in the nodal-admittance matrix are zero, all modes of analysis are very efficient, even for small as well as large circuits.

This new simulation program can accommodate circuits of unlimited size. An automatic node-renumbering algorithm and implicit integration techniques yield excellent performance at low cost.

AEDCAP is available as an inhouse installation and as a timesharing service. Inhouse versions of the program are available at an installation cost of $\$ 15,000$ plus a $\$ 600$-per-month leasing fee.

The program operates on IBM 360 to 370 computers under OS, TSO and CP-67/CMS systems.

CIRCLE NO. 269


The flowchart of a new software program shows how circuit analysis is made simple. The program accommodates circuits of unlimited size.

## Touch Tone terminal has alphanumeric keys



Wavetek Data Communications, 9045 Balboa Ave., San Diego, Calif. Phone: (714) 279-2200. $P \& A$ : \$700; 90 days.

Model T-500 is a desktop Touch Tone terminal with a 49-key alphanumeric keyboard containing letters, numerals, punctuation marks and special characters for inputting data to a computer via an audio-response system. The answer from the computer is in the form of a spoken audio message which can be easily heard on the T-500's built-in speaker.

CIRCLE NO. 270

## Low-cost minicomputers are designed for OEMs



Digital Equipment Corp., Maynard, Mass. Phone: (617) 897-5111. Price: see text.

Two new low-cost minicomputers are designed for the OEM. Called the PDP-8/M and the PDP-11/05, they are priced as low as $\$ 2362$, and $\$ 3069$, respectively (quantities of 100). Base price of the PDP$8 / \mathrm{M}$ is $\$ 3690$ and the PDP-11/05 is $\$ 4795$. Each includes a central processor, 4096 words of core memory, power supply and an operator's console for the PDP-8/M and a programmer's console for the PDP-11/05.

CIRCLE NO. 271

## 7-in.-reel transport handles dual formats

Kennedy Co., 540 W. Woodbury Rd., Altadena, Calif. Phone: (213) 798-0953. Price: \$1900.

The model 87007 -in.-reel synchronous tape transport is the first dual-format 800/1600-character/ in. tape system. It reads and writes IBM-compatible tape in either 7track 200,556 or 800 -bit/in. or $9-$ track 800-character/in. NRZI and 1600 -character/in. phase-encoded formats. Tape speeds of 10 to $18-3 / 4 \mathrm{in}$./s allow for data transfer rates up to 30 kHz .

CIRCLE NO. 272

## Magnetic-tape sensor balances tape output

Spectronics, Inc., 541 Sterling Dr., Richardson, Tex. Phone: (214) 231-9381. Availability: stock.

A new beginning-of-tape/end-oftape sensor for use in magnetic tape drives provides a balanced tape-signal output eliminating detection errors. Each of its two channels utilizes an LED and a phototransistor pair. The SS403 is available with special configurations designed upon request.

CIRCLE NO. 273

## Modem interface unit mates PDP8/E system

Scidata, Inc., 1250 Warren Dr., Norcross, Ga. Phone: (404) 4485620. Price: $\$ 795$.

A new modem interface for the PDP8/E computer features RS232 standard data and control lines. Designed for use with 103 and 202 series data sets, the interface is also ideal for local CRTs and other bit serial terminals used as peripherals. Its crystal-controlled transmission rate is pin-selectable at $150,300,600,1200,4800,9600$ baud.

CIRCLE NO. 274

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Brewster, N. Y. 10509 / (914) 279-8091

## Electronic calculator is priced under $\$ 200$



Eldorado Electrodata Corp., 601 Chalomar Rd., Concord, Calif. Phone: (415) 686-4200.

The model 8C desk-size calculator with an eight-digit readout, a full four-function keyboard, a constant key, overflow and minus signs and both fixed and floating decimals retails under $\$ 200$. The machine's logic circuitry is based on LSI technology. This allows it to be pre-packaged on a single, tiny chip that is less than one square inch in size.

CIRCLE NO. 275

## Digital strip printer is small and portable



Heller Roberts Instruments Corp., Dataline Div., 700 Jamaica Ave., Brooklyn, N.Y. Phone: (212) MI74600.

A compact digital impact strip printer provides recognizable hardcopy readout as an independent desktop or portable data terminal. The unit prints on pressure-sensitive paper tape at up to 30 characters/s and displays the 64-character ASCII subset (full alphanumeric plus symbols) spaced at 9 characters/in. It measures $7-5 / 8$ by 4 by 2-1/2 in.

CIRCLE NO. 276

## 125-ns plated-wire memory costs $4.9 \not \subset /$ bit

Toko, Inc., Memory Div., 1-17, 2Chome, Higashi-Yukigaya, OhtaKu, Tokyo, Japan. P\&A: 4.9 c/bit ( 50 quantities) ; 3 months.

Model HS-150 is a direct competitior of bipolar semiconductor memories. The new memory of 2000 words of 72 bits each has $125-\mathrm{ns}$ read access time, $150-\mathrm{ns}$ read cycle time, and $300-\mathrm{ns}$ write cycle time. Since the new memory operates in the non-destructive readout mode, part of it can be used as read-write memory and part as a random-access read-only memory.

CIRCLE NO. 277

## Disc-pack assembly records 200 tracks/in.

Applied Magnetics Corp., 75 Robin Hill Rd., Goleta, Calif. Phone: (805) 964-4881.

A new flying-head assembly for use with disc-pack drives permits twice the storage capacity over standard IBM 2314 drives by recording 200 tracks/in. on the disc surface instead of the normal 100 . Model ERW-306301 consists of a read/write section, a straddle erase section and a mounting arm. The assembly comes in a ramp-load or torsion-arm-load configuration.

CIRCLE NO. 278

## Alterable U-core ROMs price under $1.5 \not \subset / b i t$

Datapac Inc., 3180 Redhill Ave., Costa Mesa, Calif. Phone: (714) 546-7781. P\&A: under 1.5 $/$ bit; stock.

A complete line of low-cost standard off-the-shelf U-core alterable ROMs are available. The customer need only supply his desired data contents in either punched tape, cards or tabulated listing formats. Capacities range from 8 kbits to over 200 kbits per system. A variety of basic word/bit configurations are available.

CIRCLE NO. 279

## Interactive terminals offer 2 display formats



Photophysics, Inc., 1601 Stierlin Rd., Mountain View, Calif. Phone: (415) 969-9500. $P \& A: \$ 3430$, \$4650; 60 days.

Two new interactive display terminals are models 80 and 84, both stand-alone, fully buffered units with 12 -in. screens and switchselectable data rates of 1200 baud (asynchronous) or 2400 baud (syncronous). Model 80 displays 1000 characters in 25 lines of 40 characters/line and model 84 displays 960 characters in 12 lines of 80 characters/line.

CIRCLE NO. 280

Numeric keyboard complements terminals


Eastern Dynamics Corp., 1158 Suffolk Ave., Brentwood, N.Y. Phone: (516) 231-8800. P\&A: \$295; 30 days.

A new 10-key numeric keyboard operates in line or local modes through any ASCII coded terminal while the terminal keyboard remains fully operational. The KBG25 features a single-key carriage return line feed, rubout and X-off functions. Eight option keys are available for selecting a combination of any of 64 functions or symbols. The KBG- 25 allows users to generate data via an addingmachine keyboard arrangement.


Another power semiconductor first from International Rectifier, the DO-4 sized and priced 20F silicon rectifier series now frees you from having to use bulky and expensive DO-5 packaged units to handle 20 Amps (avg.) requirements! They offer a great deal more than just their small-size-to-high-current handling capability and low cost. Highlighting just a few: They can handle peak one-cycle surges to 400 Amps and have an unusually high $\mathrm{I}^{2 t}$ rating of $650 \mathrm{~A}^{2} \mathrm{sec}$. They display low leakage currents ( $I_{R}$ ) of only 2.0 mA , average, at $+150^{\circ} \mathrm{C}$ junction temperature.
Their economy and performance make them ideal for a broad range of commercial, consumer and industrial applications such as battery chargers, motor controls, power supplies, transmitters and transceivers.
They're available in a choice of forward or reverse polarity, in seven peak reverse voltage versions ( 50 V , $100 \mathrm{~V}, 200 \mathrm{~V}, 300 \mathrm{~V}, 400 \mathrm{~V}, 500 \mathrm{~V}$ and 600 V ), from your International Rectifier Industrial Distributor. For production requirements and/or applications aid, contact your local IR sales office or call the factory.

[^6]

High-Speed Digital Power
Supplies. Unique Series 3530 offers 0 to $\pm 100 \mathrm{~V}$ and 0 to 10 A . Remotely programmed in BCD or binary (14-bit with sign) at speeds greater than 10 kHz and accuracies of $0.1 \%$. Digital input controls output with TTL logic levels. Isolation 10pf AC and 2,000 megohms DC. Repeatability $0.005 \%$.
SRC/MOXON
CIRCLE NO. 131

\$84.00 Isolated Miniature
Power Supplies. Compact Model 3564 exceeds specifications of high priced supplies. Low 0.1 pF AC and 10,000 megohms DC isolation (due to fiber glass front panel, precise component placement and multi-shielded XFMR). Ripple 75 uV p-p. Front panel 10 -turn pot. adjusts output $0-25 \mathrm{VDC}, 0$ to 200 mA .
SRC/MOXON CIRCLE NO. 132


## Universal Signal Conditioner.

Excite transducers, troubleshoot or calibrate complete systems with Model 2541 or 2545. Modular plug-in construction allows individual channels to be adapted for conditioning of strain gages, thermocouples, RTB's, potentiometers, etc. Highest performance in industry.
SRC/MOXON
CIRCLE NO. 133


## Heat exchangers cool up to 20 kW

Wakefield Engineering Inc., Audubon Rd., Wakefield, Mass. Phone: (617) 245-5700.

A new series of 12 standard liquid-to-air heat exchangers are available to cool high-power electronic components or for any cooling system requiring heat-transfer capability up to 20 kW . The series includes single and double-pass versions which use one or two of each of three standard fan sizes. Units range in sizes from 7 by 7 in. to 12 by 23 in .

CIRCLE NO. 282

## Pin/socket connector has wire-wrap contacts

Amphenol Industrial Div. of Bunker Ramo Corp., 1830 S. 54 th Ave., Chicago, Ill. Phone: (312) 2421000.

A new, miniature pin-and-socket connector features fixed-pin wirewrapped contacts. As an addition to the MinRac 17 series, the connector's locked-in wire-wrappable contacts eliminate the need to solder for termination. Suited for rack-and-panel, cable-to-panel or cable-to-cable applications, it is intermateable and intermountable with other D-shaped connectors.

CIRCLE NO. 283

## Gold cermet paste allows hybrid rework

Bala Electronics Corp., Cermalloy Div., 14 Fayette St., Conshohocken, Pa. Phone: (215) 828-4650.

With S4300C high-purity gold cermet paste, hybrid-circuit processors can now rework circuits by removing a defective IC chip, then replacing it with a working device without deteriorating the integrity of the completed circuit. Specially formulated for chip and wire bonding operations, the paste is said to be excellent for beam-lead bonding in hybrid work.

Three ceramic packages enhance edge-mounting


Metalized Ceramics Corp., West River Industrial Park, Providence, R.I. Phone: (401) 331-9800.

Called "InCert," a new ceramic edge-mount IC-package family consists of a 64-lead double-sided package with 32 contacts/side and a 40lead miniature double-sided package that measures 1 by 0.75 in . In the latter package, the chip cavity has a plated seal ring. A third new InCert package is a 40 -lead singlesided unit that is interchangeable with edge-mount packages already in use.

CIRCLE NO. 285

## 40-lead edge-mount package raises density



Owens-Illinois, Inc., Electronic Materials Group, Box 1035, Toledo, Ohio. Phone: (419) 242-6543.

A new 40-lead MOS/LSI edgemount circuit package is designed for quick plug-in and interchangeability in high-density circuit applications. The new-generation alumina circuit package is built from a matched set of materials including ceramic, dielectric glasses and thick films. It features a thickfilm die-attach area, which eliminates the need for gold preforms.

## CMOS multiplexers upgrade performance

Harris Semiconductor, Melbourne, Fla. Phone: (305) 727-5407. P\&A: $\$ 39.95$ (100 quantities); stock.

A new series of fully decoded, eight-channel CMOS analog multiplexers offers high switching speeds with low power dissipation. The HI-1818 and HI-1828 combine CMOS and dielectric isolation performance over -55 to $+125^{\circ} \mathrm{C}$. The merging of CMOS and dielectric isolation results in low leakage currents and fast switching speeds. CIRCLE NO. 287

## Tiny transistor chips mate thin/thick films

European Electronic Products Corp., 10150 W. Jefferson Blvd., Culver City, Calif. Phone: (213) 838-1912. $P \& A$ : 42\& (1000 quantities); stock.

New miniature npn (ET60) and pnp (ET61) transistors are available for thin and thick-film applications in audio-frequency input stages and switching networks. They are rated for base-emitter voltages of 5 V , output capacitance of 8 pF , power dissipation of 150 mW and collector-emitter voltages of 32 V . The transistors handle $100-\mathrm{mA}$ collector currents.

CIRCLE NO. 288

## Low-noise, low-drift IC op amp costs \$15

Precision Monolithics, Inc., 1500 Space Park Dr., Santa Clara, Calif. Phone: (408) 246-9222. P\&A: \$15 (250 quantities); stock.

The SSS725EJ is a low-cost monolithic op amp that competes with the more expensive modules in transducer amplifier and other instrumentation applications. It features drifts of $0.6 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ and $40 \mathrm{pA} /{ }^{\circ} \mathrm{C}$ maximum, offset of 0.5 mV and maximum input noise of $15 \mathrm{nV} / \sqrt{\mathrm{Hz}}$ at 10 Hz .

CIRCLE NO. 289

FET analog switches come in TO-5/TO-8 cans

J. W. Microelectronics Corp., Philadelphia, Pa. Phone: (215) DA98681. Price: from $\$ 10.40$ to $\$ 28.25$.

Two new high-speed FET analog switches are the model HC1273, packaged in a 12 -lead TO-8 can and the model HC1265, packaged in an 8-lead TO-5 can. Both use thick-film screening techniques and stacked wafers. Model 1273 can be used as a dpst or spdt switch. It is available to operate up to $+125^{\circ} \mathrm{C}$. Model 1265 is used as a dpst switch. CIRCLE NO. 290


## with Model RF-828 Frequency Synthesizer Specs \& Prices



RF COMMUNICATIONS, INC.
Electronic Instrumentation Operation 1680 University Avenue, Rochester, N. Y. 14610
Telephone: 716-244-5830; TWX: 510-253-7469
A Subsidiary of Harris-Intertype Corporation

Programmable switch handles 1 A at 100 V


ITT Jennings, Div. of ITT Corp., 970 McLaughlin Ave., San Jose, Calif. Phone: (408) 292-4025.

A low-profile 4pdt switch, type JAEP, features contacts each of which can handle 1 A at 100 V . This tiny switch-only 1.2 by 1.02 by 0.32 -in.-is suitable for sandwiching between closely spaced PC boards. It is available with either 4 on-off positions or 1-through-4 progressive positions. Its contacts come with a choice of silver or gold alloy.

Temperature sensors are self checking


Sense, Inc., Box 280, Bethayres, Pa. Phone: (215) 659-4160. $P \& A$ : from \$125; 60 days.

The series IVSC sensors accurately measure temperature under varying input voltages and are self checking without calibrated temperature inputs. They use preselected elements whose outputs bear a pre-determined relationship to each other, and whose summed outputs represent the excitation voltage. The comparison of these outputs yields a unique indication of the sensor calibration.

CIRCLE NO. 292

Miniature capacitors are $30 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ stable


Republic Electronics Corp., 176 E. zth St., Paterson, N.J. Phone: (201) 279-0300. Availability: stock to 4 wks.

A new line of miniature capacitors are available with ultra-stable features. Designated as Micro MuCaps, they feature temperature coefficients of $\pm 30 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ over -55 to $+125^{\circ} \mathrm{C}$. Capacitance range is 1 through 390 pF . Micro MuCaps are rated at 50 V dc and are $0.15-\mathrm{in}$. long by $0.055-\mathrm{in}$. in dia. They are used at high frequencies. CIRCLE NO. 293


## Little Giant Rotary Switches 16,20,24 Positions, 1-12 Decks

New military style, sealed rotary switches offer optimum capacity - to-size ratio. Up to 24 positions per pole available. Diameters from $1.125^{\prime \prime}$ to 1.281"; behind - panel depths from $.916^{\prime \prime}$ (1 deck) to $4.829^{\prime \prime}$ ( 12 decks). Enclosed, with molded-in terminals. Fixed stop or continuous rotation. Raised contacts allow are to make and break in the
air - prevent tracking across insulation material.
Like to know more? Write, or phone for more details on Series 53, 57, 59 or our latest general engineering catalog: Grayhill, Inc., 565 Hillgrove Ave., La Grange, Ill. 60525 (312) 354-1040.


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## Pt resistance elements come in many sizes

Hy-Cal Engineering, 12105 Los Nietos Rd., Santa Fe Springs, Calif. Phone: (213) 698-7785.

A complete line of platinum resistance elements is offered for industrial and laboratory applications for use as parts of systems and for use where a resistance probe design is not warranted. The line has 14 standard diameters and lengths in ceramic-covered elements and waterproof styles. Standard $100-\Omega$ strain-free elements are provided with either copper or platinum leads.

## Compensated oscillators drift but 1 ppm/year

Amperex Electronic Corp., Hicksville, N.Y. Phone: (516) 931-6200. Price: $\$ 45$ (1000 quantities).

A new line of temperature-compensated crystal oscillators is available in two frequency groups of 4.5 to 15 MHz and 20 to 60 MHz , all with long-term stability of $\pm 1 \mathrm{ppm}$ per year. Output frequency is stable within $\pm 2 \mathrm{ppm}$ over the entire operating range of -20 to $+70^{\circ} \mathrm{C}$. Output wave shape is a distorted sine wave with the first harmonic approximately 15 dB down.

CIRCLE NO. 295

## DIP resistor units use discretes/thick-films

Dale Electronics, Inc., Box 609, Columbus, Ga. Phone: (402) 5643131.

A new series of DIP resistor networks feature a choice of thickfilm or discrete-film resistive elements. Designated as type FDP, they offer power ratings to 0.05 W per resistor. Maximum dissipation is $1 / 2 \mathrm{~W}$ per package over -55 to $+150^{\circ} \mathrm{C}$. Produced with up to 15 elements, the new networks range from $10 \Omega$ to $1 \mathrm{M} \Omega$ per resistor.

Solid-state relay is housed in a DIP


Teledyne Relays, 3155 W. El Segundo Blvd., Hawthorne, Calif. Phone: (213) 679-2205. Price: $\$ 4.45$ (1000 quantities).

A unique new concept in solidstate relays is the model 640-1 SerenDIP which is housed in a TO116 DIP. The spst, four-terminal device is capable of bounceless switching up to 0.1 A at 50 V ac or dc, and at switching rates up to 100 kHz . The SerenDIP is fully TTL compatible and replaces most DIP reed relays (pin-for-pin basis) and FET analog switches.

CIRCLE NO. 297

## Precision pots are priced from \$5



New England Instrument Co., 14 Kendall Lane, Natick, Mass. Phone: (617) 873-9711. $P \& A$ : from $\$ 4.99$ (100 quantities); stock.

The new Econopot model C is specially designed to meet low-cost applications. Standard models come in servo and bushing designs with a $7 / 8-\mathrm{in}$. dia and a choice of $1 / 8$ or $1 / 4$-in.-dia shafts. Bushing models have an optional bushing length of $3 / 8$ or $1 / 4-\mathrm{in}$. All models feature infinite resolution, resistance from 1 k to $50 \mathrm{k} \Omega$, and linearities from 1 to $0.25 \%$. Power rating is 1 W .

CIRCLE NO. 298

> specialized capacitors

## to your specifications at stock prices

Standard Condenser has designed and produced thousands of specialized capacitors for industry. In fact, what you think of as "special" may be among the many designs already available from stock at Standard. However, if you require capacitors of unusual shape, size, value and material, our engineering department will help you design and produce them to your exact specifications at stock prices. For immediate action, send us a sketch and complete details.


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Semiconductor Division

## SCHAUER

Manufacturing Corp.
4511 Alpine Ave. Cincinnati, Ohio 45242
Telephone: 513/791-3030

Tiny time-delay relay has a 10-A dpdt output


Midtex, Inc., 10 State St., Mankato, Minn. Phone: (507) 388-6286. Availability: 4 to 6 wks.

A compact time-delay relay designated type 613 is a delay-onoperate unit with either a 5 or $10-\mathrm{A}$ dpdt contact output. Its input voltages are available as standard voltages- 24 and 120 V ac , and 24 , 48 and 100 V dc. Delay-on-operate time ranges from 1 to 100 s . Delay tolerance is $5 \%$ and repeatability is $3 \%$

CIRCLE NO. 299

## Fixed active filters have up to 8 poles


A. P. Circuit Corp., 865 West End Ave., New York, N. Y. Phone: (212) 222-0876.

A line of fixed-frequency active filters are available in three versions: low, high or bandpass. Up to 8 poles and an attenuation slope as steep as 48 dB /octave can be provided for the low or highpass modules. These steep-rolloff filters have wide applications at low frequencies where they eliminate bulky and costly chokes.

CIRCLE NO. 300

## Tantalum capacitors cost as low as 10¢

International Electronics Corp., Melville, N. Y. Phone: (516) 6947700. P\&A: see text; stock to 6 whs.

A new line of low-cost tantalum capacitors designed for industrial and commercial equipment features units with costs down to $10 ¢$ each, in production quantities. Utilizing a high-grade tantalum pellet with an epoxy coating, the DIT series comes in straight or crimp-ed-radial-lead models for insertion into PC boards. They are available with 0.1 to $100-\mu \mathrm{F}$ values.

CIRCLE NO. 301

## Energy storage device has high capacitance

Gould Ionics, Inc., Box 1377, Canoga Park, Calif. Phone: (213) 3411040.

Popularized for its very high capacitance density (160 farads/ in. ${ }^{3}$ ), the ESD 3-in.-dia capacitor has a low resistance value ( $0.8 \Omega$ for a 1 -in.-dia rating). A primary application of these high-capacitance, low-resistance ESDs is for standby power of volatile semiconductor memories or other circuits where loss of power is inconvenient, expensive, or catastrophic.

CIRCLE NO. 302

## Thin-film resistors span 0.5 to $10 \Omega$

Film Microelectronics, Inc., 17 A St., Burlington, Mass. Phone: (617) 272-5650. $P \& A$ : 37 ¢ to $\$ 1.45$ per chip; stock.

A new series of low-resistancevalue thin-film chip resistors for hybrid circuitry and low-VSWR microwave terminations are available. Resistance values range from 0.5 to $10 \Omega$ in three sizes: 0.025 by 0.025 in . with $50-\mathrm{mW}$ power dissipation; 0.02 by 0.04 in. with $75-$ mW dissipation ; and 0.05 by 0.05 in. with $100-\mathrm{mW}$ dissipation.

CIRCLE NO. 303

## Compact rocker switches decrease in cost to 24ф

Stackpole Components Co., Box 14466, Raleigh, N.C. Phone: (919) 828-6201. Price: see text.

New low-cost RJ-16 rocker switches feature flexibility of operation with positive, compact, snap-action mechanisms. They can be used with or without their rocker knobs and brackets. Price is approximately 38 e per switch in quantity. Without a rocker knob and bracket, a switch costs about $24 \phi$.

CIRCLE NO. 304

## Silicon zener diodes drop dynamic impedance

Semtech Corp., 652 Mitchell Rd., Newbury Park, Calif. Phone: (805) 498-2111.

A new series of silicon zener diodes feature the lowest dynamic impedance of any such available devices-they can be operated at a lower bias current for a given dynamic impedance requirement than any zener currently available. They are presently being produced with a nominal voltage of 30 to 120 V for 1,3 and $5-W$ applications.

CIRCLE NO. 305

## Conductive-plastic pots increase service life

Markite Div. of GCA Corp., 155 Waverly Pl., New York, N.Y. Phone: (212) 675-1384.

Low-cost, conductive plastic infinite-resolution potentiometers built to withstand service life in the one-to-two-million-cycle range are available for all types of industrial applications. Types BT11 and BW11 are available in a resistance range of 100 to $200 \mathrm{k} \Omega$. Linear or tapered outputs can be supplied over required electrical angles and linearity is available down to $\pm 0.2 \%$. Dielectric strength is rated at 1000 V to ground.

CIRCLE NO. 306

## Metalized capacitors are small in size



S\&EI Mfg., 1800 Parthenia St., Northridge, Calif. Phone: (213) 349-4111.

Series 22 of Mini-Miniature metalized polycarbonate capacitors are available in standard case styles. Produced in a variety of encasements in 50 and $100-\mathrm{V}$ de ratings and space-saving sizes, they range from 0.001 through $50 \mu \mathrm{~F}$ with tolerances to $\pm 1 \%$. A typical size for a $10-\mu \mathrm{F}, 50-\mathrm{V}$ capacitor is 0.58 in . in dia by 1.16 in . in length.

CIRCLE NO. 307
Time-delay circuit features flexibility


Nytronics, Inc., Orange St., Darlington, S.C. Phone: (803) 3935420. Availability: stock.

A new high-stability, epoxymolded delay element known as the Wee Bit module can be used alone or connected in series to provide a wide range of delays and delay-to-rise-time ratios. Standard delay times are from 10 to 200 ns with $\pm 5 \%$ tolerances. Bandwidths, decreasing with increasing delay times, range from 25 down to 1.5 MHz . Standard impedance is $500 \Omega$ $\pm 10 \%$. Other delay times and impedances are available.

CIRCLE NO. 308

> Four dual transistors with excellent matching characteristics, high current gain, and high voltage breakdown.

We got to making linear I.C. dual NPN transistors because we couldn't find good enough ones to put in our circuit modules. Now we're ready to share.

The AD-813 has outstanding input characteristics: 0.5 mV $\max$. $\mathrm{V}_{\mathrm{BE}}$ differential, $2.5 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ max. $\mathrm{V}_{\text {BE }}$ drift. The AD-812 has an extremely high and linear gain of 400 minimum at $10 \mu \mathrm{~A}$ to 350 minimum at 5 mA . The AD-811 has all around excellent input and gain performance and a minimum $\mathrm{BV}_{\text {сво }}$ of 45 V . And the AD- 810 has a remarkably good performance combined with a remarkably low price.

All four are specified from $-55^{\circ}$ to $125^{\circ} \mathrm{C}$. All are hermetically sealed. All are available in quantity now. Send for the full specifications. Analog Devices, Inc., Norwood, Mass. 02062


# evaluation samples 



## Cable clamps

A new line of plastic cable clamps known as Pro-Grip clamps are made from propionate cellulose to meet the needs for economy, dependable cable support and long life. Pro-Grip clamps are ultraviolet stabilized for extreme weather resistance and are ideally suited for outdoor applications. The line is available in 36 different sizes and types to fit wire and bundles from $1 / 8$ to 3 in . in dia. Standard color is transparent brown. Samples are offered. Weckesser Co., Inc.

CIRCLE NO. 340


## Insulated magnet wire

Samples are being offered of a new Kapton polyimide insulated magnet wire. Its construction consists of an AWG 32 silver-plated conductor that is insulated with three mils of Kapton. Kapton constructions can be used for long periods at temperatures up to $240^{\circ} \mathrm{C}$, and short periods up to $400^{\circ} \mathrm{C}$. The Kapton film insulation exhibits dielectric strength as high as $7000 \mathrm{~V} / \mathrm{mil}$. All wire gauges are available from AWG 34 to AWG 8. Berk-Tek, Inc.

CIRCLE NO. 341

# design aids 

## application notes

## IC voltage regulators

A new applications note on the 723/823 IC voltage regulators shows design engineers how to best apply the IC regulators for more efficient, trouble-free designs. The 16-page note includes a wealth of information on IC regulators, including basic theory of operation and parameters. A discussion of bias limitations and how to improve ripple rejection, external sensing, thermal effects, foldback current limiting, increasing output current capabilities and special applications are also included. Teledyne Semiconductor.

CIRCLE NO. 344

## Temperature controls

A 10-page guide on temperature controls is especially valuable in explaining control theory, making competitive comparisons and evaluating the importance of various control features. It defines temperature repeatability, accuracy, resolution, sensitivity and stability. Succinct differentiation is made between such terms as two-position and two-mode control. Other portions of the guide cover thermistors, resistance bulbs and thermocouples, phase-angle firing and time, current and positioning-proportioning controls. The Apparatus Controls Div. of Honeywell.

CIRCLE NO. 345

## Impatt diodes

The use of impatt diodes for microwave power generation and amplification is discussed in a 32 -page application note. The note acquaints the reader with principles of operation and construction of impatt diodes and describes how the diodes are designed into oscillator and amplifier circuits. Several practical impatt-diode circuit designs are described. The note is liberally illustrated with electrical and mechanical diagrams, and with graphs that describe circuit performance. Hewlett-Packard Co.

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## new

literature

## Teflon data

A two-unit data kit on the subject of Teflon solves most problems encountered in machining and fabricating parts, components and assemblies from stock Teflon shapes. Part I of the kit is an eight-page leaflet that presents an all-out review of the properties and uses of Teflon stock shapes and how to machine them on standard equipment. Part II is a four-page leaflet that complements and expands on the eight-page leaflet and shows typical and atypical parts and com-ponents-many so intricate or complex as to require special jigs and fixtures. Commercial Plastics \& Supply Corp.

CIRCLE NO. 347

## High-voltage supplies

A six-page catalog describes a line of high-voltage power supplies. Spellman High Voltage Electronics Corp.

CIRCLE NO. 348

## Capacitive ROM

A six-page technical description is available on the new capacitive read-only memory (CROM) system which features cycle times down to 125 ns , at less than $2 \phi$ per bit. Integrated Memories, Inc.

CIRCLE NO. 349

## Circuit breakers

Four optional and four prototype designs of hydraulic-magnetic circuit breakers for OEM front-panel applications are briefly described and illustrated in a six-page brochure. Heinemann Electric Co.

CIRCLE NO. 350

## Resolver/synchros

An eight-page condensed catalog describes computer-interface, auto-matic-test, phase-sensitive, and resolver/synchro instruments, components and sub-systems. North Atlantic Industries, Inc.

CIRCLE NO. 351


## Relays

A new eight-page catalog contains complete information on lines of general-purpose, multiple-arm, junior and tubular relays. Comprehensive data such as life and reliability characteristics are included, and with detailed electrical, mechanical and environmental specifications, dimensional drawings, photographs, applications and ordering information. Wheelock Signals, Inc.

CIRCLE NO. 352

## Fork/crystal oscillators

An eight-page comprehensive catalog covers standard and cus-tom-built tuning-fork and crystal oscillators. Fork Standards, Inc.

CIRCLE NO. 353

## PC connectors

A 16-page handbook displays seven basic connector series designed for use with printed-circuit boards. Positronic Industries, Inc., Connector Div.

CIRCLE NO. 354

## Metric hand tools

An illustrated 20-page catalog describes a complete line of quality metric hand tools and sets. Bevco.

CIRCLE NO. 355

## Core memories

New literature introduces a full line of core memory systems with storage capacity from 4 k by 9 bits to 65 k by 36 bits. Fabri-Tek, Inc.

CIRCLE NO. 356

## Laser trimming

A 25-page bound manual describes laser resistor trimming and ceramic scribing techniques and equipment. Apollo Lasers, Inc.

CIRCLE NO. 357

## Optoelectronic products

A 12-page catalog describes new optoelectronic devices such as displays, LEDs, IR-emitting diodes and phototransistor opto-isolators. European Electronic Products Corp.

CIRCLE NO. 358

## High-isolation amplifier

A four-page data sheet describes an isolation amplifier module that uses carrier techniques for transformer coupling to achieve extreme isolation between the FET-input circuitry and subsequent amplifier stages. Analog Devices, Inc.

CIRCLE NO. 359

## Delay lines

A new 20-page catalog describes a complete line of electromagnetic delay lines. ESC Electronics Corp.

CIRCLE NO. 360

## Back-plane connectors

A new 20-page illustrated brochure discusses the technology and design of back-plane connector systems. Cinch Mfg. Co.

CIRCLE NO. 361

## High-density DIP panels

A new and revised 28 -page catalog describes high-density packaging panels and related accessories for interconnecting dual-in-line ICs. Augat, Inc.

CIRCLE NO. 362

## Thin-film materials

Literature on the use of thinfilm etchants for microelectronic circuits is available. Transene Co., Inc.

CIRCLE NO. 363

# bulletin board 

## Photodetectors

A four-page publication shows a new line of cadmium-sulfide and cadmium-sulfo-selenide photodetectors. The brochure provides information on performance characteristics of the new line, including three types of materials and five basic product configurations. AllenBradley Co., Electronics Div.

CIRCLE NO. 364

## Selector switches

A complete line of selector switches is included in a new 16 page catalog. CTS Corp.

CIRCLE NO. 365

## Instruments

A new short-form catalog covers digital multimeters and measuring systems, oscilloscopes, tube and transistor testers, data-collection terminals and card and industrial readers. Hickok Electrical Instrument Co.

CIRCLE NO. 366

## Microwave instrumentation

A four-page catalog describes and illustrates a line of power, pulse, video, general-purpose, wideband, laboratory and low-cost modular amplifiers for commercial and military use from de to 1 GHz . C-COR Electronics, Inc.

CIRCLE NO. 367

## Microelectronic packages

A 16-page illustrated catalog of microelectronic packages shows dimensional drawings of 45 standard package configurations with ceramic, glass or metal bases. National Beryllia Corp.

CIRCLE NO. 368

## Touch-Tone receivers

A 12-page brochure details a complete line of Touch-Tone receivers ranging from small key systems and computer terminal receivers through PABX/EPABX to central office receivers. International Components Corp.

CIRCLE NO. 369

## of product news and development

Hewlett-Packard has begun marketing instructional videotapes on electronic subjects for use by scientific and technical organizations, hospitals, medical schools, colleges and universities. The tapes cover three broad subject categories: tutorial, operational and maintenance. The subjects range from electronic measurement techniques to nurses' training to basic transistor theory. Hewlett-Packard will initially offer approximately 70 taped courses, ranging in length from 25 minutes to 9 hours. All tapes are available in Sony 1-in., Sony 1/2-in. (EIAJ-compatible) and A.mpex 1 -in. monochrome videotape formats, ranging in price from $\$ 65$ to $\$ 150$, depending on tape length.

CIRCLE NO. 370

Fabri-Tek, Inc., Minneapolis, Minn. has developed a new core memory unit that doubles the capacity of IBM $360 / 50$ systems.

CIRCLE NO. 371
Mepco, Inc., Morristown, N. J. has expanded its CV series of ceramic capacitors to include new dielectric materials, styles, expanded capacitance values and new colors.

CIRCLE NO. 372

Codi Semiconductor, Fairlawn, N.J., has announced the formation of a diode design service known as the "Diode Clinic." Based on its ten years of experience in designing and manufacturing diodes, the company will be making available with each diode it sells ten self-addressed cards for the user to indicate his specific diode problems on. These cards, when mailed to Codi Semiconductor, will be followed up with either postal or telephone correspondence or field visits by Codi's applications engineers.


## Design Data from Manufacturers

Advertisements of booklets, brochures, catalogs and data sheets. To order use Reader-Service Card. (Advertisement)

## Mini-Computer + Real-Time Analyzer for Automatic Testing



Complete Spectrum Processing System SP-6-1 automatically scales, equalizes, processes underwater acoustic data and noise/vibration signatures for production testing. Monitors on-line signals (nuclear reactor noise, heart sounds, machine tool vibration), recognizes aberrations, gives alarm. Setup conditions are entered via teletype unit as requested by computer. System prints out all spectral data or only frequency amplitude above preset levels. Data also pre-recordable on tape, played back often faster than real time. Analysis over frequency bands as wide as 40 kHz are possible. Many options.

CIRCLE NO. 174

## Federal Scientific Corporation

a subsidiary of Elgin National Industries, Inc.
615 West 131st Street, New York, N. Y. 10027.

## Ferroresonance is the Key to Simple Voltage Stabilization



In a new 6-page brochure, Kepco outlines the fluxoscillating stabilizer technique, applied to modern power supplies with special emphasis on the requirements of integrated logic systems.
High reliability, lack of overvoltage risk, exceptional line-noise isolation, inherent current limiting, these power supplies have what it takes at remarkably low cost.
The brochure describes the 58 different Kepco "PRM" stabilizers spanning the range from 4.5 V to 240 V d-c with ratings from 60 W to 300 W . There are charts and performance graphs plus complete mechanical installation data.

CIRCLE NO. 175

Kepco, Inc. 131-38 Sanford Avenue Flushing, New York 11352
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 supplies for microelectronics are available in the world's largest line of high power density, high efficiency supplies: 54 off-the-shelf models. From 100W to 500W; from 3VDC to 30VDC; single, dual, triple outputs; commercial, military, export models. Now in use by such leaders as Burroughs, Control Data, Honeywell, IBM, Litton, NCR, RCA, Univac, and many Government installations. Shown (left): Model SP631, 5VDC/100A, typical efficiency $70 \%, 8.50^{\prime \prime} \times 6.81^{\prime \prime} \times 8.85^{\prime \prime}, 21.5$ lbs., $\$ 695$. (Right): Model SP601, 5VDC/20A, typical efficiency $65 \%, 3.25^{\prime \prime} \times 6.50^{\prime \prime} \times 7.50^{\prime \prime}, 6$ lbs., $\$ 400$. Custom development and production also available.80 Dupont Street,
Plainview, L. I., N. Y. 11803

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