# Electronic Design 

IC voltage regulator is adjustable externally. It can deliver from 1.8 to 30 volts with regulation and temperature stability better than 1\%. Although output current
is 20 mA , addition of external power transistors extends outputs beyond 2 amps. Now you can use monolithic ICs in your next power supply design. (Page 92.)




1415A-150 psec Time Domain Reflectometer

hp 140A: PERFORMANCE IN ANY DIRECTION 20 MHz Wideband - High-Sensitivity, no drift - 150 ps TDR - 12.4 GHz Sampling - Variable Persistence and Storage

SIMPLIFY DESIGN OF MICROWAVE AND PULSE CIRCUITS WITH 150 psec TDR-The versatile hp 140A scope gives you a choice of 17 plug-ins which cover the entire spectrum of oscilloscope measurements. You can, for example, use a 1415A Time Domain Reflectometer plug-in to quickly determine the magnitude and nature of each resistive or reactive discontinuity in coaxial components such as attenuators, cables, connectors and delay lines. This 150 psec system enables you to locate each discontinuity to within an inch.

The 1415A is a completely self-contained system consisting of a fast rise pulse generator, single channel sampler, and time base. No additional vertical or horizontal amplifiers are required, eliminating introduction of additional chances of error. The vertical channel is calibrated in reflection coefficient for direct readout, with a maximum sensitivity of $.005 / \mathrm{cm}$ for measurement of extremely small discontinuities. Full 10 cm vertical display area gives additional resolution. Distances can be read directly on the horizontal axis-air or polyethylene dielectrics. The compact control panel contains only those controls necessary for TDR measurements-making the 1415A much simpler to operate than comparable systems costing twice as much.

Accessories include Rise Time Converters which eliminate reflections beyond the bandwidth of interest, 75 -ohm adapters, and a Susceptance Standard which gives direct readings of reactive discontinuities. Extensive "how-to" data is available in hp Application Notes 62, 67 and 75 and is yours for the asking.

Price of the hp 1415A TDR System with the standard hp 140A mainframe is $\$ 1645$. An alternate mainframe (141A) provides the additional advantages of variable persistence and storage. Price: \$2325. Ask your hp Sales Engineer for brochure (Data Sheet 140A) with specs on the TDR system. Or, write to HewlettPackard, Palo Alto, California, 94304. Phone (415) 326-7000. In Europe: 54 Route des Acacias, Geneva.


## "Sync-able" Oscillators

Each of these compact, solidstate oscillators can be phaselocked to an external frequency source. Or, they can furnish synchronizing signals to other instruments. These capabilities permit many applications never before possible with a generalpurpose laboratory oscillator. For instance:
Several of these oscillators can be locked to a frequency standard to provide a highly stable signal for each station in a production setup. Thousands of dollars can be saved over current practices.
They can be used as frequency multipliers, since they can lock to a harmonic as well as to the fundamental frequency. Multiplying in this manner is more precise and more convenient than manually setting two different oscillators.
They can serve as high-Q filters. If a small signal (1 volt or less)
with distortion, noise, and hum is applied at the SYNC jack, this signal will appear "cleaned up" and amplified at the oscillator output terminals.
The constant-amplitude signal available at the SYNC jack can be used as a separate output to trigger a counter, scope, or other oscillator.
Other features common to all three of these oscillators are: solid-state circuitry, flat output, low distortion, high accuracy, small size, internal power supply, and low cost. There is no better oscillator value on the market. For complete information, write General Radio Company, 22 Baker Avenue, W. Concord, Massachusetts 01781; telephone: (617) 369-4400; TWX: 710 347-1051.

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Datapulse welcomes technical employment inquiries.

[^0]
# Electronic <br> Design 

## NEWS

## 13 News Scope

17 Commercial inertial is ready for take-off
Airlines expect FAA approval this year for navigation systems that could speed air traffic.
21 New gains made in optical communications
Bell reports 3 modulaturs each use only about 1.5 mW of power per megahertz of bandwidth.
24 All-optical loop improves optical memory Elimination of electronic transition in data path allows bandwidth to be in. creased with less distortion.
29 Washington Report
44 Letters
49 Editorial: War is hell-and let's face it: it costs like it, too.

## TECHNOLOGY

52 Simplify feedback amplifier design with a set of straightforward equations that can be applied to a wide variety of circuits.
58 IC bidirectional counters cost less, are just as efficient as two unidirectional counters, and can be designed with RT micrologic.
66 Put operational amplifiers to work in four applications unusual and useful enough to win top honors in an industry-wide contest.
74 Tunnel-diode circuits pave the way to ultra-high-speed digital circuits required for wide-band, high-resolution communications systems.
82 Ideas for Design

## PRODUCTS

92 Cover Feature: IC voltage regulator is externally adjustable.
Cover Photo: The new IC voltage regulator from National Semiconductor Corp. was photographed by Lee Reeves, who used an everyday egg carton and special lighting to achieve a surrealistic effect. The device, mounted in an 8 pin TO-5, is shown alone and on a PC board with a booster transistor in a regulator circuit with a finned heat sink.
112 Semiconductors: Silicon npn transistor has 1000 -volt breakdown.
98 Components 122 Production Equipment
116 Test Equipment 124 Microwaves
120 Materials
Departments

| 80 | Book Reviews | 132 | Reprints |
| :--- | :--- | :--- | :--- |
| 126 | Design Aids | 134 | Advertisers' Index |
| 128 | New Literature | 136 | Designer's Datebook |

Reader Service card inside back cover

[^1]
## DC voltage

## this



## New Bendix modules can shrink your costs down to size, too.

These miniaturized Bendix ${ }^{\circledR}$ modules are a series of complete DC voltage regulators in TO-3, high-dome, transistor packages. Think of the space you can save over the hand-wired circuits and card-type units you're using now. It's a great way to improve your design without compromiseand cut labor costs as well as component costs in the process.

Each module contains a complete silicon, solid state, comparator amplifier assembly that weighs in at only one half ounce. These 1-amp modules come as series regulators of $5,6,12$, 18 and 24 volts or as shunt regulators of $5,6,9$ and 12 volts. Load regulation from no load to full load is $\pm 1 \%$ with a low temperature coefficient of $0.04 \% /{ }^{\circ} \mathrm{C}$ typical. Maximum power
dissipation at $25^{\circ} \mathrm{C}$ case is 25 watts. And that's not all. The TO-3 configuration fits all standard sockets and heat sinks.

Call your local Bendix office or Bendix Semiconductor Distributor. Just ask for more details about the incredible shrinking regulator. Or write us direct: Bendix Semiconductor Division, Holmdel, N. J.


APPLICATION SCHEMATICS

*For external trim adjustment




Certainly, our new MC1533 provides open-loop gain that's 15,000 higher than the next best. (Typically, 60,000 - with 40,000 guaranteed minimum). But, it also features extremely low temperature drift (for better stability) combined with low input offset voltage and gain adjustability, too. Best of all, it's competitively priced . . . and, available RIGHT NOW!

This extremely flexible operational amplifier sets new performance standards for a variety of applications. The high gain and temperature stability are extremely valuable for use in sensitive applications such as an integrator. Design simplicity and lower costs (because there's seldom a need to cascade) result from the added gain and low input offset voltage in other applications such as summing amplifiers, source followers, twin tee filters and oscillators.

For an even wider range of applications, you can now get two different series of these state-of-theart devices, in 10-pin TO-5 or flat-
pack, priced as low as $\$ 15.00$ (100-up). Here are some of the documented specifications that make it all possible:

| CHARACTERISTICS | MC15336 | MC14336 |
| :---: | :---: | :---: |
| Price (100-up) | \$34.00 | \$15.00 |
| Power Dissipation | 120 mW | 120 mW |
| Operating Temp. Range | -55 to $+125^{\circ} \mathrm{C}$ | 0 to $75^{\circ} \mathrm{C}$ |
| Open Loop Voltage Gain - Avol (min.) | 40,000 | 30,000 |
| Voltage Drift With Temp. (Typ.) | $\begin{gathered} 8 \mu V /{ }^{\circ} \mathrm{C} \\ \left(\mathrm{~T}_{\mathrm{A}}=-55 \mathrm{TO}+25^{\circ} \mathrm{C}\right) \\ 5 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C} \\ \left(\mathrm{~T}_{A}=+25 \mathrm{TO}+125^{\circ} \mathrm{C}\right) \end{gathered}$ | $\begin{gathered} 10 \mu V /{ }^{\circ} \mathrm{C} \\ \left(\mathrm{~T}_{\mathrm{A}}=0 \mathrm{TO}+25^{\circ} \mathrm{C}\right) \\ 8 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C} \\ \left(\mathrm{~T}_{\mathrm{A}}=+2570+75^{\circ} \mathrm{C}\right) \end{gathered}$ |
| Input Inpedance (typ) | 1 Megohm | 600 Kohms |
| Output Voltage Swing* | $\pm 12$ volts | $\pm 12$ volts |

## ${ }^{*} \pm 15 \mathrm{~V}$ Supply with RL 2 Kohms

For complete details on the World's Best Op Amp Integrated Circuit, send for our data sheets and the Application Notes described at right. For immediate delivery on devices you'll want to try, contact your nearby Motorola franchised semiconductor distributor.

HOW TO USE HIGH GAIN OP AMP DESCRIBED IN TWO NOTES
Designing source followers, twin tee filters and oscillators, and high impedance voltmeters are presented as examples of the wide variety of applications for the MC1533 operational amplifier. In addition, three complementary audio frequency amplifiers are discussed, each using an MC 1533 to obtain the desired voltage gain and reduce the distortion figure. Motorola Application Notes 248 and 275 are sure to be valuable additions to your library of integrated circuit information. Send for them today!


The MC1533 is considered a multistage amplifier, and is constructed to best use the advantage of monolithic integrated circuits. The dotted lines separate the stages.
where the priceless ingredient is care!


## Reliability and price levels for every ER application



Mepco, using MIL-R-55182 rev. C as a base, now makes metal film resistors available in three reliability and cost levels to meet your specific application.

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## MEPCO




Think Hybrid....


## Now you can get performance with

The plain facts are that Amperex can design and manufacture - to your specifications - the hybrid integrated circuit you need, and can do it faster, better and more economically than any other source known to us.

Why? Because Amperex special production line methods and Amperex thin-film/LID circuit technology are way ahead of the field. After all, who would have more skill in substrate processing and in microminiature circuitry than a leading producer of high-performance transistors? And who would be better able to apply LID semiconductor assembly techniques than the company who invented the LID?

Our batch-processed, large-volume runs of hybrid IC devices made with Amperex LIDS (off-the-shelf items and custom-produced items for special systems requirements) offer high performance at low cost, plus a third big bonus - small size.

Amperex hybrids offer resistance values from 50 ohms to 300 kilohms with stabilities better than $1 \%$ over 2000 hours at $250^{\circ} \mathrm{C}$; capacitance values from 10 pico-

# economy as well as size and Amperex hybrid integrated circuits. 

farads to 2 microfarads. Precise masking, alignment and exposure produce circuit line widths of only 2.5 microns ( 100 microinches), allowing us to design for extremely small circuit areas. Dissipation can be as high as 6.5 watts per square inch of film area.

The extremely successful ATF-401 operational amplifier is a typical example of an Amperex 'off-the-shelf' hybrid IC. At $\$ 29.00$, in hundred lots, the ATF- 401 outperforms many discrete op amps, and without exception, it outperforms every monolithic op amp available today. Since it is fully frequency-compensated internally, it requires noexternal circuitry which would increase its effective size.

Other examples, of even greater interest to today's markets can be taken from among this list of Amperex custom-designed hybrid IC's: Low-noise DC Ampli-


ATF-401 OP AMP BEFORE ENCAPSULATION
fiers - Special Digital Interface Circuits - Signal Conditioners • Solid-State Commutating Switches • RF and IF Amplifiers and Limiters - Power Supply Regulators • Audio Amplifiers, Modulators and Demodulators.

The plain facts, then, lead to only one practical conclusion: If your product has reached the stage where you must begin thinking in terms of microcircuitry, it's not enough to think size and performance only . . . think costs, too! . . . think hybrid . . . think Amperex!

Ask Amperex about custom hybrid IC's for your linear applications, for impedance matching, logic transformation, current and voltage drive, low-noise amplification and any other application you can think of.

Write: Amperex Electronic Corporation, Semiconductor and Receiving Tube Division, Department 371, Slatersville, Rhode Island, 02876.


TOMORROW'S THINKING IN TODAY'S PRODUCTS

# Did You Know Sprague Makes 51 Types of Foil and Wet Tantalum Capacitors? 

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Type 120D polarized plain-foil Type 121D non-polarized plain-foil Type 122D polarized etched-foil Type 123D non-polarized etched-foil

## ASK FOR BULLETIN 3602C

ON READER-SERVICE CIRCLE 161

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CL44 cup style, uninsulated
CL45 cup style, insulated
CL55 rectangular, both terminals insulated
CL64 tubular, uninsulated
CL65 tubular, insulated

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## News



Inertial navigators are under evaluation by commercial airlines; FAA certification is expected shortly. Page 17


3-story-high lens antenna tested for use in antimissile systems. Page 36


Modulated light beams employed in closedloop delay circuits may be the heart of future
multipurpose computers. The light is delayed between concave spherical mirrors. Page 24

## Also in this section:

Broadband light modulation is accomplished at less than a watt. Page 21
Thin film measures the speed of micrometeorites in space. Page 33
News Scope, Page 13 . . Washington Report, Page 29 . . . Editorial, Page 49


# SILICONIX Epoxy FETs 

(The only thing cheap is the price!)
Here's just a sampling of the hundreds (thousands?) of ways that FETs can work for you. If price has made you timid, fear no more. Siliconix epoxy FETs give you a new dimension in costing for industrial and commercial applications. What's more, they are available now, really, from distributor stocks. Check brief specs below, then check the free offer below that.

| EPOXY N-CHANNEL JUNCTION FIELD-EFFECT TRANSISTORS |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | E100 |  | E101 |  | E102 |  | E103 |  | Unit |
|  | Min. | Max. | Min. | Max. | Min. | Max. | Min. | Max. |  |
| IGSs@ 25 ${ }^{\circ} \mathrm{C}$ |  | -0.5 |  | -0.5 |  | -0.5 |  | -0.5 | nA |
| Vp | -0.3 | -10 | -0.3 | -1.5 | -0.8 | -4.0 | -2.0 | -10 | V |
| I dos | 0.2 | 20 | 0.2 | 1.0 | 0.9 | 4.5 | 4.0 | 20 | mA |

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## News scope

## Electronic firms await SST's economic boom



Now that the intense design competition for the supersonic transport jet has ended, the electronics industry is looking forward to a fat share of what could become a $\$ 25$ billion market by the mid-1970s.

However, the hundreds of subcontractors that may eventually be involved in the program may have to wait a while yet. Though the basic size and shape of the aircraft have been settled, it is still not clear when the Administration will give the go-ahead to start on the next phase of the SST program-the construction of two prototype aircraft.

Because of the Vietnam war and other budgetary considerations, some industry spokesmen believe that Congress and the Administration may delay the SST program.

Nevertheless, the $\$ 1.5$ billion that the Federal Aviation Agency has allotted to build the 338 -ton superjet is expected to find its way through the prime contractors, Boeing and General Electric, to subcontractors in all 50 states.

Barring undue delays, test aircraft are slated to be airborne by 1970, and regular flights may begin by 1974 . The total cost of bringing the aircraft into scheduled service, including funds raised by private manufacturers, is estimated at $\$ 4.5$ billion.

An FAA spokesman estimated that 15 to 20 per cent of the $\$ 35$ million cost of each SST would go
on electronic equipment. He expected each aircraft to carry about 1900 pounds of such equipment. The superjet may require also advanced automatic check-out and monitoring systems. Its short-range communications equipment will be substantially the same as that of today's commercial airliners, except that there will be a steady trend toward microminiaturization.

## New search for aircraft anticollision systems

The nation's airlines are embarking on a new phase in their continuing efforts to develop a practical standard collision avoidance system.

The Air Traffic Control Committee of the Air Transport Association announced formation of a working group-composed primar-
ily of electronics experts-that will examine proposed methods of applying time and frequency techniques to collision avoidance systems.

In the time-frequency technique, aircraft transmit their altitude on a common frequency at very precise times. The difference in microseconds between assigned times of a transmission and actual time of receipt by airborne computers carried onboard the aircraft determines the distance between the aircraft. The computers divide distance by rate of closure to predict "time to go until collision."

Stanley Seltzer, director of the association's air navigation and traffic control branch said: "The airline goal is to get a common system, one where airline collision avoidance equipment will work cooperatively with equipment carried by both civil and military aircraft. The airlines believe that the advent of an operational common collision avoidance system will be speeded by developing a proposed standard as quickly as possible."

The Air Traffic Control committee has already evaluated two proposed design concepts for collision avoidance systems-one by Collins Radio, the other by McDonnell Aircraft. Both of these proposals were reported to have met basic airline requirements ; however, the two systems were not compatible with each other.

Chairman of the new technical working group is Howard Mehrling, director, electronics-electrical engineering for Eastern Airlines. American, Braniff, Pan American, Transworld and United have named representatives to the group.

Meanwhile, development of a time-frequency airborne collision avoidance system, that is reported


System sought to avoid collisions like last year's XB70A crash

## News

SCOPC $_{\text {continued }}$
to be able to operate without ground-station synchronization or the expensive atomic clock required in current time-frequency systems, has recently been announced by TRG, Inc.

## U.S. sets uniform price for its R\&D reports

A fixed-price system has been introduced for Government-sponsored research and development reports published by the Clearinghouse for Federal Scientific and Technical Information. Single copies of documents cost $\$ 3$ on paper or $65 \phi$ in microfiche. The new price is less than the average old price, which was based on document size.

The customer buys order coupons from the Clearinghouse and uses them as scrip to pay for the documents he wants. He merely fills in his name and address and the number of the desired document on the coupon and mails it in. No further payment is required. It is hoped that the new system, which does not apply to certain individually announced reports and multiple-copy orders, will enable the Clearinghouse to fill orders quicker.

Coupons for paper copies may be purchased singly or in books of 10 , and for microfiches in books of 50 , from the U.S. Dept. of Commerce, Clearinghouse for Scientific and Technical Information, Springfield, Va. 22151.

## Junior satellite tracker pierces the Iron Curtain

A British satellite tracking station, which more than once has detected Soviet space launchings before Moscow announced them publicly, uses the latest in sophisticated electronic equipment. True or false?

Answer: False. It uses a surplus World War II radio outfit, a borrowed toy globe, some paper clips and copper wire.

The makeshift monitoring post is being operated by students of the Grammar School in Kettering, Eng-
land, under the direction of physics teacher G. E. Perry. A 40-foot antenna has been set up on the school's physics building.

Perry says the station tracks all satellites transmitting on frequencies up to about 20 MHz . Mainly these are capsules in the Soviet Cosmos series.

The globe, Perry explains, was borrowed from his daughter. "We use a clip and a piece of copper wire around the globe to plot the satellites," he says. "The wire represents the orbit and is attached to a curved support."

Since 1962, when the tracking began, 1700 radio observations have been made by the students, and the orbits of 75 satellites have been put on record.

## Artificial northern lights -courtesy of NASA

The National Aeronautics and Space Administration (NASA) plans to lob a small electron accelerator into the ionosphere for research into auroral phenomena. If successful, the accelerator will artificially generate an effect similar to the northern lights.

The experiment, slated for June, will involve launching the accelerator from Wallops Island, Va., to an altitude of 240 miles atop an Aerobee 350 rocket. The rocket would then turn downward so that the accelerator can direct electron beams toward the earth's atmosphere.

Dr. George F. Pieper, assistant director of NASA's Space Science Directorate, said that it was hoped to generate a series of auroral flashes at an altitude of about 60 miles. Each flash, coming at tensecond intervals, would last a couple of seconds and extend over a range of from half a mile to six miles. Red and green were expected to be the dominant colors of the multicolored spectacle, which should be visible over a wide area.
The experiment will mark the begining of further research into the little understood auroras. Dr. Pieper said that later an attempt may be made to shoot an electron beam from one hemisphere to another, so that the auroral effect would appear above the diametrically opposite side of the earth from where it originates.

Parametric amplification achieved in infrared

Bell Laboratory scientist, Dr. C. Kumar Patel, reports having achieved the first parametric amplification of infrared light.

Dr. Patel used tellurium to achieve a $3-\mathrm{dB}$ gain in intensity of laser light at a wavelength of 17.9 microns.

The scientist said that parametric amplification of far infrared light is an important means of increasing the intensity of a weak light source, and is a first step toward constructing a parametric oscillator, that is a tunable source of coherent infrared radiation.

Optical parametric amplifiers and oscillators are currently being investigated for possible use in future laser communications systems.
In optical parametric amplification, three light waves of different frequencies interact within a nonlinear material-in this case tellurium. Energy from one wave, at the "pump" frequency is transferred to two other waves: the signal wave, which is amplified, and the "idler" wave, which is generated as a byproduct of parametric amplification.

In Dr. Patel's experiment, light beams from a carbon dioxide laser and a helium-neon laser were combined and focused into a $7-\mathrm{mm}$-long tellurium crystal. The idler wave had a wavelength of 25.9 microns.

## Two devices help light the way to car safety

Two new electron safety devices for use in automobiles have been spotlighted by the Bendix Radio Corp. of Baltimore.

One is a solid-state hazard-warning and turn-signal flasher, reported to be insensitive to voltage and temperature variations. Bendix claims for it a life expectancy five times greater than that of conventional bimetal flashers.
The other device ties the car's entire external lighting system into a central indicator, which shows at a glance whether it is functioning properly. In case of a failure of a' headlight, tail light, brake light, turn signal, back-up light or licenseplate light, the dash-mounted indicator lights up and remains lighted until the defect has been repaired.

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# Commercial inertial is ready for take-off 

## Airlines expecting FAA approval this year for navigation systems that could speed traffic aloft

Ron Gechman<br>West Coast Editor

Inertial guidance-the key to accurate navigation in interplanetary flight and long undersea travelseems destined for a less exotic but highly practical role this year: the commercial airlines hope to receive Federal Aviation Agency approval to use it as part of a giant step forward in modernizing their navigation systems.

The major problem in adapting inertial for aircraft-gyro precision in a small package-has been overcome, manufacturers say. As with many aircraft innovations, military prototypes have led the way here for commercial development. Sperry Gyroscope of Great Neck, N. Y., and the Litton Guidance and Control Group, Woodland

Hills, Calif., have developed and tested commercial aircraft inertial systems over the last three years. The competition is growing. Other companies working on inertial navigators for the commercial market include AC Electronics, Bendix, General Electric, General Precision, Autonetics, Nortronics and TRW Systems.

Nor is the potential application limited to the airlines. At least one maker of private aircraft, Grumman, has designed the airframe of its forthcoming Gulfstream II twinengine business jet so that it can be equipped with an inertial system, if the buyer prefers.

Why do the airlines want inertial navigation capability? They now can fly all over the world with the help of ground-based navigation aids and such airborne equipment


Inertial navigation equipment is adjusted before take-off by Pan American World Airways pilot, Capt. D.L. Combs. The Sperry SGN-10 system, which is being installed on the first of 70 Pan Am airliners, is completing engineering evaluation tests. The airline hopes to receive Federal certification for the equipment this year.
as VHF radio, VOR (Visual Omni Range), DME (Distance Measuring Equipment) and Doppler radar. The main advantages of inertial guidance are these:

- Navigation is possible anywhere without external aids. This gives airline crews greater flexibility in selecting flight paths, particularly over areas where ground aids are sparse or nonexistent.
- Greater accuracy and reliability are claimed than for any other method now in use. This would permit the use of narrower air traffic lanes in congested areasover the North Atlantic, for exam-ple-and would facilitate operations in bad weather.
- Instantaneous position reports can be obtained; the time lapse inherent with external navigation aids is eliminated. This is important as jet speeds increase. The proposed supersonic transports, for example, will be dependent on inertial navigation.
- Great-circle courses-the shortest distances between any two points on the earth-are computed automatically.
- A crew navigator is not needed; the pilot can operate the inertial system alone. The savings in eliminating one crew member, airline officials believe, could pay for the inertial systems in a few years.


## Installation begun by Pan Am

Pan American World Airways was the first airline to install experimental inertial navigation equipment on its aircraft. It began a system evaluation in 1963, when it conducted an R\&D program for the FAA with a Litton platform. Two years later it completed a follow-up program, using an advanced Litton platform. Both Litton systems were military versions used in the $\mathrm{F}-104$ and F-4 aircraft.

Subsequently Pan Am began evaluating a prototype of a Litton commercial system, designated the

## (inertial, continued)

LTN-50. This evaluation is still under way, and Litton has declined at this time to make public technical details of the system.

Between the two earlier Litton programs, Pan Am checked an inertial system developed by Sperrythe SGN-10 - and eventually it signed a contract for enough Sperry systems to equip 55 airliners, a figure that was then raised to 70 .

Capt. William Moss, director of the Pan Am inertial navigation project, says the Sperry system is completing an engineering evaluation stage, and the airline hopes to get FAA certification by late spring or early summer. Over 6000 hours of flight operations have been logged with the Sperry system, Moss says, and Pan Am has started to equip its airliners-seven so far -with dual inertial systems. Dual systems permit the use of one system to check the performance of the other.

At least three other airlines plan to install inertial navigation systems this year. Alitalia, the Italian carrier, has ordered two Sperry systems for its new DC-8 jetliners, which will be delivered early this year. British Overseas Aircraft Corp. is also equipping some of its aircraft with Sperry systems and plans to test them over the next year. Lufthansa, the German air-
line, has purchased two such systems for installation in a Boeing 707.

The proposed jumbo jetliners, designed to carry 350 to 450 passengers, are considered sure-fire candidates for inertial navigation. Litton and AC Electronics, for example, are finalists in a competition to furnish the system for the Boeing 747, and a choice is scheduled to be made next month.

Even though a fair amount of experience is available from military aircraft inertial systems, the development of commercial units has not been easy. The problem lay in the different needs of commercial and military users. For commercial use, pure inertial systems must remain accurate during flights lasting up to 10 or 12 hours. The military systems, installed in tactical aircraft, typically do not operate for more than one or two hours at a time. Therefore the drift rate in a commercial system must be sufficiently low so that after 10 or 12 hours of operation it still works accurately.

Sperry says that the "breakthrough" for it was the "successful marriage of the easily maintained Rotorace gyro with the highly reliable, compact Mark 12 digital computer." The result was the SGN-10, which can be operated by the same person who flies the aircraft.

Before take-off the pilot merely feeds the latitude and longitude of his departure point into the system's display unit and then switch-


How inertial navigation could affect North Atlantic traffic: At present one airliner occupies a "cocoon" of air (right) that is 120 nautical miles wide and the equivalent of 30 minutes of flight time long. Increased accuracy of inertial navigation systems could allow six aircraft to occupy the same space.
es to the "align" mode. The system automatically aligns itself to the local vertical and true north, and it is ready to navigate.
"The latitude and longitude of up to nine check points, or destinations, can be entered and stored in the computer's memory unit," according to Sperry. "These check points determine the desired flight path in great-circle arcs. It is possible to change any destination at any time during the flight, and the system will instantly compute the great-circle track from the present position to the new destination."

The system's display panel gives the pilot continuous information aloft on the aircraft's ground speed ; distance and time to destination; miles to the left or right of the track and track angle error (from which the speed and direction of the wind can be figured) ; true heading and grid heading.

## Hands-off operation

The inertial system can be linked to the aircraft autopilot, to provide full hands-off navigation and flight. Pan Am is also investigating the feasibility of linking the navigator to a Boeing-Bendix automatic landing system. Future system modifications will probably bring about the interfacing of the autopilot, guidance and landing systems.
The computer in the Sperry system has a magnetic core memory and 5632 words of 21 bits. Ninety per cent of the memory is nondestructive read-out, and the remaining 10 per cent of destructive read-out is used for the input of latitude and longitude coordinates of check points.

The system accuracy was demonstrated last Nov. 10 when a regularly scheduled Pan Am flight, with a full load of passengers and four FAA observers, flew the polar route from London to Seattle. A dual inertial system navigated the plane during the entire 10 -and-a-halfhour, 4800-nautical-mile flight, and the plane was reported never more than 12 miles off course.

Microelectronics has played a significant role in increasing the accuracy and reliability and reducing the size and weight of inertial systems. Around 1950, for example, an AC Electronics inertial system in-

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## NEWS

(inertial, continued)
stalled in a B-52 bomber weighed 4000 pounds, and it also took 72 hours of preparation to set it up before each flight. By contrast, the system that Pan Am is evaluating weighs about 50 pounds; it is packaged in a box that is 10 inches wide, 29 inches deep and $10-5 / 8$ inches high.

International airlines and the FAA are highly interested in inertial navigation because it can greatly ease traffic problems on over-water routes and on one route in particular-the North Atlantic. At present, traffic over the Atlantic is so dense that flights are assigned to arbitrary air corridors. In normal jet operations the corridors measure up to 120 nautical miles wide and have a 2000 -foot separation in altitude. The jets proceed through these corridors as a series of moving "cocoons." The length of each cocoon is the mileage equivalent to 30 minutes' flying time. During peak flight periods an airline may be forced to fly several hundred miles off its optimum route, just to get through at a time when it wants to. This costs fuel, time and money. With improved navigation accuracy, the size of the individual corridors could be reduced, with a subsequent increase in traffic.

The North Atlantic is not the only route where inertial systems would be helpful. Pacific flights from the West Coast to Honolulu, Tahiti, the Fiji Islands, Australia, Guam and Tokyo must cover long distances with relatively few ground aids for navigating. The New York-Buenos Aires flight also poses a navigation problem, because it cuts over the heart of South America, where ground aids are scarce.
As for the future, the highflying, high-speed SSTs appear heavily committed to inertial navigation. Ground aids like VOR, for example, are not much good above 45,000 feet. The VOR stations have been set up so that, for aircraft flying below 45,000 feet, interference does not occur between two stations operating on the same frequency. Above critical altitude, serious interference can occur. - -

# New gains made in optical communications 

## Bell reports 3 modulators each use only about 1.5 mW of power per megahertz of bandwidth

Ralph Dobriner<br>Chief News Editor

Three new light modulators developed by Bell Telephone Laboratories make it economically and technically feasible to put communication signals onto a laser beam, according to the company.

The new devices make it possible to impress broadband communications signals onto both pulsed- and cw-laser beams at modulation powers of about 1.5 mW for each megahertz of bandwidth, Bell scientists said. Most earlier modulators, using such materials as potassium dihydrogen 'phosphate (KDP), lithium niobate and barium titanate, re-
quired either too much power or had insufficient optical bandwidth to be practical, they said. Moreover, the physical characteristics of the materials changed fairly rapidly. (For a fuller discussion of problems, see "Electro-Optics," a special report, ED No. 22, September, 1966, pp. 50-67.)

The devices newly developed by Bell's Murray Hill, N. J., center are:

- An electro-optic modulator in which visible light passing through a lithium tantalate crystal is modulated by an electric field that is pulsed rapidly through the crystal.
- A magneto-optic modulator containing a gallium-doped yttrium


PCM system with two lithium tantalate modulators, is checked by R. T Denton of Bell Telephone Laboratories. A schematic of the setup is also shown. The modulators use high-speed signals to encode light pulses from a laser.
iron garnet (-YIG) crystal, in which near-infrared light waves are continuously modulated by varying the direction of the crystal's internal magnetic field.

- An electro-optic modulator in which a reverse bias applied to a gallium phosphide diode modulates visible or near-infrared light traveling along the plane of the diode p-n junction.

This is reported to be the first use of lithium tantalate and galli-um-doped YIG materials as light modulators. The gallium phosphide modulator was first announced by Bell in 1964; however, no details were given at that time.

Bell scientists decline to predict when optical communication systems, with their enormous band-width-handling capabilities, might be used commercially. Present systems more than fulfill all requirements for the near future. One scientist suggested that an optical system would be needed if, say, there were a consumer demand for millions of Picturephones in large cities.

## PCM experiments pressed

The lithium tantalate electro-optic modulator, developed by R. T. Denton, T. S. Kinsel, F. S. Chen and A. A. Ballman, of the Bell scientific staff, is currently being used in an experimental system for high-speed transmission of pulse code modulation (PCM) signals.

In this system the modulator uses signals generated at 224 million bits a second to encode light pulses from a helium-neon laser.
Because the laser pulses are of considerably shorter duration than the PCM pulse repetition period, several high-speed PCM signals, converted to optical pulses, can be multiplexed onto a single laser beam.

The width of the pulses from the helium-neon laser permits four pulses to be inserted during each PCM pulse repetition period, so that the maximum potential speed of this optical PCM system is 896 meg abits (million bits) per second, ac-

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[^2]spectrum) is continuously modulated by a varying signal current applied to the coil. The strength of this signal determines the percentage of modulation at any time.

From the crystal, the light passes through an analyzer, which converts the fluctuating plane of polarization into an amplitude-modulated light wave. The wave is detected by a high-speed germanium photodiode, which demodulates the signal impressed on the light beam.

According to LeCraw, a bandwidth of 200 MHz and 40-per-cent modulation has been achieved at room temperature with a modulating power of less than one-tenth of a watt. LeCraw notes that a bandwidth of 200 MHz is sufficient to transmit about 50,000 telephone calls or 30 television programs.

## Gallium phosphide modulates

The gallium phosphide modulator was developed by A. Ashkin, M. Gershenzen, D. F. Nelson and F. K. Reinhart. It consists of a semi-conductor p-n junction with a mounting, and input and output lenses.

The diodes are basically electrooptic phase modulators, but they can be converted by conventional techniques for use as amplitude modulators, the scientists report. Large phase differences can be achieved with small modulating voltages, they said. The modulators can be operated at room temperature.

When reverse bias is applied to the diode, two polarization components of an incoming light wave travel at different velocities in the p-n junction. The change in velocity gives a phase modulation to each polarization component.

Amplitude modulation is achieved by passing the phase-modulated components through an output polarizer.

According to the scientists, this type of modulator has exhibited phase modulation corresponding to over 80 -per-cent intensity-modulation of visible light ( 0.63 microns) when excited with 1.5 mW of power for each megahertz of modulation bandwidth.

This test of modulation efficiency was made at 51.1 MHz , but modulation can be achieved in this diode at all frequencies up to 7000 MHz , the scientists said. -


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# Laser loop: New concept for optical memory 

## Elimination of electronic transition permits high-speed storage of digital and analog data

Neil Sclater<br>East Coast Editor

Circulating memories that store modulated light beams may lead to multipurpose computers that will handle more data at a faster speed than existing systems.

Such optical memories have been developed by Bell Laboratories, of Murray Hill, N.J., and scientists there say that improved computers will become feasible when other components can match the higher speeds attainable with the new memories.

Bell's latest optical memory is a closed-loop gas laser, which includes a unique scheme that employs concave spherical mirrors to delay light. Another version contains the same delay scheme, with one exception: the feedback loop requires opticalelectrical conversion. Both memories, Bell says, are potentially superior to existing acoustic memories.

The advantages of the optical schemes over present acoustic mem-
ories, according to Donald Herriott, a Bell physicist, are superior bandwidth capabilities and the absence of frequency dispersion-a problem with sonic units.

Herriott says that optical delay lines offer the possibility of speeding nonlinear calculations like multiplication and division in future computer systems operations. They can also be used for improved correlation of returned signals from sonar and radar systems.

Both the all-optical and the op-tical-electronic memories are based on folded optical delay lines, first reported by Herriott and Dr. Harry Schulte, also a physicist, as a result of their work at Bell Laboratories in the summer of 1965. In the electronic system, light energy from a laser is converted into an electronic signal and then back to an optical beam. The all-optic scheme achieves the closed loop by using the lightamplification property of a laser.

Like other memory units, both optical memories store information
until it is called for in computation. A finite delay time is achieved as a light beam successively ricochets off the mirrors as many as 1000 times. The cycle of transmitting and recelving is continued until gating transfers in new information or clears the existing information.

## Light beam is folded

In the experimental setup, two: spherical mirrors about 10 feet apart face each other. Small holes in the reflecting surface permit the coherent light beam to be injected at an angle. The geometry of the arrangement permits multiple re-. flections without overlap (the focal points of the mirrors must be greater than half the distance of separation). This is comparable to folding a two-mile-long laser beam into a 10 -foot space. Information that is modulated into the light beam can be stored and retrieved 10 microseconds later. The delay line can store up to 10,000 bits, which can be read out serially one bit every nanosecond.

Because the light beam has no dispersion, information modulated into the laser beam is not distorted.


1. Two laser optical memory schemes depend on spherical mirrors to bounce a modulated light beam back and forth, thus keeping the information in storage. The system at left uses an optic-to-electronic loop to circulate data and

compensate for optical losses. The system at right is a gas laser loop. The large amplifier originates the beam and amplifies all reflected beams on each pass. The small laser reads in and erases data.


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## NEWS

## (optical memory, continued)

The periodic focusing performed by the spherical mirrors keeps the system's diffraction losses low.

The maximum number of beam reflections is limited by the area of the mirrors and their scattering loss (diffraction losses are negligible). During 1000 reflections the beam's light energy is reduced by scattering by only about 20 dB .

To make the most effective use of the mirrors, a slight cylindrical distortion is ground into both mirrors. An otherwise elliptical pattern of reflection then assumes the shape of the familiar Lissajous pattern. This lens design permits the greatest number of reflected spots in the smallest surface area. The output beam can be separated from the others by discriminating in both angle and position.

The present $10-\mu$ s storage time is, according to Herriott, very easy to obtain with acoustic delay lines. In fact, he says, time delays of 10 to 100 times this are possible with bandwidths of 100 MHz . And in the future, the bandwidth of the acoustic memories may well reach 2 GHz . Acoustic delay lines, however, are limited by dispersion at these frequencies and can handle only digital information. The optical delay lines,

2. One of the two spherical mirrors used to trap and reflect a laser beam is adjusted by Donald Herriott of Bell Telephone Laboratories. It is one end of an optical delay line.
on the other hand, would not be limited in this manner.

Herriott says that dispersionthe change of speed of propagation with frequency-is a problem in acoustic devices, whereas diffrac-tion-the loss of light out of a collimated beam-is the main trouble with laser transmission systems.

## Electronic link in memory

One of the Bell optical memories, using an optic-to-electronic-to-optic conversion (Fig. 1), was constructed by Schulte to demonstrate the feasibility of employing an electronic feedback loop to recirculate data to the folded optical delay line.

The optical delay makes use of two $7.5-\mathrm{cm}$-diameter spherical mirrors with slight cylindrical astigmatism (Fig. 2).

The focal lengths of the mirrors were far greater than the mirror separation distance, thus giving a beam pattern cross-section almost as large as the mirrors.

The laser used to inject the initial modulated light beam is of the helium-neon type, producing a light output of about 20 mW at $6328 \AA$. This beam is in the visible red region.
The detector used to convert the light beam into an electrical signal is a photomultiplier, that is fast enough to follow the modulation, and has low-noise characteristics.

The regenerator uses transistors to reshape the pulses, recognize the pulses that are received and locate their position.

The amplifier boosts power in the pulses to drive the modulator pulse amplifier. The modulator is a potassium tantalate niobate (KTN) or a lithium niobate device. (see "New gains made in optical communication," p. 21).

Present gas lasers cannot be modulated by varying the pumping power at the high frequencies required for effective storage. Time is needed -on the order of a microsecondto decelerate the electrons through collision with atoms. Hence, a separate modulator is required for these lasers.

## Laser loop returns light

The second version of the optical memory at Bell Laboratories (Fig. 1) is actually a loop gas laser amplifier. This scheme, constructed by Willis Yocom, another Bell physi-
cist, uses a folded optical delay line. The laser amplifier is positioned between the two mirrors and not only generates the beams but amplifies them to overcome attenuation losses.

Amplification occurs each time the beam passes through the laser amplifier, which is essentially a gas laser without end mirrors. The complete system can lase by itself.

The focal points of the spherical lenses overlap only slightly, to permit all light beams to pass through the laser amplifier.

The amplifying laser contains a helium-neon gas mixture and achieves only enough amplification to cancel losses in the delay line.

The all-optical memory eliminates some of the problems encountered with the optical-electrical loop. It can operate with both analog and digital signals and may ultimately operate at higher frequencies. The choice of system would depend on engineering applications.

The loop laser system can be modulated with both light and dark pulses. A light pulse occurs when a light beam is injected from a separate laser; a dark pulse occurs when light is canceled for a short period by the modulator.

The system needs no separate laser source to oscillate. A separate laser can be used to erase dark pulses-that is, to eliminate light canceled from the system.

The second optical laser (marked optional in Fig. 1) is unnecessary for oscillation but compensates for the additional loss caused by highspeed modulators.

## Optical memories at work

Optical delay lines may make higher frequencies possible in the relatively long-term future, Herriott says. They allow information to be stored with repeat rates that can be slighly different, so that one set of stored information can be shifted with respect to another in a manner that allows cross correlation to be done between sets of information.

Herriott explains that optical memories can be used to correlate radar signals or equivalent sound. It can be used for shifting digital information, so that digital nonlinear processes can be carried out more rapidly. These applications depend on reports, high modulation rates and analog storage. - ■


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report by members of the Congressional Armed Services and Appropriations Committee, who have long opposed Defense Dept. support of general research. It will probably also be cited by Defense Secretary Robert S. McNamara when he makes his anticipated announcement of cuts in Pentagon-supported research in line with new budget guidelines.

## Probe into communications likely

A sweeping probe into the technology of communications in the U. S. seems more and more likely during 1967. The only questions that remain appear to be who will make the study and how binding will its results be on whom.

First there was a report of the Commerce Dept. Technical Advisory Board on overcrowding of the electromagnetic spectrum, a situation that the panel terms "the silent crisis." Then came the contention of Commissioner Nicholas Johnson of the Federal Communications Commission that the commission must make deeper studies of communications technology. Then Chairman George P. Miller (D-Calif.) of the House Science and Astronautics Committee and his subcommittee chairman for science, research and development, Emilio Q. Daddario (D-Conn.) began to indicate that the House may take up the "silent crisis" as a cause. Commissioner Johnson, the young and controversial Maritime Commissioner for a brief term, wants the FCC to receive a budget increase to conduct the study. Capitol Hill observers, however, believe that such a study is likely to be carried out by some sector of the Commerce Dept., probably the National Bureau of Standards. This being the case, observers predict a struggle between Commerce and the FCC. Since the FCC is so independent-minded, such a wrangle would have been likely even without the outspoken and tenacious Johnson.

A call to battle, which is likely to be big and bitter, is the Commerce Dept. Technical

# Washington <br> Report <br> continued 

Advisory Panel's recommendation that the Federal Government should set up a research and development agency, "which has as its primary objective the improvement of the over-all effectiveness of utilization of the electromagnetic spectrum." Although the panel concedes that such an agency would serve the FCC, it would also be responsive to the Director of Telecommunications Management (a White House office), the State Dept. and myriad other Federal bodies that either use the electromagnetic spectrum heavily or are charged with allocating its use and watching out for U.S. interests in worldwide allocations.

In addition to monitoring the increasing use of the electromagnetic spectrum and sounding the alarm whenever it sees overcrowding, the proposed agency would also seek areas where research and development programs on the spectrum's better utilization, or even on totally. new means of communication, might be conducted.

These are precisely the areas that old hands at the FCC have been trying to have assigned to the commission and that Commissioner Johnson is now loudly demanding. He has pointed out that all but $\$ 2$ million of the FCC's $\$ 17$ million budget has gone on salaries. He believes that the Federal Government should at least equal industry in its ability to perform communications research and development. And it is at this point that Capitol Hill observers believe that Johnson kills his argument.
Johnson points to Bell Laboratories which employs 15,000 scientists and claims that the Government should have at least as great a facility. Congressional technology committee staff say that there is slim chance that the Government would deliberately set out to create such an agency, and absolutely no chance at all at this juncture. This is why they believe that the probe would be handed over to a few extra staff taken on by the Government's already huge and well-equipped National Bureau of Standards.

## NBS studies remote access

A National Bureau of Standards study is to take the use of computers and ADP systems a step further into the fields of scientific communication and library organization and
administration. The NBS Office of Standard Reference Data is using the Project MAC computer at MIT in an effort to promote the bureau's expertise in remote on-line access to computers with the aim of standardizing citation indexing and bibliographic coupling in retrieval systems.
NBS hopes that references in more recent papers to earlier papers would be able to lead researchers back through all previous relevant citations at high speed by means of computers. This is already possible in some libraries, but the researcher must be on the premises, must be using that library's catalog system and must be content with only that library's catalog of references. What NBS hopes to realize is a system whereby a researcher may query any major library's catalog and receive a full list of references, whether or not they are filed in the library where he is actually working. The technology already exists, officials point out, but there remains the mammoth task of bringing all the major libraries' systems together.

## Antennas integrated with ship's design

Officers of the U.S. Navy's Ship Systems Command believe that they may be on top of the problem of incorporating the multitude of shipboard antennas that are necessary now into the basic hull design and structure of the next generation of ships. Problems have included interference and the difficulty of separating whole critical sections of hulls and superstructures by nonconductive seals.
The command has just given the green light to DECO Electronics, Inc., of Washington, D. C., which has been working on the problem, to move into the second phase of its study.

At the same time the Navy has been circulating a request to firms with "secret" clearance to begin applying what DECO had already learned during the first phase of its research. An unspecified Navy agency-presumably a West Coast unit of the Ship Systems Commandgave a number of firms until early this month to turn in rough sketches and brief descriptions of a matching antenna and transmitter network that could be made an integral part of a small, experimental craft. The chosen company will have either to have experience in both electronics and small-boat-building or to be a joint venture set up to handle the project. The Navy wants it to design, develop and build the antenna system and then integrate it into the basic design and structure of an actual boat. Says one command officer: "The antenna system has got to work and the boat's got to float."

## Tektronix Type 453 oscilloscope has

- 50 MHz Performance - Dual Trace Delayed Sweep - Easy-to-Use Triggering



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The Type 453 is a portable instrument with the built-in high performance and environmental capabilities normally found only in multiple plug-in instruments.
The vertical amplifier is specified at the probe tip and provides Dual Trace, DC to 50 MHz with 7 ns risetime at $20 \mathrm{mV} / \mathrm{div}$. (DC to $40 \mathrm{MHz}, 8.75 \mathrm{~ns} \mathrm{~T}_{\mathrm{r}}$ at $5 \mathrm{mV} /$ div.) Full sensitivity $\mathrm{X}-\mathrm{Y}$ and $1 \mathrm{mV} /$ div measurements may be easily made.
The Type 453 trigger system takes the guesswork out of triggering. Pushing all the lever switches up provides a sweep and the most often used trigger logic. The sweep triggered light gives the operator a positive indication of a triggered sweep and the automatic triggering provides greater usability.
You can operate the delayed sweep with ease. Lever control to the right and HORIZ DISPLAY switch to A INTEN DURING B gives delayed sweep operation. Set-
ting the B TIME/DIV and the DELAY-TIME MULTIPLIER to meet your requirements and switching to DELAYED SWEEP allows complete measurements to be made.
The Type 453 is a continuation of the Tektronix tradition of quality workmanship. Its design and layout make it easy to maintain and calibrate. Transistors plug in and are easily removed for out-of-circuit testing. An accurate time ( $0.5 \%$ ) and amplitude ( $\pm 1 \%$ ) calibrator permits quick field calibration.
The front panel protection cover carries all the accessories with the complete manual carried in the rain and dust cover. The Type C-30 Camera and a viewing hood that fits in the rain cover also are available.
Type 453 (complete with probe and accessories) . . . . $\$ 2050.00$
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## Takes

 what hurts out of MegahertzIs your budget too tight tor your bandwidth? Here's quick and permanent relief-Data Instruments S43A. Everything about this instrument is designed for sophisticated requirements-except the price. The main frame including the time base and horizontal amplifier is $\$ 420$. Six vertical amplifiers ranging in price from $\$ 85$ to $\$ 170$ give the unit broad operating capabilities-Bandwidths to 25 MHz with a risetime of 14 nsec . And sensitivities to $100 \mu \mathrm{v} / \mathrm{cm}$. Narrow band and wide band amplifiers are also available as well as an envelope monitor with a tuned bandwidth to 32 MHz .

The 4 inch, flat-faced PDA tube provides accurate and unambiguous viewing. It is available in a variety of phosphors and has a removable graticule with controlled edge lighting. An extremely reliable time base provides sweep speeds to $.5 \mu \mathrm{sec} / \mathrm{cm}$ in 22 precisely calibrated ranges with single shot and lockout. It also has neon indication when the time base is armed. It features rock steady triggering in a number of modes and the horizontal amplifier provides 10 X expansion to 500 KHz .

For those who want even more performance there is the D43A. This is a double beam scope giving two simultaneous 25 MHz traces on a 4 inch tube. The main frame is $\$ 515$, and it accepts the same vertical amplifiers as the S43A. Each instrument is fully guaranteed for one year, and field and factory servicing are provided.

If your budget is pinching you (and even if it isn't) why not arrange for a demonstration of the S43A? We have a man in your area and it doesn't hurt to look. At $\$ 23$ a MHz it doesn't hurt at all.

## data instruments

Data Instruments Division•7300 Crescent Boulevard, Pennsauken, N.J. 08110 on reader-service card circle 21

# Thin film measures 'star dust' speed 

A thin-film sandwich six-millionths of an inch thick has been developed to measure the speed and direction of micrometeoroids streaking through outer space.

The device is said to be capable of measuring particle speeds of 150 thousand miles an hour without slowing the 'star dust' particles or diverting them from their course.

Developed by the Union Carbide Corp.'s Electronics Div., the detector consists of a film of parylene, an organic polymer, some four-millionths of an inch thick, with an aluminum film about one-millionth of an inch thick vapor-deposited on each side. The polymer has high dielectric characteristics and resistance to radiation.

When a micrometeoroid particle, which may weigh as little as a millionth of a milligram, strikes the dual surface of the device, the aluminum films are short-circuited and a pulse is recorded. The elapsed time as the particle punctures first one and then the other indicates its speed, and the relative points of puncture on the two units indicate the direction of travel.

The polymer used in the "parylene pellicle," a company official says, was selected for its almost negligible resistance to the passage of meteoroid particles, which, if fragmented in passage, would give misleading results.

The particle detector was designed for experiments on satellite sounding rockets and could be designed into large-area arrays for proposed spacecraft, according to Union Carbide.

The parylene material has a variety of other applications, the company said. It could, for example, be used as a window in an orbiting spectrometer which would be transparent to electrons but opaque to infrared radiations. It could also be used as a specimen support for an electron microscope. Acting as a "zero-mass" substrate, it would make possible an extremely fast-response thermometer. The material can also be used in optical applications such as polarizing, beam-splitting and filtering. -


# Best Op Amp 

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| Parameter | Model 111 | Model 105 | Model 106 | Model 108 | Model 114 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Open-Loop Gain-min. | 15,000 | 30,000 | 250,000 | 100,000 | 500,000 |
| Rated Output-min. | $10 \mathrm{~V}, 2.5 \mathrm{~mA}$ | $10 \mathrm{~V}, 2.5 \mathrm{~mA}$ | $10 \mathrm{~V}, 5 \mathrm{~mA}$ | $10 \mathrm{~V}, 2.5 \mathrm{~mA}$ | $10 \mathrm{~V}, 10 \mathrm{~mA}$ |
| Bias Current-max. | 200 nA | 50 nA | 50 nA | 2 nA | 2 nA |
| vs. temp.-max. | $2 \mathrm{nA} /{ }^{\circ} \mathrm{C}$ | $0.7 \mathrm{nA} /{ }^{\circ} \mathrm{C}$ | $0.7 \mathrm{nA} /{ }^{\circ} \mathrm{C}$ | $0.2 \mathrm{nA} /{ }^{\circ} \mathrm{C}$ | $0.2 \mathrm{nA} /{ }^{\circ} \mathrm{C}$ |
| Offset Current-max. | 20 nA | 5 nA | 5 nA | 2 nA | 2 nA |
| vs. temp.-max. | $1 \mathrm{nA} /{ }^{\circ} \mathrm{C}$ | $0.2 \mathrm{nA} /{ }^{\circ} \mathrm{C}$ | $0.2 \mathrm{nA} /{ }^{\circ} \mathrm{C}$ | $0.05 \mathrm{nA} /{ }^{\circ} \mathrm{C}$ | $0.05 \mathrm{nA} /{ }^{\circ} \mathrm{C}$ |
| Input Impedance |  |  |  |  |  |
| differential | $200 \mathrm{k} \Omega$ | $1 \mathrm{~m} \Omega$ | $1 \mathrm{~m} \Omega$ | $4 \mathrm{~m} \Omega$ | $4 \mathrm{~m} \Omega$ |
| common mode | $50 \mathrm{~m} \Omega$ | $100 \mathrm{~m} \Omega$ | $100 \mathrm{~m} \Omega$ | $500 \mathrm{~m} \Omega$ | $500 \mathrm{~m} \Omega$ |
| Bandwith | 1.5 mHz | 2 mHz | 2 mHz | 0.5 mHz | 0.5 mHz |
| Voltage Drift-max. |  |  |  |  |  |
| Model A | $20 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ | $20 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ | $20 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ | $20 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ | $20 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ |
| Model B | - | $10 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ | $10 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ | $10 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ | $10 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ |
| Model C | - | $5 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ | $5 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ | $5 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ | $5 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ |
| Price (1-9) | \$13 | $\begin{array}{ccc}\text { A } & \text { B } & \text { C } \\ \$ 16 & \$ 21 & \$ 26\end{array}$ | $\begin{array}{ccc}\text { A } & \text { B } & \text { C } \\ \$ 21 & \$ 26 & \$ 31\end{array}$ | $\begin{array}{ccc}\text { A } & \text { B } & \text { C } \\ \$ 28 & \$ 33 & \$ 38\end{array}$ | $\begin{array}{ccc} \text { A } & \text { B } & \text { C } \\ \$ 35 & \$ 40 & \$ 45 \end{array}$ |

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## Antimissile lens antenna tested

## Three-story-high radar array, suitable for hard silo, is fed by one transmitter

A microwave lens antenna the size of a billboard is testing the feasibility of using a single transmitter in antimissile phased-array systems. Such systems could be adapted to hardened silos that would protect the radar array from nuclear attack.

Other phased-array systems use individual transmitters to feed each antenna-radiating element, and many require separate transmitting and receiving elements.
The tests are being conducted by the Army with a Sperry Hard Point Demonstration Array Radar (Hapdar). The radar transmits an elcctronically scanned pencil beam in two dimensions (see "Phased arrays break the inertia barrier," ED 16, July 5, 1966, pp. 20-23).
A single klystron illuminates the three-story-high lens from inside a sheltering enclosure with a five-horn monopulse feed. Phase-
shifting elements of the lens collimate the incident spherical-wave front to a plane wave and steer the pencil beam that is formed.

Sperry's Great Neck, N. Y., Gyroscope Div. built the planar microwave lens radar for the Army Missile Command at the White Sands Missile Range, N. M.

The lens array consists of radiating elements driven by three-bi digital phase shifters. All phase shifters are diode-controlled by the beam-steering unit. The bcamsteering unit translates comi uter commands into the proper logic for actuating each individual phase shifter driver. The drivers set the on-off state of the corresponding diodes of the phase sl.ifters to form the antenna beam in the desired direction.

The elements of the lens are printed dipole collectors, connect-
(continued on p. 40)


A planar microwave lens antenna co!limates and steers the beam of Sperry's Hard Point Demonstration Array Radar. The system has but one transmitter, and its beam scans in two directions under computer command. Diode phase shifters permit both sending and receiving with the same antenna.


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The easy-to-read display sector may include: polarity; numerical readout with decimal point; function such as degrees $C$, inches $\mathrm{Hg}, \mathrm{PSI}$, etc.; out-of-range; and the number of the point being measured. Used with a Howell Multipoint Scanner, input signals may be mixed (linear or non-linear). An example of the Indicator-Scanner System might be a 40 -point Scanner with 20 inputs for pressure and 20 inputs for temperature with a single Indicator displaying both sets of data. The data can also be made available in binary-coded decimal or ten-line parallel decimal form at a connector for input to a computer, printer or tape punch.

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The servo-digital indicators shown above are nullbalance instruments utilizing the TA'POT ${ }^{\circledR}$ slidewire. The units are available in various ranges and various direct readouts.

BH103 Digital Data System


The BH 103 incorporates a 100-point Scanner with direct digital readout and prints the data on folded paper. i.e., EVENTS, TEMP ${ }^{\circ} \mathrm{F}$, TIME.

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# STARTING SALARIES OF ENGINEERS ARE DECEPTIVELY HIGH 

By James M. Jenks



Two separate studies of the salaries made by college graduates appear to contradict the commonly held belief that engineers today make out better financially than their classmates who major in non-technical subjects.
Both surveys were conducted by large universities. The first polled graduate engineers; the second, company executives. And both resulted in identical findings! That is, the average engineer today - despite a deceptively high starting salary-climbs fast but not far.
The need for technically trained men in recent years has exceeded the supply to such an extent that companies have been forced to bid for their services - to actually set-up "recruiting" offices on college campuses all over the country. Thus, starting salaries have gone up and up. But the income ceiling for these technicallytrained men is lower than that for managerial personnel.
Despite the substantial head start engineers have, the differential in money earned over a ten-year period averages out at $\$ 7,000$ more for the management man.

And from the tenth year on, the administrator's salary obviously outstrips that of the engineer by a wider and wider margin.

This, of course, is not to say that engineering students would be wise to shift to the study of business adminis-tration-or that working engineers face a bleak future. Quite to the contrary, the continuing growth of technology means that men with technical backgrounds are as ideally qualified for the highest rewards industry has
to offer-if they also have a knowledge of the underlying principles of business.

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 Titania DivisionChattanooga, Tennessee 37405 Sixty-Fifth Year of Ceramic Leadership
(continued from p. 36)
ed to strip-line, three-bit digital phase shifters. The phase shifter connects to a probe that is coupled to the radiating horn's interior.

About 4000 dipoles collect energy on the receiving side of the lens for transfer to the radiation side.

The radiator side of the lens contains 3750 elements, of which 2165 are active horn radiators connected to phase shifters. According to Sperry engineers, their method of connecting collectors and radiators creates a space-tapered array on the radiating side of the lens.

The engineers say that the lens has a directive gain of 39.5 dB , with total losses of 3.6 dB . The average side-lobe level is 40 dB .

The radar system has completed most of its acceptance tests, including the simultaneous tracking of several aircraft going in different directions.

While the lens is presently posi-


A phase-shifter module is installed in the collecting side of the large microwave lens in the new radar.
tioned 30 degrees from the vertical, Sperry engineers say it could be set horizontally, flush with the ground, in a hardened silo. - ■

## 2500-MHz TV links medical facilities

A $2500-\mathrm{MHz}$ broadcast television system-reported to be the largest educational TV setup in the medical field-will link five medical facilities in Atlanta, Ga.

This spring, the Radio Corporation of America will install television transmitters for broadcasting medical training programs in Grady Memorial Hospital and the Public Health Service Audiovisual facility (U.S. Dept. of Health, Education and Welfare). These two, and the Veterans' Administration Hospital, Emory University School of Medicine and Hospital, and the Georgia State Department of Health, will have receiving systems.

Roof-top antennas and "down converters" will change the $2500-\mathrm{MHz}$ signals into standard TV frequencies to receive the programs.

According to A. M. Miller, vice president of RCA's Instructional Electronics Dept., $2500-\mathrm{MHz}$ television is ideally suited for linking the five medical facilities since no
location is more than 7.2 miles from a transmitter.

This, he said, is well within the limited broadcasting range of Instruction Television Fixed Station Service-the formal designation of $2500-\mathrm{MHz}$ television established in 1963 when the Federal Communications Commission set aside 31 channels in that frequency band for educational use.

Two TK-60 TV cameras will be supplied to Grady Memorial Hospital. These will be mounted on a specially designed console that can be moved about the hospital to laboratories, auditoriums and elsewhere for broadcasting purposes.

Since the TV system is designed for two-channel operation, transmitters at Grady and the Public Health facility can each be broadcasting a different program simultaneously, or they can go on the air separately.

Last May, RCA installed a 2500 MHz educational TV system linking several hundred Catholic schools in the New York archdiocese. ■


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## Vectors are better kept as vectors

Sir:
The nomograph given by R. L. Peters ["Simplify vector analysis," ED 26, Nov. 22, 1966, pp. 82-84] for the purpose of adding vectors is certainly a clever one, and I should not wish to say anything against it. I think, though, that something should be said against its use in the way suggested: ". . . the first step is to reduce them (the vectors) to the coordinate components. . . ."

In fact, if you do that, have you not just given up using vectors? There is much to be said for keeping the vectors as vectors in the problem as long as possible, and not rushing to introduce $\mathbf{i}, \mathbf{j}$ and $\mathbf{k}$. This is the way that Newton worked; you will not find a coordinate in the whole Principia Mathematica. Unfortunately, the direct use of vectors died out for many years, until it was revived by Gibbs, Heaviside, Chapman, Milne and others about the beginning of this century.

Anyone interested in seeing what can be done by using vectors as vectors might consult E. A. Milne, Vectorial Mechanics (London: Methuen \& Co., 1948 and 1957) and G. Nadeau, Introduction to Elasticity (New York: Rinehart \& Winston, Inc., 1964).
H. L. Armstrong

Queen's University
Kingston, Ont.
Canada

## Equation is as easy as working with a diagram

 Sir:Regarding J. L. Earl's Idea for Design ["Impedance diagram simplifies complex load characterization," ED 24, Oct. 25, 1966, p. 104], this is a useful technique for determining the reactive and resistive components of an impedance when one has limited measuring equipment at hand. However, I might suggest that calculation of the re-
sistance and reactance is equally easy, if one does not want to bother with the drafting tools for the graphical solution.

The necessary equation can be derived as follows:


Given a measured impedance $|Z 1|$ to which is added in series a known resistor $R$, resulting in a new measured impedance $|Z 2|$, find the resistive ( $R 1$ ) and reactive ( $X 1$ ) components of $\left|Z_{1}\right|$ :

$$
\begin{align*}
& |Z 1|^{2}=R^{2}+|Z 2|^{2}-2 R|Z 2| \cos \theta  \tag{1}\\
& |Z 2| \cos \theta=R 1+R \tag{2}
\end{align*}
$$

Substituting Eq. 2 into Eq. 1 and solving for R1 give:

$$
\begin{align*}
|Z 1|^{2} & =R^{2}+|Z 2|^{2}-2 R(R 1+R) \\
& =R^{2}+|Z 2|^{2}-2 R R 1-2 R^{2} \\
& =\mid Z 22^{2}-R^{2}-2 R R 1 ; \\
R 1 & =\left(|Z 2|^{2}-|Z 1|^{2}-R^{2}\right) / 2 R \\
X 1 & =\left(|Z 1|^{2}-R 1^{2}\right)^{1 / 2} . \tag{4}
\end{align*}
$$

## A. T. Snyder

Senior Group Engineer
Missile \& Information Systems Div. The Boeing Co.
Seattle, Wash.

## The author replies

Sir:
I was pleased to read A. T. Snyder's comments on my Idea for Design. His derivations of the mathematical formulas for $R 1$ and $X 1$ are entirely correct, and may be used if one can remember the law of cosines or has a table of identities at hand. Most of us in the profession these days use mathematics so little it is surprising that we remember our "times tables."

Another derivation of the equations is as follows:

$$
\begin{align*}
& |Z 1|^{2}=R 1^{2}+|X 1|^{2}  \tag{1}\\
& |Z \mathcal{Z}|^{2}=(R 1+R)^{2}+|X 1|^{2} \tag{2}
\end{align*}
$$

If $|X 1|^{2}$ of Eq. 2 is replaced with its equivalent from Eq. 1, then:

$$
|Z 2|^{2}=(R 1+R)^{2}+|Z 1|^{2}-R 1^{2} .
$$

Solving for $R 1$ yields:
(continued on $p .46$ )


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## LETTERS

$R 1=\left(|Z 2|^{2}-|Z 1|^{2}-R^{2}\right) / 2 R$;
and from Eq. 1 :

$$
\begin{equation*}
X 1=\left(|Z 1|^{2}-R 1^{2}\right)^{1 / 2} \tag{4}
\end{equation*}
$$

Equations 3 and 4 are the same as those of Mr. Snyder.
If one has access to a calculator, the values of $R 1$ and $X 1$ can be found fairly quickly and accurately. To grind out the solutions in longhand would be too tedious for most of us. A table of logarithms would reduce the work somewhat, but most likely one would decide to accept slide-rule accuracy. Tabulated below are the values of $R 1$ and $X 1$ calculated by three different methods:

|  | Longhand |  | Slide rule |
| :--- | :---: | :---: | :---: |
| $\overline{\mathrm{R} 1}$ | Impedance <br> diagram |  |  |
| $\overline{\mathrm{X} 1}$ | 13.6 | 13.8 | 13.5 |

In submitting my idea to Electronic Design, it was my intention to show that one can return to fundamentals for the solution to a particular problem when the $\$ 500$ impedance bridge is, for some reason, not available. The method of solution for many of our problems is a matter of personal preference. For some it may be as difficult to find a compass and a sheet of graph paper as it is for others to lay hands on a table of trigonometric identities and logarithms. More than half the engineers I know would have to borrow the slide rule to work out that good enough. solution, and only a very few would grind it out the long way.

It was enjoyable, finally, to review some elementary mathematics.

## J. L. Earl

Sr. Instrumentation Engr.
Lockheed Missiles \& Space Co.
Santa Clara, Calif.

## Silicon FET found to operate at $77^{\circ} \mathrm{K}$

## Sir:

It was quite interesting to read the article in which you compared germanium and silicon field-effect transistors [ED 22, Sept. 27, 1966, p. 81]. I agree that the Ge FET lends itself to higher-frequency operation than a Si device. I feel that
it is the user of the FET who must decide between advantages and disadvantages when he selects the specific device. Essentially time will tell what the outcome of the Ge FET will be.

However, I must fully disagree that a Si FET will not operate below $-50^{\circ} \mathrm{C}$. Back in 1964 we performed experiments, which we repeated in 1965, with Si FETs at $77^{\circ} \mathrm{K}$, or liquid-nitrogen temperature, and we found that if proper $V_{p}$ is selected, nothing in the world stops the Si FET from operating at that temperature. We even built a two-stage amplifier which, when immersed in liquid nitrogen, operated as well as in free air.

Geza Csanky
Engineering Manager
Special Devices
Dickson Electronics Corp.
Scottsdale, Ariz.

## Accuracy is our policy

In "FET amplifier and relay in a TO-5 can operates directly off lowlevel microcircuitry" (not microcuitry, as printed), the product cover feature in ED 28, Dec. 6, 1966, pp. 118 and 120 , the contact resistance is wrongly stated. The end of the second paragraph on p. 120 should read: "Initial contact resistance is $0.1 \Omega$, increasing to a maximum of $0.2 \Omega "$ (not $1 \Omega$ and $2 \Omega$, respectively, as printed). The values in the table of specs on p. 120 are correct.

In "Signal generators $1.62-420$ MHz" list, ED 27 (Signal Generator Reference Issue), Nov. 29, 1966, p. 26, Marconi Instruments points out that its model 2002 (listed in section SG-2) has an output of 1 volt, not 0.08 volt as printed.

In "Sweep generators 950-4000 MHz" list, ED 27 (Signal Generator Reference Issue), Nov. 29, 1966, p. 40, Jerrold Electronics' model $900-\mathrm{C}$ (listed in section SW-14) is shown as having a sweep width of 10 kHz to 1 MHz . This is, in fact, only the narrow-band capability. The unit also has a $500-\mathrm{kHz}$-to-$400-\mathrm{MHz}$ wide-band capability.

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## War is hell — and let's face it: it costs like it, too

As the war in Vietnam heats up, more and more is heard about the low reliability of much military electronic equipment. Statements are made by Government advisers and others about the need to keep large numbers of manufacturers' representatives there to maintain and repair the equipment. This is hard to reconcile with our universally recognized advanced electronic technology, our achievements in space, and the impressive variety of reliable electronic and electrical consumer products that U.S. industry turns out.

Before an official hunt for scapegoats is begun, and before the design engineer (with no lobby to defend him) is blamed, investigators would do well to examine Government production procurement policies.

Production orders are commonly placed through IFBs (Invitations For Bids). The IFB awards are made on the basis of many factors, of which technical excellence and a contractor's reputation frequently are comparatively minor points. Right at the start, qualified firms may be precluded from bidding through so-called set asides. These stem from the Government's practice of setting aside parts of some orders for labor-surplus areas or for small-business encouragement. The social goal is admirable, but there is no guarantee that the final equipment will be the best. And, of course, in all IFBs the lowest bidder wins-even though the way he cuts corners may be dubious.

The same specifications can be interpreted differently by two contractors-and 100 per cent legally correctly in each case, too! A conscientious contractor, accustomed to manufacturing quality products, will naturally interpret the specifications as the minimum requirement. A "cost-saving" contractor will be looking for loopholes to enable him to squeak by. As a result, equipment that "meets the specifications" sometime fails the acid test on the battlefield.

The old saying, "You get what you pay for," is still valid. Here's an actual case: Not too long ago the Government called for bids to modify the circuitry of an alarm box, designed to alert troops to chemical-warfare attack. The IFB also called for an insulated container, so the alarm could be used in the winter. A small, marginally equipped company won, with a bid 50 per cent below that of the next bidder. The delivered equipment featured phenolic peg boards (radio-amateur variety) instead of glass-epoxy PC cards, cotton cocoons instead of double-wall, epoxy-filled insulating foam. Yet it was all "in spec."

If the Government wants the best equipment, its first and only loyalty must be to our fighting men-not to an arbitrary set of procurement rules. It's time we repaired the rules instead of the equipment.

Peter N. Budzilovich

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## Technology



Tunnel-diode building blocks pave road to high-speed digital systems. Page 74


Bidirectional decade counters excel wherever totals need addition and subtraction. Page 58

## Also in this section:

Simplify feedback amplifier design with a set of straightforward equations. Page 52
Contest-winning op-amp applications combine the useful with the unusual. Page 66
Ideas for Design. Pages 82 to 88

## Simplify feedback amplifier design with this set of straightforward equations that can be applied to a wide variety of circuits.

Most existing information on feedback amplifier design and analysis treats these useful circuits in a general sense. That is, signal flow or block diagram techniques are employed to investigate the effects of feedback on impedance levels, gain, sensitivity of circuit characteristics with respect to active-element parameter changes, stability, and the like. Even though these general techniques succeed in adapting basic feedback concepts to the problem of feedback amplifier design, an attempt to design a specific feedback network can be overcomplicated, particularly because of the relatively complex analysis involved.

Fortunately, there is another way-a straightforward set of design equations for a specific, widely used class of small-signal feedback amplifiers that employ an impedance element in the feedback path between the output and input terminals (Fig. 1). Although a single-stage transistor amplifier will be used to develop these equations, the approach can also be applied to any three-terminal, small-signal, multistage amplifier with a phase shift that is an odd multiple of 180 degrees. Unlike many existing design approaches, this one includes consideration of the effects of biasing resistances. Finally, a design example will be presented to demonstrate how the derived equation can be put into practice.

To start, consider a three-terminal active circuit in "black box" form (Fig. 1) that drives a load of impedance $Z_{L}$ from a source of internal impedance $Z_{s}$. A two-terminal impedance element, $Z_{f}$, is connected between input terminal 1 and output terminal 2. If the active network is presumed to be linear, it can be represented by any one of a number of commonly used two-port equivalent circuits. ${ }^{1}$ In this case, hybrid parameters will be used. Shunting of the input and output ports by the biasing resistors of the active network is accounted for by resistances $R_{b}$ and $R_{0}$, respectively, as shown in Fig. 2.

Hybrid parameter $h_{12}$ is a measure of the

[^3]amount of voltage feedback inherent in the electronic device itself. For transistors operating in the common-base or common-emitter mode and for the grounded-grid and grounded-cathode-tube configurations, $h_{12}$ is very small (typically less than $10^{-3}$ volt/volt) and can be assumed to be zero. Even though $h_{12}$ is approximately unity for the emitter- and cathode-follower circuits, these circuits can be excluded from the present discussion, since a connection such as that shown in Fig. 1 would yield a positive feedback system. With $h_{12}$ taken as zero, the input impedance is:
\[

$$
\begin{equation*}
Z_{i}=h_{11} R_{b} /\left(h_{11}+R_{b}\right) \tag{1}
\end{equation*}
$$

\]



1. Design equations are derived through use of this block diagram representation of a feedback amplifier.

2. Hybrid parameters are used to develop an equivalent circuit for the amplifier of Fig. 1. The shunting effects of biasing elements are accounted for by $R_{b}$ and $R_{0}$.
and the output impedance is:

$$
\begin{equation*}
Z_{o}=R_{0} /\left(1+h_{22} R_{0}\right) . \tag{2}
\end{equation*}
$$

The voltage gain, $G_{0}$, is:

$$
\begin{align*}
G_{0} & =e_{2} / e_{1} \\
& =-h_{21} Z_{o} / h_{11} . \tag{3}
\end{align*}
$$

Consequently, Eqs. 1, 2, and 3 can be used to derive a second equivalent circuit for the active network of Fig. 1. This circuit appears in Fig. 3a; Fig. 3b depicts the entire feedback amplifier using the equivalent representation of Fig. 3a.

Voltage gain for the network of Fig. 3b is determined by writing nodal equations for terminals 1 and 2:

$$
\begin{align*}
& e_{1} / Z_{i}+\left(e_{1}-E_{s}\right) / Z_{s}+\left(e_{1}-E_{o}\right) Z_{f}=0 ;  \tag{4a}\\
& E_{o} / Z_{L}+\left(E_{o}-G_{0} e_{1}\right) / Z_{o}+\left(E_{o}-e_{1}\right) / Z_{f}=0 . \tag{4b}
\end{align*}
$$

Solving for the transfer function gives:

$$
\begin{equation*}
E_{o} E_{s}=\frac{\left[Z_{i} /\left(Z_{i}+Z_{s}\right)\right]\left[G^{\prime}+\left(Z_{o} \| Z_{L}\right) / Z_{f}\right]}{1+Z_{o} \| Z_{L} / Z_{f}+\left(1-G^{\prime}\right) Z_{i} \mid Z_{s} / Z_{f}}, \tag{4c}
\end{equation*}
$$

where

$$
\begin{equation*}
G^{\prime}=G_{0} Z_{L} /\left(Z_{o}+Z_{L}\right) . \tag{4d}
\end{equation*}
$$

Equation 4 c gives the expression for the voltage gain of the feedback amplifier of Fig. 1 in terms of source, input, output, load, and feedback impedances, and the parameter $G^{\prime}$. In Fig. 3b, note


## (b)

3. Assuming that hybrid parameter $\mathbf{h}_{12}$ equals zero, the transistor equivalent (a) can be used to produce the complete feedback amplifier equivalent circuit shown in (b).

4. Reducing a feedback amplifier into block diagram form allows the application of stability, sensitivity and other important concepts to the design of the amplifier.
that $G^{\prime}$ is simply the open loop-voltage gain of the loaded amplifier.

Because $G^{\prime}$ is directly related to the gain of the active element, for high-beta transistors ( $\beta \geq 50$ ), pentodes, and high-mu triodes ( $\mu \geq 20$ ), $\left|G^{\prime}\right| \gg 1$ and $\left|G^{\prime}\right| \gg\left(Z_{o}| | Z_{L}\right) / Z_{f}$. With this assumption, and letting $F(s)=E_{o} / E_{s}$, Eq. 4c becomes:

$$
\begin{equation*}
F(s)=\left[\frac{Z_{i}}{Z_{i}+Z_{s}}\right]\left\{\frac{G^{\prime}}{\left.1+G^{\prime}\left[\frac{Z_{o}-G_{0}\left(Z_{i} \| Z_{s}\right)}{G_{0} Z_{t}}\right]\right\}}\right. \tag{5a}
\end{equation*}
$$

This can be rewritten in the simple form:

$$
\begin{equation*}
F(s)=G /(1+G H), \tag{5b}
\end{equation*}
$$

where
$G=G^{\prime} Z_{i} /\left(Z_{i}+Z_{s}\right)$,
$H=\left[\left(Z_{i}+Z_{s}\right) / Z_{i}\right]\left\{\left[Z_{o}-G_{0}\left(Z_{o} \| Z_{s}\right)\right] / G_{0} Z_{f}\right\}$.
Equation $5 b$ can be recognized as the "standard form" feedback gain, which can be derived by employing block diagram techniques for the system depicted in Fig. 4.

In effect, therefore, the general feedback configuration of Fig. 1 has been reduced to the standard-form block diagram of Fig. 4. The advantage of this manipulation is that theories of stability, sensitivity, and other important concepts, which have been extensively developed in the systems literature, can be applied to the feedback amplifier considered here. ${ }^{2,3}$ However, care must be exercised when employing these theories, since the literature generally presumes that $G$ and $H$ are independent functions. Inspection of Eqs. 5 c and 5 d shows that only when $Z_{i} \gg Z_{s}$ and $G_{0}$ $\gg\left(Z_{o} / Z_{i} \| Z_{s}\right)$ are $G$ and $H$ approximately independent of one another. For pentode amplifiers, these two approximations are valid; for some triode and transistor networks, they are too restrictive, and this must be considered during design. This interdependence complicates the gain sensitivity expression for the amplifier.

A major purpose of employing feedback around a given open-loop amplifier is to reduce the dependence of over-all amplifier gain on activedevice parameters. The circuit designer may often focus his attention on variations of system gain with respect to fluctuations in the active parameter $h_{21}$. A measure of the amount of dependence of gain on $h_{21}$ is the so-called sensitivity factor, $S$, which may be defined as:
$S=(d F / F) /\left(d h_{21} / h_{21}\right)=\left(h_{21} / F\right)\left(d F / d h_{21}\right)$.
Equation 6 may be expressed in terms of the circuit of Fig. 1 by separately evaluating both terms on the right-hand side. Using Eq. 5:

$$
\begin{equation*}
h_{21} / F=(1+G H) h_{21} / G, \tag{7a}
\end{equation*}
$$

and
$d F / d h_{21}=\left[\left(d G / d h_{21}\right)-\left(G^{2} d H / d h_{21}\right)\right] /(1+G H)^{2}$.

Substituting Eqs. 3, 4d and 5c into Eq. 7a yields:

$$
\begin{align*}
& h_{21} / F=-(1+G H)\left(h_{11} / Z_{o}\right)\left[\left(Z_{i}+Z_{s}\right) / Z_{i}\right] \\
& {\left[\left(Z_{o}+Z_{L}\right) / Z_{L}\right] ;} \tag{7c}
\end{align*}
$$

hence, using Eqs. 7b and 7c, Eq. 6 becomes:

$$
\begin{align*}
& S=[1 /(1+G H)]\left(h_{11} / Z_{o}\right)\left[\left(Z_{i}+Z_{s}\right) / Z_{i}\right] \\
& {\left[\left(Z_{o}+Z_{L}\right) / Z_{L}\right]\left[G^{2}\left(d H / d h_{21}\right)-\left(d G / d h_{21}\right)\right] .} \tag{7d}
\end{align*}
$$

To put Eq. 7d into more useful form, we can evaluate the two derivatives in the last bracketed term. Using Eqs. 3, 4 d and 5c gives:
$d G / d h_{21}=-\left[Z_{i}\left(Z_{i}+Z_{s}\right)\right]\left[Z_{L}\left(Z_{L}+Z_{o}\right)\right]\left(Z_{o} / h_{11}\right)$.
Substituting Eq. 3 into Eq. 5d leads to an expression for $G^{2} d H / d h_{21}$ :

$$
\begin{align*}
H= & -\left[\left(Z_{i}+Z_{s}\right) / Z_{i}\right]\left[\left(h_{11} / Z_{f} h_{21}\right)\right. \\
& \left.-\left(Z_{i} \mid Z_{s}\right) / Z_{f}\right] ; \\
d H / d h_{21}= & {\left[\left(Z_{i}+Z_{s}\right) / Z_{i}\right]\left(h_{11} / Z_{f}\right)\left(1 / h_{21}{ }^{2}\right) ; } \\
G^{2} d H / d h_{21}= & {\left[\left(Z_{i}+Z_{s}\right) / Z_{i}\right]\left(h_{11} / Z_{f}\right)\left[Z_{i} /\left(Z_{i}+Z_{s}\right)\right]^{2} } \\
& {\left[Z_{L} /\left(Z_{L}+Z_{o}\right)\right]^{2}\left(Z_{o} / h_{11}\right)^{2} . } \tag{8b}
\end{align*}
$$

After algebraic manipulation, Eqs. 8a and 8b substituted into Eq. 7 d give the desired result:

$$
\begin{align*}
S= & {[1 /(1+G H)]\left\{\left(Z_{o} / Z_{f}\right)\left[Z_{L} /\left(Z_{L}+Z_{o}\right)\right]\right.} \\
& {\left.\left[\left(Z_{i}+Z_{s}\right) / Z_{i}\right]\left[Z_{i} /\left(Z_{i}+Z_{s}\right)\right]+1\right\} . }  \tag{c}\\
= & {[1 /(1+G H)]\left\{G H Z_{o} /\left[Z_{o}-G_{0}\left(Z_{i} \| Z_{s}\right)\right]+1\right\} . }
\end{align*}
$$

Eq. 8c can be further simplified if note is taken of the fact that product $G H$ is the return ratio, $T_{\text {; }}$ for the feedback amplifier. ${ }^{4}$ Therefore, letting:

$$
\begin{equation*}
T=G H, \tag{8d}
\end{equation*}
$$

Eq. 8 c becomes:
$S=[1 /(1+T)]\left\{1+T /\left[1-G_{0}\left(\left.\overline{Z i}\right|^{\mid} \bar{Z}_{s} / Z_{o}\right)\right]\right\}$.
In a given design, sensitivity factor $S$ is usually given, or its value is dictated, by the requirements imposed on the feedback amplifier. It will therefore be convenient to have return ratio $T$ expressed as a function of $S$. Manipulation of Eq. 8e yields:

$$
\begin{equation*}
T=(1-S) /\left\{S-\left[1 /\left(1-G_{0} Z_{i} \mid \| Z_{s} / Z_{o}\right)\right]\right\} . \tag{8f}
\end{equation*}
$$

Everything on the right hand side of Eq. 8f is known and thus the return ratio necessary to fulfill sensitivity specifications can be calculated.

## Sample design illustrates method

To indicate the simplicity with which all of the previously developed equations can be employed in a practical design, consider the amplifier of Fig. 5a, together with its mid-frequency small-signal equivalent circuit, shown in Fig. 5b.

In Fig. 5a, $R_{1}, R_{2}, R_{3}$, and $R_{e}$ serve to bias the transistor at the desired quiescent operating point. Capacitors $C_{1}, C_{2}$, and $C_{e}$ are chosen to provide the desired response at low frequencies, while $C_{f}$ is employed to ensure that feedback resistance $R_{f}$ does not affect the quiescent operating point. Capacitor $C_{f}$ should appear as an ac short in comparison with $R_{f}$ at the signal frequen-

5. Straightforward design equations developed in the text can be used to obtain parameter values for this feedback amplifier (a) whose equivalent circuit (b) is developed with the assumption that $h_{\text {re }}$ equals zero.
cies of interest. Of course, if $R_{f}$ is large enough, $C_{f}$ may be omitted.
The example will show the calculations which are necessary to arrive at values for $R_{f}$ and $R_{L}$ to fulfill a given set of specifications. Therefore, assume $R 1=180 \mathrm{k} \Omega, R 2=39 \mathrm{k} \Omega, R 3=12 \mathrm{k} \Omega$, and $R_{e}=3.3 \mathrm{k} \Omega$. The method used in obtaining these values, and also capacitor values, can be found in the literature. ${ }^{5}$ Furthermore, assume, for the transistor employed, that $h_{i e}=3.5 \mathrm{k} \Omega, h_{o e}$ $=11 \mu \mathrm{mho}, h_{f e}=300$, and that a voltage gain of -50 is to be obtained from a source resistance, $R_{s}$, of $100 \Omega$. The sensitivity of voltage gain with respect to perturbations in $h_{f e}$ is specified as $20 \%$. STEP 1: Comparison of Fig. 5b with Fig. 2 reveals that $R_{b}=R 1 \| R 2=(39)(180) /$ (219) $=32 \mathrm{k} \Omega$, and $R_{0}=R 3=12 \mathrm{k} \Omega$. STEP 2: Eq. 1 gives $Z_{i}=h_{11}\left\|R_{b}=h_{i e}\right\| R_{b}=$ (3.5) $(32) /(35.5)=3.16 \mathrm{k} \Omega$, while Eq. 2 yields $Z_{o}=R_{0} /\left(1+h_{22} R_{0}\right)=R_{0} /(1+$ $\left.h_{o e} R_{0}\right)=(12) /(1.132)=10.6 \mathrm{k} \Omega$.
STEP 3: Eq. 3 is used to calculate $G_{0}=-h_{21} Z_{o} /$ $h_{11}=-h_{f e} Z_{o} / h_{i e}=-(300)(10.6) /$ $(3.5)=-917$. This is the open-circuit voltage gain with no feedback element connected.
STEP 4: The required return ratio can be calculated from Eq. 8 f by first noting that $Z_{i}\left\|Z_{s}=Z_{i}\right\| R_{s}=(3.16)(0.1) /(3.26)=$ $0.097 \mathrm{k} \Omega$. Then, from Eq. 8f:
$T=(1-0.2) /\{0.2-1 /[1+(917)(0.097 /$ $10.6)]\}=8.52$.

STEP 5: The required closed-circuit voltage gain without feedback, from Eq. 5 b, is: $G=(1+G H) F=(1+T) F=(9.52)$ $(-50)=-476$. Consequently, $H=$ $T / G=(8.52) /(-476)=-0.0179$.
STEP 6: Equation 5c shows that $G^{\prime}=\left(Z_{i}+Z_{s}\right)$ $G / Z_{i}=(3.26)(-476) /(3.16)=-491$; whence, from Eq. 4d: $Z_{L}=Z_{o} /\left[\left(G_{0} /\right.\right.$ $\left.\left.G^{\prime}\right)-1\right]=10.6 /[(917 / 491)-1]=12.2$ $\mathrm{k} \Omega$.
STEP 7: The value of $R_{f}$ ( or $Z_{f}$ ) can now be computed from Eq. 5 d :
$R_{f}=Z_{f}=\left[\left(Z_{i}+Z_{s}\right) / Z_{i}\right]\left\{\left[Z_{o}-G_{0}\left(Z_{i} \|\right.\right.\right.$
$\left.\left.\left.Z_{s}\right)\right] / H G_{0}\right\}=(3.26 / 3.16)[10.6+(917)$ $'(0.097) /(0.0179)(917)]=6.26 \mathrm{k} \Omega$.
Either a $5.6-\mathrm{k} \Omega$ or a $6.8-\mathrm{k} \Omega, 10 \%$-tolerant resistance can be effectively employed for $Z_{f}$ in this design. The design of the circuit at mid-frequencies is now complete.
Although the synthesis of a particular transistor amplifier was considered, the procedure developed is applicable to a large class of $N$-stage amplifiers, having a net phase shift of $180(N)$ degrees, where $N$ is an odd integer. In fact, the design procedure is applicable to any amplifier which can be represented by the equivalent circuit of Fig. 3.

The primary advantage in representing the amplifier in block diagram form is the resultant simplicity of the design equations. Furthermore, although only a mid-frequency design was discussed, the fact that the amplifier can be represented in system form as in Fig. 4 implies that Bode, root locus, root contour, and Nyquist techniques can be effectively implemented in a more complex design, where feedback impedance $Z_{f}$ must be chosen to fulfill not only sensitivity specifications but also special frequency-response requirements. Finally, besides voltage gain and sensitivity, other circuit characteristics can be expressed in terms of the easily manipulated block diagram variables, $G$ and $H$. For example, application of circuit theory to Fig. 3 will easily show that the impedance seen by the source is very nearly the parallel combination of $Z_{i}$ and $Z_{f} /$ ( $1-G$ ).

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[^4]
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# IC bidirectional counters cost less <br> than two unidirectional counters. Here are three that can be designed with RT micrologic. 

Bidirectional decade counters-counters that count either up or down-are extremely useful for keeping track of totals wherever digits are both added and subtracted. Far more convenient and cheaper than two unidirectional counters, they are perfectly suited for such applications as pulse summing (as in maintaining the correct slack in a loop of magnetic tape), position indicating (as in locating nuclear-reactor control rods), and measuring (as in counting fringes in an optical interferometer). Decade up-down counters display these results in decimal form rather than in binary.

Here are three designs for such counters-a Modulo 10 shift decade, a binary-quinary decade, and a 1-2-4-8 binary-coded decimal (BCD) decade. All three can be constructed with an industrial line of integrated circuit flip-flops and resistor-transistor logic. ${ }^{1}$ RTL gates are available in an epoxy package. RTL is also the least expensive integrated logic on the market.

All three counters offer equal performance. The Modulo 10 is easiest to design, because there are no redundant codes. Since it is a shift register with five flip-flops, it returns to zero from nine without the need for a complex feedback loop to direct it to do so. Although its code is not as popular as the $1-2-4-8$ code, the Modulo 10 decade counter is fine for counting, dividing and multiplying when its output need not interface with other equipment.

Since the two other counters can count past 10 , some sort of feedback must be included in the design.

## Modulo 10 shift decade counter uses J-K flip-flops

A Modulo 10 shift counter ${ }^{2}$ uses five J-K flipflops, ${ }^{3}$ which produce the states shown in Table 1.

[^5]The logic levels for Table 1 and subsequent tables are 0 volts for the logical 1 and +0.9 volt for the logical $0 . Q$ outputs refer to the 1 output of the $J-\mathrm{K}$ flip-flop.
The use of the J-K truth table in Table 1 makes it possible to fill in the values for the various set and clear inputs to the J-Ks.

For example, the set and clear inputs for forward direction to the $Q_{1}$ flip-flop are illustrated as columns $Q_{1 s}$ and $Q_{1 c}$. It is assumed that the Modulo 10 shift decade starts out in the reset state, where all $Q$ outputs are 1 s . Thus, in the forward direction, it is necessary to change $Q_{1}$ from a 1 to a 0 on application of the first trigger command. By use of the J-K truth table, it is seen that applying either a $0-1$ or a $1-1$ to the set-clear inputs of $Q_{1}$, produces a 0 output (that is, an $\bar{X}_{n}$ output). Since the set input ( $Q_{1 s}$ ) can be in either state ( 0 or 1 ), it is a $\phi$ symbol (don't-care state) in Table 1. However, the clear input ( $Q_{1 C}$ ) must be in the 1 state to produce the correct output. This analysis produces the $\phi-1$ state for the $Q_{1 s}-Q_{10}$ inputs for the reset state of the forward direction. Other values of the $Q_{1 s}-Q_{1 c}$ column are derived in a similar manner. Columns for $Q_{2 s}$ through $Q_{5 s}$ and $Q_{2 c}$ through $Q_{5 c}$ are also derived by a similar technique.

It is now possible to write the switching function for each set and clear input. Since the unit is to be a shift counter, the trigger input ( $T$ ) to the J-Ks will be tied to the counter input: no switching function is required for the $T$ inputs.
To determine the switching function, weights of $Q_{1}=1, Q_{2}=2, Q_{3}=4, Q_{4}=8$, and $Q_{5}=16$ are assigned to the J-K outputs. A decimal equivalent number can now be assigned to each state of the counter, as shown in Table 1. These numbers are equal to the sum of the weights of the $Q$ columns which have logical 1s in their rows.

The switching functions are arrived at by summing the decimal equivalent values for the desired input. In this process the $\Sigma_{1}$ notation denotes that the switching function has the value 1 at that particular set of decimal values. Similarly the $\Sigma_{0}$


1. The logic symbol for an RTL gate. Available in epoxy, resistor-transistor logic is inexpensive.

Table 1. Code for Modulo 10

| Decimal output | Decimal equivalent |  | Forward |  | Reverse |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 31 | $\begin{array}{llllll}1 & 1 & 1 & 1 & 1\end{array}$ | $\phi$ | 1 | $\phi$ | 0 |
| 1 | 30 | $\begin{array}{lllll}0 & 1 & 1 & 1 & 1\end{array}$ | 0 | $\phi$ | 1 | $\phi$ |
| 2 | 28 | 000111 | 0 | $\phi$ | 0 | $\phi$ |
| 3 | 24 | 000011 | 0 | $\phi$ | 0 | $\phi$ |
| 4 | 16 | 0000001 | 0 | $\phi$ | 0 | $\phi$ |
| 5 | 0 | 00000 | 1 | $\phi$ | 0 | $\phi$ |
| 6 | 1 | 10000 | $\phi$ | 0 | $\phi$ | 1 |
| 7 | 3 | 110000 | $\phi$ | 0 | $\phi$ | 0 |
| 8 | 7 | 111100 | $\phi$ | 0 | $\phi$ | 0 |
| 9 | 15 |  | $\phi$ | 0 | $\phi$ | 0 |


2. The Modulo 10 shift decade is the easiest to design because it requires no feedback loop to return it to zero
after it reaches nine: its code has precisely ten positions. But it is hard to interface with accessories.
notation denotes that the switching function has the value 0 at the given set of decimal values. All other decimal values are don't-care values.

Using $F=$ forward direction and $R=$ reverse direction, the switching functions for $Q_{1 s}$ and $Q_{10}$ become:

$$
\begin{aligned}
Q_{1 S} & =F \cdot\left[\Sigma_{1}(0)+\Sigma_{0}(16,24,28,30)\right] \\
& +\mathrm{R} \cdot\left[\Sigma_{1}(30)+\Sigma_{0}(0,16,24,28)\right], \\
Q_{10} & =F \cdot\left[\Sigma_{1}(31)+\Sigma_{0}(1,3,7,15)\right] \\
& +\mathrm{R} \cdot\left[\Sigma_{1}(1)+\Sigma_{0}(3,7,15,31)\right] .
\end{aligned}
$$

Switching functions $Q_{2 S}$ through $Q_{5 s}$ and $Q_{2 c}$ through $Q_{5 C}$ are derived in an identical manner.

Any one of a number of minimization techniques, such as the Quine-McCluskey method, ${ }^{5}$ will reduce the switching functions to the following form:

$$
\begin{aligned}
& Q_{1 s}=F \cdot \overline{Q_{5}}+R \cdot Q_{2}, Q_{1 C}=\overline{Q_{1 S}} \\
& Q_{2 S}=F \cdot Q_{1}+R \cdot Q_{3}, Q_{2 C}=\overline{Q_{2 S}} \\
& Q_{3 s}=F \cdot Q_{2}+R \cdot Q_{4}, Q_{3 C}=\overline{Q_{3 S}} \\
& Q_{1 s}=F \cdot Q_{3}+R \cdot Q_{5}, Q_{4 c}=\overline{Q_{4 S}} \\
& Q_{5 S}=F \cdot Q_{4}+R \cdot \overline{Q_{1}}, Q_{5 C}=\overline{Q_{5 S}}
\end{aligned}
$$

The carry output from the counter is derived in a manner similar to the derivation of the set and clear inputs to the J-Ks. However, there is one major difference and this concerns the use of the forward ( $F$ ) and reverse ( $R$ ) commands. To explain this, it is necessary to note that the J-K flip-flop will change states only on application of

Table 2. Code for binary-quinary

| Decimal output | Decimal equivalent | $\begin{aligned} & \text { J-K flip-flop } \\ & \text { output } \\ & 1 \end{aligned} 24888$ | Forward |  | Reverse |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 15 | 11111 | $\phi$ | 0 | $\phi$ | 1 |
| 1 | 7 | 11110 | $\phi$ | 0 | $\phi$ | 0 |
| 2 | 13 | $1 \begin{array}{llll}1 & 0 & 1 & 1\end{array}$ | $\phi$ | 0 | $\phi$ | 0 |
| 3 | 5 | 1010 | $\phi$ | 0 | $\phi$ | 0 |
| 4 | 9 | 0001 | $\phi$ | 0 | $\phi$ | 0 |
| 5 | 1 | 1000 | $\phi$ | 1 | $\phi$ | 0 |
| 6 | 8 | 0001 | 0 | $\phi$ | 1 | $\phi$ |
| 7 | 0 | 0000 | 0 | $\phi$ | 0 | $\phi$ |
| 8 | 10 | 01001 | 0 | $\phi$ | 0 | $\phi$ |
| 9 | 2 | 0100 | 1 | $\phi$ | 0 | $\phi$ |

a trigger input command. The set and clear inputs may change state in between counts without a change in the state of the J-K. Since the carry output provides the input command to the next stage, it cannot change states between counts, as would happen in a normal direction control change. Therefore the carry output of the decade, which is a function of the $F$ and $R$ commands, must be a function of a command that is static between counts. Such a command is the count input (Advance) itself. Using this command in the carry output logic provides the logical carry given below:

3. The binary-quinary decade's logic schematic uses only four J-K flip-flops, but it uses 19 RTL gates: four more
than either of the other two codes. Its output is not a very popular code.

Table 3. Code for 1-2-4-8 BCD decade

| Decimal output | Decimal equivalent | $\begin{gathered} \text { J-K flip-flop } \\ \text { output } \\ 1 \\ 1 \end{gathered} 2488$ | Forward $\mathrm{Q}_{2 \mathrm{~S}} \mathrm{Q}_{2 \mathrm{C}} \mathrm{Q}_{2 \mathrm{~T}}$ | Reverse $\mathrm{Q}_{2 \mathrm{~S}} \mathrm{Q}_{2 \mathrm{C}} \mathrm{Q}_{2 \mathrm{~T}}$ |
| :---: | :---: | :---: | :---: | :---: |
| 0 | 15 | 11111 | $\phi \quad \phi \quad 0$ | $\begin{array}{lll}\phi & 0 & 1\end{array}$ |
| 1 | 14 | $\begin{array}{lllll}0 & 1 & 1 & 1\end{array}$ | $\phi \quad 11$ | $\phi \quad \phi \quad 0$ |
| 2 | 13 | $1 \begin{array}{llll}1 & 0 & 1 & 1\end{array}$ | $\phi$$\quad \phi \quad 0$ | $1 \quad \phi \quad 1$ |
| 3 | 12 | 00011 | $1 \begin{array}{lll}1 & \phi & 1\end{array}$ | $\phi$$\quad \phi \quad 0$ |
| 4 | 11 | $\begin{array}{llll}1 & 1 & 0 & 1\end{array}$ | $\begin{array}{llll}\phi & \phi & 0\end{array}$ | $\begin{array}{llll}\phi & 1 & 1\end{array}$ |
| 5 | 10 | 0101 | $\begin{array}{llll}\phi & 1 & 1\end{array}$ | $\begin{array}{llll}\phi & \phi & 0\end{array}$ |
| 6 | 9 | 1001 | $\phi \quad \phi \quad 0$ | $1 \quad \phi \quad 1$ |
| 7 | 8 | 0001 | $1 \begin{array}{lll}1 & \phi\end{array}$ | $\phi$ |
| 8 | 7 | $\begin{array}{llll}111 & 1 & 0\end{array}$ | $\phi \quad \phi \quad 0$ | $\begin{array}{lll}\phi & 1 & 1\end{array}$ |
| 9 | 6 | 0110 | $\begin{array}{lll}\phi & 0 & 1\end{array}$ | $\phi$$\quad \phi \quad 0$ |

$$
C=\overline{\left[F \cdot Q_{4} \cdot \bar{Q}_{5}+R \cdot Q_{1} \cdot Q_{5}\right] \overline{\text { Advance }}}
$$

The logical functions required of the counter are synthesized with the RTL gate ${ }^{6}$ circuit shown in Fig. 1.

The logical diagram for the Modulo 10 shift decade is shown in Fig. 2.

## Binary circuit drives quinary

An up-down decade can be made with a binary and a quinary circuit to produce the truth table of Table 2. The binary circuit is used to drive the quinary circuit. The binary circuit is illustrated
by the $Q_{4}$ output and the quinary by $Q_{1}$ through $Q_{3}$ outputs of Table 2. Using the same weighting for the $Q$ outputs as for the Modulo 10 shift decade, the decimal equivalent column is produced. With the J-K truth table, the $Q_{1 s}$ and $Q_{1 c}$ columns can be filled in for both directions, as illustrated in Table 2.

Since the $Q_{4}$ flip-flop operates as a binary, its set and clear inputs may be held at ground potential (logical 1) while toggling is performed by a trigger from the input to be counted.

Other set and clear inputs, the quinary shift and the carry output codes are derived in a similar manner. The code of Table 2 produces the following switching functions:

$$
\begin{aligned}
Q_{1 S} & =F \cdot\left[\Sigma_{1}(2)+\Sigma_{0}(0,8,10)\right] \\
& +R \cdot\left[\Sigma_{1}(8)+\Sigma_{0}(0,2,10)\right] \\
Q_{1 G} & =F \cdot\left[\Sigma_{1}(1)+\Sigma_{0}(5,7,9,13,15)\right] \\
& +R \cdot\left[\Sigma_{1}(15)+\Sigma_{0}(1,5,7,9,13)\right]
\end{aligned}
$$

These switching functions are derived in a manner identical to those of the Modulo 10 shift decade. Switching functions for $Q_{2 S}$ through $Q_{4 S}$ and $Q_{2 c}$ through $Q_{s c}$ are similarly derived.

Following minimization of the switching functions, the results are:

$$
\begin{aligned}
& Q_{1 S}=F\left(Q_{2} \cdot \overline{Q_{4}}\right)+R\left(\overline{Q_{2}} \cdot Q_{4}\right) \\
& Q_{1 C}=F\left(\overline{Q_{3}} \cdot \overline{Q_{4}}\right)+R\left(Q_{2} \cdot Q_{4}\right) \\
& Q_{2 S}=F\left(Q_{1} \cdot \overline{Q_{4}}\right)+R\left(\overline{Q_{3}} \cdot Q_{4}\right) \\
& Q_{2 C}=F\left(Q_{1} \cdot \overline{Q_{4}}\right)+R\left(Q_{3} \cdot Q_{4}\right)
\end{aligned}
$$


4. The 1-2-4-8 binary coded decimal decade uses only four flip-flops and 15 gates. Its code is the most popular
of the three, and an inexpensive decoder-driver-display is available. It interfaces easily with other equipment.

5. The 1-2-4-8- BCD decade counter can be constructed on a single printed-circuit board. The four flip-flops are located on the lower portion of the left edge.

6. This small unit contains the decoder, driver and nixie tube for the 1-2-4-8 decade counter.

## The J-K flip-flop: how it works

The J-K flip-flop is a sequential circuit, and its output depends on the state of the previous inputs (see illustration below). This is indicated in the accompanying truth table where the previous input is $t=n$ and the present output is $\mathrm{t}=\mathrm{n}+1$. Application of a trigger command, with the set and clear inputs at logical 0 (Row a), will not produce any output change. Application of a trigger command, with the set and clear inputs at logical 1 (Row d), will produce a change of state in the output. A 0-1 input on the set-clear terminals (Row b) will always produce a 0 output. A $1-0$ input (Row c) will always produce a 1 output, on application of a trigger command.


LOGIC SYMBOL

$$
\begin{aligned}
& Q_{3 S}=F\left(Q_{2} \cdot Q_{4}\right)+R\left(Q_{1} \cdot Q_{4}\right) \\
& Q_{3 C}=F\left(\overline{Q_{2}} \cdot \overline{Q_{4}}\right)+R\left(Q_{2} \cdot Q_{4}\right)
\end{aligned}
$$

The carry output is derived in a similar manner, producing:

$$
C=\overline{\left.\left[F \cdot \overline{\left(Q_{1}\right.} \cdot Q_{2} \cdot \bar{Q}_{4}\right)+R\left(Q_{1} \cdot Q_{2} \cdot Q_{4}\right)\right] \overline{\text { Advance. }}}
$$

The logical diagram for the binary-quinary decade is shown in Fig. 3.

## Up-down decade yields 1-2-4-8 BCD code

An up-down decade can produce a $1-2-4-8 \mathrm{BCD}$ code (see Table 3). Using the J-K truth table, the code for the set and clear inputs to the J-K flip-flops is determined. This results in the $Q_{2 s}$ and $Q_{2 c}$ columns for both forward and reverse direction of Table 3. Since the $Q_{1}$ code is a binary code, its set and clear inputs are held at ground potential (logical 1). $Q_{2 T}$ is the J-K flip-flop trigger command for the $Q_{2}$ state.

After the switching equations have been written from the decimal equivalent number, as already described, the functions may be minimized. The result is the following logical functions:

$$
\begin{aligned}
& Q_{1 s}=Q_{2 s}=Q_{3 s}=Q_{4 s}=Q_{1 c}=Q_{3 c}=1 \\
& Q_{1 T}=\text { Advance }
\end{aligned}
$$

Table 4. Comparison of logic elements

|  | Number of Logic <br> Elements |  | Decoder <br> RTL |  |
| :--- | :---: | :---: | ---: | :---: |
| Tran- |  |  |  |  |
| Decade | J-K Flip-Flop RTL Gates | Gates | sistors |  |
| Modulo 10 | 5 | 15 | 5 | 10 |
| Bi-Quinary | 4 | 19 | 10 | 10 |
| $1-2-4-8$ BCD | 4 | 15 | 9 | 10 |

$$
\begin{aligned}
& Q_{2 T}=\overline{\left[F \cdot \overline{Q_{1}}+R \cdot Q_{1}\right] \overline{\text { Advance }}=Q_{4 T}} \\
& Q_{3 T}=\overline{\left.\left[F\left(\overline{Q_{1}} \cdot \overline{Q_{2}}\right)+R\left(Q_{1}\right) \overline{\left(\bar{Q}_{4}\right.}+Q_{2} \cdot \overline{Q_{3}}\right)\right] \overline{\text { Advance }}} \\
& Q_{3 c}=F \cdot Q_{4}+R\left(\overline{Q_{3}}+\overline{Q_{4}}\right) \\
& Q_{4 c}=F \cdot\left(\overline{Q_{2}} \cdot \overline{Q_{3}}\right)+R\left(Q_{2} \cdot Q_{3}\right)
\end{aligned}
$$

The carry output is derived in a similar manner:

$$
C=\overline{\left[F\left(\overline{Q_{1}} \cdot \overline{Q_{4}}\right)+R\left(Q_{1} \cdot Q_{2} \cdot Q_{3} \cdot Q_{4}\right)\right] \overline{\text { Advance }} .}
$$

The trigger inputs to the J-Ks are conditioned by the input pulse to be counted (Advance) ; the set and clear commands do not need this conditioning. The logical diagram for the 1-2-4-8 BCD decade is shown in Fig. 4.

## The decades compared with each other

The total number of logical elements in both the counter and decoder for each of the three decade codes is presented in Table 4.

The table shows that the 1-2-4-8 BCD decade uses fewest logic elements of the three counters; of the decoders. that of the Modulo 10 requires fewest logic elements. The choice of code may be influenced by the input code of the equipment with which the counter will interface. For example, an inexpensive integrated decoder-driver accepts only the $1-2-4-8$ BCD code. The $1-2-4-8$ BCD code is, in fact, the most popular of the three codes.

Figure 5 shows the printed-circuit board of the $1-2-4-8$ BCD decade; it contains four J-K flip-flops and 15 dual RTL gate elements. The commercially available decoder driver with a suitable Nixie tube appears in Fig. 6. -

## References:

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3. $\mu L$ 923 J-K Flip-Flop, Data Sheet (Mountain View, Calif.: Fairchild Semiconductor, May, 1964).
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5. Ibid.
6. Fairchild Planar Epitaxial Micrologic, 914 Dual TwoInput Gate Elements (Mountain View, Calif.: Fairchild Semiconductor, April, 1964).
7. Fairchild $C \mu L 990$ Decimal Decoder-Driver, Data Sheet (Mountain View, Calif.: Fairchild Semiconductor, July, 1965).


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# Put operational amplifiers to work <br> <br> in four applications unusual and useful enough <br> <br> in four applications unusual and useful enough to win top honors in an industry-wide contest. 

 to win top honors in an industry-wide contest.}

New and unusual applications for operational amplifiers are constantly being found by designers. Here are four that were considered so unusual and useful that they received top honors in a design contest.

The four ideas were entered in last spring's Fairchild Operational Amplifier Contest, which focused on applications for the Fairchild A00-9 and A00-10 operational amplifiers. (See page 70 for specs.) Although they apply to a specific device, each of these blue-ribbon entries is readily adapt-

## On-line double integration

The stabilized operational amplifier can be used to perform real-time, on-line double integration for displacement vs time data systems. It dispenses with the need for computer processing during data reduction and the accompanying delay, expense and loss of accuracy.

The need for double integration is apparent in system response or earth-motion measurements involving accelerometer instrumentation. Until recently, to obtain the change in distance as a function of elapsed-time information, one on-line integration was performed, and then down-time data reduction by computer was called for. Double integration in the field (on-line) was virtually impossible because of the limitations imposed by operational amplifier drift and zero shifting.

The new breed of operational amplifiers with built-in stability and minimal drift makes on-line double integration easy. Moreover, it is now possible to make accurate measurements even when hours pass between the moment of final adjustment and the beginning of test operation.

Besides the operational amplifier, all that is needed in the way of additional circuitry is resistive and capacitive components that satisfy the mathematical requirements of the double-integration function. These are connected in the input
able to operational amplifiers in general:

- Double-integration circuit, submitted by Gordon J. Hansen, Sandia Corp., Albuquerque, N. M.
- Multipurpose oscillator, submitted by D. F. Palmer, Research Triangle Institute, Durham, N. C.
- Dual-limit comparator, submitted by Richard D. Wood, Sunnyvale, Calif.
- Wheel-slippage prevention system, submitted by L. J. Lawson, Lear Siegler, Inc., Cleveland.


1. Double integration is performed on-line when appropriate resistive and capacitive components are added to the input and feedback loop of a stable operational amplifier.
and feedback circuits of the amplifier (Fig. 1).
The Laplace transformations for the input and feedback networks of the subsystem are:

$$
\begin{gather*}
Z_{i}(s)=\frac{R 1^{2} C 1}{s}\left[s^{2}+\frac{2 C 4+C 1}{R 1 C 1 C 4}+\frac{1}{R 1^{2} C 1 C 4}\right],  \tag{1}\\
Z_{l}(s)=\frac{2}{C 3}\left\{\begin{array}{c}
s^{2}+\frac{4 R 2+R 3}{2 R 2 R 3 C 3}+\frac{1}{R 2 R 3 C 2 C 3} \\
{[s+(1 / R 3 C 3)]^{2}[s+(1 / R 2 C 2)]}
\end{array}\right\} \tag{2}
\end{gather*}
$$

Making $C 4 \gg C 1$ and $R 3 \gg R 2$ has the following effect:

$$
\begin{equation*}
Z_{i}(s)=\frac{R 1^{2} C 1}{s}\left(s+\frac{2}{R 1 C 1}\right)\left(s+\frac{1}{2 R 1 C 4}\right), \tag{3}
\end{equation*}
$$

## Oscillator building block

An operational amplifier as a key stage in an oscillator network secures subsystem versatility. The relatively simple subsystem blocks produced in this manner can be used for programing, multiplexing or demodulating.

The use of operational amplifiers in oscillator circuits is neither well-known nor widespread, but the combination yields:

- A stable audio oscillator (dc to 500 kHz ) variable by programing or manual means.
- An oscillator with a linear FM characteristic that can be frequency-multiplexed or phase-locked for demodulation, and thus serve as a versatile laboratory instrument.

The basic unit is an RC phase-shift oscillator that has an operational amplifier as the active component (Fig. 2). Design equations for the common oscillator configurations are available in many handbooks, but operational amplifiers can be used over a much wider range than these equations imply. Their phase shift decreases from

$$
\begin{equation*}
Z_{f}(s)=\frac{2}{C 3}\left\{\frac{[s+(1 / 2 R 2 C 3)][s+(2 / R 3 C 2)]}{[s+(1 / R 3 C 3)]^{2}[s+(1 / R 2 C 2)]}\right\} . \tag{4}
\end{equation*}
$$

Component values are chosen such that $R 1 \mathrm{C} 1=$ 4 R2 C3; R3 C2 $=4$ R1 C4, and R2 C2 $=R 3 C 3=$ $T_{c}$. Therefore:

$$
\begin{align*}
& E_{o}(s) / E_{i}(s)=Z_{f}(s) / Z_{i}(s), \text { and } \\
\frac{Z_{f}(s)}{Z_{i}(s)}= & \frac{2}{R 1^{2} C 1 C 3}\left\{\frac{s}{[s+(1 / R 3 C 3)]^{3}}\right\} \\
= & \frac{2 C 4}{T_{c}{ }^{2} C 3}\left\{\frac{s}{\left[s+\left(1 / T_{c}\right)\right]^{3}}\right\} \tag{5}
\end{align*}
$$

Taking the inverse transformation for a unitstep input for Eq. 5 produces:

$$
\begin{equation*}
e_{o}(t)=-\left(C 4 / T_{c}^{2} C 3\right) t^{2} \epsilon^{-t / T_{c}} \tag{6}
\end{equation*}
$$

Note that Eq. 6 is the form which performs shortterm double integration (via the $t^{2}$ term) with long-term stability (shown by exponential).

This circuit can be constructed with components of only $10 \%$ tolerances; polystyrene or similar precision capacitors are not necessary. The circuit will hold a zero indefinitely without the need for shorting relays. It will perform accurate double integrations over periods of several seconds.
$180^{\circ}$ at higher frequencies, and so requires less phase shift, and yields less attenuation, in the RC network. Figure 3 depicts a simple RC lag network used in the basic oscillator.

A typical oscillator, designed to operate at 100 kHz , appears in Fig. 4. Its frequency variation with RC, $1 / \mathrm{RC}$, and amplifier gain, $A$, is shown in Fig. 5. These curves demonstrate that the frequency can be varied manually or remotely to provide a laboratory oscillator ( 13 kHz to 300 kHz ) with a nearly linear characteristic over a wide range.

The open-loop gain of the oscillator (Fig. 4) never exceeds approximately 2.2 . Component nonlinearities therefore stabilize the amplitude of oscillation. In other cases it may be desirable to replace some of the linear components with varactors, or thyrite, thermite or soft-kneed Zener diodes. These same nonlinear components make remote operation possible.

The ability to angle-modulate this oscillator broadens its use for instrumentation; its operating frequency range is suited for hard-wire links in underground, underwater, or other remote

2. Versatility is increased in an RC phase-shift oscillator when an operational amplifier is used as the active element.


QOOI O.OI
NORMALIZED FREQUENCY SCALE: RC $f$ FOR THE RC NETWORK; $\left.\frac{0.1}{100 f_{h}}+1\right)$
\& FOR AMPLIFIER, WHERE $A=\frac{R_{f}}{R_{i}}$ AND $f_{n}=$ GAIN-BANOWIDTH PRODUCT
3. Simple RC lag network yields good performance from operational amplifier oscillator.
(continued on p. 68)
(continued from p. 67)
experiments. It is in such applications that it is especially important to have a very stable, lowdrift operational amplifier.

## Feedback element sets modulation mode $\mathbf{E}$

The simplest way to achieve modulation is to replace feedback resistor $R_{f}$ with a soft-kneed Zener diode and a series linear resistor. A proper choice of resistance values would then allow gain $A$ to vary nearly linearly with input voltage. A bias, then, is needed for bipolar signals.

The circuit of Fig. 6 is the final configuration of

4. Open-loop gain never exceeds 2.2 in this oscillator designed to operate at 100 kHz .

5. Curves show nearly linear characteristic of oscillator over wide frequency range.

6. Oscillator can easily be modulated by external signal. Frequency multiplexing as well as phase locking to an external signal are also possible.

## Dual-limit comparator

A comparator with two limits can be constructed from a stable operational amplifier with a few integrated gates and J-K flip-flops.

Two controls are shown on the circuit diagram (Fig. 7): the first sets the nominal input; the second controls the deviation from this nominal value. The reference input shown is an ac signal, the frequency of which must be considerably higher than the input voltage that is being compared.

The low-drift operational amplifier acts as a dc comparator operating about the zero input level. This is illustrated by waveform $e_{1}$; the $e_{2}$ output voltage represents the operational amplifier input signal in binary form. The reference signal is delayed somewhat less than $90^{\circ}$ and is applied as a square wave to the J-K flip-flops. The flip-flops respond to a rising clock pulse signal only, and are not affected by a steady-state voltage in any manner. As illustrated by the logic and the waveforms, the flip-flops monitor the comparator voltage serially. If the comparator signal alternates while being sampled, the exclusive-OR gate generates a logical 1 output. Thus, only one operational amplifier forms a dual-limit comparator.
the basic oscillator. Modulation voltages can be applied to the input as shown. The resultant. frequency change is predictable, as Fig. 5 demonstrates.

Frequency multiplexing is also a simple matter because a number of control voltages can be applied with good isolation. Application of a signal with a component at the basic oscillator frequency will cause mixing in the nonlinear $R_{f}$. All mixing components (except the low-frequency one) will be attenuated, since they occur beyond twice the oscillator's frequency. This low-frequency component will be amplified and modulate $R_{f}$. The result is phase locking and tracking of the incoming signal by the oscillator.

7. Op amp functions as dc comparator operating about the zero input level.

## Wheel slippage sensors

The common, disturbing and even disastrous problem of wheel slippage in railway drive systems can be completely eliminated by a unique application of highly-stable operational amplifiers. The system senses the tendency toward slippage before it occurs, and initiates corrective action.

A block diagram of the slippage prevention system is shown in Fig. 8. The de input signals to the system are obtained from Lear Siegler torque sensors. These ac inductive-type transducer-demodulators produce a dc output voltage which is proportional to the actual torque delivered to each axle (tractive force to the rail). The sensor out(continued on p. 70)

8. Five op amps form sensing system to detect wheel slippage on railway cars.

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9. Slippage is preceded by torque breakover point, which can be detected by the system before actual slippage occurs.
put-voltage profile of an axle going into a slippage condition is shown in Fig. 9. The sharp change in sensor output is seen to occur at the instant a tendency toward loss of wheel traction begins (torque breakover point). This torque variation gives warning of an eventual loss of wheel traction that will occur if no preventive action is taken.

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## A00-9 and A00-10 specifications

|  | A00-9 | A00-10 |
| :---: | :---: | :---: |
| DC gain | $4 \times 10^{7}(\mathrm{~min})$ | $5 \times 10^{7}(\mathrm{~min})$ |
| Drift | $<1 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ | $<0.5 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ |
| Long-term drift | $2 \mathrm{~V} / 100 \mathrm{hrs}$. | $1 \mathrm{~V} / 100 \mathrm{hrs}$. |
| Output | 40 mA at $\pm 100 \mathrm{~V}$; <br> 10 mA at $\pm 140 \mathrm{~V}$ | 5 mA at $\pm 25 \mathrm{~V}$ |



## 



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# Tunnel-diode circuits pave the way 

## to the ultra-high-speed digital circuits required for wide-band, high-resolution communication systems.

Future communication systems will require higher-speed digital circuits in order to provide high-resolution intelligence, coding capability and multiplexing features, and to employ available RF bandwidths efficiently. An investigation of highspeed digital circuits showed that the most promising means of obtaining logic at uhf rates was a three-phase, clocked, tunnel-diode circuit. To obtain some indication of system performance, feasibility models of a shift register generator and comparator generator were built and error rate measurements taken.

The problems encountered-capacitive coupling, ground-plane noise, and coaxial-cable cou-pling-are not peculiar to this specific system, but are general difficulties that have to be overcome if any high-speed digital system is to be operated successfully. The error data are indicative of the error rates that can be achieved in the present state of the art.

High-speed logic systems such as the one described are likely to find application in:

- Data links for the transmission of wide-band sensor data.
- Economical data links for the transmission of many TV, voice and data channels on one link.
- Communications equipment on deep space probes.

The first logical step in developing this uhf digital equipment is to set up techniques and circuits from which standard logic blocks such as flip-flops, gates, and inverters may be evolved. These blocks should, in turn, be compatible with each other and be capable of implementing different types of system operation.

## Shift register is system's foundation

A system (Fig. 1) developed at Fairchild Hiller consisted of two seven-stage, psuedorandom code generators which were synchronized and whose outputs were fed into a comparator to generate error pulses if the outputs of the two code genera-

[^6]tors differed. These error pulses were fed into a string of five binary dividers to reduce the maximum error rate to one that could be measured with readily available commercial counters.
With the exception of the lower-speed binary dividers, the entire system was built from the standard block shown in Fig. 2.

The basic building block is a shift registergenerator module which can also perform as a flip-flop, binary divider, AND gate, OR gate, NAND gate, NOR gate, and modulo-two adder for two inputs. The schematic of this module is shown in Fig. 3. It uses a three-phase sinusoidal clock, to obtain isolation between input and output of the block, and diode coupling between phases.

Prior to the final design of the circuit, an extensive study had shown that the tunnel diode was the only element available that would give successful ultra-high-speed operation. The study also showed that a tunnel-diode, stored-charge diode circuit should be capable of operation near 1 GHz , with a fan-out of 3 . Laboratory models of the circuit, however, were capable of only limited logic-function operation (i.e., AND, OR, etc.) at that frequency. The root of the problem was capacitive coupling between components and the groundplane structure and series lead inductance.

Meanwhile, system operation at several hundred hertz was possible with the circuit shown in Fig. 3, a basic three-phase clocked circuit with AND and OR functions determined by setting the bias currents to the single-phase stages used. Final card layout of the circuit was the result of a tradeoff in spreading components apart to avoid coupling and yet keeping them close enough together to avoid excessive phase shift. Ground-plane construction was a necessity.
Two basic modules connected as a binary divider and their input and output waveforms are shown in Fig. 4a. Figure 4b shows them in a fourstage pseudorandom code generator and the code output waveform. Waveforms were photographed from a Tektronix 661 sampling oscilloscope.
Figure 5 shows how the three-phase clock was derived. Two criteria for the power divider network were good clock source voltage stability and clock outputs to each card with a $25-\mathrm{dB}$ minimum


1. Uhf logic forms the basis of a high-speed communica-
tion system. Code generator outputs are fed to a comparator which generates output error pulses if the code generator outputs differ.
2. Shift register generator module is the basic building block for the uhf logic system. This block can perform the functions of a flip-flop, binary divider and various gates.
3. Circuit details for the block diagram of Fig. 2. A threephase sinusoidal clock is used to obtain isolation between input and output.


INVERTER SIDE


R3, R7, R15
R6, R8
$430 \Omega$
R6, R8
$\begin{array}{lllll}\text { R17 } \\ \text { R19, R22 BOURNS TRIMPOT } & 5 k & 271-1-502 \quad 6450 \mathrm{~A} \\ 510 \Omega\end{array}$
R19, R22 $\qquad$ G.E $820 \Omega$
$252 A$

R18, R21
TDI, TD4, TD6, TD8, TDIO
$\mathrm{Cl}, \mathrm{C} 2, \mathrm{C} 3, \mathrm{C} 4, \mathrm{C} 5, \mathrm{C} 6$

- G.E. $252 A$ 470 pF
isolation between clock outputs at $f_{c}$ (clock frequency). The phase delays were generated with coaxial-line phase shifters.


## Reliable code generators needed

The first step in operating the system was to have the two seven-stage pseudorandom code generators function reliably.

Detailed connections of the seven-stage generator and its coded output waveform are shown in Fig. 6. After the first generator was set up and operating, its output was connected to the input of the second generator. Reliable operation then became increasingly difficult, and even more so as the outputs were compared and the errors counted.

Although stable operation was hard to keep up, the system would run stably for an hour or solong enough to make error rate measurements as an indication of system performance. Table 1 shows several typical measurement runs made when system operation was optimum.
The percentage errors figure was computed according to the formula:

$$
\% \text { errors }=N_{e} / N_{b} \times 100 .
$$

The number of errors, $N_{e}$, is the count registered on the commercial counter at time $T$ multiplied by 32 , which is the count of the five-stage system counter. The number of bits $\left(N_{b}\right)$ is the bit rate multiplied by $T$.
During operation, errors did not occur random-

4. Two basic shift-generator modules are connected to form a binary divider (a). These two modules can also be connected to form a four-stage pseudorandom code generator (b).
ly, several errors at a time, but tended to occur in groups ranging in size from 100 to 300 errors per group. Table 2 shows figures for a group error rate measurement.

The reasons for the drop in operating reliability as the number of building blocks increased are not peculiar to this system. Such problems will be present in any tunnel-diode digital system operating in the region of several hundred MHz or higher. The problems encountered were:

- Noise
- Capacitive Coupling
- Reflected Signals

Noise: Figure 7 shows a curve of a typical $15-\mathrm{mA}$ -peak-current tunnel-diode combination used on the input $\left(\phi_{1}\right)$ and output $\left(\phi_{3}\right)$ stages of the tunnel-diode cards. Two load lines are shown: LL1 shows the load line for a $13-\mathrm{mA}$ bias and $L L 2$ shows the load line for a $10-\mathrm{mA}$ bias. Point $A$ on the curve shows the operating point of the tunnel diode for a $2-\mathrm{mA}$ data current input. It can be seen from the curve that, if the diode is operated at $A$, only 50 mV of noise at 2 mA is needed to trigger the tunnel diode falsely into its high state. This problem is reduced by providing as good a ground as possible and by lowering the bias current to some point, such as $B$, where the noise present, $I_{n}$, is insufficient to cause false triggering.

This method of providing noise immunity has drawbacks, for fan-out capability is decreased by the amount shown on the curve in Fig. $7\left(I_{c}-I_{d}\right)$.


25 db MIN ISOLATION BETWEEN ALL PORTS
5. Three-phase clock signals are derived and distributed with this circuit. Phase delays are generated with coaxialline phase shifters. Clock voltage stability and isolation between clock outputs were important design criteria.

First, ground noise and induced noise, $I_{n}$, lower fan-out. Then, since the operating point is lower in bias current, more data current is required at the input, and thus the fan-outs of the modules driving this stage are effectively reduced further because more of their total output current is required per card.
Capacitive coupling: Two types of capacitive coupling were encpuntered: parasitic capacitive coupling between components, and capacitive coupling between points on either side of the printed board, with G10 board material acting as a dielectric.

Although the basic frequency involved is the clock, the fast rising edge of the tunnel-diode output ( 0.4 ns ) contains frequency components much higher than the clock frequency. The 0.4 -ns edge measured by the sampling scope corresponds approximately to a $1-\mathrm{GHz}$ signal. At 1 GHz the impedance of the $1-\mathrm{pF}$ capacitor is:

$$
\begin{equation*}
Z=1 / 2 \pi f C=106 \Omega . \tag{1}
\end{equation*}
$$

The tunnel-diode voltage change of 0.5 volt is capable of coupling considerable current through this impedance. Equation 2 shows the coupling current through 1 pF to be 3 mA :

$$
\begin{equation*}
I_{c}=0.5 \mathrm{~V} / 160 \Omega=3 \mathrm{~mA} . \tag{2}
\end{equation*}
$$

Actual measurement shows this to be somewhat less than predicted, but only in the order of milliamperes. Thus we can see that this current is

Table 1. Single error percentages.

| Run | Time (min) | \% errors |
| :---: | :---: | :--- |
| 1 | 3 | 0.0013 |
|  | 7 | 0.0011 |
|  | 11 | 0.0009 |
| 2 | 5 | 0.0003 |
|  | 10 | 0.0004 |
|  | 15 | 0.0005 |
| 3 | 5 | 0.00062 |
|  | 10 | 0.00057 |
|  | 15 | 0.00058 |
|  | 25 | 0.00053 |
|  |  |  |

Table 2. Group error percentages.

| Time $(\min )$ | \% group errors |
| :---: | :---: |
| 2 | $5.5 \times 15^{-6}$ |
| 4 | $5.5 \times 10^{-6}$ |

capable of introducing false errors if bias levels are not reduced to eliminate the problem. But again, lowering the bias lowers fan-out and increases input drive requirements.

Values of parasitic capacitance between components were difficult to determine but experimentation showed them to be small with the packaging configuration used. Capacitance between points on opposite sides of the printed board such as the 0 output and the 1 output showed capacitances of $0.5-1.5 \mathrm{pF}$ until the boards were modified to decrease the coupling. The modification consisted of spreading the points farther apart by insertion of another piece of glass board. This same modification was used on the input pads on the cards.

The parasitic capacitance between components does put some limitations on packaging. An attempt to increase the packaging density resulted in an increase in parasitic capacitance to such an extent that the circuit would only operate at a much lower frequency.
Reflected signals: Ideally the length of the connections, $l$, between cards should be as short as possible ( $l \ll \lambda$ ), as on the code generator trays. Several places on the system, however, require the use of coaxial lines for signal routing, such as the connection between the two generator trays and between the generator inputs and the comparator. Where the length of the connection would be less than one clock wavelength, a longer connection, which must be some multiple of a wavelength,

6. Shift register modules are connected to form a sevenstage pseudorandom code generator (top). Two of these generators are used in the system. The coded output waveform of one generator is shown at the bottom.

7. Effects of bias levels on tunnel-diode operation are shown by the load lines plotted on the characteristic curve for a $15 \cdot \mathrm{~mA}$-peak tunnel diode. LL1 represents a bias current of 13 mA while LL2 is for a $10 \cdot \mathrm{~mA}$ bias.

8. Coaxial coupling is used between system stages (a). Diodes D2 and D3 are needed for isolation while diode D1 provides the level shift necessary for the output of tunnel diode TDI to overcome the combined threshold of diodes D2 and D3. Part (b) shows the effect of reflected signals if the bias of D1 is not low enough.

$\ell T D=\underbrace{\ell D C+\ell C L}_{\text {BIAS }}+\underbrace{\ell D A T A+\ell n+\ell \ell_{\mathrm{C}}+\ell R}_{\text {VARIABLE }}$
(b)
9. An ideal tunnel diode is shown in (a) together with its bias and data currents. Actual tunnel diode is shown in (b).
must be used.
The problem encountered in these coaxial connections is one of reflected signals. It stems from the difficulty of connecting two nonlinear, low-impedance devices through a properly terminated coaxial cable. Figure 8a shows the method of coupling that was used. The resistance values shown were selected for the bias conditions at which the various diodes operate. Diodes D2 and D3 are both needed for isolation. Because the 1 output level of tunnel diode TD1 is insufficient ( 0.6 V ) to overcome the combined threshold of diodes D2 and D3 ( 0.8 V ), diode D1 was inserted to provide the level shift needed ( 0.5 V ).

Although the diode resistances approximate the 50 -ohm termination needed for the coaxial cable, and isolation diodes D2 and D3 minimize the reflections that are introduced, some reflected signal $\left(I_{r}\right)$ can still be coupled back into TD1 unless the bias of TD1 is reduced below the level of the reflected signal.

Figure 8 shows the effect of reflected signals if the bias of $D 1$ is not reduced enough. A data bit $A$ is shifted into TD1 and one wavelength or bit-time later the data are shifted into TD2. At the instant TD2 is switched to its high-voltage state, the line is abruptly terminated in a high impedance instead of 50 ohms because $D 2$ and $D 3$ are suddenly reverse-biased. The result is that a reflected current bit, $B$, is sent back down the coaxial line two bit-times later than the original data pulse and coupled into TD1. The way to avoid this problem was again to reduce the bias, which, as in the case of noise, further reduced the fan-out.

Figure 9 illustrates the effect of the major problems in system operation: Fig. 9a shows the ideal tunnel diode with its bias and data currents; Fig. 9b shows the practicable case. As system complexity increases, the signals on the ground planes and the number of interconnections increase, and-predictably-system operation becomes more unstable.

Two of the problems discussed-capacitive coupling and noise - are directly related to frequency. As frequency increases, the impedance of the coupling decreases:

$$
\begin{equation*}
Z_{c}=1 / \omega C \tag{3}
\end{equation*}
$$

The noise problem and signals on the ground are due primarily to the series inductance in the ground plane which causes it to deviate from an ideal ground. From:

$$
\begin{equation*}
Z_{L}=\omega L \tag{4}
\end{equation*}
$$

it can be seen that the series impedance of the ground increases with frequency also.

As would be expected, when operation at higher frequencies was attempted, the problems were too great to allow the system to perform, even though individual logic functions could be made to operate at a considerably higher speed.


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## Book Reviews



## A glut of gaslights for every design need

## Applications of Neon Lamps and

 Gas Discharge Tubes, Edward Bauman (Carlton Press, New York), 160 pp. \$2.95.Here is a book full of circuits, display devices, memory elements, etc., all using neon glow lamps and gas discharge tubes. Its 160 pages are packed with practical examples of oscillators, timers, switches, and voltage regulators. The book should be useful to any electronic engineer, i.e., circuit and equipment designers, production and test engineers, systems engineers. It is written in simple language and the material is arranged in order of increasing complexity to aid understanding.

While most of the lamps discussed in the book are manufactured by Signalite, Inc., understanding the operation of each circuit should make it possible to use other lamps in similar applications. In this sense, the book is fairly objective and it can very well serve as a general reference on applications of neon glow lamps and gas discharge tubes.
—Peter N. Budzilovich

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## UJT and ac current source used to divide frequency

This circuit will divide an input sine-wave frequency by 2 to 20 depending on the setting of R4. With appropriate circuit values, the input sine-wave frequency may be from 10 to 100 kHz . The maximum output pulse frequency will be in the vicinity of 50 kHz .

The value of $R 1$ is such as to allow the generator, $E_{I N}$, to drive Q1 into cutoff or saturation. When Q1 is saturated, a voltage drop will be developed across $R 3$, which will be matched by the sum of $V_{B E}$ of $Q 2$ and the voltage drop across $R_{4}$ and $R 5$ created by $Q 2$ emitter current. Virtually all Q2 emitter current becomes Q2 collector current, which will charge $C 1$. The magnitude of $Q 2$ emitter or collector current will be determined by the setting of $R 4$. When $Q 1$ is cut off, $Q 2$ is also cut off, and C1 can discharge only by means of negligible leakage current through $Q 2, Q 3$, or its own internal resistance.

Then, for each cycle of sine-wave input, one pulse of constant amplitude current will charge C1.
The circuit can be proportioned so that the capacitance of $C 1$, the magnitude of the current pulse as established by $R 4$, and the frequency of current pulses as established by the $E_{I N}$ genera-

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1. Switching current source Q2 charges C1 in steps up to the "firing" of Q3. The number of steps depends on R4, R5, and C1.
tor, will be sufficient periodically to charge $C 1$, in steps, to a voltage level equal to the peak point of unijunction transistor Q3. At that point, C1 will discharge through Q3 and $R 7$ to the valley point of Q3. The period of the charge and discharge of C1 can be made any exact multiple of the period of $E_{I N}$ by proper adjustment of $R_{4}$. The discharge of C1 through $R 7$ will create an output voltage pulse across $R^{7}$, which will be a predetermined submultiple of the frequency of $E_{I N}$.

## Effects of temperature and R4 variations

| Input <br> frequency <br> $\left(f_{\text {IN }}\right)$ | Dividing <br> multiple <br> $(N)$ | Temp <br> $\left({ }^{\circ} \mathrm{C}\right)$ | R7 |  |
| :---: | :---: | :---: | :---: | :--- |
| 60 Hz | 5 | 30 | 120 | Range of R4 <br> to hold given N <br> (ohms*) |
| 60 Hz | 20 | 30 | 120 | 315 to 440 |
| 60 Hz | 5 | 50 | 120 | 310 to 2390 |
| 60 Hz | 20 | 50 | 120 | 2250 to 2380 |
| 100 kHz | 5 | 30 | 56 | 205 to 250 |
| 100 kHz | 20 | 30 | 56 | 1745 to 1795 |
| 100 kHz | 5 | 45 | 56 | 205 to 250 |
| 100 kHz | 20 | 45 | 56 | 1762 to 1820 |



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## BRIEF SPECIFICATIONS

Input Ranges: 7005A 1,10,100 mV/in.; 1, $10 \mathrm{~V} / \mathrm{in}$. 7005AM $0.4,4,40,400 \mathrm{mV} / \mathrm{cm} ; 4 \mathrm{~V} / \mathrm{cm}$
Input Resistance: Potentiometric-1 mV/in. range; $100 \mathrm{~K}-10 \mathrm{mV} / \mathrm{in}$.; 1 Megohm-0.1, $1,10 \mathrm{~V} / \mathrm{in}$.
Accuracy: $\pm \mathbf{0 . 2 \%}$ at full scale; linearity: $\pm \mathbf{0} .1 \%$ of full scale; dead band: $\pm \mathbf{0 . 1 \%}$ of full scale.
Model 17108A External Time Base provides 5 sweep speeds either axis 0.5 to $50 \mathrm{sec} / \mathrm{in}$. ( $\mathbf{\$ 1 7 5 ) .}$

To ensure maximum stability of the circuit's performance as a frequency divider, the time constant (C1 and $R^{7}$ ) should be such that the pulse width across $R 7$, when Q3 fires, will be roughly equal to half the period of $E_{I N}$. This time constant will permit unijunction transistor $Q 3$ to sink all of the remaining portion of the current pulse from Q2 collector; the sinking of this current is what finally causes C1 to fire Q3.

Circuit capability was investigated at different frequencies, temperatures, and dividing multiples. Circuit values valid for all conditions appear in the schematic. Missing values are given in the table above. These values are related to the input frequency and dividing multiple.

An idea of circuit stability is given in the table by the range of $R_{4}$ values, where the dividing multiple, $N$, remained constant. For example, at 60 hertz in a divide-by-five circuit, the mean value of $R_{4}$ is 380 ohms. This value could be varied $\pm 15 \%$ with no change in the dividing multiple. The tabulation shows only minor changes in the $R_{4}$ range with temperature changes of $20^{\circ} \mathrm{C}$. Also, a $\pm 10 \%$ change in the power supply voltage had no more effect on the range of $R_{4}$ than a $20^{\circ} \mathrm{C}$ temperature change.
J. C. Rich, Engineer, Test Equipment Engineering Quality Control, General Electric, St. Petersburg, Fla.

Vote for 110

## Photocell output squared with IC digital systems

Entering photoelectric data into an integratedcircuit digital system often requires the design of special circuits using conventional components and requiring additional power supply voltages.

The circuit described here accomplishes this with a standard IC logic network and a few passive components. This approach will work with most optical shuttering applications that involve a nearby source of light, such as a punched-tape reader or code wheel.
With the IC used, the open-circuit voltage at the input of $Z 1$ (a) is near 4 volts (Fig. 1). The current flowing from the input of $Z 1$ (a) through the photoconductor to ground is sufficient to sense the impedance variation; no other bias is required.
With the photoconductor illuminated, the impedance is low enough to cause the output of Z1 (a) to change state. Resistor R1 provides positive feedback to the input of $Z_{1}(b)$ through a diode level converter. Thus the circuit operates as a Schmitt trigger, and when the illumination


1. Photoelectric data is entered into IC logic with a minimum number of discrete components.

2. Any slowly varying voltages can be squared with the circuit of Fig. 1, provided that such voltages do not exceed the limitations of the logic. The notation of Fig. 1. is used.
reaches a threshold level, the output of $Z 1(c)$ will rapidly change state.

The output rise and fall time is 50 ns , regardless of the rate of change of the illumination level. The small amount of inherent hysteresis prevents ambiguities due to irregular shutter motion and fluctuations in the light source.

The circuit may also be used to square up any slowly varying voltage as long as the input voltage limitations of the logics are not exceeded. With the values shown, the threshold voltage is about 0.7 volt with a $200-\mathrm{mV}$ hysteresis (Fig. 2).
Harold E. Clupper, Senior Staff Engineer, ITT Federal Laboratories, Fort Wayne, Ind.

Vote for 111

## "Diode follower" provides undistorted variable pulse

Proper design of collector load can make the output of a saturating pulse amplifier variable without distortion. Rectangularity of the pulse is greatly affected by the position of the wiper arm in the basic circuit (Fig. 1a). The fall time of the output pulse is proportional to the resistance between the wiper arm and the collector.

The circuit of Fig. 1b has the transfer characteristics of an emitter-follower. The capacitordiode combination provides a low-impedance path for turning off the saturated transistor without affecting turn-on time. The variable dc impedance


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[^7]

Pulse amplitude can be varied over a wide range without affecting its rectangularity (b), by the addition of a diode and a capacitor to a commonly used circuit (a).
of the circuit allows the collector quiescent point to be shifted, while the zero reference level is established by saturation of the transistor during conduction. These features provide a rectangular pulse of an amplitude that is variable over the full range of the potentiometer.

William Staewen, Electronics Engineer, Sinai Hospital, Baltimore.

Vote for 112

## Fiberoptics sense water level

This technique meets the unique requirements for measuring the depth of flow on a water table used to develop fluid-logic elements. These requirements are accuracy ( $\pm 0.005 \mathrm{in}$.), no disturbance of water flow, and provision for simultaneous readings in many places.

The heart of the system is a fiberoptic light pipe in the form of a Y (see diagram). Light is directed into one arm of the Y, emitted through 50 per cent of the fibers at the common end (base of Y), and reflected back from the water surface through the remaining fibers to the opposite arm of the Y. This light is filtered so that a narrow


1. Distance between the input-output end of the fiberoptic light pipe and the water surface is read out by the photocell.
band of infrared frequencies impinges on an EG\&G SD100 photodiode. Resistance change in the diode due to a change in reflected infrared rays is read on a digital voltmeter. Multiple channels can be recorded on a strip-chart recorder.

This system is linear over a range of half an inch. The reflected light density is an exponential function of distance, but the geometry of the system compensates as distance increases, and an acceptable linear operational range results.

A power supply is required for the prefocused projector-type lamp to avoid $60-\mathrm{Hz}$ noise. A condensing lens is used to increase the amount of light entering the light pipe. The infrared filter eliminates interference from ambient light, so the system may be used in a normally lighted laboratory.
J. M. Phillips and J. K. Shane, Component Test Equipment Development Div., Sandia Corp., Albuquerque, N. M. Vote for 113

## Pulse-width discriminator eliminates delay line

This pulse-width discriminator (see Fig. 1a) is useful in discriminating against pulses of less than a specified time duration. The discrimination pulse-width is continuously variable from $1 \mu \mathrm{~s}$ to 3.5 ms .

Pulse-width discrimination is usually accomplished with a delay line and AND gate. Delay

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1. Current source Q3, switched by the amplifier Q1 and Q 2 , and capacitor C 1 determine the discriminator timing.
lines, however, are generally quite large and of only a few microseconds' delay. The wide discrimination range of this circuit cannot be obtained with a variable delay line.

With no pulse applied, transistor $Q 1$ is OFF; Q2 is saturated and shunts current source Q3 to ground. The base of Q4 is thus at approximately ground potential. Transistors Q4, Q5 and Q6 comprise a Schmitt trigger with a trigger level at the base of Q4 of approximately 3 volts.

Therefore, with no pulse applied, triggering voltage $V_{T}$ is below the Schmitt-trigger level. When a positive pulse is applied, Q1 switches ON and Q2 turns OFF. The current from current source $Q 3$ is now applied to capacitor $C 1$, and the voltage to the Schmitt trigger begins to increase linearly at a rate given by:

$$
\begin{equation*}
\Delta V / \Delta t=i / c=\left[V_{c c}-\left(V_{1}+V_{b e 3}\right)\right] / R 6 C 1, \tag{1}
\end{equation*}
$$

where $V_{1}$ is the voltage at the base of Q3 set by potentiometer $R_{4}$.
The time required for the voltage to reach the 3volt Schmitt-trigger level is therefore given by:

$$
\begin{equation*}
\Delta t=\Delta V R 6 C 1 /\left[V_{c c}-\left(V_{1}+V_{b e 3}\right)\right] . \tag{2}
\end{equation*}
$$

If the input pulse duration is greater than discrimination time $\Delta t$, the voltage across $C 1$ will reach the Schmitt-trigger level and fire the Schmitt. When the input pulse returns to zero, Q2 is again switched ON and discharges $C 1$ to approximately zero volts. This resets the Schmitt trigger; thus an output pulse of approximately 16.0 volts' amplitude will be generated at the output. The time duration is given by:

$$
\text { output pulse time }=\text { input pulse time }-\Delta t . \quad \text { (3) }
$$

Since the output pulse time must be zero or a positive quantity, it can be seen from Eq. 3 that, if
the input pulse time is less than $\Delta t$, no output pulse will be generated.

Voltage $V_{1}$ can be varied from +4.4 volts to +18.0 volts. From Eq. 2 this gives a continuously variable discrimination time range of $1 \mu \mathrm{~S}$ to infinity. The upper discrimination time, however, is limited owing to the requirement that the Darlington configuration, Q4 and Q5, must saturate. Therefore, the minimum current from current source Q3 must be sufficient to keep Q4, Q5 saturated. This current is given by:

$$
\begin{align*}
I_{3 m W} & =V_{c c} /\left[(R 7+R 10) \beta_{4} \beta_{5}\right] \approx 18 /(2200) 10^{4} \\
& =8.2 \times 10^{-7} \mathrm{~A}, \tag{4}
\end{align*}
$$

assuming $\beta_{4}=\beta_{5}=100$.
This gives a maximum discrimination time from Eq. 2 of:
$\Delta t=\Delta V(c / i)=\left(3 \times 10^{-9}\right) /\left(8.2 \times 10^{-7}\right)=3.65 \mathrm{~ms}$.
Changing $R 6$ and choosing good switching transistors makes it possible to lower the minimum discrimination time to 200 ns or better.
$R 5$ limits the minimum value of $V_{1}$ to 4.4 volts in order to ensure the proper bias at Q3 collectorbase junction at all times.
C. P. Pittman, Advanced Technology Operations, Beckman Instruments, Inc., Scientific and Process Instruments Div., Fullerton, Calif. VOTE FOR 114

## IFD Winner for Oct. 11, 1966

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## Products



A single chip prevents voltage dip. This monolithic IC is externally adjustable, delivering 1.8 to 30 V . Page 92


Silicon npn transistor has $1000-\mathrm{V}$ breakdown and $40-\mathrm{MHz}$ gain-bandwidth product. Great for driving CRTs. Page 112


More give to the inch in this thermoset laminate. Page 120

## Also in this section:

Silicon power transistors available in low-profile flat-packs. Page 113
Ordinary scopes become microwave receivers with YIG-tuned plug-ins. Page 116
Open-air cryogenics without frost problems. Page 122

# IC voltage regulator is externally adjustable. Booster transistors give 2-A outputs. 


#### Abstract

National Semiconductor Corp., Microcircuits Div., 2950 San Ysidro Way, Santa Clara, Calif. Phone: (408) 245-4320. P\&A: $\$ 60$; stock to 2 wks .


A single 35 -mil square monolithic integrated circuit delivers regulated voltage, externally adjustable from less than 2 to more than 30 volts. Operating as a linear, dissipating or a high-efficiency switching regulator, National Semiconductor Corporation's LM100 voltage regulator delivers up to 20 mA . An external transistor can be added for currents to 250 mA . A second external power transistor gives the regulator 2-A capabilities.

Regulation is better than $1 \%$ for varying load and line conditions. The 8-pin TO-5 device features $1 \%$ temperature stability over the full MIL range, external resistor-adjustable short-circuit current limiting, fast transient responses and a $10-$ to $100-\mathrm{mW}$ standby dissipation. Typical performance specs are given in the Table.


A schematic of the IC is shown in Fig. 1. Basically it is a single-stage differential amplifier with a Darlington emitter-follower output. The use of a pnp, Q2, as a collector load ensures high gain and good supply voltage regulation. Connecting a small external capacitor from the collector to the base of the amplifier and isolating the load in the Darlington emitter-follower keeps the regulator from oscillating with any resistive or reactive load. Excellent transient response is also provided.

The collector of output transistor Q12, brought out separately, and $620-\Omega$ emitter-base resistor R8 permit addition of an external pnp transistor for higher currents. This resistor is shorted out when the regulator is used without the external transistor.

The value of output current limit is determined by an external resistor between the current limit and regulated output terminals. The negative temperature coefficient of the emitter-base voltage of Q10 and the temperature coefficient differential between Q12 and Q10 cause the current limit to halve as chip temperature increases from 25 to $150^{\circ} \mathrm{C}$. The regulator delivers maximum current

2. Output of basic regulator circuit is set by R1 and R2 with potentiometer R3 providing fine adjustment. Reference capacitor cuts output noise.

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at room temperature. It is still protected when the output is shorted and the dissipation increases: the current will decrease as the chip heats, holding dissipation to a safe level. This current-limiting scheme works only when the two transistors are in close thermal contact, as in this monolithic IC.

The regulator is stable without bypass capacitance on the output (if external booster transistors are not used), so it is possible to obtain extremely rapid current limiting for sensitive transistor loads. If current limiting is not needed, load regulation can be improved by shorting together the current limit terminals ( $\mathrm{R}_{\mathrm{sc}}=0$ ). Short-circuit protection is obtained by connecting a resistor between the current limit terminals: ${ }^{\circ}$

The output impedance at high frequencies can be reduced somewhat by addition of a low-inductance (solid tantalum) capacitor. A $0.1-\mu \mathrm{F}$ capacitor on the reference bypass terminal will cut noise inherent in the reference diode.

A basic regulator circuit it shown in Fig. 2. The output voltage is set by R1 and R2 with fine adjustment provided by potentiometer R3. The resistance seen by the feedback terminal should be approximately $2.2 \mathrm{k} \Omega$ to minimize bias current drift.

Higher output currents and better load regulation can be obtained by adding external "booster" transistors. Output currents are then limited only by the power-dissipating and current-handling capabilities of the boosters. Use of these transistors as the series pass elements also reduces internal dissipation and prevents temperature drift due to heating of the internal reference.

One circuit capable of up to 200 mA load current with $1 \%$ regulation is shown in Fig. 3. When external transistors (such as this 2 N 2905 A ) are used, it is necessary to bypass the output terminals close to the IC to suppress oscillations in the minor feedback loop around the external transistor and the output transistor. If even greater output currents are required, it is necessary to add a second booster to provide more current gain. The pnp Q1 then drives the npn power transistor Q2 (2N3055 in Fig. 3). Here both the input and

## Typical performance

| Input voltage range | $7.5-40 \mathrm{~V}$ |
| :--- | :--- |
| Output voltage range | $1.8-30 \mathrm{~V}$ |
| Output-input voltage differential | $2.5-30 \mathrm{~V}$ |
| Load regulation $\left(\mathrm{R}_{\mathrm{so}}=0, \mathrm{I}_{\mathrm{o}}<15 \mathrm{~mA}\right)$ | $0.1 \%$ |
| Line regulation | $0.03 \% / \mathrm{V}$ |
| Output voltage TC | $0.003 \% /{ }^{\circ} \mathrm{C}$ |
| Output noise voltage | $0.005 \%$ |
| Long.term stability | $0.1 \%$ |
| Standby current drain | 1 mA |
| Minimum load current | 1.5 mA |

output terminals must be bypassed with lowinductance capacitors.

A switching regulator is shown in Fig. 4. It is designed for an application where an $18-V d c$ source supplies a 5 -V, 1-A digital system. Conversion efficiency is better, than $80 \%$ at full load. Regulation, load and line, exceeds $1 \%$. The overshoot for a 0.2 -A load transient is 0.15 V ; recovery time is 0.36 ms . The output ripple is 40 mV at a $5-\mathrm{kHz}$ switching frequency. Dissipation is 0.3 W in the series-pass transistor Q1 and 0.5 W in the current-return diode D1.

An external npn and pnp are cascaded to handle the output current. This regulated output is fed back through a resistive divider that determines the output voltage. The regulator is made to oscillate by applying positive feedback to the reference terminal through R4.

In applications such as integrated logic circuitry, performance can be improved if the regulator output changes with temperature such as to operate the load at its optimum voltage. Optimum performance can be realized by powering the devices with a voltage that decreases with increasing temperature. The circuit is shown in Fig. 5. Silicon diodes are used in the feedback divider to give the required negative temperature coefficient. Diode-connected transistors (base shorted to collector) can be used for greater accuracy.

CIRCLE NO. 211

3. Booster transistor Q1 brings outputs to 200 mA . Addition of Q2, C3 and R4 (color) gives 2-A output. Tantalum bypass capacitors C2 and C3 prevent oscillations.

4. Switching regulator supplies 5 V , 1 A from an $18-\mathrm{Vdc}$ source at $80 \%$ efficiency. When Q1 and Q2 are off, inductor L1 supplies the load with D1 as a return path.

5. Temperature compensating regulator has $5.5-\mathrm{V}$ output at $-55^{\circ} \mathrm{C}$, decreasing to 4 V at $125^{\circ} \mathrm{C}$. Diodeconnected transistors rather than silicon diodes will increase accuracy.

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CIRCLE NO. 214


Digital line receiver has high noise immunity
Siliconix Inc., 1140 W. Evelyn Ave., Sunnyvale, Calif. Phone: (408) 245-1000. Price: $\$ 31.80$ (military), $\$ 23.80$ (commercial), (100).

This digital line receiver is applicable in military and industrial logic applications requiring high noise immunity. Designed primarily as a signal discriminator at the end of a coax transmission line in digital data systems, the circuit is also useful as a voltage comparator. Consisting of two gates driven by a differential amplifier, the unit has a TTL input and complementary outputs compatible with DTL and TTL logic. Input threshold is set by the differential amplifier reference voltage which may be adjusted over a 3 - to 4.5 -volt range.

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| (lin. operation) | +10 dBM (max.) |
| External AGC range | 50 dB (min.) |
| N.F. | 7 dB (max.) |
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CIRCLE NO. 217


Transistor oscillator
generates sine waves
Solid State Electronics Corp., 15321 Rayen St., Sepulveda, Calif. Phone: (213) 785-4473. P\&A: \$185; 1 wk.

The Models S-100 and S-200 silicon transistor oscillators are epoxyencapsulated units designed to create a sine wave signal. Both are plug-in modules; $\mathrm{S}-100$ is a tubular design while $\mathrm{S}-200$ is rectangular. Temperature rating is -20 to $+85^{\circ} \mathrm{C}$. Due to the ruggedized design, applications are seen in missiles and airborne areas.

CIRCLE NO. 218


## Micromin cap meets environmental specs

Bourns, Inc., Trimpot Div., 1200 Columbia Ave., Riverside, Calif. Phone: (714) 686-7404.

This capacitor, encased in a high temperature plastic, molded body of only $0.1 \times 20$-in. has a terminal strength of 5 pounds. In addition, it meets all the applicable environmental conditions of MIL-C-11015C for voltage temperature limits, life, vibration, temperature cycling, moisture resistance and immersion. Its gold-plated nickel leads are weldable and solderable.

CIRCLE NO. 219

## What company do you call when you need a standard operational amplifier to meet exceptianal requirements?



## CEES Sentificive Uew WHF fececiver



## Six Crystal-Controlled Frequencies. Plus Continuous $30-300 \mathrm{mHz}$ Tuning in Two Bands

Meet a new and unusual VHF receiver from CEI. The Type 952 provides AM, FM and CW reception throughout the $30-300 \mathrm{mHz}$ range, while also offering six switch-selectable crystalcontrolled frequencies within the $100-150 \mathrm{mHz}$ range.
The receiver's full frequency range is covered in two bands $-30-90$ mHz and $60-300 \mathrm{mHz}$-with accurate tuning facilitated by a long steel tape dial. IF bandwidths of 50 and 300 kHz are selectable at the front panel; video and audio outputs are provided from the bandwidth selected, and a built-in BFO operates with either bandwidth when the CW mode is selected.

Sensitivity, stability and operating flexibility are outstanding. For full information about the 952's unusual features and performance, please contact:

## COMMUNICATION ELECTRONICS INCORPORATED

6006 Executive Blvd., Rockville, Md. 20852 • Phone: (301) 933-2800 • TWX: 710-824-9603


## COMPONENTS



Three-position toggle provides quick transfer
Master Specialties Co., 1640 Monrovia, Costa Mesa, Calif. Phone: (213) 642-2427.

A detenting action allows quick and positive transfer in the Series 21 three-position toggles. A choice of three actions is offered in the line: momentary-maintain-momentary, maintain-maintain-maintain, maintain-maintain-momentary. The switching mechanism transfers contacts to within $1^{\circ}$ of the toggle lever's $34^{\circ}$ travel arc.

CIRCLE NO. 220


## Modulator-demodulator for PC mounting

Gopic Designs Co., 2166 Chatsworth Blvd., San Diego, Calif. Phone: (714) 222-6948. P\&A: $\$ 34$ (1 to 9); stock.

One of a new series of ring mod-ulator-demodulators is designed for mounting directly on closely-spaced PC cards. Precisely balanced transformers and carefully selected carbon film resistors and silicon diodes ensure adequate carrier suppression for most communications and frequency translation circuits. Carriers may range from 2.5 to 1000 kHz and beyond while the signal frequency may be as low as zero.

CIRCLE NO. 221


## Analog multiplier has $10-\mathrm{MHz}$ upper limit

Optical Electronics Inc., P.O. Box 11140, Tucson, Ariz. Phone: (602) 624-3605. P\&A: \$140 (1 to 9); 30 days.

This single-quadrant multiplier, with a $10-\mathrm{MHz}$ upper frequency limit, claims the hiqhest frequency response available. Featuring all sol-id-state circuitry, the multiplier has matched inputs and an adjustable equation coefficient. Nominal transfer equation is $\mathrm{E}_{\mathrm{o}}= \pm 0.05 \mathrm{XY}$, where X and Y are inputs. Maximum error is $1 \%$ of full scale.

CIRCLE NO. 222


## Ac-dc magnetic switch has long operating life

Electro-Tec Corp., P. O Box 667, Ormond Beach, Fla. P\&A: \$4.20 each; stock.

This ac-dc permanent-magnet proximity switch, designed for counting and positioning of moving devices, can be installed behind any non-ferrous shield. Its electrical life is in the hundreds of millions of operations. It can detect the presence of a specific magnetic body at a predetermined distance from a reference point. Available in NC and NO positions, it will withstand shock and vibration up to 50 G .


## Modular coax switch has isolated points

Matrix Co., 9119 Desoto Ave., Chatsworth, Calif. Price: approx. $\$ 45$ per point.

This coax switch, in a modular construction, is single-throw, 5point, and switches both center conductor and shield. It is available in 1-by-2 and 1-by-20 formats. Each switch is completely isolated from the case and from all other switch points. Designed for airborne use, it meets MIL and RFI specs.

CIRCLE NO. 224


## Differential amplifier based on hybrid ICs

Zeltex, Inc., 1000 Chalomar Rd., Concord, Calif. Phone: (415) 6866660. Price: $\$ 47$ in 100 lots.

Hybrid integrated circuitry is cited as the key to stability for the model 161 differential amplifier. Key specifications of the unit are a $25 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ drift, a gain of 80,000 , an output of $\pm 10 \mathrm{~V}$ at 4 mA , a $150-\mathrm{nA}$ input offset and an input impedance of $20 \mathrm{M} \Omega$. Model 161 is packaged as an epoxy cube measuring $0.5 \times 0.5 \times$ $0.4-\mathrm{in}$. It mounts flush to the board without lead spraying.

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do yau call for new ideas in analag comparatars?

PPC-1 and PPC-2 ultra-sensitive relay and fast comparators.


Sensitivity: $200 \mu \mathrm{~V}$ Repeatability: $50 \mu \mathrm{~V}$ Response Time: $<1 \mathrm{~ms}$ Contacts: 1A, 2A, 1 C Cost: In moderate quantities: PPC-1 < \$30; PPC-2 < \$75


PPC-1 (for P.C. mounting)


PPC-2 (panel mounting) with ten-turn trip level adjustment

All specs typical @ $25^{\circ} \mathrm{C}$.
Write, call, TWX or circle the card.
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 Antenna that operates from 2100 MHz to 2300 MHz , provides circular polarization with $\alpha$ maximum ellipticity of 2 db and $\alpha$ gain of 6 db !
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The Apollo S-Band Antenna is similar to the Amecom C-Band models that are being used on Gemini and Apollo spacecraft and missile testing programs.
The Problem Solvers of Amecom's Antenna Systems Department will be pleased to review the hard-to-meet temperature requirements for antennas in your missile, rocket or space project.
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## ANALOG CONTROL SIGNALS

PD \& C's new Model 102 Analog Memory Device makes continuous, trouble-free operation of process control a practical, economical reality. When a disruption of incoming data occurs, Model 102 retains the last valid signal to within $1 \%$ for over an hour, permitting service to be maintained until the input can be restored. Eliminates costly downtime and maintenance interruptions.

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6406 S. E. Foster Rd. • Portland, Oregon 97206 ON READER-SERVICE CARD CIRCLE 64


Delay line claims
$60 \%$ smaller size
Vidcor Corp., 730 Centinela Ave., Inglewood, Calif. Phone: (213) 678-4024.

This $1-\mu$ s delay line claims to be $60 \%$ smaller than its competitors. In a $1.5 \times 0.3 \times 0.3-\mathrm{in}$. thermosetting resin case, it operates at $105^{\circ} \mathrm{C}$, and has an impedance of 1 $\mathrm{k} \Omega$. The dielectric withstands 300 Vdc. Leads are of tinned Dumet and are spaced for standard 0.1-in. circuit board grids.

CIRCLE NO. 228


## Miniature RF capacitors take high voltage

JFD Electronics Co., Components Div., 15th Ave. at 62nd St., Bklyn., N. Y. Phone: (212) 331-1000.

Miniature glass-encapsulated RFcapacitors, designed for use in airborne, spaceborne and mobile transmitters, are available in values from 20 pF , or lower, to 3000 pF . Typical is the $1000-\mathrm{pF}$ unit rated at $2500 \mathrm{Vdc}, 1 / 2-\mathrm{in} .^{2}$ and $1 / 8$ to $1 / 16$ in. thick. Standard tolerances from 1 to $20 \%$ are furnished. The units meet MIL-Std 202. The dielectric can withstand temperatures from -55 to $125^{\circ} \mathrm{C}$.

CIRCLE NO. 229


## PC connectors have "bellows" contacts

Viking Industries, Inc., 21001 Nordhoff St., Chatsworth, Calif.

A commercial-grade series of PC connectors comes in all standard contact configurations from 6 to 22 , single and double row. The contacts are of the "bellows" design, copper alloy with gold flash. The insulator material is filled alkyd. Pierced or dip-solder terminations are optional. The connector is available in three mounting styles.

CIRCLE NO. 230


## Triax accelerometer weighs only 8 grams

Columbia Research Labs., Inc., MacDade Blvd. \& Bullens Lane, Woodlyn, Pa. Phone: (215) 532-9464.

Capable of measuring simultaneous acceleration in three axes, the model 612 accelerometer is described as the world's smallest. The unit measures $0.5 \times 0.5 \times 0.33-\mathrm{in}$. and weighs 8 grams. A second feature of the unit is its use of detachable cable allowing easy replacement of cables on site. Sensitivity of the model 612 TX for each of its three axes is $5 \mathrm{pk} \mathrm{mV} / \mathrm{pk} \mathrm{G}$ and charge sensitivity is $1.5 \mathrm{pC} / \mathrm{G}$.

CIRCLE NO. 231

What
company da yau call far law cost, high input impedante amplifiers?

These low cost DDC operational amplifiers make high stability integrator circuits easy.


MODEL D-16
Zin Common Mode: $1.6 \times 1{ }^{9} \Omega$ $Z_{\text {in }}$ Differential: $0.7 \times 10^{8} \Omega$ Ios Either Input: 1 nA Stability $\mathrm{I} \triangle \mathrm{t}: 0.1 \mathrm{nA} /{ }^{\circ} \mathrm{C}$ Stability $\vee \triangle t: 10 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ Output 11 V @ 2.2 mA Price: (1-9): $\$ 38$.


MODEL D-15 (FET) Input Impedance: 1011 ohms Initial los: 10 pA FFO: 35 KHz , either input Stability: $10 \mathrm{pA} /{ }^{\circ} \mathrm{C}$
Price: D-15, $15 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ - $\$ 75$ (1-9)
DK-15, $35 \mu \mathrm{~V} /{ }^{\circ} \mathrm{C}$ - $\$ 45$ (1-9)

All specs typical @ $25^{\circ} \mathrm{C}$.
Write, call, TWX or
circle the card.
We'll send detailed data
or evaluation samples.


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710 PCM SYSTEMS require as little as 26 cubic inches and weigh as little as 30 ounces. Space and weight are saved in Type 710 Microcircuit PCM Systems because they use MEMA.*

Provides up to 256 channels of high, low
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*Micro Electronic Modular Assemblies, a high density integrated circuit packaging concept developed by TELEDYNE, INC
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12964 Panama Street Los Angeles, California 90066 Phone (213) 870-9831 TWX: 9103436855


Custom meter scales read your way
Triplett Electrical Instrument Co., Bluffton, Ohio. Phone: (419) 3514912. Availability: 2 to 6 wks.

Meters which read exactly what the designer wants read are available through a custom meter-scale service. A drawing of the required meter-face is made to the designer's specifications, photographed and printed on a white-faced aluminum plate shaped to mate the specified meter. Various color codings and sizes are available.

CIRCLE NO. 232


## Wirewound trimmer has isolated elements

IRC, Inc., 401 N. Broad St., Philadelphia. Phone: (215) 922-8900. P\&A: \$3.56 (100); 4 weeks.

In addition to its line of precision rectangular wirewound trimmers, IRC's type 600 is designed to perform under environmental requirements of MIL-R-27208. A specially designed wiper block system effectively isolates electrical elements. The unit is housed in a rugged diallyl phthalate case and is offered with either PC pins or teflon-insulated leads. Resistance values of 10 $\Omega$ to $50 \mathrm{k} \Omega \pm 5 \%$ are available. The units are rated at 1 W at $70^{\circ} \mathrm{C}$.

CIRCLE NO. 233


## one gio Pedsoll

You should buy Syntron Avalanche Silicon
Controlled Rectifiers is for their inherent transient protection provided by the avalanche characteristics.

SCR AC Phase control at $90^{\circ}$ is easy but $L \frac{d i}{d t}$ transients can cause indiscriminate failures. Can you afford to be without the additional reliability provided by Syntron Avalanche SCR's?

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# goes up to 500 MHz 

But the price goes below \$180

New production techniques (you'll detect some in the outer case, for example) now make possible a lower-priced, highperformance mixer from HP.
You can use the 10514A Double Balanced Mixer for extracting the sum or difference of two frequencies, or as a modulator, spectrum generator, phase detector, current-controlled attenuator, frequency doubler, or for extending the range of spectrum analyzers.
Features include: range of 200 kHz to 500 MHz (to dc on one port), excellent balance, flat response, low noise ( 7 dB max. noise figure to $50 \mathrm{MHz} ; 9 \mathrm{~dB} \max$. to 500 MHz ) and low intermodulation. And the price is low, too: $\$ 180$ each, and even lower in quantity. Printed circuit board version (10514B) also available. For additional information contact your local HP field engineer, or write HewlettPackard, Palo Alto, California 94304. Tel. (415) 326-7000; Europe: 54 Route des Acacias, Geneva.

HEWLETT
PACKARD

An extra measure of quality


## Analog multiplier has no hysteresis error

Transmagnetics, Inc., 134-08 36th Road, Flushing, N. Y. Phone: (212) 539-2750. P\&A: \$150; 4 to 6 wks.

Model 365 four-quadrant analog multiplier has a $3-\mathrm{dB}$ bandwidth from 200 to $50,000 \mathrm{~Hz}$. The $1 / 3$ in. ${ }^{3}$ component generates the product of a dc and an ac input. Inherently resistive, the unit's features include complete absence of hysteresis errors and military-range temperature specifications. Applications are in fire control, radars and automatic flight directors.

CIRCLE NO. 234


## Solid-state compensator completely self-powered

Whittaker Corp., 12838 Saticoy St., North Hollywood, Calif. Phone: (213) 781-8950.

With a reference accuracy of $\pm 0.5^{\circ} \mathrm{F}$, the RC19 reference junction compensator is completely selfpowered. The solid-state component is intended for such applications as aircraft and missiles and it is solidly encapsulated in epoxy for ruggedness. Allowable ambient range is -15 to $200^{\circ} \mathrm{F}$. It will withstand linear acceleration beyond 10 G and vibration up to 15 G at 2 kHz .

CIRCLE NO. 235


## Pulse transformers for PC applications

Fugle-Miller Laboratories, Inc., 301 Central Ave., Clark, N. J. Phone: (201) 381-2727.

A line of pulse transformers are miniaturized for printed-circuit applications. Sizes range from $0.2-\mathrm{in}$. diameter by $0.2-\mathrm{in}$. high to $0.5-\mathrm{in}$. by $0.5-\mathrm{in}$. Pulse characteristics include less than $20-\mathrm{ns}$ rise and fall and pulse widths range to $20 \mu \mathrm{~s}$. Either Dumet or nickel terminals are provided.

CIRCLE NO. 236


## Remote-switched gain in low-noise preamp

Ithaco, Inc., 413 Taughannock Blvd., Ithaca, N. Y. Phone: (607) 272-7640.

Remotely switchable gain is the leading feature of the model 155 low-noise preamplifier. The amplifier is designed for remote applications where widely varying signal amplitudes are encountered. The two switchable gain-states are 20 and 40 dB with a stability of $1 \%$. Passband is 0.5 Hz to 200 kHz . Noise, referred to input, is $3 \mu \mathrm{~V}$ $\max$ and an internal divider net permits injection of a calibration signal.

CIRCLE NO. 237

RCA
integrated circuits

## RCA LINER CIRCUITS Go FIEXXBLE...IOWCOST



4-TRANSISTOR ARRAY
CA3018 $\$ 1.50$ (1000+)

- excellent transistor match and tracking
- transistor $\mathrm{f}_{\mathrm{T}}=400 \mathrm{MHz}$ typ
- optimum lead arrangement for reduced Miller effect
- 12-lead TO-5 style package


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- excellent diode match
- built-in temperature stability
- low leakage
- 10-lead TO-5 style package


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Here are the performance benefits of integrated circuit active devices, plus complete design freedom in the selection of passive components. Plug these two important new RCA integrated circuit devices into your breadboards and include them in your production plans now, for:

## CA3018

- 100 MHz Cascode Amplifiers
- Final IF Ampl and 2nd Det
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- Cascode Video Amplifier

CA3019

- Ring and shunt modulator
- Double-stage limiter
- Mixer
- Analog switches
- Gates for chopper-modulator

FOR FURTHER INFORMATION check your local RCA Representative-or contact your RCA Distributor for his price and delivery. For literature, write Section LCG1-3, Commercial Engineering, RCA Electronic Components and Devices,
Harrison, N. J. 07029.
CA3018 and CA3019 NOW IN DISTRIBUTOR STOCKS.

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## NYLON CONNECTORS

Up to 15 circuit capacity with these low-cost, miniature nylon connectors! Contacts automatically crimped to leads, then securely snap-lock into the housings. Positive polarity prevents misconnections and integral mounting ears provide easy panel installation.

Model 1625-3
Compact three circuit unit for independent circuit isolation.

Model 1625-9
Nine circuit con nections for fast, multiple circuit wiring.

Model 1625-12
Twelve independent circuits are povided with this unit.

Model 1625-15 Up to 15 separate sircuits in this space saving connector.


Write for complete specifications and samples on any of these connectors.

MOLEX® PRODUCTS COMPANY 5235 Katrine Avenue Downers Grove, Illinois 60515 (312) 969-4550 TWX 910-695-3533


## Current shunts allow 0.005\% dc readings

Julie Research Laboratories, Inc., 211 W. 61st St., New York. Phone: (212) 245-2727. P\&A: $\$ 50$ to $\$ 400$; stock.

A new line of high-current shunts are said to allow $0.005 \%$ dc measurements at 10 A . The line, designated CS-106, is air-cooled and packaged fin-type, cylindrical and regular rectangular. Resistance values range 0.01 to $0.1 \Omega$ and current ranges 0 to 50 A . For taking current readings, the resistors are used in conjunction with a ratio set.

CIRCLE NO. 238


## Voltage memory card monitors transients

Micro Instrument Co., 12901 Crenshaw Blvd., Hawthorne, Calif. Phone: (213) 679-8237. Price: $\$ 150$.

Single or repetitive voltage transients in pulse widths from dc to $100 \mu \mathrm{~s}$ are monitored by the Model 52221. The voltage memory card is used with an external meter and power supplies and mounts on the user's equipment. It converts the peak amplitude of any input signal of $100 \mu \mathrm{~s}$ or longer into a dc output and holds there until reset or keyed by a higher voltage.

CIRCLE NO. 239

## not when you specify Clarostat!



SERIES

57, 57EM
Single Turn Wire-Wound Potentiometer designed to meet miniaturization needs.

Resistance Range: Standard to 25 K ohms. Special to 100 K ohms. Tolerance: Standard $\pm 10 \%$, Special to $\pm 3 \%$. Linearity: Standard $\pm 2.0 \%$. Power Rating: Standard 2W @ $70^{\circ} \mathrm{C}$, derated to zero @ $150^{\circ} \mathrm{C}$. Weight: .25 oz.


## SERIES

42BM, 42CM
Single Turn Wire-Wound Potentiometer controls from 1 to 20 circuits simultaneously.

Resistance Range: To 100 K ohms. Tolerance: Standard $\pm 5 \%$, Special $\pm 1 \%$, Linearity: Standard $\pm 1 \%$, Special $\pm .15 \%$. Power Rating: Standard 3 W @ $40^{\circ} \mathrm{C}$, derated to zero @ $105^{\circ} \mathrm{C}$.

sERIES 62
10 Turn Precision Potentiometer designed for industrial instrumentation.
Resistance Range: to 100 K ohms. Tolerance $\pm 5 \%$. Linearity: $0.25 \%$ Absolute. Power Rating: 2 W @ $25^{\circ} \mathrm{C}$, derated to zero @ $85^{\circ} \mathrm{C}$. Maximum Number of Turns: 10. Weight: 1.34 oz .

series 76
High Resolution Precision Miniature Trimming Potentiometer.
Resistance Range: 100 ohms to 20 K ohms standard. Tolerance: Standard $\pm 5 \%$, Special $\pm 2 \%$. Power Rating: 0.75 W @ $85^{\circ} \mathrm{C}$, derated to OW @ $150^{\circ} \mathrm{C}$. Maximum Number of Turns: 11, Weight: .01 lbs. Printed circuit or solder lug terminations.


SERIES 54M
Single Turn Vari-Phase Precision Potentiometer features external, individual cup phasing.

Resistance Range: To 300K ohms. Tolerance: Standard $\pm 5 \%$. Linearity: 0.25\% Absolute. Power Rating: 2W @ $25^{\circ} \mathrm{C}$, derated to zero @ $85^{\circ} \mathrm{C}$. Maximum Number of Turns: 10. Weight: 1.34 oz .

Choose a potentiometer that doesn't measure up to specifications and it is usually because of lack - not luck. Lack of stability - lack of ability to withstand any one of the catalog of extremes to which pots may be subjected.

But specify Clarostat and you select a precisely designed potentiometer - so reliable - so stable, there is little or no chance of failure under the most rugged conditions.

If you would rather take chances with your potentiometers, you don't want Clarostat. For when you specify Clarostat, there is no such thing as pot luck.


CLAROSTAT MFG. CO., INO. DOVER, NEW HAMPSHIRE


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Dependable, precise Methode fork contacts, receptacles, contact strips and disposable contact strips are available in all sizes and configurations; fully interchangeable and compatible; featuring the originally designed split leg construction which saves you money.

Connectors available with 3 to 51 contacts in all popular termination styles.

## We invite your inquiry.

Connector Division

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## COMPONENTS



## Submin trimmer series set for solid-state

Technology Instrument Corp. of Calif., 850 Lawrence Dr., Newbury Pk., Calif. Phone: (805) 498-2165.

Two series of MIL-spec submin trimmers, the TT1 and TS1, are designed for use with solid-state circuitry and plug-in cards. The "transistor can" TT1 trımmers are below $0.333-\mathrm{in}$. in diameter and less than 0.2 -in. high. The square-type YD1 provides nine options at standard values from $50 \Omega$ to $20 \mathrm{k} \Omega$. Both types are manufactured to meet or exceed MIL-R-27208A.

CIRCLE NO. 240


## Operational amplifier drives deflection coils

Burr-Brown Research Corp., International Airport Industrial Park, Tucson, Ariz. Phone: (602) 2941431. P\&A: $\$ 175$; stock to 3 wks.

The Model 1555 operational amplifier is said to be ideal for driving high speed deflection coils for display and readout. Features include a unity gain bandwidth of 15 Mhz , a slew rate of $100 \mathrm{~V} / \mu \mathrm{s}$, input impedance at $10^{10} \Omega$ and an output of $\pm 10 \mathrm{~V}$ at 100 mA . Other specs include a dc open-loop gain of 100 dB and a $1-\mathrm{MHz}$ full-power response.

CIRCLE NO. 241


## Variable inductor designed for long life

Delta Electronics, Inc., 4206 Wheeler Ave., Alexandria, Va. Phone: (703) 836-3133.

Production units of the RVI variable capacitor series endure 50,000 end-to-end traverses without deterioration. They are designed for commercial and military applications where long-life and relability are required. Range of stock inductors is from 0.06 to $12 \mu \mathrm{H}$. Current rating is 40 A at 4 MHz in free air and 50 A under forced-air cooling. The rotary design includes a contact that consists of a compoundcontoured roller riding on a silverplated shaft.

CIRCLE NO. 242


Matrix Co., 9119 Desoto Ave., Chatsworth, Calif. Price: approx. $\$ 30$ per card.

An audio bandswitch module capable of 20 million operations affords true balanced line on switched shield coax operation. Applications include studio audio, telephone and teletype systems, computer interface networks, and instrumentation scanning systems.

CIRCLE NO. 243


If your design requirements need printed circuit connectors of exceedingly high quality, but your design application is such that you do not need connectors made to military specifications . talk to METHODE.
We stock a full line of dependable Reli-acon printed circuit connectors from the largest to the smallest sizes with a variety of contact designs. And all Reli-acon connectors are made to rigid quality standards that give you the reliability you need. without paying the premium prices that mil spec. connectors command. However, Methode's MIL-C.21097B fully approved connectors are available to meet your military requirements.

Write for illustrated catalog with full engineering specifications.
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Now, for only $\$ 98$, you can enjoy the luxury of a laborenjoy the luxury of a labor-
atory power supply right on your own bench. Acopian's new K55 Power Supply delivers 300 ma over an adjustable range of 1.25 to 30 volts DC. It is voltage regulated, all silicon, and electronically protected against shorts. It weighs only three pounds.
Availability? Acopian's usual three days, of course. Get three days, of course. Get
complete information from your local Acopian representative or write Acopian Corp., Easton, Pennsylvania. Phone: (215) 258-5441.


## Silicon npn transistor has great potential -1000 volts

MS Transistor Co., 80-02 51 Ave., Elmhurst, N.Y. Phone: (212) 4783134. P\&A: $\$ 52.60$ (MST), $\$ 55.60$ (MSP), 1 to 99; stock.

Solid-state devices continue to enter previously exclusive vacuum tube territory. Npn silicon mesa transistors will operate in a linear mode up to 1000 volts.

No longer engineering rarities, the stock units have the high-voltage capabilities, a $40-\mathrm{MHz}$ gainbandwidth product and few, if any, of the tradeoffs.

For example, in the MSP-100, $\mathrm{V}_{\text {CE(SAT) }}$ is 1.6 V at a collector current of 20 mA and a base current of 5 mA . This is much lower than most high-resistivity, highvoltage units. $\mathrm{BV}_{C E(S A T)}$ is 1 V max, again at a $20-\mathrm{mA}$ collector current. The device can switch a minimum of 20 mA in the "on" state to a minimum of 1000 V in the "off" state. The MST device (TO-5) is rated at 2 W at $100^{\circ} \mathrm{C}$ case and the MSP (MD-14) is rated at 5 W .

The transistors should find many new applications. For example, in a differential amplifier configuration, it is possible to obtain a $20-\mathrm{V}$ p-p sine wave. This application is typical of electrostatic CRT deflection plate drivers. Low-deflection sensi-
tivities usually occur with CRTs that use high voltages for acceleration and brightness. With the 1000 V devices, CRTs which worked well with vacuum tube circuitry can now be used. Use as electroluminescent drivers also is feasible. The devices can be used to drive the essentially capacitive load by running class A stages in a series push-pull configuration.

In addition, many standard applications can be extended. For example, these transistors can be operated from a $120-, 240$ - or $480-$ Vac line. All that is needed is a simple full-wave rectifier to convert the ac to dc and a capacitive filter. An inverter can be designed that will operate from rectified and filtered line voltage, convert to a higher frequency and then transform to other voltages. The resulting power supply will have a much smaller transformer and will be much lighter.

As a line-operated audio amplifier with a FET as an input stage, the transistor forms a high-input-impedance combination with a highvoltage output. This is the closest solid-state device to a pentode. The transistors also can replace SCRs in applications where a base turn-cff and high speed are desired.

CIRCLE NO. 244


## Si power transistors available in flat-packs

Solitron Devices, Inc., 1177 Blue Heron Blvd., Riviera Beach, Fla. Phone: (305) 848-4311. Availability: stock.

For space reductions, the SDT 8100 series of transistors offer flatpack profiles and the manufacturer's standard 20 and $30-\mathrm{A}$ transistor specs. The new series can be specified to the electrical characteristics of the $2 \mathrm{~N} 3597-99$, the MHT 8002-03, 8012-13, 8015-16, 8045, $8070-71$ and 8301-04. The primary application for these new packagestyled devices is seen as switches on circuit boards.

CIRCLE NO. 245


## Silicon resistors detect temperature

Vector Div. of United Aircraft, Southampton, Pa. Phone: (215) 355-2700.

Called "Tempistors," a line of temperature compensating resistors are designed for positive temperature compensation of electronic circuits. The Tempistors are axiallead, epoxy-encapsulated units with rated full load at $100^{\circ} \mathrm{C}$ linearly derated to zero at $150^{\circ} \mathrm{C}$. Resistance values are available from $68 \Omega$ to $10 \mathrm{k} \Omega$.

[^8]
## LOOK WHAT HAPPENED WHEN WE INVENTED PLANETGEAR ${ }^{\circ}$ TO REPLAGE THE OLD GENEVA



HSI 44 Series Planetgear Pulse Counters have proven they can make more than $500,000,000$ pulse counts at 40 pulses per second.
This is possible only with the patented HSI Planetgear transfer mechanism. It greatly minimizes the problems of noise and wear - especially at high speed. We've also made the Series 44 drive extremely simple and reliable, using the HSI 2-wire stepper motor. No logic circuitry is required, only a simple SPST switch or solid state equivalent.
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1500 Meriden Road, Waterbury, Conn. 06720/Area Code (203) 756-7441 ON READER-SERVICE CARD CIRCLE 75

## hramer

## Microwave transistor in ceramic package

KMC Semiconductor Corp., Parker Road, RD 2, Long Valley, N. J. Phone: (201) 876-3811. $P \& A: \$ 250$ each (1 to 9), \$125 each (100); stock.

This silicon transistor in a ceramic package is designed for broad-band amplification and as a tunable oscillator. It will o eerate at frequencies thru 4 GHz . Specifications include a guaranteed 40mW output at 2.5 GHz when used as an oscillator. Max frequency is typically 4.6 GHz . The case is completely nonmagnetic for YIG tuned applications. The size of the package in nominally 0.06 -in. ${ }^{3}$, adaptable for IC or printed circuits.

CIRCLE NO. 247

## FREE! An all-new 1967 Buyer's Guilde Ior Electronics

Over 500 pages 60,000 electronic components listed by manufacturer's number. (There are no "special" house numbers to confuse you.) Top name brands like G.E., Fairchild, Motorola, Bourns, Raytheon, Sprague, many others. Plus - handy cross index price and ordering information. No design engineer, purchasing agent or buyer should be without it!

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114

## Premium FETs at regular prices

Siliconix Inc., 1140 W. Evelyn Ave., Sunnyvale, Calif. Phone: (408) 245-1000. Price: \$2.55-\$3.20 (in 100 lots).

Priced in the range of many industrial applications, the 2 N 4338 41 n-channel junction FETs give tight parameter control in smallsignal amplifiers. These devices offer a three-to-one ratio in $V_{P}$, $I_{\text {DSS }}$ and $g_{f s}$ ranges. $I_{\text {GSS }}$ is below 100 pA and max noise is 1 dB at 1 kHz .

CIRCLE NO. 248

## Selenium photocell has linear surface response

International Rectifier, 233 Kansas St., El Segundo, Calif. Phone: (213) 678-6281.

An output linearity of $1 \%$ is constant for all points on the light-sensitive surface of these cells. Selenium is chosen for the similarity of its spectral response to that of the human eye. Custom manufactured, the cells can be furnished in a variety of shapes, including circular, and in lengths to four feet.

CIRCLE NO. 249

## Infrared laser diode for safety devices

$R C A$, Electronic Components \& Devices, 415 S. 5th, Harrison, N. J. Phone: (201) 485-3900.

Called solid-state injection lasers, these gallium arsenide devices are designed for use in automotive safety devices and "secure" communications systems for ships, aircraft and spacecraft. They range in size from that of a kernel of corn (2 W) to that of a pencil eraser ( 50 W). Such infrared light has good fog penetration, and its invisibility and the directionality of the beam reduce intercept possibility.

CIRCLE NO. 250

## Reference diodes for voltage standards

Motorola Semiconductor Products, Inc., Box 955, Phoenix. Phone: (602) 273-6900.

Temperature-compensated zener diodes, claiming higher accuracy than unsaturated standard cells, are offered for use as reference standards in equipment requiring a high degree of long-term voltage stability. The diodes can be open- or short-circuited without degradation.

CIRCLE NO. 252

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## TEST EQUIPMENT



## Ordinary scopes made microwave receivers

Electro/Data, Inc., 3130 Benton St., Garland, Tex. Phone: (214) 2721731.

Ordinary Tektronix 500 series and letter-series scopes can be converted to wide-band microwave receivers. The PN1000 series of plugin modules cover three frequency ranges, 120 to $1200 \mathrm{MHz}, 500$ to 5000 MHz and 1200 to $12,000 \mathrm{MHz}$. Each is YIG-tuned and provides a panoramic display of the input on the scope face. Either the entire frequency spectrum or any segment can be displayed.

CIRCLE NO. 253


## Video module joins spectrum analysis group

The Singer Co., Metrics Div., 915 Pembroke St.,. Bridgeport, Conn. Phone: (203) 366-3201.

A video-frequency module has been added to the family of spectrum analyzer units housed in the RTA- 5 and TA- 2 main frames. The VR-4 provides 0 to $25-\mathrm{MHz}$ coverage in a single pre-set scan. 50 kHz to 5 MHz continuously adjustable sweep widths which can be centered anywhere from 0 to 25 MHz , and phase-locked $500-\mathrm{Hz}$ to $50-\mathrm{kHz}$ continuously adjustable sweep widths. The minimum resolution is 200 Hz .

CIRCLE NO. 254


## Function generator features versatility

Wavetek, 8159 Engineer Rd., San Diego, Calif. Phone: (714) 2792200. Price: $\$ 795$.

Built-in sweep capability allowing internal or external voltage control, with manual control from front panel or external pulse or gate, is featured in the model 114 VCG. The unit offers both sweep and hold mode, and triggered sweep, with nine simultaneous outputs, including a monitor of the sweeping voltage. It generates sine, square, triangle and sine-squared waveforms over a range of 0.0015 Hz to 1 MHz .

CIRCLE NO. 255


## Precision delays switch in 10 -ns increments

Science Accessories Corp., 65 Station Plaza, Southport, Conn. Availability: stock.

These 2 - and 4 -unit high-precision delay instruments operate in the nanosecond range with typical accuracies of $\pm 20 \mathrm{ps}$ for each setting. Each unit provides switchable delays of from 1 to 63 ns in increments of 1 ns . Both models employ coax cables interconnected by stripline sections, and handle up to $\pm 1500 \mathrm{~V}$ with input and output impedances of $50 \Omega$. They are designed to fit standard $19-\mathrm{in}$. rack panels.

CIRCLE NO. 256


## $\mathrm{Ac} / \mathrm{dc}$ demodulator linear to 0.1\%

Natel Engineering Co., Inc., 7129 Gerald Ave., Van Nuys, Calif. Phone: (213) 782-4161. P\&A: \$350 to $\$ 450$; 30 days.

A five-range ac-to-dc converter/demodulator is designed to operate at a variety of input levels. Input ranges are 0 to 1,0 to 10,0 to 100 mV rms and 0 to 1 and 0 to 10 V . Input impedance is over $750 \mathrm{k} \Omega$ with output below $1 \mathrm{k} \Omega$. Gain is 1000 to 5000 and isolation between input, output and dc ground is 50 $\mathrm{M} \Omega$.

CIRCLE NO. 257


## Power meter stands high RF environments

PRD Electronics, Inc., 1200 Prospect Ave., Westbury, N. Y. Phone: (516) 334-7810.

An RF shield encases the measuring circuitry of the type 6690 peak power meter, allowing it to operate accurately in high-RF environments. A cutoff tube is used in conjunction with the range switch for extreme attenuation of the RFI. Full-scale readings of 30,100 and 200 mW are covered in four steps with an accuracy of $\pm 0.2 \mathrm{~dB}$. In the narrow-band position, the instrument is capable of making accurate measurements below 5 mW .

CIRCLE NO. 258


## REFERENCE AND REGULATION TO 3OKV

Voltage regulation can never be better, nor more stable, than the reference voltage. And you'll never find a better reference than Victoreen diodes, high-voltage equivalents of Zener diodes. But, unlike Zeners, Victoreen HV diodes are available in voltage ranges from 350 to 30,000 volts.

Unexcelled as a stable reference voltage source, a Victoreen HV diode can also be used alone as a simple shunt regulator, as a DC coupling element, etc. For Space-Age applications, you get a lot of bonuses, too - small cubage, light weight, resistance to high heat, high vibration and high accelerations to 2000 g in some models. Victoreen HV diodes are unaffected by ambient light.

From the positively exotic to the commercial, if you have a voltage regulation problem in the range of $350-30,000$ volts, quickly contact our Applications Engineering Department.

[^9]


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## Instrument 511

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ON READER-SERVICE CARD CIRCLE 83


## "Tough-job" laminate solves cable problems

Nelco, 481 Canal St., Stamford, Conn. Phone: (203) 324-4181.

The usual disadvantages of a thermoset polymeric system are said to be eliminated in the "Flexiglas 6100" epoxy-glass copper-clad laminate for contoured printed circuitry and flat cabling. The material, in 0.003 -in. thicknesses can be folded around a $0.02-\mathrm{in}$. mandrel without fracture or permanent deformation. With this flexibility, Flexiglas 6100 combines a temperature tolerance as high as $500^{\circ} \mathrm{F}$. It can be resoldered repeatedly without degradation.

CIRCLE NO. 261


Ceramic heater set in aluminum body
Thermal Engineering \& Design Co., 217 Ash Street, Akron, Ohio. Phone: (216) 535-5761.

A new heater unit is based on a ceramic element solidly embedded in a finned aluminum radiator. One model now in production is rated 750 W at $120 / 240 \mathrm{Vac}$ and has a resistor core of $1-\mathrm{in}$. diameter by $2-1 / 2-\mathrm{in}$. long. The radiator of this unit spreads the heat over $80 \mathrm{in}^{2}{ }^{2}$ for the equivalent of $9.3-\mathrm{W} / \mathrm{in} .^{2}$.

CIRCLE NO. 263


## Resistor bobbins have four slots

Plasmetex Industries, 8217 Lankershim Blvd., North Hollywood, Calif. Phone: (213) 767-1532.

Calibration of wirewound resistors is said to be facilitated by the presence of four termination slots in a line of molded bobbins. The molded-in lead wires, AWG 20, withstand an axial pull of five pounds. Other advantages cited for these bobbins are mechanical strength of the basic thermosetting material, high dielectric, dimensional stability and low moisture absorption.

CIRCLE NO. 262


## Coax connector line includes 8 types

Cinch-NuLine, 10105 Sixth St, Minneapolis. Phone: (312) 467-1321.

The NCM series of miniature RF connectors initially includes eight types; 4 cable connectors, 3 feedthrus and a PC board connector. All have common interface mating with connector types such as OSM, BRM and SRM. They meet the specs of MIL-C-39012. Rated vswr is 1.3 to 10 GHz , insertion loss is 0.15 dB and leakage protection is -90 dB from 2 to 3 GHz .

CIRCLE NO. 264


## Go 1, Go 2, Go 3 . . . with Series "G"!

As you can see, something new has been added to our connector line. They've gone modular. Now you can stylize your electronic equipment from front to back, get all three kinds of service-signal, power, coaxial-from one basic housing style.
You no longer need a different connector for each type of circuit in your product. AMP's Series "G" Connectors are designed so you can "go" with one, two, or three modular inserts for the exact combination of contact types you want. Inserts are available in either diallyl phthalate or general purpose phenolic with numbered cavities for one or more of these types:

Type I-\#12 screw machine pin and socket power contact
Type II, III and III(+)-regular signal circuit pin and socket contacts
Type IV-miniature coaxial contact
Type XII-new 35 -amp stamped and formed power contact
New subminiature COAXICON* contact

Designed for rugged, dependable performance as well as flexibility, Series " G " Connector shells are two-piece cast aluminum. They consist of a polarized two-piece shell and retainer plate for easy, drop-in assembly of the modules. And, they're available with floating bushings or locking springs, so you can use them equally well for rack-and-panel mounting, service drops, and in-line hook-up applications.
Try this new connector concept in your engineering designs. You'll get all kinds of service . . . with style! It's the practical way to cut inventory costs, too. Write today for complete details.
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[^10]
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ON READER-SERVICE CARD CIRCLE 85



Open-air cryogenics without frost problems
MRC Corporation, Route 303, Orangeburg, N. Y. Phone: (914) 3594200. $P \& A: \$ 2190$ (basic unit); 6 wks.

The "Cryo-collimator" provides cooling down to $-180^{\circ} \mathrm{C}$ without frost. The system cools with a jet of gaseous nitrogen which is completely surrounded by a second stream of nitrogen at room temperature. The movable system holds 15 liters of liquid nitrogen in a dewar, delivering $40 \mathrm{ft}^{3} /$ hour to the nozzle at a temperature of $-180^{\circ} \mathrm{C}$. The basic model cools for three hours and optional filling capabilities allow 4hour or indefinite operation.

CIRCLE NO. 265


## MIL-type fans use aluminum castings

McLean Engineering Labs., Princeton Junction, N. J. Phone: (609) 799-0100. Price: from \$19.95.

Ring fans are based on axial flow aluminum castings and are said to provide MIL-spec reliability. Push and pull airflows are available to 395 cfm . Dimensions are 8-1/2-in. or $10-1 / 4$-in. diameter. The fans are driven by a $115-\mathrm{V}, 50$ to $60-\mathrm{Hz}$ sin-gle-phase motor.

CIRCLE NO. 266


## IC tester gives digital readout

Aerotronic Associates, Inc., Contoocook, N. H. Phone: (603) 746-3141.

The AA model 1061 IC test set is offered as a standard tester for ICs, thin-film networks and other semiconductors.

It features a digital readout for both current and voltage, a Kelvin matrix and digit-switch programing. Power supplies and/or readout can be connected in series or between leads of the device under test. Adapters are available for various IC packages and to provide multiple testing.

CIRCLE NO. 267


## Automatic circuit bonder has infrared detection

Texas Instruments, Inc., 3609 Buffalo Speedway, Houston. Phone: (713) 526-1411. Price: about $\$ 2000$.

No direct contact is needed for this bonder to sense temperature at joints of PC boards and wiring assemblies. Infrared detection automatically controls joint temperature within $\pm 3 \%$ once bonding mode and temperature have been selected. The device performs welding, brazing, hard and soft solder and bonding operation. The power supply has a capability of 10,000 watt-seconds. CIRCLE NO. 268


1. Rigid one-piece Terminal Supports and Pins are riveted into mortised positions on Epoxy Resin Terminal boards.
2. Contact Leads are protected from stresses that affect relay adjustment. With the capsule in final position the contact leads are soldered to the rigid terminal supports. Stresses on the terminal pins, such as occur when the pins are plugged into the board, cannot transmit to the leads.
3. Great dielectric strength. The Nylon bobbin provides insulation from the circuit board and mechanical protection for the glass capsule.
4. Wide space between PC Pins. Pins for $0.062^{\prime \prime}$ holes are spaced on multiples of $0.200^{\prime \prime}$.

5. The unique design concept makes pos-
sible extra reliability at NEW low cost.


Size 1—available* with up to 4 form C or 7 form A dry reed contacts; also 7 form A Mercury-Wetted contacts
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*Furnished custom-built with contact combinations and ratings to specifications. Popular combinations and ratings for standard voltages in stock for immediate shipment.

> Send for literature describing the widest selection of Mercury-Wetted and Dry Reed Relays.


## High Q Air Capacitors!

Get accuracy and tuning stability with Johanson Variable Air Capacitors. High Q-Low Inductance makes units suitable for VLF to UHF applications. Low temperature coefficient.

Working Voltage 250 V DC at from $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$. All units are hermetically sealed.
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## MANUFACTURING CORPORATION

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Stripline connectors simplify assembly
Elpac, Inc., 3760 Campus Dr., Newport Beach, Calif. Phone: (714) 546-8640.

Designed for end-mounting on stripline circuit boards, these miniature microwave connectors have solid Teflon dielectric. The heattreated beryllium copper center contact tab makes it possible to precut the stripline board to exact length and complete the assembly with a minimum of effort. The connectors mate with OSM, BRM and NPM connectors.

CIRCLE NO. 269


## Mixer-preamps pre-aligned, calibrated

Airtron Div. of Litton Industries, Morris Plains, N J. Phone: (201) 539-5500.

In radar, space communications and guidance applications, a series of mixer-preamplifiers eliminate the problem of mixer to preamp compatibility. For example, the range of the model AGMOM-A in this line is 4.2 to 4.4 GHz . It offers 1.4 vswr , noise of 8.5 dB and a preamp gain of 25 dB . Frequencies from 0.5 GHz to 8 GHz are offered in the line with a max vswr of 2 and IF centers from 20 to 400 MHz .

CIRCLE NO. 270


## Bolometer mount attuned to system

PRD Electronics Inc., 1200 Prospect Ave., Westbury, N. Y., Phone: (516) 334-7810.

Designed particularly for systems applications, the Type 627AM1 is a fixed-tuned coax bolometer mount. The mount consists of a length of $3 / 8-i n$. coax line, a bolometer or thermistor housing and a filter net. A disk-type transducer is used in the mount. Basic units cover a range of 500 MHz to 10 GHz with N and BNC connectors available.

CIRCLE NO. 271


## Cw gas laser produces 75 watts

Coherent Radiation Lab., 932 East Meadow Dr., Palo Alto, Calif. Phone: (415) 328-1840. P\&A: \$8250; 60 days.

With an output wavelength of 10.6 microns, the model 40 gas laser system is capable of producing ew power of over 75 watts. The gas is a mixture of $\mathrm{CO}_{2}$, nitrogen and helium. In either industrial or lab applications, its major feature is its ability to provide a continuous energy density beyond $10^{6}$ watts $/ \mathrm{cm}^{2}$. A complete system consists of a service console, a control unit and the laser head.


## Ku-band diode switch has $20-\mathrm{W}$ capability

Alpha Industries, Inc., 381 Elliot St., Newton Upper Falls, Mass. Phone: (617) 969-6480.

Covering the frequency range of 15.4 to 15.6 GHz , this spdt diode switch has cw power handling capabilities of 20 W . Insertion loss is rated at 1 dB max with a switching speed of 150 ns . Isolation is 25 dB min. Both waveguide and coax models are available at 0.2 to 18 GHz in spst and single-pole, multithrow configurations.

CIRCLE NO. 273


## Parametric amplifier pumped by an avalanche

American Electronic Laboratories, Inc., Box 552, Lansdale, Pa. Phone: (215) 822-2929. $P \& A: \$ 4595 ; 90$ to 129 days.

The pump source for the PAR 1612A parametric amplifier is an avalanche diode oscillator. The amplifier is tunable over the 2.2 - to $2.3-\mathrm{GHz}$ telemetry band has a max noise figure of 2.5 dB . Use of the avalanche diode oscillator, rather than a klystron, is said to reduce size while improving over-all reliability. Max dimensions of the unit are $4-1 / 8 \times 5-1 / 2 \times 1-5 / 16-\mathrm{in}$.


## Fiberglass Sleeving

retains its flexibility and electrical properties in continuous operation at temperatures up to $155^{\circ} \mathrm{C}$. Even after 1000 hours, it will not crack when bent $180^{\circ}$ around a mandrel. Constructed of closely woven fiberglass, it is thoroughly impregnated and uniformly coated with modified acrylics, making it compatible with most wire enamels and encapsulants and resistant to oils, acids, alkalies, jet fluid, tox and water. Good resistance to abrasion and cut-through-non-wicking. Write for samples, data and prices.

L. FRANK MARKEL \& SONS

Norristown, Pennsylvania 19404 insulating tubings and sleevings high temperature wire and cable

## Design Aids



## Tap drill card

A handy pocket-sized plastic chart converts fractions or drill sizes to decimal equivalents and tap drill sizes. The L.S. Starrett Co.

CIRCLE NO. 293


Decibel conversion slide rule
This "Ballantine Rule" may be used to convert volts or millivolts to dB at any desired zero decibel power reference, or vice versa. It also shows the relationships of gain and loss existing between decibels, voltage and current ratios and power ratios. The reverse side may be used to find squares and square roots, reciprocals, logarithms and $\mathrm{dB} /$ ratio conversions when the $d B$ value is large. A handy wallet-sized card is included which also performs the voltage ratio/dB conversion. A full set of instructions accompanies these useful tools. Ballantine Laboratories, Inc.

CIRCLE NO. 294


## RF filter slide chart

A handy 3 -section slide rule provides specification data for microwave filter design. The $6-1 / 2 \times 10-$ in. chart has three sections. The first, "Chebishev Response," gives the degree of attenuation at various frequencies for 2 - to 19 -pole filters. "Reflection-Transmission Relationships," section two, gives voltage and power reflection/transmission characteristics and loss in $d B$ for various vswr. The "Bandpass" section three shows attenuation and bandwidth characteristics for 2 through 6-section filters, as well as bandwidth and insertion loss for various types of filters. Telonic's 45-page 1967 catalog is included with the rule. Telonic Engineering Co.

CIRCLE NO. 295


## Waveguide scaling factors

Standard EIA waveguide sizes WR2300 through WR3 are tabulated on a handy 3 -hole punched notebook page. Electrical and mechanical specs, JAN flange references and a cross-reference between EIA waveguide, WR and JAN waveguide RG numbers are included. The reverse side lists convenient factors for scaling designs from one waveguide size to another. Microwave Development Labs., Inc.

CIRCLE NO. 296


Plastics selector slide chart
A variety of specifying information is given on this slide chart for users of high-pressure laminated plastic sheets, tubes and rods. The chart provides physical, electrical and mechanical properties, industrial and MIL specs, application types, characteristics and colors. INSUROK Div., Richardson Co.

CIRCLE NO. 297


Miniature lamp calculator
A circular slide rule determines miniature incandescent lamp characteristics at voltages other than the catalog design voltage. For varying application voltages, candlepower factor, life factor and current factor are read out in the appropriate windows. The reverse side of the rule is a standard Mannheim slide rule.

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## Buyer's guide

The 1967 Cramer Buyers Guide for Electronics is now available. The 500 -page industrial catalog lists and describes over 60,000 electronic components and products that are available from this distributor. Manufacturer's part numbers, not house numbers, are used throughout and an index, ordering instructions and price information are included. Cramer Electronics, Inc.

CIRCLE NO. 275

## Transducer conditioning

This 16-page paper, dealing with today's state-of-the-art input conditioning, emphasizes constant current techniques. Frequency response of constant current power supplies, and guarding and shielding for input conditioners are explored. B \& F Instruments, Inc.

CIRCLE NO. 276

## Memory products

A brief history of memory core development, with highlights of memory product manufacturing facilities and capabilities illustrates typical memory planes and stacks for commercial, industrial and military use. Indiana General Corp.

CIRCLE NO. 277


## Power converter catalog

Ten lines and 199 models of power conversion equipment are described in the new Wanlass catalog. Included are details on voltage regulators, power supplies and a line filter that removes SCR spikes. Wanlass Electric Co.

CIRCLE NO. 278

## Pilot light catalog

A four-page brochure on pilot lights, light assemblies, individual sockets and multi-socket strips and short slide-base lamps is available. It provides complete details on connections, mounting, and assembly, along with all dimensional data. Industrial Devices, Inc.

CIRCLE NO. 279

## Pulse equipment

Four loose-leaf data sheets cover a switching time meter, pulse amplifier, pulse transformer, and pulser accessories. The units operate in the 5 - to $25-\mathrm{ns}$ range. E-H Research Laboratories, Inc.

CIRCLE NO. 280

## PC laminates

A new, four-page brochure describes properties and specifications of unclad and copper-clad laminates for use in microwave and high-frequency printed circuits. Dodge Fibers Corp.

CIRCLE NO. 281

# EIMAC 

The EIMAC 4CV250,000C is the world's highest power tetrode. It is designed for service in super-power broadcast transmitters, and was developed on the foundation of technology which produced its "little brother," the hundredkilowatt 4CV100,000C, now used by the USIA. The giant new vapor-cooled tube combines high power gain with long life. Vapor cooling is accepted as an efficient and economical method of cooling in advanced broadcast systems. As EIMAC's latest addition to its line of power tetrodes, the $4 \mathrm{CV} 250,000 \mathrm{C}$ is ideally suited for service as an audio modulator, a pulse modulator, or a regulator, and as an rf amplifier in linear accelerators. Ready now for the superpower transmitters of the future, this 250 kW tetrode is another example of how EIMAC's experience in power tube technology paves the way for the developments of tomorrow. For a power tube to fit your needs-big or small -write Product Manager, Power Grid Tubes, or contact your nearest EIMAC distributor.

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## Synchro/resolver catalog

A wide variety of synchros and resolvers are included in catalog J66-1066. A special section for system designers provides information on applications. Other general data are provided in the form of curves, graphs, photos and diagrams. General Precision, Aerospace Group.

CIRCLE NO. 282

## Instant switching

This bulletin gives information on completely enclosed $1 / 2-\mathrm{in}$. rotary switches with adjustable stops. The bulletin includes complete information on electrical and mechanical features, and charts giving type numbers, number of possible poles per deck and the maximum number of positions per pole. RCL Electronics.

CIRCLE NO. 283

## Plastic testing

Brief accounts and diagrams of standard tests on plastics as well as frequently used conversion tables are provided by a 40 -page booklet. The book is intended as a ready reference text on ASTM and other plastic tests. Celanese Plastics Co.

CIRCLE NO. 284

## Word indicator lights

Ten pages of text and diagrams describe the company's "push-to relamp" miniature, illuminated word indicators for military and commercial equipment. Master Specialties Co.

CIRCLE NO. 285


## Circuit board boxes

Instant organization is covered in a new brochure on circuit board boxes. Providing both catalog information and description, the brochure covers the manufacturer's complete line. Panel Controls Corp.

CIRCLE NO. 286

## Detector mounts

Five new technical bulletins are available, describing the company's high-sensitivity detector mounts. Included in these bulletins is information on features, applications, typical specifications, and dimensional drawings for each of the detector mounts. American Electronic Labs., Inc.

CIRCLE NO. 287

## Variable band-pass filter

A 2-page illustrated data sheet describing the KH model 3100 solidstate variable band-pass filter includes a composite graph of maximally flat, and transient-free, fre-quency-response modes. Oscilloscope photographs of these reponses are shown. Krohn-Hite Corp.

CIRCLE NO. 288

## The chips are in

A mounting package that makes possible solderless, pressure contact mounting of microcircuit flat-packs on PC' cards is described in a fourpage brochure. Card files and equipment drawers are also described. Scanbe Mfg. Corp.

CIRCLE NO. 289


## Japan buyers guide

American representatives of Japanese electronic equipment manufacturers are listed in a comprehensive directory. Including an alphabetical section and a product section, this reference gives name, address, phone number, telex number and cable address for each listing. Products are classified into twenty groups from batteries to TV receivers.

Available for $\$ 4$ from Dee Company, Suite 103C, 10639 Riverside Drive, N. Hollywood, Calif.

## Telemetry products

This catalog describes electromechanical and solid-state commutators and multicoders, FM telemetry products, amplifier products and PCM components and systems. Also included are miscellaneous telemetry products such as solid-state differential signal adaptors, inflight voltage calibrators, etc. General Devices, Inc.

CIRCLE NO. 291

## Pulse counter

A 2-page, 2-color brōchüre describes a pulse counter and storage device, used with the manufacturer's scanning transmitter to count accurately and reliably randomly occurring pulses such as watt-hour pulses originating in a remote station, and transmit the total count to a master station. Quindar Electronics, Inc.

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## NEW LITERATURE

## Miniature op-amps

A data sheet describing Melcor's economy line of miniature operational amplifiers contains performance specifications, with limits, for all models. Outline drawings and typical applications are included. Melcor Electronics Corp.

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## Reprints Available

The following reprints are available free and in limited quantities. To obtain single copies, circle the number of the article you want on the Reader-Service Card.

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New York, N.Y. 10022

## Schweber Catalog of Westinghouse Semiconductors



Schweber catalog
of Westinghouse
semiconductors

This 20-page catalog condenses the Westinghouse semiconductor line for quick, easy reference. The index is arranged in four main group-ings-Transistors, Thyristors, Rectifiers, and Rectifier Assemblies. There are 17 sub-headings. The dimensional outline drawings are unusually elegant and suitable for blueprint reproduction. Short texts precede each grouping explaining salient features and device technology.

## Schweber Electronics

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## Solid-Jacketed MicroCoax Cable

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MicroDelay Division's new Bulletin 202C describes miniature solid-jacketed coaxial cable providing total shielding and improved environmental stability. Seamless tubular jackets of copper, aluminum and stainless steel eliminate crosstalk and attenuation problems. More than 25 MicroCoax combinations of solid metal jackets, dielectrics and conductors, as catalogued in this bulletin, are stocked in O.D.'s from $0.250^{\prime \prime}$ to $0.020^{\prime \prime}$ for immediate shipment.

## MicroDelay Division

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## Potentiometers, Switches \& Turns-Counting Dials



A four-page, two-color short-form catalog from Duncan Electronics, Inc., gives complete specifications and prices on Duncan's precision multi- and single-turn potentiometers for both military and commercial applications. Also included are details on Duncan's Series 60 and 80 turns-counting dials designed for use with multi-turn potentiometers; non-linear potentiometer information for log, trig, empirical, or other forms of non-linear functions; and precision commutator switches for use either individually or in ganged assemblies with potentiometers and other switching devices.

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Advertiser Page
AMP, Incorporated ..... 121
Acopian Corporation ..... 112
Aerovox Corporation ..... 95
Aladdin Electronics ..... 42
Alexander Hamilton Institute ..... 16
Allen-Bradley Co ..... 16
Amecon Division, Litton Industries ..... 102
American Enka Corporation Brand Rex Division ..... 89
American Lava Corporation ..... 40
Amperex Electronic Corporation ..... 8, 9
Analog Devices, Inc. ..... 34, 35
Augat, Inc. ..... 131
Beckman Instruments, Inc., Helipot Division ..... 27
Belden Manufacturing Company ..... 127
Bendix Corporation, The, Semiconductor Division ..... 4, 5
Bourns, Inc ..... 15
Bulova Electronics Division of Bulova Watch Company ..... 116
Clairex Corporation ..... 124
Clarostat Mfg. Co., Inc. ..... 109
Clifton Precision Products, Division of Litton Industries ..... 71
Collins Radio Company ..... 37
Communication Electronics, Incorporated 100
Coors Porcelain Company100
41
Cramer Electronics, Inc. ..... 114
Dale Electronics, Inc ..... Cover III
Data Devices Corporation ..... 99, 101, 103
32
Datapulse Instruments Division. Datapulse, Incorporated ..... 2
Deltron, Inc. ..... 46
Digital Equipment Corporation ..... 79
Duncan Electronics, Inc ..... 133
Eimac, A Division of Varlan Associates . . 129 ..... 129
Eldre Components, Inc.
Electronic Design ..... 132, 135
General Electric Company, Semiconductor Products Department ..... 65
General Radio Company ..... 1 ..... 1
Hathaway Instruments, Inc. ..... 36
Hayden Book Company, Ine ..... 64, 132
Haydon Switch \& Instrument Inc. ..... 113
Heath Company ..... 63
Hewlett-Packard, Moseley Division ..... 83
Hewlett-Packard Cover II, ..... 106
Honeywell, Computer Control Division
38
Howell Instruments, Inc
IEEE Convention and Exhibition ..... 131
IRC, Inc. ..... 43
Instrument Products, 3 M Company ..... 120
85
International Rectiffer Corp. ..... 85
JFD Electronics Co./Components ..... 93 Division ..... 124
Advertiser Page
K \& M Electronics Corp. ..... 102
Korad, A Subsidiary of Union Carbide Corporation ..... 33
L E L Division, Varian Associates ..... 98
Leeds \& Northrup, Components Division ..... 44
Littelfuse ..... 118
Litton Industries, Electron Tube Division ..... 73
Magnecraft Electric Co. ..... 123
Mallory Capacitor Company ..... 22
Markel \& Sons, L. Frank ..... 125
Mepco, Inc.
$.110,111$
$.110,111$
Methode Electronics, Inc. ..... 119
Microsemiconductor Corporation ..... 96
Microwave Expositions, Inc. ..... $\begin{array}{r}90 \\ \hline\end{array}$
Molex Products Co. ..... 108
Motorola Semiconductor Products, Inc. ..... 6
Ohmite Manufacturing Company ..... 115
Pacific Data \& Controls ..... 102
Pamotor, Inc. ..... 128
Philco-Ford Corporation, Microelectronics Division ..... 87
RCA Electronic Components and Devices ..... 55
RCL Electronics, Inc.
RCL Electronics, Inc.
Radio Cores, Inc. ..... 130
Rowan Controller Company, The ..... 25
Salon International des Composants Electroniques ..... 56
Sanders Associates, Inc. ..... 118
Schweber Electronics ..... 133
Semcor Division of Components, Inc. ..... 97
Shalleross, A Subsidiary of Cutler ..... 47
Hammer
Hammer
50
50
Sigma Division, Sigma Instrumen
Signetics Integrated Circuits, A Subsidiary of Corning Glass Works ..... 45
Siliconix Incorporated ..... 12
Sprague Electric Company ..... 10, 20
Syntron Company ..... 105
TRW Semiconductors ..... 48
Tektronix, Inc., Oscilloscopes ..... 31. 72
Teledyne Telemetry, A Teledyne Company 104
Telrex Laboratories ..... 122
Tracor, Inc. ..... 70
Ulano ..... 19
Uniform Tubes, Inc. ..... 133
Victoreen Instrument Company, The ..... 117 Wang Laboratories, Inc. ..... 134
Zippertubing Co., The ..... 136
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| :---: | :---: | :---: | :---: | :---: |
|  | Amers |  | Ammo | (tame |
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| 19 | 21 | 22 | 23 | 24 | 25 |  |
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## Jan. 19-20

Institute-Computer Aid for Reliability Analysis of Electronics (Milwaukee) Sponsor: University of Wisconsin; C. L. Brisley, Director, Engineering Center for Postgraduate and Professional Development, The University of Wiscon$\sin , 600$ W. Kilbourn Ave., Milwaukee, Wis. 53203.

Jan. 21
Quality Control Conference (Pomona, Calif.) Sponsor: ASQC; S. Roy Wood, Reliability Mgr., Aero-ject-General Corp., 11711 Woodruf Ave., Downey, Calif. 90240

## Jan. 31-Feb. 2

Circuit Design by Computer -Tutorial Symposium (New York City) Sponsor: New York University; M. B. Goldin, New York University, University Heights, New York, N. Y. 10453

Feb. 7-9
Winter Convention on Aerospace \& Electronic Systems (Los Angeles) Sponsor: IEEE, G-AES; D. Traitel, Electro-Optical Systems, 300 N. Halstead, Pasadena, Calif.

Feb. 14-17
Electronic Packaging Conference (New York City) Sponsor: Society of Automotive Engineers, Inc.; A. J. Favata, SAE, 485 Lexington Ave.. New York, N. Y. 10017

## Feb. 15-17

International Solid-State Circuits Conference (Philadelphia) Sponsors: IEEE, University of Penn.; Lewis Winner, 152 W. 42 St., New York, N. Y. 10036

## Feb. 20-24

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[^2]:    $\star$ Advt. No. 4-1028-2/3 page-Electronics-February 6, 1967
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