W56940 BRIEF



40-NOTE POLYPHONIC RINGTONE CHIP

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W56940 BRIEF



1. DESCRIPTION

The W56940 is designed for the ringtone application of cell phones with 40-note MIDI plus ADPCM/PCM synthesis. This single chip consists of a CPU interface, FIFO buffering, a wavetable sound set, a MIDI sequencer, a wavetable synthesizer, DAC, and a power amplifier that can drive an 8-ohm speaker directly.

With an on-chip, high-quality, GM-compliant sound set, the W56940 supports up to 128 instruments and 47 drums for playback of any MIDI song with up to 40 simultaneous notes. MIDI songs in various formats can be downloaded and played back by passing the song to the CPU interface in the W56940.

The ADPCM/PCM synthesizer is designed to allow for customized SFX (sound effects), in addition to ordinary MIDI songs. The sound effects can be played along with the MIDI tunes for more fantastic sound experiences.

Synchronization between MIDI, SFX, LED, and MTR is done using embedded events in the MIDI file, which the W56940 decodes and processes accordingly. The LED and MTR output pins, each with 256-level control output, can be used for brightness and speed control, respectively.

In addition, API (Application Program Interface) source code is provided for the CPU to control the W56940 easily via the memory bus interface.

2. FEATURES

- General MIDI compliant sound set: 128 instruments + 47 drums
- 40-note, 100%-wavetable MIDI synthesizer with ADPCM/PCM synthesis
- High quality sound
- Simultaneous playback of
 - Two MIDI song files,
 - Two ADPCM files, or
 - ADPCM + MIDI
- Karaoke feature support
- MIP support for SP-MIDI format
- Middleware API source code provided for function control and CPU interface
- Multiple Format Parser that supports
 - o MIDI: SMF, SP-MIDI, i-Melody, RTTTL, etc.
 - Audio: stereo WAV @ 48 KHz
- Stereophonic DAC, 12-bit resolution per channel
- On-chip power amplifier to drive 8Ω speaker directly
- Equalizer embedded to meet speaker response
- Stereophonic headphone outputs available



- Line-In to receive external analog signals
- LED output with 256 levels of brightness
- MTR output with 256 levels of speed
- Harmonized synchronization between MIDI, SFX, LED, and MTR
- Embedded PLL operates on 1.7 ~ 36 MHz clock input
- Power supply

o VDD: 2.7 ~ 3.3 volts

○ SPVDD: 2.7 ~ 4.5 volts

○ IOVDD: 1.65 ~ VDD

• Operating current: 20 mA (25°C at no load)

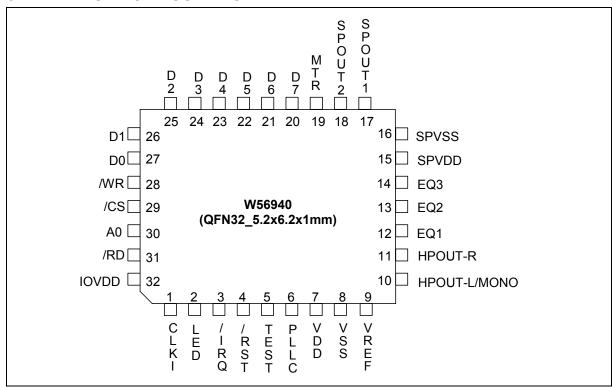
Standby current: 1 uA @ 25°C

• Operating temperature: -20°C ~ 85°C

Package

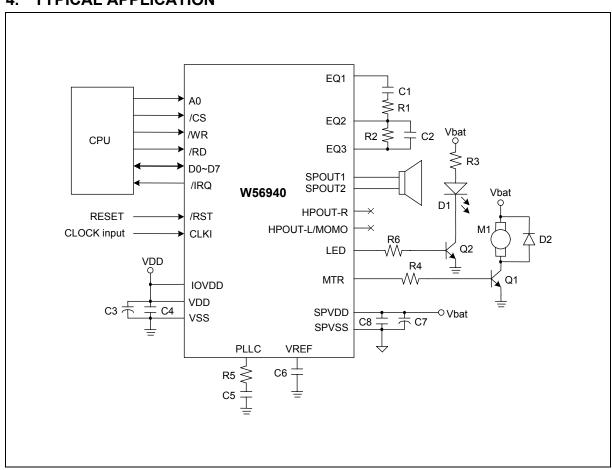
o 32-pin plastic QFN (5.2mmx6.2mmx1mm)

3. PACKAGE CONFIGURATION





4. TYPICAL APPLICATION



5. REVISION HISTORY

VERSION	DATE	PAGE	DESCRIPTION
A1	Sep. 2004	-	-
A2	May 31, 2005	-	Revised by Brent and add important notice



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