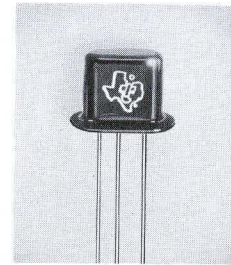




N-P-N GROWN JUNCTION SILICON TRANSISTOR

9 to 20 beta spread

Specifically designed for high gain at high temperatures



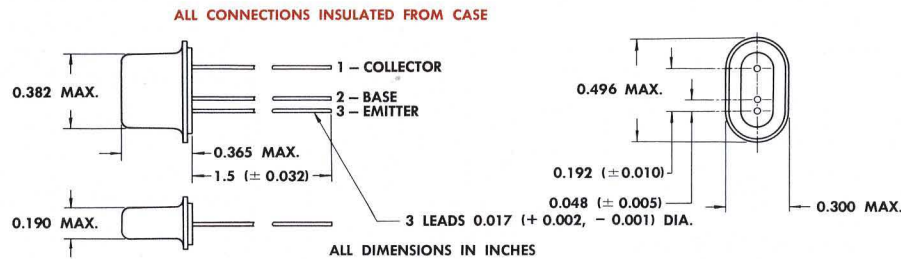
TYPE 2N117  
BULLETIN NO. DL-S 896  
REPLACES BULLETIN NO. DL-S 745  
MARCH, 1958  
APRIL, 1957

qualification testing

All units are heat cycled from -65°C to +175°C. This test consists of fourteen cycles, four at 95% relative humidity (from -65°C to +75°C). Also, the hermetic seal is checked by pressure testing. All units are completely tested for design characteristics and undergo a rigorous tumble test to check for mechanical reliability. These units are designed to meet the requirements of MIL-T-19500/35.

mechanical data

Welded case with glass-to-metal hermetic seal between case and leads. Approximate weight is 1.7 grams.



absolute maximum ratings at 25°C ambient

[except where advanced temperatures are indicated]

Collector Voltage Referred to Base	45 V
Emitter Voltage Referred to Base	1 V
Collector Current	25 mA
Emitter Current	-25 mA
Collector Dissipation	150 mW
at 100°C	100 mW
at 150°C	50 mW

junction temperature

Maximum Range . . . . . -65°C to +175°C

common base design characteristics at Tj = 25°C

[except where advanced temperatures are indicated]

		test conditions		min.	design center	max.	unit
BV <sub>CB0</sub>	Collector Breakdown Voltage	I <sub>C</sub> = 50μA	I <sub>E</sub> = 0	45	—	—	Volt
I <sub>CB0</sub>	Collector Cutoff Current	V <sub>CB</sub> = 30V	I <sub>E</sub> = 0	—	—	2	μA
		at 100°C } V <sub>CB</sub> = 5V	I <sub>E</sub> = 0	—	—	10	μA
		at 150°C } V <sub>CB</sub> = 5V	I <sub>E</sub> = 0	—	—	50	μA
		V <sub>CB</sub> = 5V	I <sub>E</sub> = -1mA	30	42	80	Ohm
h <sub>ib</sub>	Input Impedance	V <sub>CB</sub> = 5V	I <sub>E</sub> = -1mA	0.0	0.4	1.2	μmho
h <sub>ob</sub>	Output Admittance	V <sub>CB</sub> = 5V	I <sub>E</sub> = -1mA	25	120	500	X10 <sup>-6</sup>
h <sub>rb</sub>	Feedback Voltage Ratio	V <sub>CB</sub> = 5V	I <sub>E</sub> = -1mA	-0.9	-0.925	-0.953	—
h <sub>fb</sub>	Current Transfer Ratio	V <sub>CE</sub> = 20V	I <sub>E</sub> = -2mA	—	35	—	db
PG <sub>e</sub>	Power Gain*†	V <sub>CE</sub> = 5V	I <sub>E</sub> = -1mA	—	20	—	db
NF	Noise Figure*‡	V <sub>CB</sub> = 5V	I <sub>E</sub> = -1mA	—	4	—	mc
f <sub>ob</sub>	Frequency Cutoff	V <sub>CB</sub> = 5V	I <sub>E</sub> = -1mA	—	7	—	μμf
C <sub>ob</sub>	Output Capacitance (1mc)	I <sub>B</sub> = 2.2mA	I <sub>C</sub> = 5mA	—	100	200	Ohm
R <sub>cs</sub>	Saturation Resistance*						

\*Common Emitter

†R<sub>g</sub> = 1k; R<sub>L</sub> = 20k

‡Conventional Noise—Compared to 1000 ohm resistor, 1000 cps and 1 cycle band width

# TYPE 2N117

## TYPICAL CHARACTERISTICS

