December 1995



Product Selection Guide



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USING THIS GUIDE/INTRODUCTION

Using This Guide

This guide is an overview of our component offerings. It is organized to help you quickly determine what additional documentation you need to apply AT&T Microelectronics' components and capabilities in your product design.

The Product Listings describe standard products, semicustom components, and custom capabilities quick reference charts for ordering appropriate data sheets by part number; descriptions of our capabilities in standard-cell ASICs and FPGAs; and an overview of how we can work with you to develop semicustom and custom products.

Literature Code Legend:

AP — Application Note

- BC Brochure
- CA Catalog
- DB Data Book
- DS Data Sheet
- IM Information Manual
- MN Manual
- PN Product Brief
- TN Technical Note

To order literature or request additional information, call your AT&T Account Manager or call 1-800-372-2447.

Introduction

Expanding People's Capabilities Through Innovation . . .

For over a hundred years people have been communicating by voice-only telephony made significantly possible by the technology and products of AT&T Microelectronics. Through the 1990s advanced technology will be expanding people's communication possibilities through innovative methods. People have the opportunities to exchange thoughts, messages, and information via speech, signals, and writing. Visual images and pictures will enhance people's communication between one another and the world around them, while stretching beyond the limits of strictly words. As technology advances, people will understand their need to communicate is no longer determined by their location, by the time zone in which they live, or by the communication instrument available to them at a given time. Today, their ability to communicate or access information is aided by a wider choice of media and a greater accessibility range. People now have the opportunity to communicate sitting at a desk, traveling in an airplane or car, or even lounging on the beach. These communication capabilities are emerging from a convergence of the traditional communication, computing, and entertainment markets. From this merger come opportunities in new markets, with new products, and between new customers. AT&T Microelectronics, working together with the key players in these markets, is creating the vision of anytime, anywhere communications.

INTRODUCTION

Customer-Driven Solutions

AT&T Microelectronics applies the world-renowned research of AT&T Bell Laboratories, our integrated technology platforms, and our customers' needs and insights to create focused applications in line with our vision. Our efforts result in leading products and application solutions which give our customers an advantage and bring an added value to their own customers (see Figure 1). Today we're delivering industryleading solutions for networked computing, wireless communications, telecommunications, and desktop videoconferencing. Soon we'll deliver solutions for advanced consumer electronic products like HDTV. And with our state-of-the-art production facilities and design centers located throughout North America, Europe, and Asia, we can deliver these solutions on a worldwide scale.





AT&T Microelectronics Integrated Circuits Group

Lifetime Warranty

We deliver customer delight by guaranteeing our IC Products for Life.



Warranty – Effective January 1, 1995, Seller warrants to Buyer that products of its manufacture will be, on the date of shipment of the product, free from defects in material and workmanship and will substantially conform to Seller's written specifications provided to Buyer or to the specifications, if any, identified in an order and agreed to in writing by Seller. If any defect in material or workmanship or failure to meet such specifications (a "defect") appears in the product, Seller will, at its option, either credit or refund the purchase price, repair, or replace the defective product with the same or equivalent product without charge at Seller's manufacturing or repair facility provided (i) Buyer notifies Seller in writing of the claimed defect within thirty (30) days after Buyer knows or reasonably should know of the claimed defect, and (ii) Seller's examination of the product discloses that the claimed defect actually exists and (iii) in the case of optically coupled MOSFET drivers, optically coupled solid-state relays, or any product other than packaged monolithic integrated circuits, the defect appears within twelve (12) months from the date of shipment of the product.

Buyer shall follow Seller's instructions regarding return of defective product, and no product will be accepted for repair, replacement, credit, or refund without the written authorization of and in accordance with Seller's instructions. This warranty only extends to Buyer. Seller will not accept returns directly from Buyer's customers or users of Buyer's products Replaced products shall become Seller's property. In no event shall Seller be responsible for deinstallation or reinstallation of defective products or for the expenses thereof If Seller determines that the returned products are not defective, Buyer shall pay Seller all costs of handling, inspection, repairs, and transportation at Seller's then prevailing rates. Repairs and replacements covered by the above warranty are warranted to be free from defects as set forth above.

The above warranty does not apply to, and Seller makes no warranties with respect to, products that:

- are software programs, experimental or prototypes (all of which are provided "AS IS"), or
- have been subjected to misuse, neglect, accident, abuse, or operating or environmental conditions that deviate from the parameters established in applicable specifications; or
- have been improperly installed, stored, maintained, repaired, or altered by anyone other than Seller, or
- have had their serial numbers or month and year of manufacture or shipment removed, defaced, or altered.

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DIGITAL SIGNAL PROCESSORS

Part Number	Description	Package Type	Speed (ns)	Temp. Ranges	Literature
DSP32C	32-bit CMOS Digital Signal Processor 1.5K RAM/0 ROM	133-pin, ceramic PGA 164-pin BQFP	50, 60, 80	0 °C to +70 °C -40 °C to +85 °C* (I)	AP, DS, IM
DSP32C w/o External Memory Interface	2K RAM/0 ROM	68-pin PLCC	80	0 °C to +70 °C	AP, DS, IM
*Industrial tempera	e ature only available at 100 r	26			

Floating-Point DSP Products - Product Matrix

Industrial temperature only available at 100 ns.

DSP32C Development Tools - Product Matrix*

Part Number	Description				
DSP32C-SL-XXXX	Software Library containing Software Generation System (SGS) and a device simulator				
DSP32C-AL-XXXX	Application Software Library				
DSP32C-CC-XXXX	C Language Compiler including a Software Library and C-Callable Version of the Application Software Library				
DSP32C-DS-DEV-16	Development Board with 16 Kword SRAM				
DSP32C-DS-DEV-64	Development Board with 64 Kword SRAM				
DSP32C-DS-ICE	PC Board — In-Circuit Emulator POD				
DSP32C -DS-PBS	PC Board — PC Bus Interface Half-Card				
DSP32C-DS-MII	Multi-ICE Interface Box				
SIG32C-8	ISA Bus board which supports 32C 50 ns devices. Contact SignaLogic at (214) 343-0069 for details.				

*Technical/Sales support is now being handled by a third party, Valley Technologies, Contact No. 1-800-370-6661 for all issues.

Fixed-Point DSP Products – Product Matrix

Part Number	Description	Package Type	Speed (ns)	Supply (V)	Temp. Ranges	Literature
DSP16A	2K RAM/12K ROM 16-bit CMOS Digital Signal Processor	84-pin PLCC 84-pin PQFP	25, 33, 55	5	0 °C to 70 °C -40 °C to +85 °C*	AP, DS, IM
	1K RAM/8K ROM 16-bit CMOS Digital Signal Processor	84-pin PLCC	33, 55	5	0 °C to 70 °C -40 °C to +85 °C*	AP, DS, IM
	2K RAM/24K ROM 6-bit CMOS Digital Signal Processor	84-pin PLCC 84-pin PQFP 100-pin TQFP	25, 33, 55	5	0 °C to 70 °C -40 °C to +85 °C*	AP, DS, IM
DSP1610	16-bit CMOS DSP with 8K Downloadable Dual-Port RAM and 512K Boot ROM or 4K RAM and 512K Boot ROM	132-pin PQFP	25, 33	5	0 °C to 70 °C -40 °C to +85 °C	AP, BC, DS, IM, DB
DSP1611	16-bit CMOS DSP with 12K Dual-Port RAM and 1K Boot ROM	100-pin TQFP	20, 25, 30 33 38	5 3 2.7	-40 °C to +85 °C	DS, DB

Fixed-Point DSP Products - Product Matrix (continued)

			Power				
Part Number	Description	Package Type	Speed (ns)	Supply (V)	Temp. Ranges	Literature	
DSP1615	16-bit CMOS DSP	100-pin TQFP	100	3	–10 °C to +60 °C	DS, IM	
	& 24K ROM	100-pin BQFP	125	3			
DSP1616 x 11	16-bit CMOS DSP	100-pin BQFP	33, 38	5	-40 °C to +85 °C	AP, BC,	
	with 2K Dual-Port RAM and 12K ROM	100-pin TQFP				DS, DB	
DSP1616 x 30	16-bit CMOS DSP	100-pin BQFP	20, 25, 30	5	-40 °C to +85 °C	BC, DS,	
	with 2K Dual-Port RAM	100-pin TQFP	33	3		IM, DB	
	and 12K ROM		38	2.7			
DSP1617	16-bit CMOS DSP	100-pin BQFP	20, 25, 30	5	-40 °C to +85 °C	BC, DS,	
	with 4K Dual-Port RAM	100-pin TQFP	33	3		DB	
	and 24K ROM		38	2.7			
DSP1618	16-bit CMOS DSP	100-pin BQFP	20, 25, 30	5	-40 °C to +85 °C	BC, DS,	
	with 3K Dual-Port RAM	100-pin TQFP	33	3		DB	
	and 16K ROM		38	2.7			
DSP1627 x 32	16-bit CMOS DSP	100-pin TQFP	14	5	-40 °C to +85 °C	DS, DB	
	with 6K Dual-Port RAM and 32K ROM	100-pin BQFP	20	2.7			
DSP1627 x 36	16-bit CMOS DSP	100-pin BQFP	14	5	-40 °C to +85 °C	DS, DB	
	with 6K Dual-Port RAM and 36K ROM	100-pin TQFP	20	2.7			

* Industrial temperature for 25, 33, 55 ns in 84 PLCC and 33, 55 ns in 84 PQFP only.

DSP16A and DSP161X Development Tools – Product Matrix

Part Number	Description
DSP16A-SL-XXXX	Software Library containing Software Generation System (SGS) and a device simulator
DSP16A-AL-XXXX	Application Software Library
DSP16A-DS	Stand-Alone Development System
DSP16A-BD-EVAL	Development Board with PC/XT/AT Plug-in Board
DSP16A-BD-EV/25	Evaluation Board with PC/XT/AT Plug-in Board
DSP1610-ST-XXXX	Software Library containing Software Generation System (SGS) and an Integrated Development Environment (IDE)
FlashDSP™ 1600-HDS	FlashDSP 1600 Hardware Development System
DSP1610-HDS	Hardware Development System
DSP1610-EVAL2	PC Board — DSP1610 Evaluation Board
CSP1027-AC	Adds CSP1027 Functionality to DSP1602/04-DEMO, DSP1610-EVAL2, DSP161X-EVAL
T7525-AC	T7525 Codec Add-On Card. Adds functionality to DSP1610-EVAL2, DSP161X-EVAL
DSP1611-ST-XXXX	Software Library containing Software Generation System (SGS) and an Integrated Development Environment (IDE)
DSP1611-EVAL	DSP1611 Evaluation Board
DSP1616-ST-XXXX	Software Library containing Software Generation System (SGS) and an Integrated Development Environment (IDE)
DSP1616-POD	DSP1616 In-Circuit Emulation POD
DSP1616/27-DEMO	Demonstration Board. Provides Software and Hardware Evaluation Platform for the DSP1616 and CSP1027

DIGITAL SIGNAL PROCESSORS

Part Number	Description
DSP1616/84-DEMO	Demonstration Board. Provides Software and Hardware Evaluation Platform for the DSP1616 and CSP1084
DSP1616-EVAL	DSP1616 Evaluation Board with PC/AT Plug-in Board
FlashDSP 1616X-KIT	<i>FlashDSP</i> 1616 KIT. Program and Erase Flash Memory on <i>FlashDSP</i> 1616 through JTAG/HDS Port
DSP1617-ST-XXXX	Software Library containing Software Generation System (SGS) and an Integrated Development Environment (IDE)
DSP1617-EVAL	DSP1617 Evaluation Board
DSP1618-ST-XXXX	Software Library containing Software Generation System (SGS) and an Integrated Development Environment (IDE)
DSP1618-EVAL	DSP1618 Evaluation Board
FlashDSP 1618X-KIT	<i>FlashDSP</i> 1618 KIT. Program and Erase Flash Memory on <i>FlashDSP</i> 1618 through JTAG/HDS Port

DSP16A and DSP161X Development Tools - Product Matrix (continued)

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

Fixed-Point DSP Products – Product Matrix

Part Number	Description	Package	Speed	Temperature Range	Literature
Consumer DSP F	Products				
DSP1603	Development Device for DSP1604/06 with on-board Flash ROM and Dual-Port RAM, 3.3 V and 5 V	80-pin MQFP 84-pin PLCC 100-pin TQFP	30 ns	0 °C to +70 °C	DS
DSP1604/06	Low-Cost DSP with on-board ROM and RAM, 3.3 V & 5 V, and IOP Ports	80-pin MQFP 84-pin PLCC 100-pin TQFP	30 ns	0 °C to +70 °C	DS
DSP1605	Low-Cost DSP with on-board ROM and RAM, 3.3 V and 5 V, and HIF	80-pin MQFP 68-pin PLCC	30 ns	0 °C to +70 °C	DS
DSP1605F	Development Device for DSP1605 with Flash ROM and on-board RAM, 3.3 V and 5 V	80-pin MQFP 68-pin PLCC	30 ns	0 °C to +70 °C	DS
Enhanced Telepl	none Answering Devices				
LC30	Low-Cost, High-Performance TAD with ARAM Support using the DSP1605	68-pin PLCC 80-pin QFP	30 ns	0 °C to +70 °C	PN
LD30	Low-Cost, High-Performance TAD with ARAM Support using the DSP1605 with Echo Cancelling Speakerphone	68-pin PLCC 80-pin QFP	30 ns	0 °C to +70 °C	PN
LE30	Low-Cost, High-Performance TAD with NOR Flash Support (FTAD) using the DSP1605	68-pin PLCC 80-pin MQFP	30 ns	0 °C to +70 °C	PN

Fixed-Point DSP Products – Product Matrix (continued)

Part Number	Description	Package	Speed	Temperature Range	Literature
Enhanced Telep	hone Answering Devices (continued)			
LJ30	Low-Cost, High-Performance TAD with ANAND Flash Support (FTAD) using the DSP1605	68-pin PLCC 80-pin MQFP	30 ns	0 °C to +70 °C	PN
LH30	Low-Cost, High-Performance TAD with ANAND Support using the DSP1605 with Echo Cancelling Speakerphone	68-pin PLCC 80-pin QFP	30 ns	0 °C to +70 °C	PN
LF30	Low-Cost, High-Performance TAD with ANAND Support using the DSP1605 with Acoustic Echo Cancelling Speakerphone	68-pın PLCC 80-pin QFP	30 ns	0 °C to +70 °C	PN

Consumer Development Tools – Product Matrix

Part Number	Description
DSP160X-ST-MSDOS	Software Library containing Software Generation System (SGS) and an Integrated Development Environment (IDE) that supports 1602, 1605, 1606 (includes 1603F, 1605F, 1604) devices
DSP1603-FP0-POD	In Circuit Emulation Board that supports Pin 0 option
DSP1603-FP1-POD	In Circuit Emulation Board that supports Pin 1 option
FLASHDSP1603-KIT	FlashDSP1600-HDS, DSP160X-DEV, DSP160X-ST-MSD
DSP160X-DEV	Flash Target System supporting all 1604/06 devices
DSP1605-DEV-2	Flash Target System supporting all 1605 devices
DSP1605-UPDATE	2 FDMs (MQFP & PLCC) which give the DSP160X-DEV board 1605 functionality
DSP160X-TAD	5 TAD Application-Specific Modules
DSP160X-ST-MSDOS	DSP160X Software Tools
FLASHDSP1600-HDS	Flash DSP1600 Hardware Development System
LC30, LJ30, or LE30 Evaluation Board	Low-Cost Platform for Evaluating and Developing Products Based on the ETAD; Stand-alone Digital Answering Machine
LD30, LH30, or LF30	Low-Cost Platform for Evaluating and Developing Products Based on the ETAD-ECS;
Evaluation Board	Fully Operational Digital Answering Machine with Integrated Echo Cancelling Speakerphone or Acoutstic Echo Cancelling Speakerphone
SWPCUP	<i>Windows</i> -based Development Tool for TAD Host Interface development using a PC's serial port

Codecs

Part Number	Description	Package Type	Temp. Ranges	Literature
T7582	Baseband Codec for Digital Cellular Applications	44-pin PLCC	-40 °C to +85 °C	DS
CSP1027	Voiceband Codec for Cellular Handset and Modem Applications	44-pin EIAJ QFP 48-pin TQFP	-40 °C to +120 °C	DS
CSP1084	Baseband Radio Interface for IS-54 Dual-Mode Cellular Telephone Applications	80-pin EIAJ QFP 100-pin TQFP	-40 °C to +85 °C	DS

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

DIGITAL SIGNAL PROCESSORS

Modem Products

In an era that demands greater amounts of real-time voice, data, and FAX over phone lines and wireless communications channels, AT&T Microelectronics responds with a range of solutions for modem applications.

At the heart of every modem is a modem chip set. At the highest level there are two types of modem chip sets. They are data pump chip sets and complete modem chip sets.

Data Pump Chip Sets

A data pump chip set is the portion of the modem that pumps bits of data onto a telephone line or a wireless (cellular) communication channel. Data pump chip sets are for customers who have their own controller hardware and software. There are three form factors in the family of data pump chip sets:

- Desktop data pump chip sets
- Laptop data pump chip sets

• PCMCIA data pump chip sets Desktop data pump chip sets are low-cost solutions ideal for applications such as stand-alone modems. desktop PC plug-in modems, and integral desktop PC modems. The laptop data pump chip sets are intended for low-power applications and/or applications where space saving is essential; examples include notebook and laptop PCs and pocket modems. Most of today's laptop and desktop modem designs use components in Bumper Ouad Flat Packs (BOFP), formerly known as Plastic Ouad Flat Pack (POFP). The PCMCIA data pump chip sets require even less board space than the Laptop versions, and are thin enough to allow double-sided placement on Type II PCMCIA plugin cards.

Table 1 shows the AT&T modem data pump chip sets that are available. The table includes the chip set name along with the highest speed it can operate at, and the packages and devices that constitute the chip set. For example, the HSM288PD+D is the name for a DSVD data pump chip set in a PCMCIA form factor It can operate at 28.8 Kbits/s, has a DSP in a TOFP package called the 16345-AD, an interface chip in a 100TOFP called the VALV 34, an audio codec in a 100TOFP called the CSP1635, and an optional device which is an optical DAA in a 14-pin SSOP called the 2560ABL.

Table 2 offers a more in-depth view of the features available in each product family. For instance, the HSM288xD supports all features on the list except 3.3 V, Cellular, DSVD, and PLCC packaging.

Chip Set	Highest					Audio	Optional	
Name*	Speeds	Standard Features	DSP	Interface	Codec	Codec	Device	Comments
Desktop								
HSM192DD	19,200	V.32 bis - MJ Data/FAX	84 PLCC	68 PLCC	28 SOJ			
			16A32-MJ	V.32 Intfc.	T7525			
HSM288DD	28,800	V.34 - AC Data/FAX	100 BQFP	84 BQFP				
			16345-AC	VALV 34				
HSM288DD+D	28,800	V.34 – AD Data/FAX/	100 BQFP	84 BQFP		100 BQFF)	Catamaran™
		DSVD	16345-AD	VALV 34		CSP 1635		Data Pump
Laptop								
HSM192LD	19,200	V.32 bis - MJ Data/FAX	84 BQFP	84 BQFP	44 MQFP			
			16A32-MJ	V.32 Intfc.	CSP1027			
HSM192LD+3	19,200	V.32 bis - MJ Data/FAX	84 BQFP	84 BQFP	44 MQFP			3.3 V chip set
			16A32-MJ	V.32 Intfc.	CSP1027			
HSM192LD+C	19,200	V.32 bis – MJ Data/	84 BQFP	84 BQFP	44 MQFP			+C = Cellular
		FAX/Cellular	16A32-MJ	V.32 Intfc.	CSP1027			
HSM192LD+C3	19,200	V.32 bis – MJ Data/	84 BQFP	84 BQFP	44 MQFP			+C = Cellular
		FAX/Cellular	16A32-MJ	V.32 Intfc.	CSP1027			3.3 V chip set

Table 1. 16-bit Fixed Point High-Speed Modem – Data Pump Chip Sets

*Chip Set Name – The letters D, L, P refer to the package option: D = desktop, L = laptop, P = PCMCIA

Note: Part numbers are not complete, please contact customer service for complete part numbers when placing orders.

Chip Set	Highest					Audio	Optional	
Name*	Speeds	Standard Features	DSP	Interface	Codec	Codec	Device	Comments
PCMCIA								
HSM192PD	19,200	V.32 bis - MJ Data/FAX	100 TQFP	100 TQFP	48 TQFP			
			16A32-MJ	V.32 Intfc.	CSP1027			
HSM192PD+3	19,200	V.32 bis - MJ Data/FAX	100 TQFP	100 TQFP	48 TQFP			3.3 V chip set
			16A32-MJ	V.32 Intfc.	CSP1027			
HSM192PD+C	19,200	V.32 bis – MJ Data/	100 TQFP	100 TQFP	48 TQFP			+C = Cellular
		FAX/Cellular	16A32-MJ	V.32 Intfc.	CSP1027			
HSM192PD+C3	19,200	V.32 bis – MJ Data/	100 TQFP	100 TQFP	48 TQFP			+C = Cellular
		FAX/Cellular	16A32-MJ	V.32 Intfc	CSP1027			3.3 V chip set
HSM288PD	28,800	V 34 – AC Data/FAX	100 TQFP	100 TQFP			14SSOP	
			16345-AC	VALV 34			2560ABL	
HSM288PD+C	28,800	V.34 - AC Data/FAX/	100 TQFP	100 TQFP			14SSOP	+C = Cellular
		Cellular	16345-AC	VALV 34			2560ABL	
HSM288PD+D	28,800	V.34 - AD Data/FAX/	100 TQFP	100 TQFP		100 TQFI	P14SSOP	Catamaran
		DSVD	16345-AD	VALV 34		CSP1635	2560ABL	Data Pump

Table 1. 16-bit Fixed Point High-Speed Modem – Data Pump Chip Sets (continued)

*Chip Set Name – The letters D, L, P refer to the package option: D = desktop, L = laptop, P = PCMCIA

Note: Part numbers are not complete, please contact customer service for complete part numbers when placing orders

Table 2. Data Pump Chip Sets; Features by Product Family

			Product		
Features	HSM192xD	HSM192xD+C	HSM288xD	HSM288xD+C	HSM288xD+D
V.34 – 28.8			1	1	1
V.32 Terbo and Fallback	1	1	1	1	1
V.17 Fallback	1	1	1	1	1
Voice thru' mode (µ-Law & A-Law)	1	\checkmark	1	1	1
3.3 V chip set version available	1	1		· · · · · · · · · · · · · · · · · · ·	
Cellular		1		1	entre anno 1975 - 1975 - 1975 - 1975 - 1975 - 1975 - 1975 - 1975 - 1975 - 1975 - 1975 - 1975 - 1975 - 1975 - 19
DSVD					1
PLCC Package	x = D			· · · · · · · · · · · · · · · · · · ·	
BQFP Package	x = L	x = L	x = D		x = D
TQFP Package	x = P	x = P	x = P	x = P	x = P

DIGITAL SIGNAL PROCESSORS

Complete Modem Chip Sets

A complete modem chip set is just what it says, it is a complete modem chip set for customers who do not have their own controller hardware and software. It contains a data pump plus a microcontroller, which when working together, in addition to pumping bits of data over a telephone line or cellular channel, can also perform error correction, data compression, and AT command processing. There are two form factors in the family of complete modem chip sets, plus a special version of a complete modem chip set called the controllerless modem chip set. The family of complete chip sets consists of:

- Desktop complete modem chip sets (since this uses BQFP packaging, it satisfies the laptop needs as well)
- PCMCIA complete modem chip sets
- · Controllerless modem chip sets

Desktop complete modem chip sets are low-cost solutions for standalone modems, desktop PC plug-in modems, integral desktop PC modems, laptop and notebook PCs, and pocket modems. They are available in BQFP packages. The PCMCIA complete modem chip sets are used in Type II PCMCIA plug-in modem cards where less board space is needed; they are supplied in TQFP packages. They are being used more increasingly in PC plugin cards, laptop and notebook PCs, and pocket modems.

Table 3. 16-bit Fixed Point High-Speed Modem – Complete Modem Chip Sets

		Standard Features		
Chip Set Name*	Highest Speeds	or Description	Controller	DSP
Desktop				
HSM288DC	28,800	V.34 – AC Data/FAX	100 BQFP	100 BQFP
			C882-29Q	16345-AC
HSM288DC+Si	28,800	V.34 – AC Data/FAX/FDSP	100 BQFP	100 BQFP
			C889-29Q	16345-AC
HSM288DC+Vi	28,800	V.34 – AC TAM	100 BQFP	100 BQFP
			C882-29Q	16345-AC
HSM288DC+Se	28,800	V.34 – AC Data/FAX/FDSP	100 BQFP	100 BQFP
			C882-29Q	16345-AC
HSM288DC+Ve	28,800	V.34 – AC TAM	100 BQFP	100 BQFP
			C882-29Q	16345-AC
HSM288DC+D	28,800	V.34 – AD Data/FAX/DSVD	100 BQFP	100 BQFP
			C882-29Q	16345-AD
PCMCIA				
HSM288PC	28,800	V.34 – AC Data/FAX	100 VQFP	100 TQFP
			C882-29V	16345-AC
HSM288PC+C	28,800	V.34 – AC Data/FAX/Cellular	100 VQFP	100 TQFP
			C882-29V	16345-AC

16-bit Fixed Point High-Speed Modem – Controllerless Modem Chip Sets

	Standard Features					
Chip Set Name*	Highest Speeds	or Description	DSP16			
Desktop						
HSM192DW+S	19,200	V.32 bis – MS Data/	84 PLCC			
		FAX/FDSP	1632-MS			
HSM288DW+S	28,800	V.34 – AC Data/	100 QFP			
	,	FAX/FDSP	16345-AC			

*Chip Set Name - The letters D, L, P refer to the package option: D = desktop, L = laptop, P = PCMCIA.

Note: Part numbers are not complete, please contact customer service for complete part numbers when placing orders.

Included in the family of complete modem chip sets are the controllerless modem chip sets. Though they are supplied without a controller, they still use the host processor (Pentium 486, 386, etc.) of the PC along with a software driver to perform traditional Microcontroller functions. Because the controllerless modem chip sets utilize a host CPU, they allow designers to achieve the lowest cost of bill of materials and reduce the power requirements, because the need for a microcontroller chip and its RAM and ROM are eliminated.

A controllerless modem chip set is targeted for PC internal cards which utilize a *Pentium*-based windows operating system.

Table 3 shows the AT&T complete modem sets that are available. The table includes the chip set name along with the highest speed it can operate at, and the packages and devices that constitute the chip set. For example, the HSM288PC+C can operate at 28.8 Kbits/s, has a controller in a TQFP called the C882, a DSP in a TQFP package called the 16345-AD, an interface chip in a 100TQFP called the VALV 34, an audio codec in a 100TQFP called the CSP1635, and optional bus interface device in a 48TQFP called the PID2, and an optional device which is an optical DAA in a 14-pin SSOP called the 2560ABL.

Table 4 offers a more in-depth view of the features available in each product family. For instance, the HSMzzzDW (controllerless 19.2 and 28.8 Kbits/s modem chip sets) do not support ETC, autosync, DSVD and TQFP packages.

Interface	Codec	Audio Codec	Optional Bus Interface Device	Optional Device	Comments
84 BQFP					TAS (Controller)
VALV 34					
84 BQFP		28 SOJ	PNP		Half card (MTC)
VALV 34		_T7525	100 MQFP		+S = FDSP, VV, TAM
84 BQFP		28SOJ	PNP		+V = TAM
VALV 34		T7525	100 MQFP		
84 BQFP		28SOJ			Box modem,
VALV 34		T7525			+S = FDSP, VV, TAM (MTC)
84 BQFP		28SOJ			+V = TAM (MTC)
VALV 34		T7525			
84 BQFP		100 BQFP	PNP		Catamaran
VALV 34		CSP1635	100 MQFP		TAS (Controller)
			·····		
100 TQFP			48 TQFP	14SSOP	TAS (Controller)
VALV 34			PID2	2560ABL	
100 TQFP			48 TQFP	14SSOP	+C = Cellular
VALV 34			PID2	2560ABL	TAS (Controller)

		Audio	
 Interface	Codec	Codec	Comments
 68 PLCC	28 SOJ	28 SOJ	win32
V.32 Intfc.	T7525	T7525	+S = FDSP, VV, TAM (MTC)
 84 QFP		28 SOJ	win34
VALV 34		T7525	+S = FDSP, VV, TAM (MTC)

DIGITAL SIGNAL PROCESSORS

Table 4. Complete Modem Chip Sets: Features by Product Fam	ible 4. Complete Modem Chip Se	ts: Features b	v Product	Family
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	Product						
Features	HSM288xC	HSM192xC+S	HSM288CxV	HSM288xC+C	HSM288xC+D	HSMzzzDW	
V.34 - 28.8	1	1	1	1	1	1	
V.32 Terbo and Fallback	1	1	1	1	1	1	
V.42 Bis	1	1	1	1	1	1	
ETC TM			ana '' ' mananany'	1			
FAX Class 1	1	1	1	1	1	1	
FAX Class 2	1	1	1	1	1	1	
Auto Sync							
ТАМ		1	1			1	
FDSP		1				1	
DSVD							
VoiceView		1		randalos, se conderense e conserva-	1	1	
Serial (external)	1	1	1				
Parallel (internal)	1	1	1		1	1	
PCMCIA	1			1	1		
PLCC Package				a Parada ang ang ang ang ang ang ang ang ang an		zzz = 192	
BQFP Package	x = D	x = D	x = D		x = D	zzz = 288	
TOFP Package	$\mathbf{x} = \mathbf{P}$	-		x = P			

WIRELESS RF PRODUCTS

Wireless RF Products

AT&T Microelectronics RF products are developed specifically for digital cellular and digital cordless applications, according to standards generated in the geographic region in which the application is primarily targeted. The following applications are presently being targeted:

- Global System for Mobile, with a GSM standard, for Europe.
- Personal Digital Cellular, with a RCR-27 (PDC) standard, for Japan.

- American Digital Cellular, with a IS136 (TDMA) standard, for the U.S.A.
- Japan Digital Cordless (Personal Handyphone), with a RCR-28 (PHS) standard, for Japan.

All AT&T Microelectronics RF products target the transceiver functionality of a radio system. A transceiver is comprised of three functions: receiver, modulator, and frequency synthesizer(s). A generic radio block diagram is shown below.



Product	Description	Target Application	Supply Voltage
W1452	45 MHz-86 MHz, IF Amplifier/Quadrature Demodulator	All	5 V
W2005	1 GHz, Dual-Mode Cellular Receiver	IS136	5 V
W2012	1.9 GHz, Indirect-up Quadrature Modulator	PHS	3 V
W2020	GSM Transceiver	GSM	3 V

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

DIGITAL TV ICs

MPEG-2 System-Layer Demultiplexers

AT&T's single-chip MPEG-2 System-Layer Demultiplexer (MSLD) provides a standards-based easy-to-use solution for the design of systems for MPEG-2 applications.

Applications

- Integrated receiver decoders (IRD); set-top box
 – Cable TV
 - Direct broadcast satellite (DBS)
 - Video-on-demand
 - VIGEO-OII-GEIIIai
 - Interactive TV
- Headend uplink facilities
- CD-ROM video entertainment solutions
- PC multimedia solutions
- Telephone network set-top box

AT&T's AV6220A MPEG-2 System-Layer Demultiplexer (MSLD) provides users with efficient, cost-effective MPEG-2 transport layer demultiplexing. This device complies with the MPEG-2 and DVB specifications and provides a comprehensive feature set. It demultiplexes transport stream (TS) packets into packetized elementary streams (PES), elementary streams (ES), PSI, SI, systems, and private data.

The AV6220A works seamlessly with multiple MPEG-2 video, audio, and combined video/audio decoders, as well as a variety of processors. It accepts either serial or byte wide TS inputs and may also receive TS data through the host interface for CD-ROM applications. The MSLD provides clock recovery for the system 27 MHz clock and assists in the synchronization of video and audio streams.

The AV6220A is available in a lowcost, 160-pin plastic metric quad flat pack (MQFP) and uses advanced $0.55 \ \mu m$, 5.0 V, CMOS Technology that provides low power consumption of less than 1 W.

Features

- DVB, MPEG-2 standard compliant - 32 PIDs and 8 service information filters
 - -Compatible with DVB and other conditional access interfaces
- High performance
 - -Supports high transport rate of 96 Mbits/s
 - Allows flexible algorithm and system configuration
 - Off-loads the processor and allows better bandwidth
- Cost-effective solution
- -Eliminates the need for microcode development
- Achieves small chip area and low power
- Allows easy configuration by users

Part Number	Description	Package Type	Temp. Ranges	Literature
AV6220A	MPEG-2 System-Layer Demultiplexer	160-pin PQFP	0 °C to +70 °C	PN,DS

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

CMOS STANDARD-CELL ASICs

AT&T has an uncommon mix of products, technology, and support to provide powerful, cost-effective ASIC solutions in silicon. We've extended the possibilities of ASICs with higher densities, increased speed capabilities, and lower power consumption. Our rich selection of libraries and design tools make design easier, faster, and more reliable. Our ASIC libraries are comprehensive, allowing you to optimize your design and provide efficient, costeffective functionality.

Design Flexibility and Library Richness

AT&T's product focus provides you with a rich set of library elements that range from simple logic functions to complex digital and analog macrocells. AT&T's libraries are optimized to take advantage of today's sophisticated design and test methodologies, such as behavioral modeling/synthesis, and full and partial scan insertion. AT&T provides ASIC design kits for many popular commercial CAD platform options including: Mentor Graphics, Verilog, Viewlogic, IKOS, Synopsys, Quad Motive, HSPICE, Sunrise, and ZyCAD.

We have a broad selection of flipflops, registers, and adders, as well as full memory compilers. Our macrocells offer you system-level integration of microprocessors, DSPs, and peripheral and communcations controllers. These building blocks can dramatically slash design time by giving you ready-made solutions to your specific needs. Our extensive expertise in mixed analog/digital designs makes our ASICs the ideal solution for data recovery, data acquisition, and clock skew/synchronization.

Today's performance-driven designs demand both high speeds and low power consumption—without the tradeoffs associated with achieving one at the expense of the other. AT&T offers high performance in 2.5 V, 3 V, and 5 V libraries to meet these demands.

Integrated Capability

AT&T is your source for global ASIC solutions. We have design centers and manufacturing facilities located worldwide. This gives you a full multiple-source approach for flexible manufacture from a single vendor. And because we are an integrated manufacturer from silicon material through wafer fabrication and final device assembly/test, we maintain full and rigorous control over every step of the process. With AT&T, you have a clear path from concept to production and from today's state of the art to tomorrow's.

0.35 μ m CMOS Standard-Cell ASIC Libraries

ASIC Libraries	HL350C	HS350C	LV350C ¹			
Process Technology	0.35 μm	0.35 µm	0.35 µm			
	3 V CMOS	3 V CMOS	3 V CMOS			
Operating Voltages	2.7 V—3.6 V	4.5 V—5.5 V	2.3 V—2.7 V			
Leff	0.32 μm	0.46 µm	0.32 μm			
TOX	65Å	115 Å	50 Å			
Metal Interconnect	3-4 ²	3-42	3-42			
Levels						
Usable Gates	2.5 M	2.5 M	2.5 M			
Compilable Memory						
SRAM	1 Mbit	1 Mbit	1 Mbit			
ROM	4 Mbits	4 Mbits	4 Mbits			
Gate Speed (Internal, Typical, Unloaded)	54 ps	78 ps	59 ps			
Max. Toggle Rate	940 MHz	755 MHz	850 MHz			
ASIC & EDA						
Benchmark ³						
Benchmark #1	5.00 ns	5.78 ns	5.50 ns			
(Path Delay)						
Benchmark #2	860 MHz	755 MHz	775 MHz			
(Counter Max. Freq.)						
Power (µW/MHz/Gate,	0.8	1.7	0.5			
FO = 1)						
Buffer Drive	40 mA	40 mA	40 mA			
I/O Interfaces	TTL/CMOS	TTL/CMOS	TTL/CMOS			
	3 V/5 V ⁴	3 V/5 V ⁵	2.5 V/3 V ⁶			
	PCI	PCI	PCI			
	SCSI	SCSI	SCSI			
	PECL	PECL	PECL			
	Bal. CMOS	Bal. CMOS	Bal. CMOS			
	GTL/HSTL/LVDS	GTL/HSTL/LVDS	GTL/HSTL/LVDS			
I/O Capability						
QFP	304	304	304			
E-PBGA	640	640	640			
Flip-chip PBGA	>700	>700	>700			
Pad Pitch	<3 mil	<3 mil	<3 mil			
Planned 1996 introduction, preliminary information.						

2. Flip-chip, DRAM applications.

3. Worst-case slow process, temperature, voltage

4 3 V technology with 5 V tolerant I/O capability.

5 5 V technology with 3 V tolerant I/O capability.

6. 2.5 V technology with 3 V tolerant I/O capability.

CMOS STANDARD-CELL ASICs

ASIC Libraties	HL400C	HS500C HS500P ²	H\$600C	I P600C	HSOOOC
Process Technology	0.5 μm CMOS	0.5 μm CMOS	0.6 µm CMOS	0.6 µm СМОS	0.9 μm CMOS
Operating Voltages	2.7 V—3.6 V	4.5 V—5.5 V	2.7 V—5.5 V	4.5 V—5.5 V	2.7 V—5.5 V
Metal Interconnect Levels	3	3	2	2	2
Total Gates	>500K	>500K	150K	>150K	150K
Gate Speed (Internal, Typical, Unloaded)	90 ps	90 ps	120 ps	180 ps	150 ps
Max. Toggle Rate	600 MHz	650 MHz	470 MHz	285 MHz	350 MHz
ASIC & EDA 1994 Benchmark ² Benchmark #1	6.34 ns	6.11 ns	9.27 ns	15.38 ns	12.41 ns
(Path Delay) Benchmark #2 (Counter Max. Freq.)	580 MHz	650 MHz	470 MHz	260 MHz	350 MHz
Power (µW/MHz/Gate, FO = 1)	0.8	1.7	3.5	1.5	3.3
Buffer Drive	40 mA	64 mA	64 mA	64 mA	64 mA
I/O Capability	TTL/CMOS 3 V/5 V ³ PCI SCSI PECL Bal. CMOS GTL/HSTL	TTL/CMOS 5 V PCI SCSI PECL Bal. CMOS	TTL/CMOS 3 V/5 V PCI SCSI PECL Bal. CMOS GTL	TTL/CMOS 3 V/5 V PCI SCSI Bal. CMOS	TTL/CMOS 3 V/5 V PCI SCSI PECL Bal. CMOS
Pad Pitch	<4 mil	<4 mil	4—5 mil	4—5 mil	5 mil
Compilable Memory SRAM ROM	700 kbits 2.8 Mbits	700 kbits 2.8 Mbits	250 kbits 1 Mbit	250 kbits 1 Mbit	250 kbits 1 Mbit

1 Metal programmable option.

Worst-case slow process, temperature, voltage.
 3 V technology with 5 V tolerant I/O capability.

Name	Description
Digital ASIC Ma	acrocells
196KC/KB	16-bit Microcontroller
80C31/32/51/52	8-bit Microcontroller
960JX	Embedded 32-bit Risc Processor
Z80	8-bit Microprocessor
C10/15	16-bit Fixed-point DSP
C25	16-bit Fixed-point DSP
C2XLP	16-bit Fixed-point DSP
C5X	16-bit Fixed-point DSP
16C450	Universal Asynchronous Receiver/Transmitter (UART)
16C550A	Universal Asynchronous Receiver/Transmitter (UART)
85C30	Serial Communications Controller
53C94/95/96	SCSI Bus Controller
61602*	LCD Controller
83C90	AT&T Controller for Ethernet (ACE)
74LS612	Memory Mapper
82077*	Floppy Disk Controller
82365	PCMCIA Host Controller
146818A	Real-time Clock
82C37A	Programmable DMA Controller
82C54	Programmable Interval Timer
82C55A	Programmable Peripheral Interface
82C59A	Programmable Interrupt Controller
7186*	Video Scaler
PCIU	PCMCIA Card Interface Unit
PCI	Bus Interface
MPEG-1*	Video/Audio Decoder
T7901*	Single-Port ISDN Transceiver
Analog ASIC M	acrocells
FADC [5:8]	Flash Analog-to-Digital Converters (ADC) with Resolution of 5 to 8 Bits
F2ADC8	2-Step Flash ADC with 8-bit Resolution
SAR8	Successive Approximation ADC with 8-bit Resolution
SAR10	Successive Approximation ADC with 10-bit Resolution
SAR12*	Successive Approximation ADC with 12-bit Resolution
RDAC [5:8]	Resistor Ladder Digital-to-Analog Converters (ADC) with Resolution of 5 to 8 Bits
RDAC10	10-bit Resistor Ladder DAC
RDAC12*	12-bit Resistor Ladder DAC
IDAC8	8-bit Current (Video) DAC
IDAC10*	10-bit Current (Video) DAC
PLL	Phase-locked Loops with Output Frequencies to 1X, 2X, and 4X the Input Frequency
FREQSYNTH	Frequency Synthesizers
* Planned develop	oment.

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ATT3000 Series Field-Programmable Gate Arrays (FPGAs)

Description

The high-speed ATT3000 Series of FPGAs provides the benefits of high-speed, high-density, digital logic while avoiding the NRE, time delay, and risk of traditional masked gate arrays. The series is pin-for-pin and specification compatible with the *Xilinx XC3100* family and the *Xilinx XC3000* family.

The ATT3000 Series FPGAs are supported by the *ORCA*[™] Foundry Development System, which provides automatic place-and-route of netlists from user-created schematics or text-based design entry tools.

The ATT3000 regular, flexible, reprogrammable array architecture is composed of a configuration program store with three types of configurable or programmable elements: a perimeter of input/output blocks (IOBs), a core array of configurable logic blocks (CLBs), and routing resources for interconnection.

The CLB implements logic functions by using programmed look-up tables (LUTs). Functional options are implemented by user-programmable multiplexers. Interconnecting networks are implemented with metal routing lines joined by userprogrammable pass transistors. Registers or flip-flops are found in CLBs and IOBs.

The devices are customized by a configuration program stored in internal SRAM-based memory cells. The program data resides externally from the FPGAs in an EEPROM, EPROM, or ROM on the circuit board, or on a floppy or hard disk.

Features

High performance:
 Up to 270 MHz toggle rates
 -4-input LUT delays < 3 ns

- Flexible array architecture:
 - -2000 to 9000 gate logic complexity
 - -Extensive register and I/O capabilities
 - -Low-skew clock nets
 - -High fan-out signal distribution
 - -Internal 3-state bus capabilities
 - -TTL or CMOS input thresholds
 - -On-chip oscillator amplifier
- Standard product availability: – Low-power 0.6 µm CMOS technology
 - Pin-for-pin compatible with *Xilinx XC3000* and *XC3100* family
 - Cost-effective, high-speed FPGAs
 - -100% factory pretested
 - Selectable configuration modes
- ORCA Foundry for ATT3000 Development System support
- All FPGAs processed on a QMLcertified line

ATT3000 Series FPGAs

	Logic Capacity	Configurable	User	Program Data
FPGA	(Available Gates)	Logic Blocks	I/Os	(Bits)
ATT3020	2000	64	64	14779
ATT3030	3000	100	80	22176
ATT3042	4200	144	96	30784
ATT3064	6400	224	120	46064
ATT3090	9000	320	144	64160

Comparison Table

	Equivalent		LUT Delay	Toggle Rate	
Series	Xilinx Family	Speed Grade	(ns)	(MHz)	
ATT3000	XC3100	-3	2.7	270	
		-4	3.3	230	
		-5	4.1	190	
ATT3000	XC3000	-125	5.5	125	
		-100	7.0	100	
		-70	9.0	70	

	44-pin	68-pin	84-pin	100)-pin	132-pin	144-pin	160-pin	175-pin	208-pin	
Device	PLCC	PLCC	PLCC	QFP	TQFP	Plast. PGA	TQFP	QFP	Plast. PGA	Plast. SQFP	¥ 14
& Speed	M44	MOS	M84	J100	1100	H152	1144	J100	H1/5	Q208	LII.
ATT3020 -70, -100, -125 -5 -4 -3		CI CI C	CI CI C	CI CI C							DS DS DS
ATT3030				Ŭ							
-70, -100, -125 -5 -4, -3	CI CI C	CI CI C	CI CI C	CI CI C	CI CI C						DS DS DS
ATT3042 -70, -100, -125 -5 -4, -3			CI CI C	CI CI C	CI CI C	CI CI C	CI CI C				DS DS DS
ATT3064 -70, -100, -125 -5 -4, -3			CI CI C		CI CI C	CI CI C	CI CI C	CI CI C			DS DS DS
ATT3090 -70, -100, -125 -5	_		CI CI			_		CI CI	CI CI	CI CI	DS DS
-4, -3			С		_			С	С	С	DS

ATT3000 Series FPGA Device Matrix

Key: C = commercial temperature option, I = industrial temperature option.

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

AT&T Optimized Reconfigurable Cell Array (*ORCA*) Series FPGAs

Description

The AT&T Optimized Reconfigurable Cell Array (*ORCA*) series of SRAM-based fieldprogrammable gate arrays (FPGAs) from AT&T Microelectronics provides a family of high-performance, high-density, low-power, user-programmable logic circuits.

The ORCA architecture is comprised of two major programmable blocks: programmable I/O cells (PICs) and programmable logic cells (PICs) organized in a homogeneous array structure. These programmable cells are interconnected by abundant routing resources, which are placed symmetrically within the device.

Programmable Logic Cells

A PLC consists of a programmable function unit (PFU) and programmable routing resources. The PFU has a look-up table (LUT) section and a latch section. The 64-bit (four $16 \ge 1$) LUT is used for the combinatorial logic of a design. The four latches implement the sequential logic in a design. These latches can be programmed to be active on either level, or they can be used as flip-flops.

The LUTs can also be programmed to operate in one of three modes: combinatorial, ripple, or memory. In combinatorial mode, the LUTs can be programmed to realize any 4-, 5-, or 6-input logic functions. In ripple mode, the high-speed carry logic is used for arithmetic circuits. In memory mode, the LUTs can be used as two 16 x 2 or a 16 x 4 read/write or read-only memory. The programmable routing resources within each PLC are made from metal segments called routing nodes (Rnodes) connected together at configurable interconnect points (CIPs) to form user-defined nets.

Programmable I/O Cells

PICs are located along the perimeter of the device. Each PIC is comprised of I/O drivers, I/O pads, and routing resources. Each PIC can be programmed to be an input, output, or both; to have either TTL or CMOS input thresholds; or to have the input signal delayed. Other options include variable output slew rates; output current drive capabilities; 3-state output (either active-ligh or activelow); inverting the output, if desired; and/or floating (unused) pins using pull-up or pull-down resistors.

FIELD-PROGRAMMABLE GATE ARRAYS

Features

- High-performance, cost-effective 0.5 µm technology (4-input look-up table delay less than 3.6 ns)
- High density (up to 40,000 usable gates)
- Up to 480 user I/Os
- Fast on-chip user SRAM; 64 bits/ logic block
- Nibble-oriented architecture for implementing 4-, 8-, 16-, 32-bit (or wider) bus structures

ORCA Series FPGAs – Product Matrix

- Innovative, abundant, and hierarchical nibble-oriented routing resources that allow automatic use of internal gates for all device densities without sacrificing performance
- Four 16-bit look-up tables and four latches/flip-flops per PLC
- Internal fast carry for arithmetic functions
- TTL or CMOS input thresholds programmable per pin
- Individually programmable drive capability: 12 mA sink/6 mA source or 6 mA sink/3 mA source

- Built-in boundary scan (*IEEE* 1149.1)
- Low power consumption from submicron CMOS process
- Full PCI-bus compliance
- Supported by industry-standard CAE tools for design entry, synthesis, and simulation
- ORCA Foundry Development System

			Max User			
Part Number	Usable Gates	Registers	RAM Bits	User I/Os	Array Size	Literature
ATT2C04	3,500-4,300	400	6,400	160	10 x 10	DS, MN
ATT2C06	5,000-6,200	576	9,216	192	12 x 12	DS, MN
ATT2C08	7,000—8,800	784	12,544	224	14 x 14	DS, MN
ATT2C10	9,000—11,400	1024	16,384	256	16 x 16	DS, MN
ATT2C12	12,000—14,600	1296	20,736	288	18 x 18	DS, MN
ATT2C15	15,000—18,000	1600	25,600	320	20 x 20	DS, MN
ATT2C26	22,000—26,000	2304	36,864	384	24 x 24	DS, MN
ATT2C40	35,000-40,000	3600	57,600	480	30 x 30	DS, MN

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

ORCA Series FPGA – Product Matrix

	84-pin	100-pin	144-pin	160-pin	208-pin	240-pin	256-Pin	304-pin	364-pin	428-pin	
	PLCC	TQFP	TQFP	QFP	EIĄJ SQFP SQFP-PQ2	EIĄJ SQFP SQFP-PQ2	Ball Grid Array	EIAJ SQFP SQFP-PQ2	Cer. PGA	Cer. PGA	
Device	M8 4	T100	T144	J160	S208 PS208	S240 PS240	B25 6	\$304 P\$304	R364	R429	Lit.
ATT2C04	CI	CI	CI	CI	CI			—			DS
ATT2C06	CI	CI	CI	CI	CI	CI					DS
ATT2C08	CI			CI	CI	CI	CI	CI	—		DS
ATT2C10	CI			CI	CI	CI	CI	CI			DS
ATT2C12	_				CI	CI	CI	CI	CI		DS
ATT2C15					CI	CI		CI	CI	—	DS
ATT2C26	-				CI	CI	—	CI		CI	DS
ATT2C40					CI	CI		CI		CI	DS

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

Key: C = commercial, I = industrial.

Note: The package options with the SQFP/SQFP-PQ2 designation in the table above, use the SQFP package for all densities up to and including the ATT2C15, while the ATT2C26 and ATT2C40 use the SQFP-PQ2 package.

Optimized Reconfigurable Cell Array (*ORCA*) ATT2T15 (3.3 V) Field-Programmable Gate Array

The ATT2T15 is the first device in this family optimized to provide logic solutions in 3.3 V systems. This device contains approximately 15,000—18,000 usable gates and is offered in a variety of packages, speed grades, and temperature ranges.

The ORCA series FPGA consists of two basic elements: programmable logic cells (PLCs) and programmable input/output cells (PICs). An array of programmable logic cells (PLCs) is surrounded by programmable input/output cells (PICs). Each PLC contains a programmable function unit (PFU). The PLCs and PICs also contain routing resources and configuration RAM. All logic is done in the PFU. Each PFU contains four 16bit look-up tables (LUTs) and four latches/flip-flops (FFs).

The LUTs can be programmed to operate in one of three modes: combinatorial, ripple, or memory. In combinatorial mode, the LUTs can be programmed to realize any 4, 5, or 6 input logic functions. In ripple mode, the high-speed carry logic is used for arithmetic functions. In memory mode, the LUTs can be used as a 16 x 4 read/write or readonly memory. The PLC architecture provides a balanced mix of logic and routing that allows a higher utilized gate/PFU than alternative architectures. The routing resources carry logic signals between PFUs and I/O pads. The routing in the PLC is symmetrical about the horizontal and vertical axes. This improves routability by allowing a signal to be routed into the PLC from any direction.

Each PIC is comprised of I/O drivers, I/O pads, and routing resources. Each I/O can be programmed to be either an input, output, or bidirectional signal. The ATT2T15 is also capable of interfacing to 5 V devices because the I/O pads can be driven by signals of up to 6 V. Other options include variable output slew rates and pull-up or pull-down resistors.

The ORCA Foundry Development System is used to process a design from a netlist to a configured FPGA. AT&T provides interfaces and libraries to popular CAE tools for design entry and simulation.

The FPGA's functionality is determined by internal configuration RAM. The FPGA's internal initialization/configuration circuitry loads the configuration data at powerup or under system control. The RAM is loaded by using one of several configuration modes. The configuration data resides externally in an EEPROM, EPROM, or ROM on the circuit board, or any other storage media. Serial ROMs provide a simple, low pin count method for configuring FPGAs.

Features

- High-performance, cost-effective 0.5 µm technology optimized for 3.3 V operation
- 5 V-tolerant I/O buffers can be connected to external signals up to 6 V, allowing interconnection to both 3.3 V and 5 V devices (selectable on a per-pin basis)
- High density (15,000 usable gates)
- 308 user I/Os (all 5 V-tolerant)
- 1600 latches/flip-flops
- Maximum 25,600 user RAM bits
- Fast on-chip user SRAM: 64 bits/ logic block
- Nibble-oriented architecture for implementing 4-, 8-, 16-, 32-bit, or wider bus structures
- Innovative, abundant, and hierarchical nibble-oriented routing resources that allow automatic use of internal gates for all device densities without sacrificing performance
- Four 16-bit look-up tables and four latches/flip-flops per logic block
- Internal fast carry for arithmetic functions
- Individually programmable drive capability: 12 mA sink/6 mA source or 6 mA sink/3 mA source
- Built-in boundary scan (*IEEE* 1149.1)
- Low power consumption from submicron CMOS process optimized for 3.3 V operation
- Architecture-compatible to the ORCA 2C series of 5 V devices
- Supported by industry-standard CAE tools for design entry, synthesis, and simulation
- ORCA Foundry Development System support

ORCA Foundry Development System

FPGA devices are growing in size and complexity—straining the capabilities of both designers and earlygeneration tool sets. High-performance tools are critical to realizing the full potential of today's larger, more complicated devices. Such tools not only significantly shorten your design cycles, but also produce chip designs with higher device utilization and faster operating frequencies. *ORCA* Foundry is such a tool set.

Capture, Mapping, and Optimization

ORCA Foundry allows designs to be captured using device-specific libraries, vendor-independent libraries, or a combination of both. No other design tool set lets you designate the specific design capture method that best supports your requirements. As a result, vendor-independent libraries and industry-standard netlists can be easily implemented in either *ORCA* or ATT3000 devices.

ORCA Foundry's device- and architecture-specific optimization, combined with superior place and route capabilities, produces consistently high gate utilization. Of course, ORCA Foundry fully supports device-specific features, such as hard macros, RAM, and automatic routing of clocks.

With complete back-annotation, incremental mapping, and the ability to preserve hierarchy throughout the design process, *ORCA* Foundry gives you as much help in updating and debugging your design as it does in implementing it.

Advanced Place and Route Capabilities (PAR)

Using the most powerful combination of algorithms available, *ORCA* Foundry's place and route (PAR) program consistently completes designs with the fewest iterations and with no manual intervention. PAR's fast execution time and built-in incremental change capability result in the shortest possible design cycle.

With the addition of *ORCA* Foundry's AT&T *Timing Wizard* module, designers can specify frequency and timing requirements up front. AT&T *Timing Wizard* then drives PAR to meet those requirements, delivering higher-performance devices with the fastest possible operating frequencies while shortening design cycles even further.

Powerful Interactive Layout Editor (EPIC)

The AT&T Editor for Programmable ICs (AT&T EPIC) is a powerful, interactive layout editor found in ORCA Foundry that streamlines the debugging and tuning of FPGA designs. AT&T EPIC's easy-to-use graphical interface provides a choice of push button, menu-driven, or command-line editing capabilities that can be customized to suit any set of requirements. In addition, AT&T EPIC has been tuned to guarantee the fastest graphics response, eliminating the unproductive waiting while a large design is panning. zooming, or simply highlighting a net.

Many advanced features have been designed into AT&T EPIC to make working with complex devices easier. Among these are manual placement and routing, auto placement, auto routing, and integration of ORCA Foundry's powerful timing analyzer. AT&T EPIC's on-line design rule checks (DRC) can be used in logical mode (allowing changes to placement and routing, but preventing any changes to the logic during the editing session) or in physical mode (allowing logic and signals to be added and deleted while guaranteeing that changes are valid within the physical constraints of the specifiied FPGA).



ORCA Foundry's powerful editing and debugging environment, AT&T EPIC, also features tracking of hierarchical design data...

FPGA-Specific Timing Analyzer (TRACE)

AT&T TRACE provides complete analysis of a circuit's timing characteristics. Using actual component and interconnect delays, AT&T TRACE exhaustively examines every signal path and automatically evaluates the circuit for set upand hold violations, race conditions, and adherence to specified timing preferences.

AT&T TRACE runs its analysis using user-specified timing preferences (such as desired operating frequency) and feeds back detailed results that identify specifically where the design fails to meet those requirments, thereby eliminating the need to read through reams of paper to pinpoint potential timing problems.



ORCA Foundry's capabilities enable a designer to use all device-specific features.

-	Program Manager	•
File Options Window	4 Helb	
-	ORCA Foundry	
	Par Shell Foundry 7 1	•
Operation P	lace, Optimize Placement, Then Route	
-	Placement Options Router Options	
1	Disable Timing Driven	
Input Design	top ncd	Browse
Output Design	top dir	Browse
Guide Demign		Browse
Proference File	top prt	Browse
Options File	pareh opt	Browse
	Load Save Auto Save	
Ok Concel He	stp.	

Using industry standards, *ORCA* Foundry allows a designer to take full advantage of powerful *Windows* applications.

Supports Industry-Standard Platforms

PC-Based:

- *IBM* PC or compatible 486SX, 486, or *Pentium*
- *MS-DO*S 5.0 (or higher)
- Microsoft Windows 3.1 (or higher)
- RAM: 16 Mbytes minimum
- Disk: 30 Mbytes for first family, 10 Mbytes for each additional family
- Swap: 5 Mbytes permanent *Microsoft Windows* swap file
- Color VGA
- 2- or 3-button *Microsoft Windows*compatible mouse
- One parallel port for security device

Workstation-Based:

- Sun SPARCstation compatible running SunOS 4.1.3 (or higher) or running Solaris 5.3 (or higher)
- *HP* 9000 Series 400/700 running *HP*-UX 9.0.3 (or higher)
- *X-Windows* version X11R4 (or higher) and *OSF/MOTIF* 1.1
- RAM: 32 Mbytes
- Disk: 45 Mbytes for first family, 10 Mbytes for each additional family
- Swap: 32 Mbytes
- Color monitor
- 3-button mouse
- One serial port for security device

Features

- Complete, fully integrated tool set
- Supports ORCA 1C, 2C, 2T, and ATT3000 Series FPGAs
- Integrates into existing CAE environments
- True timing- and frequency-driven design
- Performs device-specific optimization and technology mapping
- Performs both automatic and manual place and route

- Performs static timing analysis
- Allows for back-annotated timing simulation

ORCA Foundry Benefits

- Automatic completion of difficult designs
- Maximum device utilization
- · Faster clock speeds
- Ease of use means fast time-tomarket benefits



Figure 1. ORCA Foundry Environment

FIELD-PROGRAMMABLE GATE ARRAYS

Development Systems for ORCA Series FPGAs

Part Number	Version Description				
ORCA Foundry Software PC Solutions:	Licenses				
ATT-ORCAVISTA-PC	7.1	Low-density starter system for AT&T FPGAs. Supports ATT3000, ATT1Cxx, ATT2C04, ATT2C06, ATT2C08, and ATT2C10 devices. Includes AT&T <i>Timing Wizard</i> and choice of CAE Vendor Kits.			
ATT-ORCAAPEX-PC	7.1	Complete support package for all ATT3000 and all ORCA 1Cxx and 2Cxx FPGAs. Includes AT&T <i>Timing Wizard</i> and choice of one CAE Vendor Kits.			
ATT-ORCAEVAL-PC	7.1	Evaluation version of complete software suite. No bit stream generation capability or download cable.			
ATT-ORCAVISAP-PC	7.1	Upgrade Vista package to Apex package.			
Workstation Solutions (S	un and HP70	00):			
ATT-ORCAVISTA-WS	7.1	Low-density starter system for AT&T FPGAs. Supports ATT3000, ATT1Cxx, ATT2C04, ATT2C06, ATT2C08, and ATT2Ç10 devices. Includes AT&T <i>Timing Wizard</i> and choice of CAE Vendor Kits.			
ATT-ORCAAPEX-WS	7.1	Complete support package for all ATT3000 and all ORCA 1Cxx and 2Cxx FPGAs. Includes AT&T <i>Timing Wizard</i> and choice of one CAE Vendor Kits.			
ATT-ORCAEVAL-WS 7.1		Evaluation version of complete software suite. No bit stream generation capability or download cable.			
ATT-ORCAVISAP-WS	7.1	Upgrade Vista package to Apex package.			
PC Solutions					
ATT-PROCAPTURE-PC	6.1	VIEWlogic PROSeries Schematic Capture (DOS/Windows).			
ATT-PROSIM-PC	6.1	<i>VIEWlogic PROSeries</i> non-VHDL Simulation. <i>PROWave</i> and <i>PROGen</i> for simulation through ATT2C26. Requires ATT-PROCAPTURE-PC.			
ATT-PROSIMHD-PC	6.1	<i>VIEWlogic PROSeries</i> non-VHDL unlimited gates simulation upgrade. Requires ATT-PROSIM-PC.			
Workstation Solutions (S	PARC only)				
ATT-PCDESIGN-SN	5.3.2	VIEWlogic Powerview Series non-VHDL ViewDraw schematic capture and ViewSim simulation through ATT2C26, including ViewWave and ViewGen.			
ATT-PVDDESIGNHD-SN	5.3.2	<i>VIEWlogic</i> Powerview Series non-VHDL unlimited gates simulation upgrade. Requires ATT-PVDESIGN-SN.			
ORCA Software Libraries	(PC)				
See Note Below	3.1	<i>VIEWlogic</i> schematic entry, synthesis, prelayout simulation library, and interface. Required when using ATT-PROCAPTURE-PC for schematic entry, <i>PROSyn</i> (available from <i>VIEWlogic</i>) for synthesis, or ATT-PROSIM-PC for prelayout unit delay simulation or for postlayout back-annotated timing simulation.			

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

Note: The ORCA libraries for Synopsys and VIEWlogic are included on the ORCA Foundry 7.1 CD

Development Systems for ORCA Series FPGAs (continued)

Part Number	Version	Description
ORCA Software Librar	ries (Sun/HP)	
See Note Below	3.1	<i>VIEWlogic</i> schematic entry, synthesis, prelayout simulation library, and interface. Required when using ATT-PVDESIGN-SN (or the <i>HP</i> equivalent available from <i>VIEWlogic</i>) for schematic entry, prelayout unit delay simulation, postlayout back-annotated timing simulation, or <i>VIEWSyn</i> (available from <i>VIEWlogic</i>) for synthesis.
ATT-LIBVRG-WS	3.0	Interface and library for <i>Verilog</i> . Required when performing prelayout simulation using <i>Verilog</i> after performing <i>ORCA</i> -specific synthesis or when performing postlayout timing simulation using <i>Verilog</i> .
ATT-LIBMN-WS	3.0	Interface and library for <i>Mentor Graphics</i> . Required when using Design Architect for schematic capture or <i>QuickSim II</i> for prelayout unit delay simulation or for postlayout back-annotated timing simulation.
ATT-LIBDA-WS	3.1	ATT-Design Automation interface and library. Required when using either ATT-Schema for schematic capture or ATTSIM for prelayout unit delay simulation or for postlayout back-annotated timing simulation.
See Note Below	3.1	High-level design link, interface, and synthesis library for Synopsys.
MOTIVE Libraries	None	<i>MOTIVE</i> Integration kit (available from Quad Design Technologies Group of Viewlogic, Inc.)
LMG Libraries	None	LMG Models (available from the Logic Modeling Group of Synopsys, Inc.)

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447

Note: The ORCA libraries for Synopsys and VIEWlogic are included on the ORCA Foundry 7 1 CD

LAN ICs

Intelligent Ethernet Hub Products

The T7202 Smart Hub Controller (SHC) and T7241 Multiple Ethernet Transmitter (METRX) represent AT&T's third-generation multiport repeater controller ICs for use in *IEEE* 802.3 10Base-T networks. The chip set provides a two-chip solution for implementation of a central network hub with extensive network management capabilities. This chip set also provides a low-cost, easy-to-design, feature-rich solution for PC-based 10Base-T repeaters.

Features

- High level of integration
- -Twelve 10Base-T ports
- -Two AUI ports
- -Dedicated MAC port
- -Dedicated expansion port
- Security
- Preprocessed network management statistics
- Per-port statistics
- Per-port collision counters
- Dedicated management report FIFO

Benefits

- Reduce system cost and improve reliability
- Address size-sensitive markets such as PC hubs
- Provide superior system performance
- Provide superior network management features in your system

Part No.	Description	Package Type	Temp. Ranges	Applications	Literature
T7202	Smart Hub Controller	132-pin PQF	0 °C to +70 °C	TP Ethernet	DS, AP, TN, PN
T7241	Multiple Ethernet Transmitter	84-pin PLCC	0 °C to +70 °C	TP Ethernet	DS, PN

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

Ethernet Switching Hub Products

The ATT1RX04 promotes cost-effective Ethernet circuit-switched hubs where each port can be independently assigned to a LAN network or segment. Circuit switching is used to provide network administrators with the ability to optimize network performance by making it easy to move any port on the network to a new LAN segment as the load or traffic patterns dictate.

The AT&T1S04 provides the circuit switch and collision handling logic for implementing a 12-port, foursegment circuit switch when used in conjunction with the ATT1RX04. In addition, it provides all the miscellaneous board-level circuitry, including the reset circuitry, and board-level address decoding. The SEABREEZE also provides a board-level reset to the ATT1RX04s that can be controlled by software or hardware. The design allows for a 4-bit hardware configured board ID.

The ATT1MX10 provides four *IEEE* 802.3 10 Mbits/s standard MACs and TP/AUI transceivers in a single 208-pin SQFP package. With deep inter-

nal FIFOs and a high speed system interface, the ATT1MX10 is intended for Ethernet frame switching and multiport bridging and routing applications.

The ATT1MX10 allows single-cycle DMA transfers directly to and from its internal transmit and receive FIFOs. The deep FIFOs enable storing multiple packets on-chip, retransmitting a packet after a collision, and rejecting runts before any DMA activity.

The ATT1MX10 also contains extensive on-chip counters and registers. This enable system designers to easily keep track of network statistics required by network management standards.

Features of the ATT1RX04

- High level of integration -Four independent repeaters
- -Four AUI/TP transceivers
- Network management and security
- Comprehensive network management
 - 32-bit counters
 - -802.3K and RMON support

- Security
 - Eavesdropping
 - -Intrusion protection
 - Customizable security implementation
- Demonstration hardware/software available

Benefits of the ATT1RX04

- Reduce system cost and improve reliability
- Provide superior system performance
- Provide superior network management features in your system

Features of the ATT1S04

- A circuit switch supporting 12 ports and providing independent connection to 1 of 4 segmented Ethernet segments
- Supports an unlimited number of configurations
- Integrates miscellaneous boardlevel circuitry to minimize other components

Benefits of the ATT1S04

• Makes Switched Ethernet A BREEZE (SEABREEZE) and reduces time to market.

Features and Benefits of the ATT1MX10

- Four 10 Mbit/s Ethernet transceivers and MACs integrated together with separate transmit and receive channel FIFOs and a DMA interface simplify the design of a frame-switching hub
- Provides extensive network management capabilities that network administrators demand in today's hub equipment
 - There are nine per-channel transmit event counters and 19 per-channel receive event counters
 - The status of each packet received is appended to the packet for custom management implementations

- A high-speed 32-bit system interface maximizes throughput on the system bus
- Direct system interface to FIFOs allows single-cycle DMA operations at 25 MHz
- -Each channel provides separate status and control leads for efficient memory management
- Separate 128-byte deep transmit and receive FIFOs are provided on a per-channel basis
- The chip complies with 802.3 by default, but also can be configured for optimal frame switching performance
- -Short preamble generation
- -Ignore transmit deferred
- Immediate retransmission after collision

- -Selectable number of collision retries
- Ignore SQE test after transmission
- The CRC generator can be enable or disabled on a per-packet basis with hardware control. This allows passing preformatted packets at high speed
- The ATT1MX10 can also be configured for full-duplex operation

Part No.	Description	Package Type	Temp. Ranges	Applications	Literature
ATT1RX04	Four Managed Repeaters for Circuit-Switched Ethernet	132-pin BQFP	0 °C to +70 °C	TP Ethernet	PN, DS
ATT1S04	Circuit Switch and Collision Handling Logic for 12-Port, 4-Segment Ethernet Circuit Switching	132-pin BQFP	0 °C to +70 °C	TP Ethernet	PN, DS
ATT1MX10	QuadMAC and Transceiver for Ethernet Frame Switching	208-pin SQFP	0 °C to +70 °C	TP Ethernet	PN, DS

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447

Ethernet Network Interface Card (NIC) Products

The T7231 Local Area Network Protocol Assist Communications Engine with ROM Controller (*LANPACER* Controller) and the T7213 Dual Interface Station Chip (DISC) provide a two-chip solution for the implementation of *IEEE* 802.3 for an Ethernet attachment unit interface (AUI) and twisted-pair wire (10Base-T) media. These two devices provide the complete solution for the *PC/XT/AT* bus interface to the network media.

Features

- High level of integration -Integrated bus interface
 - -Software medium selection
- Software link-integrity control82586 software compatibility
- Certified *Novell* and NDIS drivers
- Flexible data structure
- Support EEPROM/Flash
- Support EErROM/Flas
 Printer error monitor
- Finner entor mornic

Benefits

- Minimal component count
- High reliability
- Low manufacturing cost
- Small card applications
- Laptop applications
- · Early to market

- · Low development cost
- Easy to upgrade

The T7220A Twisted-Pair Medium Attachment Unit (TPMAU2) simplifies the design and implementation of a minimal-part-count, cost-effective medium attachment unit (MAU) between an Ethernet attachment unit interface (AUI) and the twistedpair wire media. The T7220A TPMAU2 can also be used to implement the twisted-pair wire interface on an Ethernet computer network interface card.

The T7220A TPMAU2 device requires a standard 5 V supply and consumes a maximum of 600 mW.

LAN ICs

Features

- Integrated TP and AUI drivers and receivers
- All functions integrated on a single device
- Integrated LED drivers
- Autopolarity detection/correction extended wire length
- Low jitter robust smart squelch accurate predistortion

Benefits

- Small adapter and transceiver applications
- · Early to market
- Low development cost

- Special features for product differentiation
- Proven performance, early to market
- Superior network performance

Part No.	Description	Package Type	Temp. Ranges	Application	Literature
T7220A	Twisted-Pair Medium Attachment Unit	28-pin SOJ 28-pin DIP	0 °C to +70 °C	TP Ethernet	DS
T7213	Dual Interface Station Chip	28-pin, plastic DIP 28-pin, plastic SOJ	0 °C to +70 °C	TP Ethernet	DS, PN
T7231	IEEE 802.3 LANPACER Controller	132-pin, PQFP	0 °C to +70 °C	TP Ethernet	DS, MN, PN

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

FDDI Products

The T7352 TPDDI Physical Layer Device (PHY) is a single-chip CMOS VLSI component that implements cipher scramble/descramble functions of the FDDI twisted-pair PMD as defined by the ANSI X3T9.5, the complete fiber-distributed data interface (FDDI) physical layer protocol, as well as the stream FDDI Committee.

In the TPDDI mode, the T7352 inserts the stream cipher scramble/ descramble function into the serial bit stream on the NRZ side of the NRZ <---> NRZI converters.

In the FDDI mode, the T7352 provides the connection to the FDDI physical media dependent (PMD) interface, the media access controller (MAC), and the station management (SMT) ports. It monitors and controls the media line state, exchanging this information with the SMT layer.

Features

- Single-chip device
- Low power
- · Extended CMT support

Benefits

- · Board space saving
- · Board cost saving
- No need to run 125 MHz clock on the board
- Reduced cooling requirements
- Multiport concentrator possible
- No external logic
- No additional components
- Provides additional ring management features

Part No.	Description	Package Type	Temp. Ranges	Application	Literature
T7352	FDDI/TPDDI Physical Layer Device	84-pin PQFP	0 °C to +70 °C	FDDI	DS, PN

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

100 VG/10Base-T MAC and System Interface

The ATT2MD01 is a CMOS Integrated Circuit containing all of the media access control (MAC), memory management, and EISA/ ISA system interface logic required for numerous 100VG-AnyLAN/ 10Base-T adapter card applications. The ATT2MD11 and ATT2MD12 are CMOS Integrated Circuits containing all of the media access control (MAC), memory management, and PCI system interface logic required for numerous 100VG-AnyLAN/10Base-T adapter card applications.

Both the ATT2MD01 and ATT2MD11 can be used in the following applications:

Adapter cards for PC applications
Workstations and file servers

- Network ready motherboards
- Network analyzers and test equipment
- Bridges and routers
- Remotely managed hubs

Features

- Supports *IEEE* 802.12 100VG-AnyLAN specification
- EEPROM interface enables easy implementation of jumperless products
- Complete hardware/software demonstration kit available
- ATT2MD01 supports software data structures that can be maintained across EISA slave, ISA slave, and EISA bus master implementations for both 10Base-T and 100VG-AnyLAN
- ATT2MD01 provides EISA bus compatibility for high performance

- ATT2MD01 ISA bus compatibility addresses the embedded base
- ATT2MD01 BootROM interface supports Remote Program Load (RPL) for EISA/ISA diskless workstations
- ATT2MD11 dual media access controllers (MACs) support 10/100 applications that allow for easy migration.
- ATT2MD11 provides PCI bus compatibility for high performance

Benefits

- · Minimal component count
- Low manufacturing cost
- · Early to market
- Easy migration from 10 to 100 Mbits/s
- High performance

Part No.	Description	Package Type	Temp. Ranges	Application	Literature
ATT2MD01	100VG/10Base-T MAC and EISA/ISA System Interface	208-pin BQFP	0 °C to +70 °C	100VG-AnyLAN	DS, PN
ATT2MD11	100VG/10Base-T MAC and PCI System Interface	160-pin QFP	0 °C to +70 °C	100VG-AnyLAN	DS, PN
ATT2MD12	100VG/10Base-T MAC and PCI System Interface	160-pin QFP	0 °C to +70 °C	100VG-AnyLAN	DS, PN

100 VG/10Base-T Multiport Managed Repeater

The ATT2R01 and ATT2R02 are CMOS integrated circuits that contain all the functionality necessary to implement a six-port managed 100VG-AnyLAN repeater with an optional uplink port. The ATT2R01 is designed to provide all the hub management and security features that have evolved with over four generations of 10Base-T devices. In addition, the ATT2R01 provides two levels of priority for time-sensitive applications like multimedia. Everything is built in so the system designer can get to market very quickly with a secure, managed 100 Mbit 100VG-AnyLAN hub product. The ATT2R01 supports a wide variety of product offerings through the topologies supported, number of ports supported, style of hub, remote management capabilities, etc.

Features

- Supports *IEEE* 802.12 specifications
- High level of integration allows for expansion with minimal external components
- Supports many network configurations

- Provides extensive network management capability
- Extensive built-in security
- Promiscuous mode supports network analyzers
- Board-level diagnostics simplify manufacturing tests
- Complete hardware/software demonstration kit available

Benefits

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- Minimal component count
- Reduce system cost and improve reliability
- · Early to market

LAN ICs

Part No.	Description	Package Type	Temp. Ranges	Application	Literature
ATT2R01	100VG Multiport Managed Repeater	208-pin BQFP	0 °C to +70 °C	100VG-AnyLAN	DS, PN
ATT2R02	100VG Multiport Managed Repeater	208-pin BQFP	0 °C to +70 °C	100VG-AnyLAN	DS, PN

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

100 VG Quartet Signaling Transceiver

The ATT2X01 and ATT2X02 are CMOS integrated circuits implementing the physical layer function of the proposed *IEEE* 802.12 standard for 100 Mbits/s data transmission in local area networks (LAN). The ATT2X01 implements the Quartet Signaling technology in a single integrated circuit, enabling reliable data transmission at 100 Mbits/s over voice-grade twisted-pair cabling. The ATT2X01 is a versatile building block for 100VG-AnyLAN systems.

Features

- Supports *IEEE* 802.12 100VG-AnyLAN specifications
- Enables 100 Mbits/s transmission. over 10Base-T wiring
- Complete implementation of the 100VG-AnyLAN Quartet Signaling Physical Layer function

Benefits

- Early to market
- Minimal component count

Part No.	Description	Package Type	Temp. Ranges	Application	Literature
ATT2X01	100VG Quartet Signaling Transceiver	44-pin PLCC	0 °C to +70 °C	100VG-AnyLAN	DS, PN
ATT2X02	100VG Quartet Signaling Transceiver	44-pin PLCC	0 °C to +70 °C	100VG-AnyLAN	DS, PN

Clock Recovery Circuits

When transmitting digital signals, it is very important to determine the beginning and end of each bit position. One rather expensive approach is to provide a separate clock lead to synchronize transmitter and receiver. A more cost-effective approach is to provide synchronization by recovering the clock from the suitability encoded transmitted signal itself. The data is then synchronized to this recovered clock. The T7032 and T7035 perform this clock recovery and data retiming function. These two devices offer a broad range of application flexibility, with the T7032 up to 52 MHz, and the T7035 between 47.7 MHz and 210.5 MHz.

Features

- Pin-programmable for 1 MHz to 210.5 MHz
- Only one inexpensive 3.58 MHz crystal needed
- Programmable frequency
- Single 5 V supply
- 100K ECL compatible

• Extended temperature range available. -40°C to +85°C

Benefits

- Flexibility, suitable for numerous applications
 - SONET
 - ATM
 - Fiber channel
 - ESCON
 - FDDI
 - Fiber or copper wire
- Ease of design
- Rugged

Part No.	Description	Package Type	Temp. Ranges	Application	Literature
Clock Re	ecovery				
T7032	Clock Recovery and Data Retiming Circuit (1 MHz to 52 MHz)	20-pin, plastic DIP	-40 °C to +85 °C	Transmission	DS, AP, TN
T7035	Clock Recovery and Data Retiming Circuit (47.7 MHz to 210.5 MHz)	44-pin PLCC	-40 °C to +85 °C	Transmission	DS, PN, TN

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

Coding Information

T 7351B-FC Telecommunications Generation -Processing/Speed -Package -Temperature -

- Generation
- A Second
- **B** Third, etc.

Package Options

- **B** Nonhermetic, ceramic DIP
- C Hermetic, ceramic DIP
- **D** Cerdip
- E Small-outline J-lead
- **F** Plastic quad flat pack (POFP)
- **G** Small-outline gull wing
- J Nonhermetic, leadless chip carrier
- K Hermetic, leadless chip carrier L Hermetic, ceramic leaded chip
- carrier **M** Plastic, leaded chip carrier
- **N** Nonhermetic, ceramic pin array
- P Plastic DIP
- R Hermetic, ceramic pin array

- **S** Plastic pin array
- T Plastic, leadless chip carrier
- U Nonhermetic, ceramic, leaded chip carrier
- W Chip in wafer form

Temperature Options

 $\mathbf{C} = 0 \circ \mathbf{C}$ to $70 \circ \mathbf{C}$ **E** 0 °C to 85 °C M Military $\mathbf{L} = 40 \,^{\circ}\text{C}$ to $+85 \,^{\circ}\text{C}$
ISDN Desktop ICs

The ISDN market is currently experiencing dramatic growth. This expansion is fueled by end-users' desire to obtain high-speed interconnectivity to other remote users and to services such as *Prodigy, Compuserve*, and the Internet. AT&T-ME offers both two-wire U and four-wire S/T interface devices to support digital services.

Part No.	Description	Package Type	Temp. Ranges	Literature
LH1465	ISDN dc Terminator	8-pin DIP 16-pin SOG	-40 °C to +85 °C	DS
T7121	HDLC Interface for ISDN	28-pin SOJ 28-pin DIP	-40 °C to +85 °C	DS, TN, BC, AP
T7237	U-Interface Transceiver	44-pin, PLCC	-40 °C to +85 °C	DS, PN
T7250C	Enhanced user-network interface for ISDN and proprietary terminal endpoints	44-pin PLCC	0 °C to 70 °C	DS, PN, TN, BC, AP
T7254	Quad S/T Transceiver	68-pin PLCC	0 °C to 70 °C	AP, DS, BC
T7256	Single-Chip NT1	44-pin PLCC	-40 °C to +85 °C	DS, PN, MN, BC
17259	SBus Dual Basic Rate (DBRI) Transceiver	132-pin, JEDEC PQFP	0 °C to 70 °C	DS, BC
T7264	U-Interface 2BIQ Transceiver	44-pin PLCC	-40 °C to +85 °C	DS, TN, BC
T7270	Time-slot Interchanger	48-pin PLCC	-40 °C to +85 °C	DS, TN, AP
17501	PCM CODEC	18-pin DIP 20-pin SOJ	0 °C to 70 °C	DS
T7540	Speaker Phone CODEC	40-pin DIP 44-pin PLCC	0 °C to 70 °C	DS
77901	ISA Single-Port Wide Area Connection (ISA-SWAC) Device	132-pin BQFP	0 °C to 70 °C	DS, PN
T7903	ISA Muliport Wide Area Connection (ISA-MWAC) Device	132-pin BQFP	0 °C to 70 °C	PN, DS, MN

Analog Line Card (Line Interface) Solutions



Analog Line Card products provide all of the funcions to provide Plain Old Telephone Service (POTS) in applications such as:

- ISDN + POTS
- Modem + POTS
- FITL (Fiber-In-The-Loop)
- HFC (Hybrid-Fiber/Coax)
- DAML (Digitally Added Main Line)
- PBX (Private Branch Exchange)
- Central Office

These products provide the functions required by POTS, called BORSCHT, which stands for:

- B = Battery Feed
- O = Overvoltage Protection
- R = Ringing
- S = Supervision
- C = Codec
- H = Hybrid
- T = Testing

If you look at the preceding diagram, you will see which products provide which function.

And now, the products . . .

Protection

• Protect the SLIC (Subrscriber Loop Interface Chip) from lightning or power crosses.

Part No.	Package	Lightning	Power Cross
LB1201A	8-Pin DIP	±12.5 A, 10µs x 100µs	1 Arms, 60 Hz, 25 ms
	8-Pin SONB	±18.5 A, 10μs x 160μs	6 Arms, 60 Hz, 25 ms
ATTL7591	8-Pin DIP or	±30 A, 10µs x 1000µs	3.5 Apeak, 50 to 60 Hz, 15 ms
	8-Pin SONB	± 40 A, 5 µs x 320 µs	5.0 Apeak, 50 to 60 Hz, 10 ms
		±80 A, 2µs x 10µs	• · ·

Application Note:

1. Overvoltage Protection of Solid-State Subscriber Loop Circuits.

WIDE AREA NETWORK ICs

SLIC (Subscriber Line Interface Circuit)

- Short loop to long loop capability!
- Low power—cuts power to the ph Kones!
- Thermal protection
- Select from low cost to high balance to low power

Demo Boards Available

Selection Guide

	Low Cost	High	Balance			L	ow Pow	er		311980 - 3334 8
Feature or Specification	LB1011	LB1276	LB1356	ATT L7551	ATT L7553	ATT L7554	ATT 17556	ATT L7557	ATT 17561	ATT 17564
Quiet Polarity Reversal			*		*	*				*
Switching Regulator								*	*	*
Auxiliary Battery Input with Switching							μP Control	Auto Control		
On-hook Transmission (mW) Pdiss at VBAT = -48 V		217	260	115	175	165	125	125	135	182
Pdiss Low-Power Scan State (mW)	NA	150	155	NA	NA	76	NA	NA	NA	76
Tip Open (Ground Start) State						*				*
Feedback Guaranteed Balance (58 dB)		*	*							
Spare Op Amp				*		*	*	*	*	*
Package	8 DIP	44 PLCC 24 DIP	24 DIP	44 PLCC 24 DIP	44 PLCC	44 PLCC	32 PLCC	32 PLCC	44 PLCC	44 PLCC
Demo Board	*	*	*	*		*	*	*	*	*
Design Software				*		*	*	*	*	*

*Availability.

Application Notes:

1. Using the LB1276 SLIC

2. Using the LB1356 High-Balance SLIC

3. Ground-Start Applications Using the LB1356 SLIC

4. Using the Low-Power SLIC Evaluation Board

5. Low-Power SLIC Application Examples

Switching

Line Card Access Switches (LCAS)

- Replace electromechanical relays on line cards (POTS Cards)
- No impulse noise
- No "Zero Cross" circuitry required
- Small packages



Part No.	Form	Package	Function	Power Supplies Required	Power Dissipation
ATTL7580	1-2 Form C	16 SOG	Power Ringing Access	+5 V	3 mW
ATTL7581	1 ~ 2 Form C	16 DIP/SOG	Power Ringing Access	+5 V	3 mW
ATTL7582	2 ~ 2 Form C	16 DIP/SOG	Power Ringing Access Line Access	+5 V	3 mW
ATTL7583A/B	3 ~ 2 Form C	24 DIP/28 SOG	Power Ringing Access, Test in Access	+5 V	3 mW
ATTL7583C/D	3 ~ 2 Form C	28 SOG	(Independent Logic Inputs)	+5 V	3 mW

A Version - Includes diode bridge/SER clamping circuit for fault protection.

B Version - Includes diode bridge protection only.

C Version - Same as A above, but includes an additional logic state.

D Version - Same as B above, but includes an additional logic state.

Application Notes:

- 1. Introduction to AT&T Line Card Access Switches
- 2. Switching Behavior of AT&T Line Card Access Switches

3. Impulse Noise and the AT&T ATTL758X Series of Line Card Accesss Switches

Reference Designs for POTS Applications

Design 1:

Contains: ATTL7551 SLIC T7504 Quad CODEC ATTL7581 Switch For good resistive termination

Design 2:

Contains: ATTL7554 SLIC T7504 Quad CODEC ATTL7583 Switch For complex termination

Design 3:

Contains: ATTL7590 Ring Generator

Design 4:

Contains: ATTL7551 SLIC T7504 Quad CODEC ATTL7582 Switch For complex termination

Ringing Circuits

ATTL7590 Ring Generator

- Applies a battery-backed, singleended, sinusoidal ringing signal to the telephone loop
- Ringing capability:
 -40 Vrms into 5 North American REN
 - 30 mApeak into 4 German REN
- Built-in current limiting
- Low power dissipation in idle and ringing modes
- Needs only +5 V, a high-value negative dc supply, and a digital input sequence

ATTL7596A Integrated SLIC/Ring Generator

- Ultralow-power short-loop line interface
- Integrated constant current battery feed plus ring generator
- +5 V and -48 V battery operation
- Supervision function included
- Transformer-based
- No ring relay required
- · Battery switch included

WIDE AREA NETWORK ICs

CODEC (Coder/Decoder)

- 1, 2, or 4 channels per device
- Single +5 V supply
- High immunity to latch-up

	T7502	T7503	T7504	T7548	T7570
No. of Channels	2	2	4	1	1
Package Size	20 SOJ	20 SOJ	20 DIP/PLCC	20 PLCC	20 PLCC
Power Supply	+5 V	+5 V	+5 V	±5 V	+5 V
Clock Frequency (MHz)	2 or 4	2 or 4	2 or 4	0.512	2 or 4
Temp. Range (°C)	-40 to +85	-40 to +85	-40 to +85	0 to +85	-40 to +85
Companding (µ/A-law)	A-law	µ-law	Selectable	Selectable	Selectable
Programmable Gain	Ext. Restrs.	Ext. Restrs.	Ext. Restrs.	Yes	Yes
Programmable Hybrid Balance	No	No	No	Switches	Yes
Latches	No	No	No	No	6
Second Source	No	No	No	Yes	Yes
Demo Board	Yes	Yes	Yes	No	No
Typ Active Power (9 mW)	37	37	65	72	

Application Notes:

1. Using the T7570 Single-Supply CODEC

2. T7570 Coefficient Selection Software

T7504 - Quad CODEC



Part Number System for Protector/LCAS/SLIC Products

A T T L X X X X 9 10 11 12 13



Package Configuration (position 10, 11 above)

Alpha designators for package variations.

- **B** = 8-Pin DIP
- **C** = 16-Pin DIP
- $\mathbf{F} = 24$ -Pin DIP
- **P** = 44-Pin PLCC
- **s** = 8-Pin SONB

AE = 16-Pin SOG **AJ** = 28-Pin SOG

AU = 32-Pin PLCC

Part Number System for CODECs



Temperature Ranges

- $\mathbf{C} = 0 \,^{\circ}\mathrm{C}$ to $70 \,^{\circ}\mathrm{C}$ (Commercial)
- $\mathbf{E} = 0 \,^{\circ}\mathrm{C}$ to $85 \,^{\circ}\mathrm{C}$ (Extended)
- $\mathbf{L} = -40 \,^{\circ}\text{C}$ to +85 $^{\circ}\text{C}$ (Outside Plant/Loop)

Package Types

- E = SOJ
- M = Plastic, Leaded Chip Carrier
- **P** = Plastic DIP

WIDE AREA NETWORK ICs

Transmission ICs

AT&T-ME's Transmission ICs group designs and manufactures integrated circuit solutions for copper-based data and voice transmission in the range of 1.544,000 bits per second (1.5 Mbits/s) to 51,840,000 bits per second (51.84 Mbits/s). AT&T Microelectronics' transmission IC solutions target SONET/SDH, DS3/E3, and DS1/E1 applications, which include multiplexers, digital access cross-connect systems (DACS). channel service unit (CSU) and digital service unit (DSU) equipment, channel banks, remote wireless modules, and PBX interfaces.

Wide area network routing and signaling via X.25 or LAPD protocol applications are facilitated by AT&T-ME's high-level data link control (HDLC) devices. Providing 24 or 32 channels, these devices perform HDLC functionality for both domestic and international applications. For those applications requiring data compression, AT&T-ME's adaptive delta pulse code modulation (ADPCM) transcoders provide variable bit rates and are compliant with ANSI T1.301-1987 and ITU-T Recommendations G.721 and G.726.

Interdevice communication in the AT&T solution is accomplished via a full-duplex, serial time-division-multiplexed (TDM) concentration highway interface (CHI). AT&T-ME manufactures a single-chip solution that interfaces four TDM-CHIs able to switch data in both the time and space domain over any CHI, while maintaining frame integrity for wideband data/video applications or reducing the data latency for voice applications. Applications requiring cross-connect functionality can interconnect these CHI controllers to produce switching matrixes of varying sizes.

Part No.	Description	Package Type	Temp. Ranges	Literature
T7115A	32/34 Channel Synchronous Protocol Data Formatter	64-pin PLCC	–40 °C to +85 °C	DS, AP
T7230	Primary Access Framer/Controller	68-pin PLCC	-40 °C to +85 °C	DS, PN
T7274B	Quad Differential Line Driver	16-pin DIP 16-pin SOJ	-40 °C to +85 °C	DS
T7275C	Quad Differential Line Receiver	16-pin DIP 16-pin SOJ	-40 °C to +85 °C	DS
T7281	16-Channel ADPCM	44-pin PLCC, 24-pin DIP	0 °C to 70°C	DS, AP
T7288	CEPT Line Interface	28-pin DIP, 28-pin SOJ	-40 °C to +85 °C	DS
T7289A	DS1 Line Interface	28-pin DIP, 28-pin SOJ	-40 °C to +85 °C	DS
T7290A	DS1/T1/CEPT	28-pin DIP, 28-pin SOJ	-40 °C to +85 °C	DS
T7295-1	E3 Integrated Line Receiver	20-pin DIP, 20-pin SOJ	-40 °C to +85 °C	DS
T7295-6	DS3/SONET STS-1 Line Receiver	20-pin DIP, 20-pin SOJ	-40 °C to +85 °C	DS
T7296	DS3/E3 SONET STS-1 Transmitter	28-pin DIP, 28-pin SOJ	-40 °C to +85 °C	DS
17630	Dual T1/E1 Line Interface and Framer plus HDLC Data Line Access	84-pin PQFP	-40 °C to +85 °C	PN
T7632	Dual CEPT Primary Access Framer	84-pin PQFP	-40 °C to +85 °C	DS, PN
T7690/93	3 V Quad DS1/E1 Line Interface	100-pin BQFP	-40 °C to +85 °C	DS, PN, MN

Access ICs

Broadband access integrated circuits create the communications link between the optical network unit (ONU) and the terminal device within a customer's premises [commonly referred to as customer premises equipment (CPE)] found in a fiber-to-the-curb (FTTC)/switcheddigital video (SDV) system. AT&T-ME's switched-digital video solution provides a quantum leap forward in supplying cost-effective broadband access to the home and office.

AT&T-ME's circuits in these systems provide the following functions:

- ONU
 - Framing
 - -Line interface
 - Line driving

- Set-top functions
 - Line driving
 - -Line interface
 - -Transmission convergence
 - Segmentation and Reassembly (SAR)
 - -ATM Adaptation Layer 5 (AAL5)

Part No.	Description	Package Type	Temp. Ranges	Literature
17660	16-CAP Receiver	84-pin PLCC	0 °C to +70 °C	DS, PN
T7661	Analog Front-end and QPSK Transmitter	84-pin PLCC	0 °C to +70 °C	DS, PN
T7662	Transmission Convergence plus SAR/AAL Framer	100-pin MQFP	0 °C to +70 °C	DS, PN
T7664	Quad 16-CAP Transmitter	84-pin PLCC	-40 °C to +85 °C	DS, PN
T7665	Quad QPSK Receiver	84-pin PLCC	-40 °C to +85 °C	DS, PN
T7666	Transmission Convergence Framer	128-pin PQFP	-40 °C to +85 °C	DS, PN

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

Video RF Components

Video RF components are broadband RF communication interface ICs for voice/data/video transmission over fiber, coax, and satellite.

ATTV4910 RF Transmitter

Applications

• Hybrid fiber/coax network interface device (NID)

Description

The ATTV4910 is a silicon integrated transmitter. It consists of a five-stage AGC amplifier section followed by a differential line driver. The AGC has 26 dB of gain control in 1 dB steps. The gain is controlled by 5 TTL/ CMOS compatible inputs. The line driver can provide up to 200 mA of current from a 5 V supply. The ATTV4910 is designed to operate from -40 °C to +85 °C and is available in a 24-pin SOJ package.

Features

- Minimum Pout = 17.5 dB, 5 MHz to 40 MHz, @ R(A + B − C) ≥ 24 dBc
- 5 MHz to 60 MHz small signal bandwidth, Pout ≤ 11 dBm, R(A + B - C)≥ 38 dBc
- Typical dc power consumption:
 Active mode 385 mW
 Sleep mode 15 mW
- Very fast switching times between active and sleep mode: <300 ns
- 26 dB gain range, TTL or CMOS controlled

ATTV5002 Dual Video Cable Driver

Applications

- Driving unshielded twisted pairs – ATM LAN UTP
- FTTC UTP
- 50 Ω or 75 Ω cable driver

Description

The ATTV5002 is a 150 mA, 240 MHz, unity gain stable (up to 10 pF capacitive load over process and temperature variations at Av = +1 and stable for any CLat Av = +3/-2), dual operational amplifier designed specifically for those applications where stable, highspeed, large current driving capabilities are essential.

The ATTV5002 utilizes a voltage feedback architecture to achieve input biasing currents of 300 nA and a high input impedance of 8 M Ω . In addition, the ATTV5002 is set to a modest gain of 50 dB for a maximum range of stable operation, and gain flatness of less than 1 dB up to 30 MHz while maintaining a slew rate of 500 V/µs. These features give the ATTV5002 the ability to drive over 150 mA (6 V peak to peak) into a transmission line with minimum distortion for pure mono-

WIDE AREA NETWORK ICs

tone signals and less than 10% ringing for pulse signals. Also, with a standby flow-through current in excess of 1 mA in its output stage, the ATTV5002 provides linear amplification of less than 1% from 6 mV to 6 V. The dual amplifiers are exact mirrors of each other with individual power supply bonding pads that provide optimized matching and isolation. The ATTV5002 can operate on either single or dual power supplies with a range of 5 V to 12 V, requiring 2 V of head room in each direction.

Features

The ATTV5002 is a low-cost dual video operational amplifier optimized for applications requiring high output drive capability, such as unshielded twisted pairs in a telephony or data communications environment from -40 °C to +85 °C.

- Output peak current in excess of 150 mA
- Can drive any capacitive load for Av = +3/-2
- Unity gain stable up to 10 pF loads
- Output swing to within 2 V of either supply
- Gain flatness <1 dB, dc to 30 MHz (Av = +3)
- Slew rate of 500 V/µs
- Dual or single power supply up to 12 V
- Quiescent current varies by <2% from -40 °C to +85 °C

ATTV5006 Dual Video Cable Driver

Applications

- Driving unshielded twisted pairs – ATM LAN UTP – FTTC UTP
- FIIC UIP
- 50 Ω to 75 Ω cable driver

Description

The ATTV5006 is a 150 mA, 340 MHz, dual operational amplifier designed specifically for those applications where stable, high-speed, large current driving capabilities are essential.

The ATTV5006 utilizes a voltage feedback architecture to achieve input biasing currents of 300 nA and a high input impedance of 8 M Ω . In addition, the ATTV5006 is set to a modest gain of 60 dB for a maximum range of stable operation, and gain flatness of less than 1 dB up to 30 MHz while maintaining a slew rate of 500 V/us. These features give the ATTV5006 the ability to drive over 150 mA (6 V peak to peak) into a transmission line with minimum distortion for pure monotone signals and less than 10% ringing for pulse signals. Also, with a standby flowthrough current in excess of 1 mA in its output stage, the ATTV5006 provides linear amplification of less than 1% from 6 mV to 6 V. The dual amplifiers are exact mirrors of each

other with individual power supply bonding pads that provide optimized matching and isolation. The ATTV5006 can operate on either single or dual power supplies with a range of 5 V to 12 V, requiring 2 V of head room in each direction.

Features

The ATTV5006 is a low-cost dual video operational amplifier optimized for applications requiring high output drive capability, such as unshielded twisted pairs in a telephony or data communications environment form 0°C to +70°C.

- Output peak current in excess of 150 mA
- Can drive any capacitive load for $A_V = +7/-6$
- Output swing to within 2 V of either supply
- Gain flatness <1 dB, dc to 30 MHz (Av = +7)
- Slew rate of 500 V/µs
- Dual or single power supply up to 12 V
- Quiescent current varies by <2% from 0 °C to +70 °C

High-Frequency Gallium Arsenide (GaAs)

The High-Frequency GaAs ICs listed here with the associated evaluation fixtures, were introduced in 1990. They were designed for SONET compatibility at the OC-48 data rate of 2.488 Gbits/s. These devices have now been in production for over five years. Our clock and data regenerator at the OC-96 data rate of 4.977 Gbits/s has been in production for two years. Customer requests have resulted in clock and data regenerators in production that are at non standard OC rates. Our clock and data regenerator can be factory tuned to any data rate from 450 Mbits/s to 5.5 Gbits/s, for those special needs. New products for 1996, at 10 Gbits/s, are now in development. Samples will be available at the beginning of 1996.

Block Diagram of a Fiber-Optic Regenerator



High-Frequency Products

Part No.	Description	Туре	Data Rate	Literature
LG1600FXH (any data rate)	Clock and Data Regenerator	68-lead package	up to 5.5 Gbits/s	DS
LG1600FXH5332	Clock and Data Regenerator	68-lead package	5332 Mbits/s	DS
LG1600FXH4977	Clock and Data Regenerator	68-lead package	4977 Mbits/s	DS
LG1600FXH2488	Clock and Data Regenerator	68-lead package	2488 Mbits/s	DS
LG1600FXH1244	Clock and Data Regenerator	68-lead package	1244 Mbits/s	DS
LG1600FXH1062	Clock and Data Regenerator	68-lead package	1062 Mbits/s	DS
LG1600FXH0622	Clock and Data Regenerator	68-lead package	622 Mbits/s	DS
LG1605DXB	Limiting Amplifier	16-lead package	2488 Mbits/s	DS
LG1605DXB-TR16	Limiting Amplifier	16-lead package	2488 Mbits/s	DS
		on tape and reel		
LX1608DXF	Laser Driver	24-lead package	2488 Mbits/s	DS
TF1003C	Evaluation Fixture for LG1605DXB			DS
TF1004A	Evaluation Fixture for LG1600FXH			DS
TF1006A	Evaluation Fixture for LG1608DXF			DS

FOUNDRY

AT&T Bipolar Foundry

The AT&T Bipolar Foundry organization is one in which customer input can be anything from an idea to ready masks. Our foundry is a recognized major supplier of a wide variety of high-performance bipolar technologies that span a broad analog/mixed-signal application base including products designed for interface circuitry, network computing, telecom, instrumentation/ATE, and video/RF. The proven manufacturability of our foundry technologies is complemented nicely with a full range of development and manufacturing services (summarized in table at right) performed by our technical and management staff. Additionally, AT&T utilizes their design ingenuity to offer versatile design solutions that result in highly integrated customer solutions leading to substantial cost savings and higher system performance. Also, we support commercially available PC and workstation based design tools.

Technology Offerings

AT&T provides a diverse offering of high-performance bipolar technologies that can be utilized to provide system solutions to our customers. Several of AT&T's bipolar technologies are offered in both custom or semicustom design options in which either the customer or AT&T can provide the design and layout. The foundry technologies, manufactured in world-class cleanroom facilities, fall into two major groups. First, there is the Complementary Bipolar Integrated Circuit (CBIC) consisting of CBIC-R, CBIC-U2, and CBIC-V2 technologies. They provide vertical PNPs as well as vertical NPNs having ft/BVceos ranging from 250 MHz/33 V to 10 GHz/10 V. respectively.



Bipolar Foundry Services

Development Services	Production Services
Customer Training	Wafer Fabrication
IC Electrical Design	Wafer Probe
Test Development	Product Packaging and Test
IC Layout and Mask Tooling	Product Engineering
Prototype Wafer Fabrication	
Prototype Packaging and Test	
Prototype Evaluation	

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

The second group of foundry technologies, ideal for data conversion products, was designed for ultrafast NPN, ECL-type digital and analog UHF/VHF communications. This is our Bipolar Enhanced Super SelfAligned Technology (BEST1) which offers a typical fr of 14 GHz and a minimum BVceo of 5.5 V. A technology overview and a summary of dc and ac device characteristics are shown on page 2-41.

Analog/Mixed-Signal Bipolar Technology Overview

Technology	Overview
CBIC-R	General-purpose 33 V, 250 MHz, moderate-speed complementary bipolar technology for analog/ mixed-signal applications. CBIC-R is the most mature junction-isolated complementary bipolar technology available with over 15 years of manufacturing experience. CBIC-R technology is also offered through the ALA-400 family of semicustom linear arrays.
CBIC-U/U2	High-performance 12 V, 4 GHz, high-speed complementary bipolar technologies for wideband or low-power analog/mixed-signal applications. Since CBIC-U's introduction to the marketplace and CBIC-U2's introduction in 1992, many customers have used these technologies to introduce lead-ing-edge products targeted in video/consumer, industrial, instrumentation, and data/telecommunications markets. CBIC-U's semicustom products are available in the ALA-200 family of arrays.
CBIC/V2	Very high-performance 10 V, 10.2 GHz, complementary bipolar technologies that can be utilized for very high-speed or ultralow-power analog/mixed-signal applications. CBIC/V2 is the highest-speed side wall oxide isolated complementary bipolar technologies in manufacture. CBIC-V2 was introduced into manufacture in 1989. The ALA-110 family supports semicustom designs in CBIC-V2.
BEST-1	Offers a nonoverlapping, super self-aligned, oxide isolated NPN transistor capable of a 5.5 V minimum BVceo, and a typical fr of 14 GHz. BEST-1 has been in production since 1989, and is ideal for ultrahigh-speed lower power consumption mixed-signal applications. BEST-1 is utilized for applications such as video driver distribution circuitry, high-speed data communications, and high-speed data conversion. ECL prop delays of 87 ps at a power level of 2 mW per gate have been obtained. BEST-1 semicustom solutions are available in the BE1000, BE2000, and BE4000 gate arrays.

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

Parameter	CBIC-R	CBIC-U2	CBIC-V2	BEST-1	Unit
BVceo (min)					
NPN	33	12	10	5.5	V
PNP	33	11	10	6*	V
fr (typ)		a constante de la constante de	wining day 1 (1) with day 1 (1)	and an and a second sec	
NPN	250 MHz	3.5 GHz	10.2 GHz	14 GHz	
PNP	250 MHz	2.7 GHz	4 3 GHz		_
hFE (typ)					
NPN	85	125	70	100	
PNP	110	35 ,	40	4*	
CJC (1 x typ)				INTER STATISTICS STATISTICS	
NPN	220	40	30	9	fF
PNP	340	60	50		fF
Interconnect	2 LM	2 LM	2 LM	3 LM + Poly	<u> </u>
	Ti-Pt	Ti-Pt-Au	Ti-Pt-Au	Ti-TiN-Al-TiN	_
	Ti-Pt-Au	Ti-Pt-Au	Ti-Pt-Au		
Resistors	200 & 2000	50 & 1080	80 & 1880	565	Ω/sq.
	(implanted)	(implanted)	(implanted)	(Poly)	
Trimmed Resistors	300	300	300		Ω/sq.
Capacitors	0.4	0.34	0.22	1.62 (MOS)	fF/µm ² (MNOS)
Min. Feature Size	5.0	1.5	1.5	1.5	μm

Technology Characteristics

*Lateral PNP.

FOUNDRY

Flexible Design Options

Table at the right covers the range of customer/AT&T partnership arrangements that might be followed once masks and wafers have been produced. Development begins, given customer-specified product performance requirements, with AT&T serving as technology consultants. As development commences, the division of customer/AT&T responsibility depends upon customer choice. For example, the customer might finish design through layout and finish with option 1 in table at the right, leaving AT&T only responsible for mask and wafer fabrication. Or the customer may require a turnkey solution, finished as option 6. Deliverables in options 1, 2, and 3 are either wafers or die.

Furthermore, the product may be either a custom or semicustom IC. The semicustom product offerings and component summaries are summarized in the following three tables. The essential feature of this supported IC development procedure is that it is flexible in meeting customer needs.

Customer/AT&T IC Product Development (Custom or Semicustom)

omer Option <u>4</u>	ns* 5 A	6 A
3 4 A A	5 A	<u>б</u> А
A A	А	A
A A	Α	Α
A A	Α	Α
A A	Α	Α
C A	Α	A
СС	A	A
	С	A
C	C C C C	C C A C C C

A: AT&T

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447.

Analog/Mixed-Signal Design Options

AT&T design capabilities offer highly integrated solutions resulting in substantial savings in costs along with higher system performance. This is achieved by utilizing either AT&T full-custom or semicustom design alternatives.

Full Custom

Full custom is the customizing of component types, values placement, and interconnect and requires a complete mask set. An extensive, characterized library of transistors, resistors, and capacitors is provided, which enables the designer to optimize the performance for a given application at minimal risk.

Semicustom

Semicustom is dies consisting of a standardized set of prepositioned components. Wafers are held in inventory prior to metallization. The interconnection is customized for each design, resulting in fewer masks, lower NRE, and a shorter processing interval than a full-custom design. Deliverables can be either tested or untested die, packages, or wafers.

Analog/Mixed-Signal Product Offering

			Techno	logy	
Product	CBIC-R	CBIC-U	CBIC-U2	CBIC-V2	BEST-1
Semicustom	ALA-401	ALA-201		ALA-110	BE1000
	ALA-402	ALA-202			BE2000
		ALA-210			BE4000
Full Custom	1		1	1	1

The following is a general summary of the number and type of components available on the various linear array products. For more detailed information, please request the appropriate data sheet.

Complementary Transistors B									
Product	Voltage (V)	NPN	PNP	Resistors	Capacitors	Pads			
ALA-110	10	51	41	282	14	16			
ALA-201	12	68	43	480	21	36			
ALA-202	12	136	86	960	38	48			
ALA-210	12	37	37	104	6	16			
ALA-401	33	61	61	434	7	38			
ALA-402	33	104	104	744	12	46			

Semicustom Product Component Summary

The following is a summary of the BEST-1 gate-array complexities available. The number of equivalent gates is calculated based on a gate multiplier of four transistors per gate.

Semicustom Gate-Array Complexity Options

		Equivalent	Internal	rnal I/O Buffer Fixed Power Equivalent Gates		Equivalent Gates	
Product	Voltage	Gates	Cells	Cells	& GND Pads	(D Flip-Flop with Clear)	(1-Bit Full Adder)
BE1000	5.5 V	1048	182	48	32	728	1001
BE2000	5.5 V	2780	484	92	38	1936	2660
BE4000	5.5 V	4196	728	108	38	2912	4004

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447

CAD Support

As circuits become more complex, computer-aided design (CAD) tools become more important to the successful completion of a design. AT&T Microelectronics supports a variety of commercially available PC and workstation based design tools. A description of the supported tools is offered below.

Training in the use of any of these tools will be provided at a nominal fee.

Schematic Capture

AT&T Microelectronics provides a library and software support for *Viewdraw* from Viewlogic Corporation and the schematic capture tool in the Cadence Design Systems' Analog Artist environment.

Circuit Simulation SPICE

SPICE is the primary circuit analysis tool used by analog designers. There are several versions available. AT&T Microelectronics provides transistor models compatible with SPICE 2G6. The resistor and capacitor models supplied are compatible with any simulator that allows models to be specified. In addition, some enhanced subcircuit models are available for use with MicroSim's *PSPICE*, version 4.01 or later.

ADVICE®

AT&T's own circuit simulator, *ADVICE* (a SPICE derivative), is available for use on *Sun Workstation.* It features an extended fourterminal bipolar transistors model that covers parasitic transistor behavior, operation in the quasi-saturation region, both interactive and batch execution, parameterized subcircuits, design centering, userdefinable models, and procedural simulation. AT&T Microelectronics licenses the *ADVICE* simulation tool and will provide training for a fee.

FOUNDRY

Layout

AT&T Microelectronics supports cell-based layout libraries for a number of layout editors available for both PC and *Sun Workstation* platforms. A library of CBIC primitive cells is available in *GDS II Stream* format.

Analog Artist Layout

The polygon layout editor in Analog Artist is supported with CBIC cell libraries written in Analog Artist database format. Also supplied are parameterized cells for generating valid resistor cells with desired values.

Verification

Layout verification using both AT&T and other commercial tools is supported on PC and *Sun Workstation* platforms.

Analog Artist

The verification tools PDcheck, PDextract, ERC, and PDcompare are fully supported for design rule checking, layout connectivity and parasitic capacitor extraction, electrical rule checking, and layout versus schematic checking, respectively.

Dracula

A library of command files for Cadence Design Systems' *Dracula* is available to enable design rule checking, layout connectivity and parasitic capacitor extraction, electrical rule checking, and layout versus schematic checking for layouts.

Dielectrically Isolated Wafers

AT&T also provides an applicationspecific wafer service wherein we prepare dielectrically isolated (DI) wafers for customers to finish processing in their own fab lines. In this mode, we are currently fabricating DI wafers for a variety of highvoltage and high-performance bipolar technologies.

Bonded Silicon on Insulator (SOI) Wafer

Features

- Handles like bulk silicon
- High bond strength

Specifications

- Diameter:
- 100 mm and 125 mm — 150 mm
- Orientation: <100> or <111>

Description

AT&T Microelectronics provides high-performance silicon materials to OEM customers. Process enhancements are made possible by the substrate properties which can be achieved through wafer bonding. High-volume processes are used to meet your production needs. AT&T-ME's bonding process was developed in conjunction with AT&T Bell Laboratories.

Customized Bonded Wafer Process

Silicon wafer bonding enables two wafers with different properties to be united by an attractive force. Sophisticated substrates are created with specific properties for a given process.

The bonded wafer process consists of the following steps:

- 1. Two wafers are welded together at room temperature.
- 2. Bond integrity is verified by infrared interference inspection. This step ensures that no voids are present following the initial bonding process.
- 3. The bonded wafers are annealed at high temperature to increase bonding strength.
- The device layer is thinned to the appropriate thickness by grinding and polishing.

		Device Layer Thickness	Insulating Oxide	
Wafer Type	Device Layer Thickness	Variation	Thickness	Total Wafer TTV
Thick SOI	10 μm—150 μm	±15%	0.4 μm—4 μm	<10 µm
Thin SOI	2 μm—10 μm	±0.5 μm	0.4 μm—4 μm	3 μm
Ultrathin SOI	0.07 μm—2 μm	±0.02 μm	0.1 μm—2 μm	3 µm

SOI Wafer Physical Characteristics

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CUSTOM PRINTED-CIRCUIT BOARDS AND BACKPLANES

AT&T Microelectronics' high-density, multilayer printed-circuit boards (PC boards) offer high levels of precision and performance for applications up to 22 layers and standard line widths and spaces down to 5 mils.

Choose from conventional plated holes, or select buried microvias to conserve surface area when using surface-mount components extensively All PC boards are *UL*-approved, and meet both Bellcore and IPC specifications. In addition, they're thoroughly electrically tested and inspected before being shipped to you.

AT&T Microelectronics' PC boards are custom double-sided rigid and multilayer and are available in these substrates:

- FR4
- BT (bismaleimide triazine)
- Materials for lower dielectric constant applications

Surface finishes include solder mask over bare copper with hot-air solder leveling, plus several alternative solder masks. Standard high-density capabilities include line width and spacing to 0.005 inches and drilled hole size to 0.0135 inches with 0.025-inch lands on external layers. Most models are available in five days—quality-tested and ready for your system testing.

AT&T's backplanes are available with your choice of components. We will assemble your backplane design with connectors on standard or metric grids, with *Fastech*® or other pins, and with passive and/or active components. Large sizes up to 24 inches x 24 inches are available. All backplanes are electrically tested, including level III testing when active components are part of your design.

Total quality control (TQC) and statistical process control (SPC) programs are combined with full electrical testing to help produce reliable, defect-free boards.

Personalized service from dedicated field engineers is available to all customers who wish support in the early design phases through volume production. Additional features include:

- Up to 22 layers for interconnection density
- Surface-mount technology
- Standard via, blind via, and buried microvia technologies

For additional information, call your AT&T Account Manager, or call 1-800-372-2447.

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OPTOELECTRONICS

AT&T Microelectronics is the world's largest supplier of components and subsystems for fiber-optic communications. AT&T Optoelectronics SBU addresses the telecommunications and network computing markets.

We have a long history of supplying state-of-the-art products for the telecommunications market. Applications range from Fiber-to-the-Home (FTTH), to SONET, to 2.5 Gbits/s long-haul transmission, to undersea. For these applications, AT&T is offering laser and detector components, transmitters, receivers, lithium niobate modulators, and advanced technology erbium-doped fiber amplifiers.

AT&T is also a leading player in the CATV and emerging microcellular markets. AT&T's systems-level testing allows customers to repurchase fully characterized devices. Testing includes NTSC, PAL, and cellular frequency plans. In addition to leading-edge products, AT&T Optoelectronics SBU brings years of experience to the photonics industry. Drawing on the strength of Bell Laboratories, we offer outstanding technical support. With our large commitment to fiber optics, AT&T will enable you to take advantage of leading-edge products that enable you to get to the market sooner.

Components

Device Type	Part No.	Description	Application	Features	Lit.
Modulators	2612AA	1.3 µm 4 GHz Bandwidth	High-speed telecommu-	Uses LiNbO ₃ technology	DS
	2613AA	1.3 µm 8 GHz Bandwidth	nications, analog CATV,	(Z-cut),	DS
	2622AA	1.55 μm 4 GHz Bandwidth	SONET OC-64	Excellent linearity for	DS
	2623AA	1.55 μm 8 GHz Bandwidth	Analog & digital cellular	analog applications,	DS
	2624AA	1.55 μm 16 GHz Bandwidth	communications	configurable to	DS
	2410C	Dual Output, 1.3 μm		customer specifications	DS
	2420C	Dual Output, 1.55 µm			DS
Photodetecto	ors				
InGaAs APDs	126A	Ceramic Carrier 1.5 Gbits/s	High-speed communi-	Compatible with industry-	DS
		1.5 GHz Typical Bandwidth	cations, high-speed	standard ceramic carriers	
	126B	Ceramic Carrier 2.5 Gbits/s	analog transmissions		DS
		2.0 GHz Typical Bandwidth	Submarine cable com-		
	126C	Ceramic Carrier 2.5 Gbits/s	munication systems		DS
		3.0 GHz Typical Bandwidth			
	127A	Industry-Std. Pkg. 1.5 Gbits/s	High-speed communi-	Suitable for use in harsh	DS
		1.5 GHz Biconic Connector	cations, high-speed	environments	
	127A1	Industry-Std. Pkg. 1.5 Gbits/s	analog transmissions	High coupling stability	DS
		1.5 GHz FC-PC Connector	Submarine cable com-		
	127B	Industry-Std. Pkg. 2.5 Gbits/s	munication systems		DS
		2.0 GHz Biconic Connector			
	127B1	Industry-Std. Pkg. 2.5 Gbits/s			DS
		2.0 GHz FC-PC Connector			
	127C	Industry-Std. Pkg. 2.5 Gbits/s			DS
		2.0 GHz Biconic Connector			
	127C1	Industry-Std. Pkg. 2.5 Gbits/s			DS
		3.0 GHz FC-PC Connector			

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Components (continued)

Device Type	Part No.	Description	Application	Features	Lit.
Photodetecto	ors (continu				
PINs	131A 131B	Digital 8-lead DIP SM Pigtail Digital 8-lead DIP MM Pigtail	FITL, Analog CATV	Low-cost, wide operating temperature range	DS DS
	131D	Analog 8-lead DIP SM Rotary Mechanical Splice		Wide bandwidth: >1 GHz High optical coupling	DS
	131E	Analog 8-lead DIP No Connector		stability	DS
	131G	Analog 8-lead DIP SM Rotary Mechanical Splice			DS
	131H	Analog 3-lead pkg. SM Connector			DS
	131J	Analog 3-lead pkg. SM Connector			DS
	131K	Digital 3-lead pkg. SM Rotary Mechanical Splice			DS
	131L	Analog 8-lead DIP SM Rotary Mechanical Splice			DS
	131N	Digital 8-lead DIP SM Rotary Mechanical Splice			DS
	131P	Analog 8-lead DIP SM FC/APC Connector			DS
	131R	Analog 8-lead DIP SM FC/PC Connector			DS
	1315	Analog 8-lead DIP SM FC/APC			DS
	131T	Analog 8-lead DIP SM FC/PC			
	M128C	Analog, 1.1 μm to 1.6 μm, Planar Structure	FITL, Analog CATV, Broadband	Very high linearity high responsivity, low back reflections	DS
Laser Module	es				
High-Speed Digital Distributed	246M	1.3 μm 14-pin Butterfly Package, 2.5 Gbits/s	SONET OC-12/48, long-haul hermetic package	Internal isolator, compact & lightweight, epoxy-free,	DS
Feedback	246N	1.55 μm 14-pin Butterfly Package, 2.5 Gbits/s	nonnoue prenage		DS
	246PF	Digital 1.5 DFB CW laser with			DS
Analog Isolated DFB	257CH	1.3 μ m 14-pin Butterfly Package, Multiquantum well DFB laser	CATV, video surveillance, wireless and personal comm-	Laser module with transformer coupling, internal isolator, 1000 MHz performance, 77 NTSC	DS
	257CP	1.3 μm 14-pin Butterfly Package, Multiquantum well DFB laser	CATV, video surveillance, wireless, and personal comm- unication networks	Laser module with predistortion board, internal isolator, 750 MHz performance, 77 NTSC channel load	DS

Laser Subsystems

Device Type	Part No.	Description	Application	Features	Lit.
Laser Module	es (continu	ed)			
Fabry-Perot	270А-Тур	e 1 3 μm, single-mode fiber pigtail, multiquantum well Fabry Perot laser	Telecommunications, Local loops, MANS, data communications	Wide operatring tempera- ture range, no TEC required high output power, internal backface monitor.	DS
	_270F-Type	e 1.3 μm, single-mode fiber pigtail, multiquantum well Fabry Perot laser	Narrowband video, return path systems, mixed analog and digital systems, telecommunications	Wide operating termpera- ure range, no TEC required, high output power, internal backface monitor	DS
Bidirectional	Laser Mo	dule			
	1420-Туре	e 1.3 μm Bidirectional Laser Module	LANs, MANs, Fiber- in-the-Loop Systems, Telecommunication	1.3 μm Fabry-Perot laser, InGaAsP photodetector with integral splitter, wide operating temperature range, SM fiber pigtail, high output power	DS
Transmitters					
1.3 μm Fabry-Perot	1227C	200 Mbits/s, –5 dBm, FC-PC	SONET OC-3 or OC-12 single-mode FDDI	20-pin DIP package with	DS
rubry renot	1227D	650 Mbits/s, –5 dBm, FC-PC Connector		cooler required	
	1227E	200 Mbits/s, –8 dBm, FC-PC Connector			DS
	1227F	200 Mbits/s, –8 dBm, FC-PC			DS
	1227G	650 Mbits/s, –8 dBm, FC-PC Connector			DS
	1227H	200 Mbits/s, –11 dBm, FC-PC Connector			DS
	1227J	200 Mbits/s, –5 dBm, <i>ST</i> ® Connector			DS
	1227K	650 Mbits/s, –5 dBm, <i>ST</i> Connector			DS
	1227L	200 Mbits/s, –8 dBm, <i>ST</i>			DS
	1227M	200 Mbits/s, –8 dBm, <i>ST</i>			DS
	1227N	650 Mbits/s, –8 dBm, <i>ST</i>			DS
	1227P 1227EB	200 Mbits/s, –11 dBm, <i>ST</i> Evaluation Board for 1227 Transmitter			DS DS
	1229-Туре	650 Mbits/s, Pigtailed with FC-PC, SC, or <i>ST</i> connector	SONET OC-12	Uncooled DFB laser transmitter	DS
High-Speed 1.3 µm Fabry-Perot	1238B 1238C	1062.5 Mbits/s, -8 dBm, SM-Pigtail Meets Sonet Mask Only, Not Fibre Channel Mask	Fibre channel, SONET, Serial HIPPI	Space-saving, self- contained 20-pin DIP, no thermoelectric cooler	DS DS
	1238EB	Eval. Board for 1238 Transmitter	·····	required	DS

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Laser Subsystems (continued)

Device Type	Part No.	. Description	Application	Features	Lit.
Receivers					
InGaAs PIN	1310C	InGaAs PIN, 155 Mbits/s, MM	SONET OC-3,	Pigtailed, 20-pin DIP,	DS
		FC-PC Connector	medium to high-speed	compact hermetic package	
	1310D	InGaAs PIN, 622 Mbits/s, MM	data communications		DS
	TypePart No. DescriptionApplicationFeaturesrsPIN1310CInGaAs PIN, 155 Mbits/s, MM FC-PC ConnectorSONET OC-3, medium to high-speed data communicationsPigtailed, 20-pin DIP, compact hermetic packag1310DInGaAs PIN, 622 Mbits/s, MM FC-PC ConnectorSONET oc-3, medium to high-speed data communicationsPigtailed, 20-pin DIP, compact hermetic packag1310EInGaAs PIN, 622 Mbits/s, MM FC-PC ConnectorFC-PC ConnectorSonet ompact hermetic packag1310JInGaAs PIN, 52 Mbits/s, MM FC-PC ConnectorFC-PC ConnectorSonet ompact hermetic packag1310LInGaAs PIN, 52 Mbits/s, MM FC-PC ConnectorFC-PC ConnectorSonet FC-PC Connector1310MInGaAs PIN, 52 Mbits/s, MM FC-PC ConnectorFC-PC Connector1310PInGaAs PIN, 266 Mbits/s, MM FC-PC ConnectorFC-PC Connector1310PInGaAs PIN, 52 Mbits/s, MM FC-PC ConnectorFC-PC Connector1310RInGaAs PIN, 52 Mbits/s, MM FC-PC ConnectorSONET OC-3Clock recovery and data regeneration 20-pin DIP, TIL compatible, phase- locked loop, low powerred1318A1062.5 Mbits/sFibre channel, SONETConnectorized, 20-pin DI SONETPINMultimode PigtailSONET OC-48, line terminal equipment, high-speed networksGaAs preamplifier, compact butterfly packag space-saving, self- contained 24-pin package				
	1310E	InGaAs PIN, 155 Mbits/s, MM			DS
		FC-PC Connector			
	1310F	InGaAs PIN, 622 Mbits/s, MM			DS
		FC-PC Connector			
	1310J	InGaAs PIN, 52 Mbits/s, MM			DS
		FC-PC Connector			
	1310K	InGaAs PIN, 52 Mbits/s, MM			DS
		FC-PC Connector			
	1310L	InGaAs PIN, 155 Mbits/s, MM			DS
		FC-PC Connector			
	1310M	InGaAs PIN, 622 Mbits/s, MM			DS
		FC-PC Connector			
	1310N	InGaAs PIN, 266 Mbits/s, MM			DS
		FC-PC Connector			
	1310P	InGaAs PIN, 155 Mbits/s, MM			DS
		FC-PC Connector			
	1310R	InGaAs PIN, 52 Mbits/s, MM			DS
		FC-PC Connector			
	1310S	InGaAs PIN, 52 Mbits/s, MM			DS
		ST Connector			
	1310EB	Evaluation Board for 1310			DS
		Receiver			
	1330-	155 Mbits/s	SONET OC-3	Clock recovery and data	DS
	Туре	FC-PC Connector		regeneration 20-pin DIP,	
				TTL compatible, phase-	
				locked loop, low power	
High-Speed	1318A	1062.5 Mbits/s	Fibre channel.	Connectorized, 20-pin DIP	DS
InGaAs PIN	-0	Multimode Pigtail	SONET		
InCale ADD	1210D	25 Chits/s SM EC DC Connector	SONET OC 48 line	Cats proamplifier	
IIIGaAs AFD	12100	2.5 Gbits/s, SM FC-FC Connector	terminal equipment	compact buttorfly package	D3 D6
	15190	2.5 GDRS/S, SW 37 CONNECTOR	high speed petworks	compact butterny package	D3
	12204	155 Mbite/s EC BC Connector	SONET OC 3	Space saving self	DS
	1220A	155 Mbits/s, FC-FC Connector	SONET OC 2	opace-saving, sen-	DS
	1320D	622 Mbits/s, SC Connector	SONET OC 12	contained 24-piii package	D3 D6
	12200	622 Mbits/s, FC-PC Connector	SOMET OC 12		D3
	1320D	622 MDIts/s, SC Connector	SONECT OC-12		DS

Device Type	Part No.	Description	Application	Features	Lit.
Optical Amp	lifiers and	Components			
Fiber	1712A 1712B 1712C 1712D 1712C 1712D 1712F 1713A 1713B 1713C 1713D 1713C 1713F 1713G 1713H	Pout, 11 dBm, <i>ST</i> Connector Pout, 11 dBm, FC-PC Connector Pout, 11 dBm, SC Connector Pout, 14 dBm, SC Connector Pout, 14 dBm, SC Connector Pout, 14 dBm, SC Connector Pout, 8.5 dBm, SC Connector Pout, 8.5 dBm, SC Connector Pout, 8.5 dBm, SC Connector Pout, 11.5 dBm, SC Connector Pout, 11.5 dBm, SC Connector Pout, 11.5 dBm, SC Connector Pout, 15.5 dBm, FC-PC Connector	Amplifiers for repeaters, power boosters, preamps, CATV networks, LANs, and MANs	980 nm pump, TTL compatible alarm outputs, wide operating tempera- ture range, optical input and output taps, low power consumption 1480 nm pump, TTL compatible alarm outputs, wide operating tempera- ture range, optical input and output taps, low power consumption	DS DS DS DS DS DS DS DS DS DS DS DS DS D
	1713H 1720ABC 1720BBC 1720CBC 1714ABC 1714ABC 1714ABC 1715ABC 1715BBC 1715CBC 1715CBC 1718ABC 1718BBC 1718CBC	Pout, 15.5 dBm, SC Connector Pout, 13 dBm, SC Connector Pout, 16 dBm, SC Connector Pout, 16 dBm, SC Connector Pout, 11 dBm, SC Connector Pout, 13 dBM, SC Connector Pout, 15 dBm, SC Connector Pout, 14 dBm, SC Connector Pout, 16 dBm, SC Connector Pout, 11 dBm, SC Connector Pout, 13 dBm, SC Connector Pout, 13 dBm, SC Connector Pout, 15 dBm, SC Connector	Amplifier gain blocks for repeaters, power boosters, line amplifiers, CATV, networks, LANS and MANs.	980 nm pump, optical input and output taps, wide input signal band- width 1480 nm pump for greater reliability optical input and output taps, wide input signal bandwidth	DS DS DS DS DS DS DS DS DS DS DS
Pump Lasers	263C 263D 263E 263F 263G 263H	60 mW output power 70 mW output power 80 mW output power 90 mW output power 100 mW output power 110 mW output power	Erbium-doped fiber amplifier systems	Field-proven packaging technology, compact 14-pin butterfly package, InGaAs/GaAlAs high- power quantum well chip design	DS DS DS DS DS DS

POWER PRODUCTS

AT&T Microelectronics offers a broad line of power conversion products and power protection systems to fulfill the needs of the telecommunications and electronic data processing markets.

AT&T board-mounted power modules, ranging from 0.5 W to 300 W, have small footprints, high efficiencies, and high-power densities. Our dc-dc converters range from 15 W to 1500 W, and the off-line switchers range from 50 W to 2,000 W. See details on the new line of DiskPower converters, Front Ends, and Power Shelves.

Our power systems design staff in Dallas is available to assist you in the selection of power architectures that meet your needs. Dallas is an ISO 9001 registered facility and 1994 Deming Prize winner.

Board-Mounted Power Modules

AT&T board-mounted power modules offer low profiles, high-power density, off-the-shelf, dc-dc power conversions in module sizes of 0 5 W to 300 W. Known for reliability, AT&T board-mounted power modules feature a variety of design options with typical power efficiencies in excess of 80%. State-of-the-art surface-mount technology is used to achieve high performance in a small package. MTBFs of over one million hours and a three-year warranty are standard.

A system powered by boardmounted power modules offers many user benefits. In addition to the capability for developing nonstandard voltages, the power modules can reduce the cost of power distribution by decreasing distances traveled by low voltages. Moreover, they can power a system on a fieldreplaceable basis, thereby yielding improved system reliability.

Features

- Low profiles
- High efficiencies
- 0.5 W to 300 W
- High-power densities
- Small footprints
- Remote on/off capability
- Remote sense
- Output current limiting
- Overvoltage protection
- Isolated and nonisolated models
- Input/output filtering
- External synchronization
- Parallel operation with forced load sharing
- Regulated output voltage
- -40 °C to +100 °C operating case temperature
- UL recognized
- 3-year warranty

POWER PRODUCTS

Board-Mounted Power Modules Low-Power Product Matrix (0.5 W to 50 W)

$\begin{array}{cccccccccccccccccccccccccccccccccccc$	Part Number	Nominal Input (Vdc)	Input Range (Vdc)	Output Voltage (Vdc)*	Output Current (IA)	Power, Watts (W)	Length (in.)	Width (in.)	Height (in.)	Temp. (°C)	Literature
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	112A2	5	4.5-5.5	12	0.041	0.5	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	112C2	5	4.5-5.5	15	0.100	1.5	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	112D2	5	45-5.5	25	0.030	0.75	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	112E2	5	4.55.5	12	0.125	1.5	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	113A2	5	4.5-5.5	-5	0.100	0.5	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	113AA2	5	4.5-5.5	-5	0.100	0.500	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	113B2	5	4.5-5.5	-12	0.063	0.75	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	113B3	5	4.5-5.5	-12	0.063	0.75	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	113C2	5	4.55.5	-15	0.050	0.75	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	113E2	5	4.55.5	-130	0.0005	0.065	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	113F2	5	4.5-5.5	-5	0.300	1.5	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	113F3	5	4.5-5.5	-5	0.300	1.5	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	113G2	5	4.55.5	-12	0.125	1.5	0.96	0.70	0.44	0 to +70	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RA003A	12	8.0-16.5	5	0.6	3	1.75	0.43	0.81	-10 to +50	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RA003B	12	8.0—16.5	12	0.25	3	1.75	0.43	0.81	-10 to +50	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RA003C	12	8.0—16.5	15	0.2	3	1.75	0.43	0.81	-10 to +50	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RA003BK	12	8.0—16.5	±12	±0.125	3	1.75	0.43	0.81	-10 to +50	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RA003CL	12	8.0-16.5	±15	±0.1	3	1.75	0.43	0.81	-10 to +50	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RC003A	28	16—32	5	0.6	3	1.75	0.43	0.81	-10 to +50	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RC003B	28	16—32	12	0.25	3	1.75	0.43	0.81	-10 to +50	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RC003C	28	16-32	15	0.2	3	1.75	0.43	0.81	-10 to +50	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RC003BK	28	16—32	±12	±0.125	3	1.75	0.43	0.81	-10 to +50	DS
RE003A4828-6050.631.750.430.81-10 to +50DSRE003B4828-60120.2531.750.430.81-10 to +50DSRE003C4828-60150.231.750.430.81-10 to +50DSRE003BK4828-60 ± 12 ± 0.125 31.750.430.81-10 to +50DSRE003CL4828-60 ± 12 ± 0.125 31.750.430.81-10 to +50DSRE003CL4828-60 ± 15 ± 0.1 31.750.430.81-10 to +50DSRH03A5 $4.0-7.2$ 50.631.750.430.81-10 to +50DSRH03B5 $4.0-7.2$ 120.2531.750.430.81-10 to +50DSRH03C5 $4.0-7.2$ 150.231.750.430.81-10 to +50DSRH03BK5 $4.0-7.2$ ± 12 ± 0.125 31.750.430.81-10 to +50<	RC003CL	28	16-32	±15	±0.1	3	1.75	0.43	0.81	-10 to +50	DS
RE003B4828-6012 0.25 3 1.75 0.43 0.81 -10 to $+50$ DSRE003C4828-6015 0.2 3 1.75 0.43 0.81 -10 to $+50$ DSRE003BK4828-60 ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ DSRE003CL4828-60 ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ DSRH003A5 $4.0-7.2$ 5 0.6 3 1.75 0.43 0.81 -10 to $+50$ DSRH003B5 $4.0-7.2$ 12 0.25 3 1.75 0.43 0.81 -10 to $+50$ DSRH003C5 $4.0-7.2$ 12 0.25 3 1.75 0.43 0.81 -10 to $+50$ DSRH03BK5 $4.0-7.2$ 12 0.25 3 1.75 0.43 0.81 -10 to $+50$ DSRH03BK5 $4.0-7.2$ ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ DSRH03BK5 $4.0-7.2$ ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ DSRH03BK5 $4.0-7.2$ ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ DSRH03CL5 $4.0-7.2$ ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ <	RE003A	48	28-60	5	0.6	3	1.75	0.43	0.81	-10 to +50	DS
RE003C4828-60150.231.750.430.81-10 to +50DSRE003BK4828-60 ± 12 ± 0.125 31.750.430.81-10 to +50DSRE003CL4828-60 ± 15 ± 0.1 31.750.430.81-10 to +50DSRH003A5 $4.0-7.2$ 50.631.750.430.81-10 to +50DSRH003B5 $4.0-7.2$ 120.2531.750.430.81-10 to +50DSRH003C5 $4.0-7.2$ 150.231.750.430.81-10 to +50DSRH003C5 $4.0-7.2$ ± 12 ± 0.125 31.750.430.81-10 to +50DSRH003BK5 $4.0-7.2$ ± 12 ± 0.125 31.750.430.81-10 to +50DSRH003CL5 $4.0-7.2$ ± 15 ± 0.1 31.750.430.81-10 to +50DSMA005A12 $10-15$ 5 1.00 5 2.00 1.10 <t< td=""><td>RE003B</td><td>48</td><td>28-60</td><td>12</td><td>0.25</td><td>3</td><td>1.75</td><td>0.43</td><td>0.81</td><td>-10 to +50</td><td>DS</td></t<>	RE003B	48	28-60	12	0.25	3	1.75	0.43	0.81	-10 to +50	DS
RE003BK4828-60 ± 12 ± 0.125 31.750.430.81 -10 to ± 50 DSRE003CL4828-60 ± 15 ± 0.1 31.750.430.81 -10 to ± 50 DSRH003A54.0-7.250.631.750.430.81 -10 to ± 50 DSRH003B54.0-7.2120.2531.750.430.81 -10 to ± 50 DSRH003C54.0-7.2150.231.750.430.81 -10 to ± 50 DSRH003C54.0-7.2150.231.750.430.81 -10 to ± 50 DSRH003BK54.0-7.2 ± 12 ± 0.125 31.750.430.81 -10 to ± 50 DSRH003CL54.0-7.2 ± 12 ± 0.125 31.750.430.81 -10 to ± 50 DSRH003CL54.0-7.2 ± 12 ± 0.125 31.750.430.81 -10 to ± 50 DSRH003CL54.0-7.2 ± 12 ± 0.125 31.750.430.81 -10 to ± 50 DSRH003CL54.0-7.2 ± 12 ± 0.125 31.750.430.81 -10 to ± 50 DSRH003CL54.0-7.2 ± 15 ± 0.1 31.750.430.81 -10 to ± 70 DSMA005B1210-15120.42 <td< td=""><td>RE003C</td><td>48</td><td>28-60</td><td>15</td><td>0.2</td><td>3</td><td>1.75</td><td>0.43</td><td>0.81</td><td>-10 to +50</td><td>DS</td></td<>	RE003C	48	28-60	15	0.2	3	1.75	0.43	0.81	-10 to +50	DS
RE003CL4828-60 ± 15 ± 0.1 31.750.430.81 -10 to ± 50 DSRH003A54.0-7.250.631.750.430.81 -10 to ± 50 DSRH003B54.0-7.2120.2531.750.430.81 -10 to ± 50 DSRH003C54.0-7.2150.231.750.430.81 -10 to ± 50 DSRH003C54.0-7.2 ± 12 ± 0.125 31.750.430.81 -10 to ± 50 DSRH003BK54.0-7.2 ± 12 ± 0.125 31.750.430.81 -10 to ± 50 DSRH003CL54.0-7.2 ± 12 ± 0.125 31.750.430.81 -10 to ± 50 DSRH003CL54.0-7.2 ± 12 ± 0.125 31.750.430.81 -10 to ± 50 DSRH003CL54.0-7.2 ± 15 ± 0.1 31.750.430.81 -10 to ± 50 DSRH003CL54.0-7.2 ± 15 ± 0.1 31.750.430.81 -10 to ± 50 DSRH003CL54.0-7.2 ± 15 ± 0.1 31.750.430.81 -10 to ± 70 DSMA005B1210-15120.4252.001.100.46 -10 to ± 70 DSMA005BK1210-15 ± 12 ± 0.21 <td>RE003BK</td> <td>48</td> <td>28-60</td> <td>±12</td> <td>±0.125</td> <td>3</td> <td>1.75</td> <td>0.43</td> <td>0.81</td> <td>-10 to +50</td> <td>DS</td>	RE003BK	48	28-60	±12	±0.125	3	1.75	0.43	0.81	-10 to +50	DS
RH003A5 $4.0-7.2$ 5 0.6 3 1.75 0.43 0.81 -10 to $+50$ DSRH003B5 $4.0-7.2$ 12 0.25 3 1.75 0.43 0.81 -10 to $+50$ DSRH003C5 $4.0-7.2$ 15 0.2 3 1.75 0.43 0.81 -10 to $+50$ DSRH003C5 $4.0-7.2$ ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ DSRH003BK5 $4.0-7.2$ ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ DSRH003CL5 $4.0-7.2$ ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ DSRH003CL5 $4.0-7.2$ ± 15 ± 0.1 3 1.75 0.43 0.81 -10 to $+50$ DSRH003CL5 $4.0-7.2$ ± 15 ± 0.1 3 1.75 0.43 0.81 -10 to $+50$ DSMA005A12 $10-15$ 5 1.00 5 2.00 1.10 0.46 -10 to $+70$ DSMA005B12 $10-15$ 15 0.33 5 2.00 1.10 0.46 -10 to $+70$ DSMA005BK12 $10-15$ ± 10.21 5 2.00 1.10 0.46 -10 to $+70$ DSMA005CL12 $10-15$ ± 10.7 5 2.00 1.10 0.46 -10 to $+70$ DS <td>RE003CL</td> <td>48</td> <td>28—60</td> <td>±15</td> <td>±0.1</td> <td>3</td> <td>1.75</td> <td>0.43</td> <td>0.81</td> <td>-10 to +50</td> <td>DS</td>	RE003CL	48	28—60	±15	±0.1	3	1.75	0.43	0.81	-10 to +50	DS
RH003B5 $4.0-7.2$ 12 0.25 3 1.75 0.43 0.81 -10 to $+50$ DSRH003C5 $4.0-7.2$ 15 0.2 3 1.75 0.43 0.81 -10 to $+50$ DSRH003BK5 $4.0-7.2$ ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ DSRH003CL5 $4.0-7.2$ ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ DSRH003CL5 $4.0-7.2$ ± 15 ± 0.1 3 1.75 0.43 0.81 -10 to $+50$ DSRH003CL5 $4.0-7.2$ ± 15 ± 0.1 3 1.75 0.43 0.81 -10 to $+50$ DSMA005A12 $10-15$ 5 1.00 5 2.00 1.10 0.46 -10 to $+70$ DSMA005B12 $10-15$ 15 0.33 5 2.00 1.10 0.46 -10 to $+70$ DSMA005BK12 $10-15$ ± 12 ± 0.21 5 2.00 1.10 0.46 -10 to $+70$ DSMA005CL12 $10-15$ ± 12 ± 0.21 5 2.00 1.10 0.46 -10 to $+70$ DSMA005CL12 $10-15$ ± 10.7 5 2.00 1.10 0.46 -10 to $+70$ DS	RH003A	5	4.0-7.2	5	0.6	3	1.75	0.43	0.81	-10 to +50	DS
RH003C5 $4.0-7.2$ 15 0.2 3 1.75 0.43 0.81 -10 to $+50$ DSRH003BK5 $4.0-7.2$ ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ DSRH003CL5 $4.0-7.2$ ± 15 ± 0.1 3 1.75 0.43 0.81 -10 to $+50$ DSMA005A12 $10-15$ 5 1.00 5 2.00 1.10 0.46 -10 to $+70$ DSMA005B12 $10-15$ 12 0.42 5 2.00 1.10 0.46 -10 to $+70$ DSMA005C12 $10-15$ 15 0.33 5 2.00 1.10 0.46 -10 to $+70$ DSMA005BK12 $10-15$ ± 12 ± 0.21 5 2.00 1.10 0.46 -10 to $+70$ DSMA005CL12 $10-15$ ± 12 ± 0.21 5 2.00 1.10 0.46 -10 to $+70$ DSMA005CL12 $10-15$ ± 12 ± 0.21 5 2.00 1.10 0.46 -10 to $+70$ DS	RH003B	5	4.0-7.2	12	0.25	3	1.75	0.43	0.81	-10 to +50	DS
RH003BK5 $4.0-7.2$ ± 12 ± 0.125 3 1.75 0.43 0.81 -10 to $+50$ DSRH003CL5 $4.0-7.2$ ± 15 ± 0.1 3 1.75 0.43 0.81 -10 to $+50$ DSMA005A12 $10-15$ 5 1.00 5 2.00 1.10 0.46 -10 to $+70$ DSMA005B12 $10-15$ 12 0.42 5 2.00 1.10 0.46 -10 to $+70$ DSMA005C12 $10-15$ 15 0.33 5 2.00 1.10 0.46 -10 to $+70$ DSMA005BK12 $10-15$ ± 12 ± 0.21 5 2.00 1.10 0.46 -10 to $+70$ DSMA005CL12 $10-15$ ± 12 ± 0.21 5 2.00 1.10 0.46 -10 to $+70$ DSMA005CL12 $10-15$ ± 12 ± 0.21 5 2.00 1.10 0.46 -10 to $+70$ DS	RH003C	5	4.0-7.2	15	0.2	3	1.75	0.43	0.81	-10 to +50	DS
RH003CL5 $4.0-7.2$ ± 15 ± 0.1 3 1.75 0.43 0.81 -10 to $+50$ DSMA005A12 $10-15$ 5 1.00 5 2.00 1.10 0.46 -10 to $+70$ DSMA005B12 $10-15$ 12 0.42 5 2.00 1.10 0.46 -10 to $+70$ DSMA005C12 $10-15$ 15 0.33 5 2.00 1.10 0.46 -10 to $+70$ DSMA005BK12 $10-15$ ± 12 ± 0.21 5 2.00 1.10 0.46 -10 to $+70$ DSMA005CL12 $10-15$ ± 12 ± 0.21 5 2.00 1.10 0.46 -10 to $+70$ DS	RH003BK	5	4.0-7.2	±12	±0.125	3	1.75	0.43	0.81	-10 to +50	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RH003CL	5	4.0-7.2	±15	±0.1	3	1.75	0.43	0.81	-10 to +50	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	MA005A	12	10—15	5	1.00	5	2.00	1.10	0.46	-10 to +70	DS
MA005C 12 10—15 15 0.33 5 2.00 1.10 0.46 -10 to +70 DS MA005BK 12 10—15 ±12 ±0.21 5 2.00 1.10 0.46 -10 to +70 DS MA005CL 12 10—15 ±12 ±0.21 5 2.00 1.10 0.46 -10 to +70 DS	MA005B	12	10—15	12	0.42	5	2.00	1.10	0.46	-10 to +70	DS
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	MA005C	12	10—15	15	0.33	5	2.00	1.10	0.46	-10 to +70	DS
MA005CI 12 10-15 +15 +0.17 5 2.00 1.10 0.46 -10 to +70 DS	MA005BK	12	10	±12	±0.21	5	2.00	1.10	0.46	-10 to +70	DS
	MA005CL	12	10—15	±15	±0.17	5	2.00	1.10	0.46	10 to +70	DS

*Other voltages available.

Board-Mounted Power Modules (continued) Low-Power Product Matrix (0.5 W to 50 W)

	Nominal	Input	Output	Output	Power,					
Part	Input	Range	Voltage	Current	Watts	Length	Width	Height	Temp.	~ •
Number	(Vdc)	(Vdc)	(Vdc)*	(IA)	(W)	(in.)	(in.)	(in.)	(°C)	Literature
MC005A	28	18—36	5	1.0	5	2.00	1.10	0.46	-40 to +85	DS
MC005B	28	18—36	12	0.42	5	2.00	. 1.10	0.46	-40 to +85	DS
MC005C	28	18—36	15	0.33	5	2.00	1.10	0.46	-40 to +85	DS
MC005BK	28	18—36	±12	±0.21	5	2.00	1.10	0.46	-40 to +85	DS
MC005CL	28	18—36	±15	±0.17	5	2.00	1.10	0.46	-40 to +85	DS
ME005A	48	39.5—60	5	1.0	5	2.00	1.10	0.46	-40 to +85	DS
ME005B	48	39.5—60	12	0.42	5	2.00	1.10	0.46	-40 to +85	DS
ME005C	48	39.5—60	15	0.33	5	2.00	1.10	0.46	-40 to +85	DS
ME005N	48	39.5—60	5.2	0.96	5	2.00	1.10	0.46	-40 to +85	
ME005BK	48	39.5—60	±12	±0.21	5	2.00	1.10	0.46	-40 to +85	DS
ME005CL	48	39.5—60	±15	0.17	5	2.00	1.10	0.46	-40 to +85	DS
MH005A	5	45-5.5	5	1.00	5	2.00	1.10	0.46	-10 to +70	DS
MH005B	5	4.55.5	12	0.42	5	2.00	1.10	0.46	-10 to +70	DS
MH005C	5	4.5—5.5	15	0.33	5	2.00	1.10	0.46	-10 to +70	DS
MH005BK	5	4.55.5	±12	±0.21	5	2.00	1.10	0.46	-10 to +70	DS
MH005CL	5	4.55.5	±15	±0.17	5	2.00	1.10	0.46	-10 to +70	DS
MK005CL5	48	38—72	20	120	5	2.00	1.19	0.46	-40 to +85	DS
MW005A	48	36-72	5	1.0	5	2.00	1.10	0.46	-40 to +85	DS
MW005B	48	36—72	12	0.42	5	2.00	1.10	0.46	-40 to +85	DS
MW005C	48	36—72	15	0.33	5	2.00	1.10	0.46	-40 to +85	DS
MW005AJ	48	36—72	±5	±0.5	5	2.00	1.10	0.46	-40 to +85	DS
MW005BK	48	36—72	±12	0.21	5	2.00	1.10	0.46	-40 to +85	DS
MW005CL	48	3672	±15	0.17	5	2.00	1.10	0.46	-40 to +85	DS
FE008AJ4	48	39.5-60	±5	1.2	8	2.00	2.00	0.50	-40 to +85	DS
FE008AJ3	48	39.5—60	±5	2.0, -0.4	8	2.00	2.00	0.50	0 to +70	DS
MA010A	12	10—15	5	2.00	10	2.00	1.60	0.50	-10 to +50	DS
MA010B	12	10—15	12	0.83	10	2.00	1.60	0.50	-10 to +50	DS
MA010C	12	10—15	15	0.67	10	2.00	1.60	0.50	-10 to +50	DS
MA010BK	12	10-15	±12	±0.42	10	2.00	1.60	0.50	-10 to +50	DS
MA010CL	12	10—15	±15	±0.33	10	2.00	1.60	0.50	-10 to +50	DS
MC010A	28	18—36	5	2.0	10	2.00	1.60	0.50	-40 to +85	DS
MC010B	28	18—36	12	0.83	10	2.00	1.60	0.50	-40 to +85	DS
MC010C	28	18—36	15	0.67	10	2.00	1.60	0.50	-40 to +85	DS
MC010BK	28	18—36	±12	0.43	10	2.00	1.60	0.50	-40 to +85	DS
MC010CL	28	18—36	±15	0.33	10	2.00	1.60	0.50	-40 to +85	DS
MH010A	5	4.5-5.5	5	2.00	10	2.00	1.60	0.50	-10 to +50	DS
MH010B	5	4.5-5.5	12	0.83	10	2.00	1.60	0.50	-10 to +50	DS
MH010C	5	4.5-5.5	15	0.67	10	2.00	1.60	0.50	-10 to +50	DS

*Other voltages available.

POWER PRODUCTS

Board-Mounted Power Modules (continued) Low-Power Product Matrix (0.5 W to 50 W)

	Nominal	Input	Outpu	t Output	Power,					
Part	Input (Vda)	Range	Voltag	e Current	Watts	Length	Width	Height	Temp.	T itonoturno
Number	(vac)	(vac)	(vac)		(w)	(in.)	(m.)	(in.)	(-0)	Literature
MH010BK	5	4.55.5	±12	±0.42	10	2.00	1.60	0.50	-10 to $+50$	DS
MH010CL	5	4.5-5.5	±15	±0.33	10	2.00	1.60	0.50	-10 to +50	DS
MW010A	48	3672	5	2.0	10	2.00	1.60	0.50	-40 to +85	DS
MW010B	48	36-72	12	0.83	10	2.00	1.60	0.50	-40 to +85	DS
MW010C	48	36-72	15	0.67	10	2.00	1.60	0.50	-40 to +85	DS
MW010BK	48		±12	0.43	10	2.00	1.60	0.50	-40 to +85	DS
MW010CL	48	36-72	±15	0.33	10	2.00	1.60	0.50	-40 to +85	DS
SE014S110	48	40-60	110	130	14	2.00	2.00	0.50	-40 to +85	DS
LW016AJ	48	36—75	±5	±16	16	2.00	2.00	0.375	-25 to +71	
LW020A	48	36—75	5	4	20	2.00	2.00	0.375	-25 to +71	
NH020F	5	4.5-5.5	3.3	6	20	2.50	0.24	0.55	0 to +55	DS
CC025AJ	28	18—36	±5	2.50	25	2.80	2.40	0.50	-40 to +95	DS
CC025BK	28	18—36	±12	1.04	25	2.80	2.40	0.50	-40 to +95	DS
CC025CL	28	18—36	±15	0.83	25	2.80	2.40	0.50	-40 to +95	DS
CC025ABK	28	18—36	5, ±12	5, ±1	25	2.80	2.40	0.50	-40 to +95	
CC025ACL	28	18—36	5, ±15	5, ±0.8	25	2.80	2.40	0.50	-40 to +95	
CW025AJ	48	36-72	±5	±2.50	25	2.80	2.40	0.50	-40 to +95	DS
CW025BK	48	36—72	±12	±1.04	25	2.80	2.40	0.50	-40 to +95	DS
CW025CL	48	36—72	±15	±0.83	25	2.80	2.40	0.50	-40 to +95	DS
CW025ABK	48	36—72	5, ±12	5, ±1	25	2.80	2.40	0.50	-40 to +95	
CW025ACL	48	36—72	5, ±15	5, ±0.8	25	2.80	2.40	0.50	-40 to +95	
DC025AA	28	18—36	5, 5	2.5, 2.5	25	2.80	2.40	0.50	-40 to +95	DS
DC025AF	28	18—36	5, 3.3	2.50, 2.50	25	2.80	2.40	0.50	-40 to +95	DS
DC025AJ	28	18—36	±5	±2.50	25	2.80	2.40	0.50	-40 to +95	DS
DC025BB	28	18—36	12, 12	1.04, 1.04	25	2.80	2.40	0.50	-40 to +95	DS
DC025BK	28	18—36	±12	±1.04	25	2.80	2.40	0.50	-40 to +95	DS
DC025CC	28	18—36	15, 15	0.83, 0.83	25	2.80	2.40	0.50	-40 to +95	DS
DC025CL	28	18	±15	±0.83	25	2.80	2.40	0.50	-40 to +95	DS
DC025ABK	28	18—36	5, ±12	5, ±1	25	2.80	2.40	0.50	-40 to +95	
DC025ACL	28	18	5, ±15	5, ±0.8	25	2.80	2.40	0.50	-40 to +95	
DW025AA	48	36—72	5, 5	2.50, 2.50	25	2.80	2.40	0.50	-40 to +95	DS
DW025AB	48	36—72	5, 12	5, 1	25	2.80	2.40	0.50	-40 to +95	DS
DW025AF	48	36—72	5, 3.3	2.50, 2.50	25	2.80	2.40	0.50	-40 to +95	DS
DW025AJ	48	36—72	±5	±2.50	25	2.80	2.40	0.50	-40 to +95	DS
DW025BB	48	36-72	12, 12	1.04, 1.04	25	2.80	2.40	0.50	-40 to +95	DS
DW025BK	48	36—72	±12	±1.04	25	2.80	2.40	0.50	-40 to +95	DS
DW025CC	48	36—72	15, 15	0.83, 0.83	.25	2.80	2.40	0.50	-40 to +95	DS
DW025CL	48	36—72	±15	±0.83	25	2.80	2.40	0.50	-40 to +95	DS
DW025ABK	48	36-72	5, ±12	5. ±1	25	2.80	2.40	0.50	-40 to +95	
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*Other voltages available.

Board-Mounted Power Modules (continued) Low-Power Product Matrix (0.5 W to 50 W)

	Nominal	Input	Output	Output	Power,					
Part Number	(Vdc)	Range (Vdc)	Voltage (Vdc)*	(IA)	Watts (W)	Length (in.)	Width (in.)	Height (in.)	Temp. (°C)	Literature
DW025ACI	48	36—72	5, ±15	5, ±0.8	25	2.80	2.40	0.50	-40 to +95	
CC030A	28	18—36	5	6.0	30	2.80	2.40	0.50	-40 to +100	DS
CC030B	28	1836	12	2.5	30	2.80	2.40	0.50	-40 to +100	DS
CC030C	28	18—36	15	2.0	30	2.80	2.40	0.50	-40 to +100	DS
CW030A	48	36—72	5	6.0	30	2.80	2.40	0.50	-40 to +95	DS
CW030B	48	36—72	12	2.5	30	2.80	2.40	0.50	-40 to +95	DS
CW030C	48	36—72	15	20	30	2.80	2.40	0.50	-40 to +95	DS
LW030A	48	36—75	5	6	30	2.40	2.80	0.375	-30 to +75	DS
JC030A	24	18-36	5	6.0	30	2.40	2 28	0 50	-40 to +100	DS
JC030B	24	18—36	12	2.5	30	2.40	2.28	0 50	-40 to +100	DS
JC030C	24	18—36	15	2.0	30	2.40	2.28	0.50	-40 to +100	DS
JW030A	48	36—72	5	6.0	30	2.40	2.28	0.50	-40 to +100	DS
JW030B	48	36—72	12	2.5	30	2.40	2.28	0.50	-40 to +100	DS
JW030C	48	36—72	15	2.0	30	2.40	2.28	0.50	-40 to +100	DS
JW030D	48	36—72	2	6.5	13	2.40	2.28	0.50	-40 to +100	DS

*Other voltages available.

POWER PRODUCTS

High-Power Product Matrix (50 W to 300 W)

	Nominal	Input	Output	Output	Power,					
Part	Input	Range	Voltage	Current	Watts	Length	Width	Height	Temp.	
Number	(Vdc)	(Vdc)	(Vdc)*	(IA)	(W)	(in.)	(in.)	(in.)	(°C)	Literature
JC050A1	28	18—36	5	10	50	2.40	2.28	0.50	-40 to +100	DS
JC050B1	28	18	12	4.2	50	2.40	2.28	0.50	-40 to +100	DS
JC050C1	28	18—36	15	3.3	50	2.40	2.28	0.50	-40 to +100	DS
JC050F1	28	18—36	3.3	10	50	2.40	2.28	0.50	-40 to +100	DS
JC075A1	28	18—36	5	15	75	2.40	2.28	0.50	-40 to +100	DS
JC075B1	28	18—36	12	6.3	75	2.40	2.28	0.50	-40 to +100	DS
JC100A1	28	18—36	5	20	100	2.40	2.28	0.50	-40 to +100	DS
JC100B1	28	18—36	12	8.3	100	2.40	2.28	0.50	-40 to +100	DS
JC100C1	28	18—36	15	6.7	100	2.40	2.28	0.50	-40 to +100	DS
JC100F1	28	18—36	3.3	20	100	2.40	2.28	0.50	-40 to +100	DS
JW050A	48	36—72	5	10	50	2.40	2.28	0.50	-40 to +100	DS
JW050B	48	36—72	12	4.2	50	2.40	2.28	0.50	-40 to +100	DS
JW050C	48	36-72	15	3.3	50	2.40	2.28	0.50	-40 to +100	DS
JW050F	48	36—72	3.3	10	33	2.40	2.28	0.50	-40 to +100	DS
JW075A1	48	36—72	5	15	75	2.40	2.28	0.50	-40 to +100	DS
JW075B1	48	36—72	12	6.3	75	2.40	2.28	0.50	-40 to +100	DS
JW075D1	48	36—72	2	15	75	2.40	2.28	0.50	-40 to +100	DS
JW100A	48	36—72	5	20.0	100	2.40	2.28	0.50	-40 to +100	DS
JW100B	48	36—72	12	8.3	100	2.40	2.28	0.50	-40 to +100	DS
JW100C	48	36—72	15	6.7	100	2.40	2.28	0.50	-40 to +100	DS
JW100F	48	36—72	3.3	20.0	66	2.40	2.28	0.50	-40 to +100	DS
JW150A	48	36—72	5	30	150	2.40	2.28	0.50	-40 to +100	DS
JW150B	48	36—72	12	12.5	150	2.40	2.28	0.50	-40 to +100	DS
JW150C	48	36—72	15	10.0	150	2.40	2.28	0.50	-40 to +100	DS
JW150F	48	36—72	3.3	30	99	2.40	2.28	0.50	-40 to +100	DS
FC050A	28	18—36	5	10.0	50	4.80	2.50	0.50	0 to +90	DS
FC050B	28	18	12	4.2	50	4.80	2.50	0.50	0 to +90	DS
FC050C	28	18—36	15	3.3	50	4.80	2.50	0.50	0 to +90	DS
FC050D	28	18-36	2	10	20	4.80	2.50	0.50	0 to +90	
FC050F	28	18-36	3.3	10.0	33	4.80	2.50	0.50	0 to +90	DS
FC100A	28	18—36	5	20.0	100	4.80	2.50	0.50	0 to +90	DS
FC100B	28	18-36	12	8.3	100	4.80	2.50	0.50	0 to +90	DS
FC100C	28	18-36	15	6.7	100	4.80	2.50	0.50	0 to +90	DS
FC100D	28	18—36	2	20.0	40	4.80	2.50	0.50	0 to +90	Without
FC100F	28	18	3.3	20.0	66	4.80	2.50	0.50	0 to +90	DS
FC150A	28	18—36	5	30.0	150	4.80	2.50	0.50	0 to +90	DS
FC150C	28	18-36	15	10.0	150	4.80	2.50	0.50	0 to +90	DS
FC150D	28	18—36	2	30.0	60	4.80	2.50	0.50	0 to +90	DS
FC150F	28	18—36	3.3	30.0	100	4.80	2.50	0.50	0 to +90	DS

*Other voltages available.

High-Power Product Matrix	(50 W to 300 W) (continued)
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	Nominal	Input	Output	Output	Power,			4 4	_	
Part Number	Input (Vdc)	Range	Voltage	Current	Watts	Length	Width	Height	Temp.	Literature
FC250A1		19 26		<u></u>	250	2.40	4.60	0.50	$\frac{(0)}{(0)}$	DS
FC250D1	20	10	12	20.0	250	2.40	4.00	0.50	-40 to $+100$	DS
FC250D1	28	18	12	20.8	250	2.40	4.00	0.50	$\frac{-40}{40}$ to ± 100	DS
FC250C1	28	18		10.7	250	2.40	4.00	0.50	-40 to $+100$	DS
FC250F1		18-30	<u> </u>	50	250	2.40	4.00	0.50	-40 to $+100$	DS
FEUSUA	48	38-00	2	10.0	50	4.80	2.50	0.50	0 to +90	DS
FE050B	48	38-60	12	4.2	50	4.80	2.50	0.50	0 to +90	DS
FE050C	48	38-60	15	3.33	50	4.80	2.50	0.50	0 to +90	
FE050D	48	3860	2.0	10.0	20	4.80	2.50	0.50	0 to +90	DS
FE050F	48		3.3	10.0	33	4.80	2.50	0.50	0 to +90	DS
FE050H	48	38-60	24	2.1	50	4.80	2.50	0.50	0 to +90	DS
FE100A	48	38—60	5	20.0	100	4.80	2.50	0.50	0 to +90	DS
FE100B	48	3860	12	8.3	100	4.80	2.50	0.50	0 to +90	DS
FE100C	48	38—60	15	6.7	100	4.80	2.50	0.50	0 to +90	
FE100D	48	3860	2.0	20.0	40	4.80	2 50	0.50	0 to +90	DS
FE100F	48	38—60	3.3	20.0	66	4.80	2.50	0.50	0 to +90	DS
FE100H	48	38—60	24	4.2	100	4.80	2.50	0.50	0 to +90	DS
FE150A	48	38—60	5	30.0	150	4.80	2.50	0.50	0 to +90	DS
FE150B	48	38—60	12	12.5	150	4.80	2.50	0.50	0 to +90	DS
FE150C	48	38—60	15	10.0	150	4.80	2.50	0.50	0 to +90	
FE150D	48	38—60	2	30.0	60	4.80	2.50	0.50	0 to +90	DS
FE150F	48	38—60	3.3	30.0	100	4.80	2.50	0.50	0 to +90	DS
FE150H	48	38—60	24	6.25	150	4.80	2.50	0 50	0 to +90	DS
FE200A9	48	3860	5	40	200	4.80	2 50	0.50	0 to +80	DS
FE200B9	48	38—60	12	16.6	200	4.80	2.50	0.50	0 to +80	DS
FE200F9	48	38—60	3.3	40	132	4.80	2.50	0.50	0 to +80	DS
FW050A	48	36—72	5.0	10.0	50	4.80	2.50	0.50	0 to +90	DS
FW050B	48	36—72	12	4.2	50	4.80	2.50	0.50	0 to +90	DS
FW050C	48	36—72	15	3.33	50	4.80	2.50	0.50	0 to +90	DS
FW050D	48	36—72	2	10	20	4.80	2.50	0.50	0 to +90	
FW050F	48	36—72	3.3	10	33	4.80	2.50	0.50	0 to +90	DS
FW100A	48	36—72	5.0	20.0	100	4.80	2.50	0.50	0 to +90	DS
FW100B	48	36—72	12	8.4	100	4.80	2.50	0.50	0 to +90	DS
FW100C	48	36—72	15	6.7	100	4.80	2.50	0.50	0 to +90	'DS
FW100D	48	36—72	2	20	40	4.80	2.50	0.50	0 to +90	
FW100F	48	36—72	3.3	20	66	4.80	2.50	0.50	0 to +90	

*Other voltages available.

POWER PRODUCTS

Part	Nominal Input	Input Range	Output Voltage	Output Current	Power, Watts	Length	Width	Height	Temn	
Number	(Vdc)	(Vdc)	(Vdc)*	(IA)	(W)	(in.)	(in.)	(in.)	(°C)	Literature
FW150A	48	36—72	5.0	30.0	150	4.80	2.50	0.50	0 to +90	DS
FW150B	48	36—72	12	12.6	150	4.80	2.50	0.50	0 to +90	DS
FW150C	48	3672	15	10.0	150	4.80	2.50	0.50	0 to +90	DS
FW150D	48	36—72	2	30	60	4.80	2.50	0.50	0 to +90	
FW150F	48	36—72	3.3	30	99	4.80	2.50	0.50	0 to +90	DS
FW250A1	48	36—75	5	50	250	2.40	4.60	0.50	-40 to +100	DS
FW250B1	48	36—75	12	20.8	250	2.40	4.60	0.50	-40 to +100	DS
FW250F1	48	36—75	3.3	50	250	2.40	4.60	0.50	-40 to +100	DS
FW300A1	48	36—75	5	60	300	2.40	4.60	0.50	-40 to +100	DS
FW300B1	48	36—75	12	25	300	2.40	4.60	0.50	-40 to +100	DS
FW300F1	48	36—75	3.3	60	300	2.40	4.60	0.50	-40 to +100	DS

High-Power Product Matrix (50 W to 300 W) (continued)

*Other voltages available

For additional information, call your AT&T Account Manager, your local distributor, or 1-800-372-2447

DiskPower Modules

The TW050AB and TW070AB DiskPower Converters are designed to power large-capacity disk drives and other mass storage devices. They are ideal for implementing distributed power systems in disk array systems.

The modules operate with input voltages from 36 Vdc to 72 Vdc and provide two outputs: 5 Vdc and 12 Vdc. Each output is independently regulated. The output ripple and noise are very low. The 12 V output supports surge currents up to 3.5 A for the TW050AB and up to 5 A for the TW070AB in order to spin-up large disk drives.

Other features include overvoltage protection and current limiting on each output and a remote on/off input. The modules offer a very low failure rate and reliability ten times better than the disk drives being powered.

Features

- Small-Business Card Size 2.0 in. x 3.5 in. x 0.625 in. 50.8 mm x 88.9 mm x 15.9 mm
- Low output noise
- Economical open-frame construction
- Automated assembly with all surface-mount construction offers high reliability and consistency

- Input and outputs are electrically isolated
- 2:1 input voltage range
- Remote on/off
- Overtemperature protection
- UL 1950, CSA 22.2-950, and EN60950 approvals are pending

-	IONI	01101	moutioo		
					-
			Nominal	Inout	

DiskPower Modules

Part Number	Nominal Input (Vdc)	Input Range (Vdc)	Output Voltage (Vdc)*	Output Current (IA)	Power, Watts (W)	Length (in.)	Width (in.)	Height (in.)	Temp. (°C)	Literature
TW050AB	48	36—72	5.12	1.5, 2.5 (3.5)	37.5	2.0	3.5	0.625	0 to +50	DS
TW070AB	48	36—72	5.12	2.2, 2.5 (5.0)	41	2.0	3.5	0.625	0 to +50	DS

Enhanced Distributed Power Architecture (EDPA)

RM Series Front Ends

AT&T front-end power supplies convert ac input power to a regulated, SELV dc bus voltage. They are parallelable, are power factor corrected, and can be used redundantly. AT&T's front-end power supplies feature a full complement of alarm and control functions to ease diagnostics and are available in power levels up to 2000 W.

Features

- Recognized by *Underwriters Laboratories* to *UL* 1950, certified by *CSA* to *CSA* 22.2 and also licensed to IEC950
- Meets FCC Class A EMI requirements for conducted and radiated emissions
- Autoranging for worldwide input voltage ranges
- Power factor corrected
- Outputs are overvoltage protected
- Overtemperature protection
- Redundant parallel operation
- Remote ON/OFF
- Current sharing
- Hot insertion/removal (hot plug)
- Power fail and fault alarms
- Margining

RM Series Front Ends

Watts	Input	Output		Dimensions		Part
(W)	(Vac)	(Vdc, A)	L	W	н	Number
750	85—264	48, 15.6	12	3	5	RM0750A
750	85—264	54.5, 13.7	12	3	5	RM0750H
750	85—264	56.25, 13.3	12	3	5	RM0750L
1000	85—264	48, 20.8	12	4	5	RM1000A
1000	85—264	54.5, 18.3	12	4	5	RM1000H
1000	85—264	56.25, 17.7	12	4	5	RM1000L
1500	102264	48, 31.2	12	6	5	RM1500A
1500	102-264	54.5, 27.5	12	6	5	RM1500H
1500	102—264	56.25, 26.6	12	6	5	RM1500L
2000	170—264	48, 41.6	12	6	5	RM2000A
2000	170—264	54.5, 36.7	12	6	5	RM2000H
2000	170—264	56.25, 35.5	12	6	5	RM2000L

POWER PRODUCTS

Power Shelves

AT&T Power Shelves mount in a standard 19 in. rack and provide all of the wiring and control signals for the front-end power supplies and battery interface units.

Features

- Rack mount sheet metal chassis (3U height, 19 in. rack)
- ac input module with line filtering, circuit breaker, and ac present LED
- Power system controller

Power Shelves

Number of Power Slots	Units Accomodated Part Na				
4	750 W	PS3000A4			
3	1000 W	PS3000A3			
2	1500 W/2000 W	PS3000A2			

dc-dc Converters

Capabilities

- AT&T has more than 50 years experience in designing and manufacturing custom dc-dc converters typically ranging from 15 W to 1500 W with a wide variety of optional features.
- Custom design capabilities to address both United States and European requirements and standards.
- Manufacturing capabilities are available in the United States (Dallas, TX), Europe (Malmesbury, UK), and Mexico (Matamoros).
- AT&T's Dallas manufacturing site includes fully equipped product qualification facilities to meet FCC, UL, CSA, and other world regulatory and safety agency requirements.

 Surface-mount technology is available to provide high-power densities, modular packaging flexibility, and high quality.

Features

- Wide input voltage range
- Low-profile designs
- Wide operating temperature range
- Input-to-output isolation
- Inrush protection to provide hot plug-in capability
- Meets CISPR and FCC EMI and susceptibility requirements
- Externally synchronized switching frequency
- Customized alarms for input/ output conditions
- Load sharing with redundancy and fault tolerance
- Programmable overcurrent shutdown
- · Remote sensing

- Output current limiting/shutdown
- AT&T can provide a fully customized dc-dc converter solution using customer-specified hardware.
- AT&T offers a standard family of proven high-reliability *Fastech* and TRANSPAC dc-dc converters that can be modified if necessary to meet specific application requirements. *Fastech* and TRANSPAC dc-dc converters use AT&T connector systems and are plug-in type circuit card modules which have standardized feature sets.

The following is a representative listing of AT&T *Fastech*, TRANSPAC, and custom dc-dc converter products.

TRANSFORMERS AND INDUCTORS

AT&T manufactures more than 3000 different transformers for telecommunications and power applications. This guide includes those transformers designed to meet the need for modem, ISDN, and highfrequency applications. In addition, AT&T manufactures custom power magnetics for both linear and switched-mode power supplies.

AT&T manages its transformer division from the Power Systems location in Mesquite, Texas. Each of our manufacturing locations has been ISO and BABT Certified, and AT&T Microelectronics Power Systems is the first American manufacturer to be awarded the Deming Prize for quality.

Along with the expertise that goes with 30 years of magnetics design and manufacturing experience, AT&T offers the following features and benefits.

- Low-profile packaging
- Compatibility with ITU-T, ANSI, and *IEEE* standards
- Compliance with Bellcore TR-NWT-00357 Component Reliability Requirements
- Compliance with AT&T X-74550 Assembly Process Qualification Requirements
- WSF/TIC compatibility
- International safety requirement compliance
- Surface-mount designs

Attractive Quality

When you order AT&T transformers, you receive not only reliable, high-quality products, but also our value-added commitment, which includes:

- Preorder support Our experienced Bell Laboratories magnetics designers provide the technical support required to incorporate our components into your design/production needs. In many cases, our magnetics designers have identified circuit or interface related problems which would have otherwise remained unresolved.
- **Product quality** Reliability and quality are built into our products and are ensured through validated design practices, qualification of new designs, annual requalification of manufactured products, and strict control of materials and manufacturing processes.
- Custom design AT&T offers both catalog and custom solutions to meet its customer's needs. Our designers work with each customer to ensure that the design objectives and specifications are understood. The specifications, initially expressed in terms of circuit performance, are analyzed and translated into magnetic parameters. Engineering samples are made and provided to customers for incircuit evaluation. After customer approval of engineering samples and agreement on requirements, the custom product is developed for production.

Product Availability

All transformers listed in this guide are available. Call 1-800-372-2447 to request our Transformers Selection Guide and for sales information. For technical assistance, call the Power Systems hot line 1-800-526-7819.
TRANSFORMERS

Modem Transformers

AT&T has an expanding portfolio of transformers designed to serve all segments of the high-speed modem market. All transformers meet appropriate industry and safety standards.

		Agency	Maximum Insertion Loss @ 1 kHz	Maximum Return Loss 300 Hz—3 kHz	Minimum Frequency Response 0.2 dB Shaping
Part No.	Comcode	Approvals	(dB)	(dB)	Relative to 1 kHz
2746J V.32bis	107265860	UL, CSA	1.8	23	200 Hz-5 kHz
2769A V.32bis	106684939	UL, CSA	1.2	24	200 Hz—6 kHz
2770A V.32bis	1066684947	UL, CSA	1.2	24	200 Hz-6 kHz
2778A V.32bis BABT	107240186	UL, CSA, BABT	12	15	200 Hz—3 kHz
2780A V.32bis SMT	107244246	UL, CSA	1.6	24	200 Hz—6 kHz
2791A V.32bis	107538092	UL, CSA	2.0	24	100 Hz-4 kHz
2794J V.32bis	107643868	UL, CSA Pending	1.8	23	200 Hz—5 kHz
2746K V.34	107314338	UL, CSA	2.1	22	200 Hz—4 kHz
2781A V.34 BABT	107244253	UL, CSA, BABT	20	20	200 Hz—4 kHz
2783A V.34 SMT	107244287	UL, CSA	1.8	21	200 Hz—5 kHz
2789A V.34	107434086	UL, CSA	2.4	25	100 Hz—5 kHz
2791B V.34	107538415	UL, CSA	2.6	20	100 Hz—4 kHz
2793A V.34 BABT	107593535	UL, CSA, BABT	20	20	100 Hz-2 kHz
2784A V.32bis PCMCIA*	107390114	UL, CSA	2.7	25	200 Hz—6 kHz
2792A V.34 PCMCIA	107565426	UL, CSA	27	25	100 Hz—4 kHz
2792B V.34 BABT PCMCIA	107565434	<i>UL, CSA</i> , BABT Pending	2.8	20	100 Hz—4 kHz
2786A V.34 PCMCIA	107451358	UL, CSA	2.9	30	200 Hz—5 kHz
2796A V.34 PCMCIA	1076768253	UL, CSA Pending	2.7	30	200 Hz—5 kHz

 Available with multiple terminal configurations. AT&T is developing additional PCMCIA transformers for applications requiring lower profile or lower distortion

ISDN Transformers

AT&T offers ISDN S/T- and U-Interface transformers compatible with various ISDN transceivers.

Part No.	Comcode	Associated AT&T Transceiver IC	Turns Ratio (PRI:SEC)	Maximum Primary Inductance (mH)	Maximum Primary Leakage Inductance (µH)
2754G2 U Interface	106376759	T7262/T7263	1:2.5	12.0	25 6
2754H21 U Interface	106559990	T7264, T7256 & T7237	1:1.5	34.2	76.4
2754J2 ² U Interface	107155426	T7264, T7256 & T7237	115	34.2	76.4
2754K2 ³ U Interface	107306946	T7264, T7256 & T7237	1:1.5	34 2	76.4
2768A ⁴ Dual PKG S/T Interface	106546575	T7250C, T7256	1.2.5	22 0	4.0
2776A ⁵ S/T Interface	107049942	T7250C, T7256	1.2.5	22.0	50

In Development: Dual-Package Surface-Mount S/T-Interface Unit, Dual-Package PCMCIA S/T-Interface Unit

1 For North American use only.

2. For Far East applications

3 For European, North American, and Far East applications.

4. Transmit and receive transformer in single package

5 Meets European safety agency requirements

Total Harmonic Distortion	Maximum dc R e sistance Primary (Ω)	Maximum dc Resistance Secondary (Ω)	Minimum Breakdown Voltage (Vrms)	Maximum Length x Width x Height (Inches)
–76.5 dB Max @ –10 dBm, 600 Hz	90	120	1500	0 72 x 0 63 x 0.56
–78 dB Max @ –10 dBm, 600 Hz	71	93	1500	0.62 x 0.70 x 0.35
–78 dB Max @ –10 dBm, 600 Hz	71	93	1500	0.62 x 0.80 x 0.35
–86 dB Max @ –9 dBm, 380 Hz	69	69	3000	0 71 x 0.71 x 0 565
–76 dB Max @ –10 dBm, 600 Hz	88	117	1500	0 62 x 0.80 x 0.295
-76 dB Max @ -10 dBm, 600 Hz	118	126	1000	0 69 x 0.71 x 0 472
–76 5 dB Max @ –10 dBm, 600 Hz	90	120	1000	0 66 x 0 56 x 0.481
–81 dB Max @ –9 dBm, 600 Hz	118	155	1500	0 72 x 0 63 x 0.56
–86 dB Max @ –3 dBm, 600 Hz	109	135	3000	0.71 x 0.71 x 0.495
–81 dB Max @ –9 dBm, 600 Hz	88	118	1500	0.62 x 0.80 x 0 393
–90 dB Max @ –9 dBm, 600 Hz	140	165	1000	1 145 x 0 95 x 0 50
–86 dB Max @ –10 dBm, 600 Hz	152	165	1000	0 69 x 0.71 x 0.472
-82 dB Max @ -3 dBm, 300 Hz	120	120	3000	0 71 x 0 71 x 0.42
–77 dB Max @ –10 dBm, 600 Hz	140	170	1000	0 60 x 0 33 x 0.170
–80 dB Max @ –10 dBm, 600 Hz	180	156	1000	0.66 x 0.66 x 0.172
–80 dB Max @ –10 dBm, 600 Hz	160	183	1500	0 66 x 0.66 x 0.172
–85 dB Max @ –10 dBm, 600 Hz	165	200	1000	0 75 x 0 33 x 0.172
-80 dB Max @ -10 dBm, 600 Hz	165	165	1000	0 60 x 0 33 x 0.170

Maximum Interwinding Capacitance (pF)	Maximum dc Resistance Primary (Ω)	Maximum dc Resistance Secondary (Ω)	Minimum Breakdown Voltage (Vrms)	Maximum Length x Width x Height (Inches)
	25	12.2	1000	1 03 x 1.04 x 0 48
	56	13.05	1000	1 03 x 1 04 x 0 48
	7 35	13.65	1000	1 03 x 1.04 x 0 48
	7.4	17.4	1500	1.03 x 1.04 x 0 48
100	2.3	5.8	2400	0 93 x 0 48 x 0 50
100	1.55	8.75	3000	0.68 x 1.15 x 0 51

TRANSFORMERS

DS1/T1/CEPT Line Interface Transformers

Products listed in order by turns ratio.

		Turns	Minimum Primary	Maximum Primary Leakage	Maximum Interwinding	Maximum dc Resistance	Maximum dc Resistance	Minimum ac
Apparatus	Ordering	Ratio	Inductance	Inductance	Capacitance	Primary	Secondary	Breakdown
Code	Comcode	(PRI:SEC)	(mH)	(μΗ)	(pF)	(Ω)	(Ω)	(Vac)
2745AG2	106186430	1:1 CT	2 30	2	25	10	12	500
2664AM ^{1, 4, 7}	107620130	1:1 CT	1.50	3.5	5	1.0	05	1000
2745AJ2	106727605	1:1 07 CT	1.00	0.8	75 (TYP)	05	10	1000
2745G3	106696297	1:1.14 CT	1 75	14	90	10	11	850
2745AK21	107154676	1·1.14 CT	1 25	2	5	09	08	1000
2745AL24	107194326	1:1.14 CT	1.25	18	14	0.9	0.4	1000
2741H2 ¹	106003999	1:1 36 CT	0.92	2.5	5	0.9	1.5	850
2745CA	106445711	1:1.36 CT	0.92	0.7	20	0.9	1.2	850
2741J ¹	105668727	1CT:1.37	0.92	2	8	0.9	1.3	850
2745C2	106004013	1:1.37 CT	0.68	1.2	85 (TYP)	10	1.4	850
2745AE	104450697	1:1.43 CT	1.30	1.2	90	06	09	850
2741G2 ¹	106003973	1 CT:2 CT	3.15	6.5	5	19	4.4	850
2745AF2	106004047	1 CT:2 CT	3.15	3	23	16	3.2	850
2741R2	107213860	1 CT:1:3 CT	0.93	0.5	65	1.0	3.3	850
2745AH	105710461	1:3.76 CT	0.43	0.7	45	0 4	1.5	850
2771B (SMT)	107084352	1:1.14 CT	1.75	2	80	0.8	0.8	850
2771D (SMT) ¹	107310500	1:1.14 CT	1.30	15	5	0.6	0.7	1500
2771G (SMT)	107089906	1·1 37 CT	0.70	12	50 (TYP)	0.4	0.5	850
2771C (SMT)	107089880	1 CT:2 CT	3.15	3	30	1.1	23	1500
2664AK	107594988	1:1 91CT	0.78	0.5	20 (TYP)	0.7	13	1500
2664AL	107591851	1:2.1CT	0 78	0.55	20 (TYP)	09	2.0	1500
2664AJ	107542565	1:2.43CT	0.97	0.45	20 (TYP)	1.4	34	1500
2771E (SMT) ¹	107310518	1:2 CT	3.15	3.2	5	1.0	3.4	1500
2771H (SMT)	107436495	See Note 6	1.85	1.3	23	0.74	0.97	1500
2779C (SMT)	107637815	1:1.36 CT	1.00	0.7	25	0.85	1.3	1500
2779H (SMT)	107728594	1:1.07 CT	1.65	0 53	15	1.1	1.1	1500
		1:1.37 CT	1.65	0.50	22	1.1	1.45	1500
2779J (SMT)	107714057	1:1.07 CT	13	0.8		06	1.1	1500
2779L (SMT)	107714842	1:1.14 CT	1.25	1.8	14	0.9	0.48	1500

DS3/STS-1/E3 Transformer

2745AM2 ³	107253056	1:1:1	0.07	0.1	15 (TYP)	0.20	0.40	1500	

Wide-Band Hybrid Transformer

106648009

2689J2

3 dB $75\Omega : 75\Omega + 75\Omega$

20 kHz—800 MHz

1. CT = center tap.

2. An electrostatic shield is included between the primary and secondary.

6. Transformer has a dual turns ratio (1:1.0 Band 1:1.36).7. Designed for EMI suppression in T1 circuits.

3. Can be configured for both 1:1 and 1:4 impedance ratios.

4. "Hardened" to withstand a 6 A current for 1 second with no damage.

5. Precision adjustable tuned transformer used in LC tank circuit for T1 timing recovery. Minimum Q = 160 at 1.544 MHz.

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Through-Hole Transformers for DSI Devices

Communication		Trans	mit Transformer	s R	eceive Transfor	mers	
Device	Application	Code	Comcode	Note	Code	Comcode	Note
T7288	CEPT	2745CA	106445711	1	2745AF2	106004047	1
		2741H2	106003999	2	2741G2	106003973	2
T7289, T7289A	DS1	2745G3	106696297		2745AF2	106004047	1
		2745AK2	107154676	2	2741G2	106003973	2
		2745AL2	107194326	3			
T7290A	T1	2745C2	106004013		2745AF2	106004047	1
		2745CA	106445711	1	2741G2	106003973	2
		2741H2	106003999	2			
	DS1	2745AJ2	106727605		2745AF2	106004047	1
					2741G2	106003973	2
	CEPT	2745CA	106445711	1	2745AF2	106004047	1
		2741H2	106003999	2	2741G2	106003973	2
T7690	DS1	2745G3	106696297		2745G3	106696297	
	CEPT 75.1	2745AJ2	106727605		2745AJ2	106727605	
	CEPT 75.2	2745CA	106445711	1	2745CA	106445711	1
	CEPT 120	2745CA	106445711	1	2745CA	106445711	1
T7693	DS1	2664AL	107591851	1	2664AL	107591851	1
	CEPT 75 1	2664AK	107594988	1	2664AK	107594988	1
	CEPT 75 2	2664AJ	107542565	1	2664AJ	107542565	1
	CEPT 120	2664AJ	107542565	1	2664AJ	107542565	1
T7296	DS3, E3, STS-1	2745AM2	107253056		2745AM2	107253056	

Surface-Mount Transformers for DSI Devices

Communication		Tra	Transmit Transformers			Receive Transformers		
Device	Application	Code	Comcode	Note	Code	Comcode	Note	
T7288	CEPT	2779C	107697815		2779B	107554701		
T7289, T7289A	DS1	2779G	107554693		2779B	107554701		
T7290A	T1	2779H	107728594		2779B	107554701		
	DS1	2779H	107728594	4	2779B	107554701		
	CEPT	2779H	107728594	4	2779B	107554701		
17690	DS1	2779G	107728594	4	2779G	107554693		
	CEPT 75.1	2779H	107728594	4	2779H	107728594	4	
	CEPT 75.2	2779H	107728594	4	2779H	107728594	4	
	CEPT 120	2779H	107728594	4	2779H	107728594	4	

1. Transformer with extra interwinding insulation for low capacitance.

2. Transformer with electrostatic shield for maximum circuit EMI suppression.

3. Hardened transformer. Center-tapped winding can handle 6 Amps for 1 second.

 Transformer has a dual turns ration (1:1.36 and 1:1.08) to accommodate all three applications of T7290A and all three CEPT applications the T7690.

TRANSFORMERS



General Notes: Dimensions in inches (millimeters) TH Standard Terminal Lengths: L = 0.110 (2.79), 0.155 (3.94) Terminals: Solder Coated Phosphor Bronze, 0.023 (0.584) Sq Typ

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Service	Primary Partner Benefit(s)	Other Benefits
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New Product Introduction	– Enhanced time to market	 Product design evaluation and proposals for cost reduction and reliability improvements Prototype builds, evaluation and testing to prove-in manufacturing processes Design For Manufacturability (DFM) reviews following the prototype build Each program assigned a dedicated project manager who leads a cross-functional project team throughout the life of the program Concurrent approach provides smooth transition to production
Supply Line Management	– Lower total cost of ownership – Enhanced flexibility	 Component and supplier evaluation for passive, active, PWB, and custom parts Material planning and procurement leverages approved vendors and corporate contracts to improve delivery, quality, and cost performance. Customized scheduling and production planning to meet customer order fulfillment needs (MRP II-based) EDI support Global purchasing support
Manufacturing	– Improved quality and reliability – Technology leverage	 Flexible, state-of-the-art facilities operated by a highly skilled, experienced work force. Wide range of printed-circuit assembly capabilities—from 12-mil pitch surface mount to large discrete through-hole devices Ball grid array (BGA) and 0402 passive assembly Dedicated final-system assembly areas and processes utilizing progressive assembly and material buffering Jointly developed test strategies identify the appropriate combination of cost-effective tests
Quality	 Improved quality and reliability Lower total cost of ownership 	 Total Quality Management (TQM) approach ISO 9002-certified manufacturing facilities BellCore-compliant EPA, UL, CSA, FCC, VDE product-specific certification support (BABT in 1995)

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- Legendary quality and reliability
- AT&T's global brand recognition and market leverage

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	 Secure voice/data equipment
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	– WLAN, WPBX
	 Air-to-ground communications equipment
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	– Video servers
	 Computer Telephony Integrated (CTI) Terminals

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Seller's obligations hereunder are conditioned upon: (i) Buyer giving Seller written notice within thirty days of any such claim; (ii) Seller having complete control of the defense and settlement thereof, (iii) Buyer cooperating fully with Seller to facilitate the defense or settlement of such claim; and (iv) Buyer's full compliance with this Agreement.

Notwithstanding the foregoing, Seller shall have no obligation to defend or settle any claim, and Buyer shall indemnify and save harmless Seller and its suppliers and affiliated companies from all costs, expenses, liabilities, and claims, for any such claim: (i) arising from Seller's compliance with Buyer's specifications, designs, or instructions; or (ii) relating to any Product furnished hereunder in combination with item(s), whether or not furnished by Seller, even if such combination results from the Product's necessary or inherent use or the use for which the Product is purchased.

The sale of any Product by Seller shall not in any way confer upon Buyer, or upon anyone claiming under Buyer, any license (expressly, by implication, by estoppel or otherwise) under any patent claim of Seller or others covering or relating to any combination, machine, or process in which such Product is or might be used, or to any process or method of making such Product.

THE FOREGOING STATES THE SOLE AND EXCLUSIVE REMEDY AND OBLIGATION OF THE PARTIES HERETO FOR INFRINGEMENT OR OTHER VIOLATION OF ANY INTELLECTUAL PROPERTY RIGHTS ARISING OUT OF THIS AGREE-MENT AND IS IN LIEU OF ALL WARRANTIES, EXPRESS, IMPLIED, OR STATUTORY, IN REGARD THERETO.

10. EXPORT CONTROL — Buyer acknowledges that the products sold under this Agreement and technical information transmitted in connection therewith may be subject to export restrictions under applicable law, including the U.S. Department of Commerce Export Administration Regulations ("Regulations"), and Buyer agrees to comply fully with same. Buyer assures Seller that it will not transmit, sell, transfer, or convey any such products, technical information or software, or goods produced through the use of same, to any country, or citizen or resident of a country, other than the United States without first securing the written consent, if required, of the U.S. Department of Commerce.

11. EXCLUSIVE REMEDIES AND LIMITATIONS OF LIABILITY

A. FOR PURPOSES OF THE EXCLUSIVE REMEDIES AND LIMITATIONS OF LIABILITY SET FORTH IN THIS SECTION 11, SELLER SHALL BE DEEMED TO INCLUDE AMERICAN TELEPHONE AND TELEGRAPH COMPANY, ITS SUBSIDIAR-IES AND AFFILIATES AND THE DIRECTORS, OFFICERS, EMPLOYEES, AGENTS, REPRESENTATIVES, SUBCONTRAC-TORS, AND SUPPLIERS OF EACH OF THEM; AND "DAMAGES" SHALL BE DEEMED TO REFER COLLECTIVELY TO ALL INJURY, DAMAGE, LOSS, OR EXPENSE INCURRED.

B. SELLER'S ENTIRE LIABILITY AND BUYERS' EXCLUSIVE REMEDIES AGAINST SELLER FOR ANY DAMAGES CAUSED BY ANY PRODUCT DEFECT OR FAILURE, OR ARISING FROM THE PERFORMANCE OR NONPERFORMANCE OF ANY WORK, REGARDLESS OF THE FORM OF ACTION, WHETHER IN CONTRACT, TORT INCLUDING NEGLIGENCE, STRICT LIABILITY, OR OTHERWISE SHALL BE:

- 1. FOR INFRINGEMENT, THE REMEDIES SET FORTH IN SECTION 9.
- 2. FOR FAILURE OF PRODUCT OR WORK PERFORMED, THE REMEDIES STATED IN SECTION 7.
- 3. FOR DELAYS IN DELIVERY, NONE UNLESS THE DELIVERY IS DELAYED BY MORE THAN THIRTY (30) DAYS BY CAUSES NOT ATTRIBUTABLE EITHER TO BUYER OR TO FORCE MAJEURE CONDITIONS, IN WHICH CASE BUYER SHALL HAVE THE RIGHT, AS ITS SOLE REMEDY, TO CANCEL THE ORDER WITH-OUT INCURRING TERMINATION CHARGES.
- 4. FOR DAMAGES TO REAL OR TANGIBLE PERSONAL PROPERTY OR FOR BODILY INJURY OR DEATH TO ANY PERSON PROXIMATELY CAUSED BY SELLER, BUYER'S RIGHT TO PROVEN DIRECT DAMAGES.
- 5. FOR CLAIMS OTHER THAN SET FORTH ABOVE, SELLER'S LIABILITY SHALL BE LIMITED TO DIRECT DAM-AGES THAT ARE PROVEN, IN AN AMOUNT NOT TO EXCEED \$100,000.

AT&T MICROELECTRONICS TERMS AND CONDITIONS

C. NOTWITHSTANDING ANY OTHER PROVISION OF THIS AGREEMENT, SELLER SHALL NOT BE LIABLE FOR INCI-DENTAL, INDIRECT, SPECIAL, OR CONSEQUENTIAL DAMAGES OR FOR LOST PROFITS, SAVINGS, OR REVENUES OF ANY KIND, WHETHER OR NOT SELLER HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. THIS PRO-VISION SHALL SURVIVE FAILURE OF AN EXCLUSIVE REMEDY.

12. MEDICAL AND LIFE SUPPORT APPLICATIONS — Seller does not recommend the use of any products for medical or life support applications wherein a failure or malfunction of the product may directly threaten life or cause injury and Seller will not knowingly sell its products for such use except pursuant to a written exception to this policy granted on a case-by-case basis. No warranty is made with respect to any such medical or life support use of any product.

13. ASSIGNMENT — Buyer shall not assign this Agreement or any rights or obligations hereunder without the prior written consent of the Seller. Any attempted assignment without the Seller's consent shall be void and ineffective.

14. NON-WAIVER — No course of dealing or failure of either party to strictly enforce any term, right, or condition of this Agreement shall be construed as a waiver of such term, right or condition.

15. FORCE MAJEURE — Except with respect to Buyer's obligation to make timely payments when due, neither party shall be held responsible for any delay or failure in performance of any part of this Agreement to the extent such delay or failure is caused by fire, flood, explosion, war, strike, embargo, government requirement, civil or military authority, act of God, nature or the public enemy, inability to secure material or transportation facilities, inadequate yield of products despite Seller's reasonable efforts, act or omission of carriers or any other causes beyond its reasonable control. Seller may, in the event of any such circumstance, allocate at its sole discretion its available production output among itself and its other customers, including at Seller's option those not under contract.

16. CHOICE OF LAW — The construction, interpretation, and performance of this Agreement shall be governed by the substantive laws, but not the conflicts of law, of the State of New York. The U.N. Convention on Contracts for the International Sales of Goods shall not apply to the sale of product hereunder.

17. ENTIRE AGREEMENT — Except for any written agreement between the parties relating to confidentiality of proprietary information, the terms and conditions contained in this Agreement supersede all prior oral or written understandings between the parties and shall constitute the entire Agreement between the parties with respect to the subject matter of this Agreement. This Agreement shall not be modified or amended except by a writing signed by Buyer and Seller.

For additional information, contact your AT&T Account Manager or the following:

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